

Blue Moutain KIWB1/B2

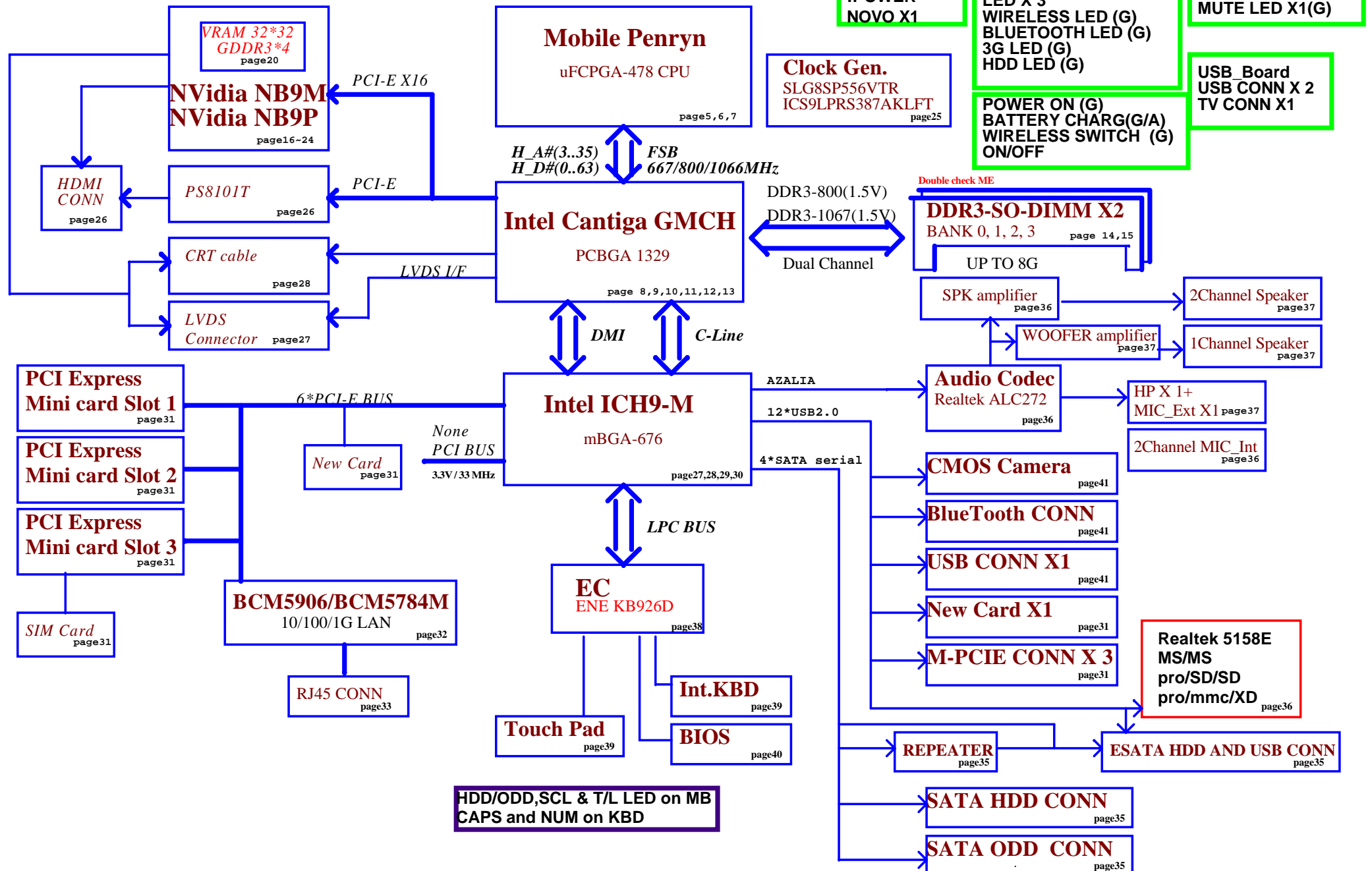
Schematics Document

Mobile Penryn uFCPGA with Intel
Cantiga_GM/PM+ICH9-M core logic

REV:0.1

Security Classification	Compal Secret Data			Compal Electronics,Ltd.		
Issued Date	2008/03/25	Deciphered Date	2008/04/	Title Cover Sheet		
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size Custom	Document Number KIWB1/B2_LA4601P	Rev 0.1
				Date: Thursday, June 26, 2008Sheet 1 of 52		

ZZZ1
15.6W_PCB_LA4601P



Security Classification		Compal Secret Data				Compal Electronics, Inc.					
Issued Date		2008/03/24		Deciphered Date		2008/04/		Title			
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.						MB Block Diagram					
						Size		Document Number		Rev	
						Custom		KIWB1/B2 LA4601P		0.1	
Date:		Thursday, June 26, 2008		Sheet		2		of 52			

DDR3 Voltage Rails

<div>power plane</div> <div>State</div>	+B	+5VALW +3VALW	+1.5V +1.8V +0.75V	+5VS +3VS +1.5VS +1.1VS +VCCP +CPU_CORE +VGA_CORE +1.8VS
S0	○	○	○	○
S1	○	○	○	○
S3	○	○	○	✗
S5 S4/AC	○	○	✗	✗
S5 S4/ Battery only	○	✗	✗	✗
S5 S4/AC & Battery don't exist	✗	✗	✗	✗

SMBUS, SPI and I2C Control Table

	SOURCE	HDMI	LVDS	CRT	HDCP	SERIAL EEPROM	NEW CARD	CLK GEN	CAP sensor	Mini CARD1	Mini CARD2	BATT	THERMAL SENSOR (VGA)	THERMAL SENSOR (CPU)
EC_SMB_CK1 EC_SMB_DA1	KB926	X	X	X	X	V	X	X	X	X	X	V	V	X
EC_SMB_CK2 EC_SMB_DA2	KB926	X	X	X	X	X	X	X	V	X	X	X	V	V
ICH_SMBCLK ICH_SMBDAT	ICH9	X	X	X	X	X	V	V	X	V	V	X	X	X
LVDS_SCL LVDS_SDA	Cantiga	X	V	X	X	X	X	X	X	X	X	X	X	X
GMCH_CRT_CLK GMCH_CRT_DAT	Cantiga	X	X	V	X	X	X	X	X	X	X	X	X	X
HDMICLK_NB HDMIDAT_NB	Cantiga	V	X	X	X	X	X	X	X	X	X	X	X	X
VGA_DDCCLK VGA_DDCDATA	VGA	X	X	V	X	X	X	X	X	X	X	X	X	X
VGA_LVDS_SCL VGA_LVDS_DAT	VGA	X	V	X	X	X	X	X	X	X	X	X	X	X
VGA_HDMI_SCL VGA_HDMI_DAT	VGA	V	X	X	X	X	X	X	X	X	X	X	X	X
HDCP_SMB_CK1 HDCP_SMB_DA1	VGA	X	X	X	X	V	X	X	X	X	X	X	X	X
FSEL#SPICS#_SB FRD#SPI_SO_SB SPI_CLK_SB FWR#SPI_SI_SB	ICH9	X	X	X	X	V	X	X	X	X	X	X	X	X
FSEL#SPICS# FRD#SPI_SO SPI_CLK FWR#SPI_SI	KB926	X	X	X	X	V	X	X	X	X	X	X	X	X

VGA and DDR2 Voltage Rails (NB9M-GS)

State \ power plane			+1.8V	+3VS +VGA_CORE +1.1VS
S0	○	○	○	○
S1	○	○	○	○
S3	○	○	○	×
S5 S4/AC	○	○	×	×
S5 S4/ Battery only	○	×	×	×
S5 S4/AC & Battery don't exist	×	×	×	×

GPIO	I/O	ACTIVE	Function Description
GPI00	N/A	N/A	Available
GPI01	IN	-	Hot plug detect for IFP link C
GPI02	OUT	H	Panel Back-Light brightness(PWM)
GPI03	OUT	H	Panel Power Enable
GPI04	OUT	H	Panel Back-Light On/Off (PWM)
GPI05	OUT	-	GPU VID0
GPI06	OUT	-	GPU VID1
GPI07	OUT	-	GPU VID2 or MEM VID
GPI08	I/O	L	Thermal Catastrophic Overtemp
GPI09	OUT	L	FAN control and/or Thermal Alert (PWM)
GPI010	OUT		Memory VREF switch
GPI011	I/O	L	SLI raster sync
GPI012	IN	-	AC power detect pin
GPI013	OUT	-	Power supply control
GPI014	OUT	-	Power supply control
GPI015	IN	-	Hot plug detect for IFP link E
GPI016	IN	-	Dongle DVI Mode control for Primary Displayport
GPI017	IN	-	Dongle HDMI Mode control for Primary Displayport
GPI018	IN	-	Dongle DVI Mode control for Secondary Displayport
GPI019	IN	-	Dongle HDMI Mode control for Secondary Displayport
GPI020	IN	-	Hot plug detect for IFP link D
GPI021	IN	-	Hot plug detect for IFP link E
GPI022	IN	-	SLI swap ready signal
GPI023	N/A	N/A	Available

VRAM POWER SEQUENCE

GDDR3 FOR 4 UNIT = 5.4A

EDP at Tj = 97C*

Power Supply Rail		NB9P-GS		NB9P-GE2	
(V)		GDDR3	DDR2	GDDR3	DDR2
NVDD	Variable	20.65A	16.96A	18.47A	16.06A
FB_DLLAVDD	1.1	10mA			
FB_PLLAVDD	1.1	10mA			
IFPC_IOVDD	1.1	80mA			
IFPD_IOVDD	1.1	80mA			
IFPE_IOVDD	1.1	160mA			
IFPF_IOVDD	1.1	160mA			
PEX_IOVDD/Q	1.1	1550mA			
PEX_PLLVDD	1.1	90mA			
PLLVDD	1.1	45mA			
SP_PLLVDD	1.1	45mA			
VID_PLLVDD	1.1	45mA			
TOTAL	1.1	2.3A			
FBVDD/Q	1.8	3.37A	2.02A	3.21A	2.25A
IFPA_IOVDD	1.8	95mA			
IFPB_IOVDD	1.8	95mA			
IFPAB_PLLVDD	1.8	70mA			
IFPCD_PLLVDD	1.8	25mA			
IFPEF_PLLVDD	1.8	85mA			
TOTAL	1.8	5.76A	3.69A	5.47A	3.96A
DACA_VDD	3.3	110mA			
DACB_VDD	3.3	120mA			
DACC_VDD	3.3	110mA			
MIOA_VDDQ	3.3	10mA			
MIOB_VDDQ	3.3	10mA			
VDD33	3.3	150mA			
TOTAL	3.3	0.51A			

POWER SEQUENCE

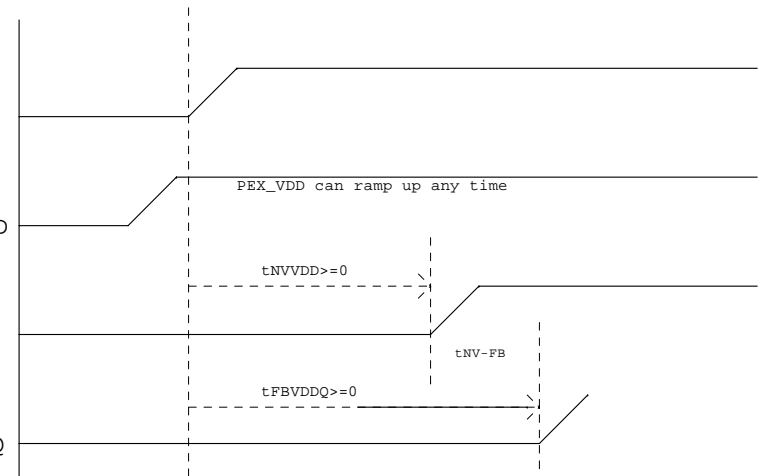
The ramp time for any rail must be more than 40us

(+3VS) VDD33

(1.1VS) PEX_VDD

(+VGA_CORE) NVDD

(1.8VS) FBVDDQ



Security Classification	Compal Secret Data			Title	
Issued Date	2008/03/24	Deciphered Date	2008/04/	VGA Notes List	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size B	Document Number
				KIWB1/B2_LA4601P	
				Date: Thursday, June 26, 2008	Rev 0.1
				Sheet 4	of 52

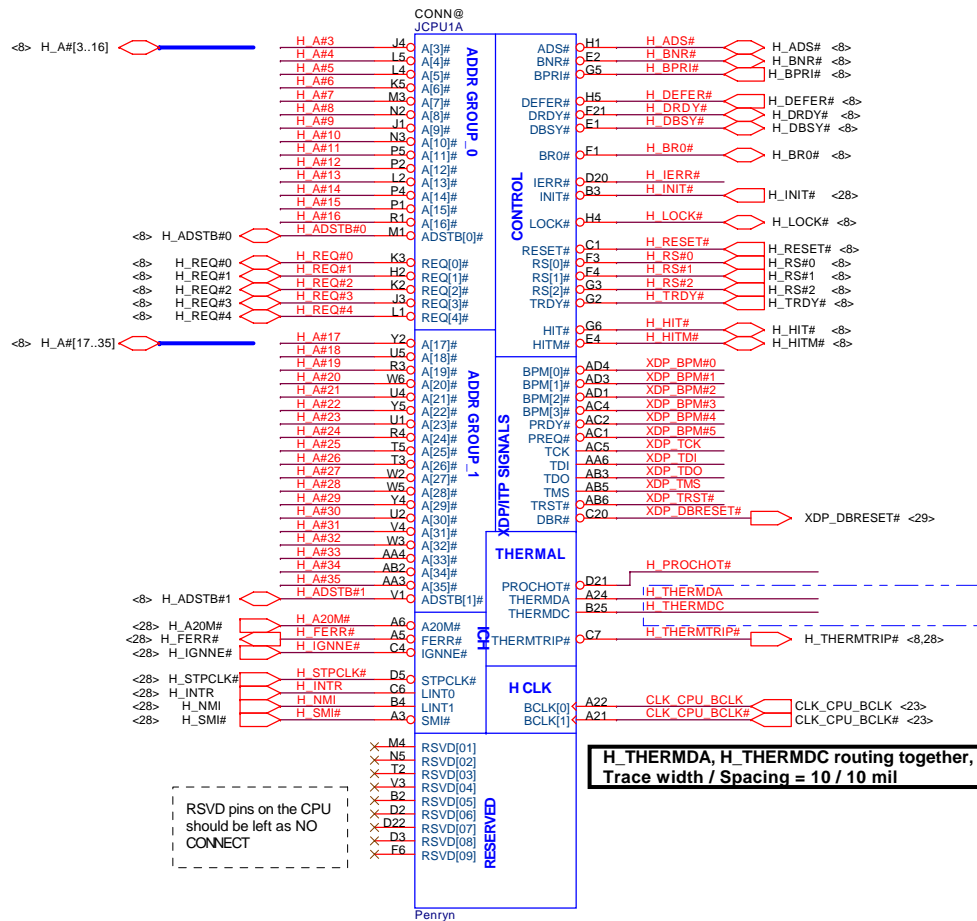
Compal Electronics, Inc.

VGA Notes List

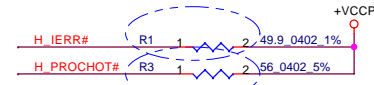
KIWB1/B2_LA4601P

Rev 0.1

Sheet 4 of 52

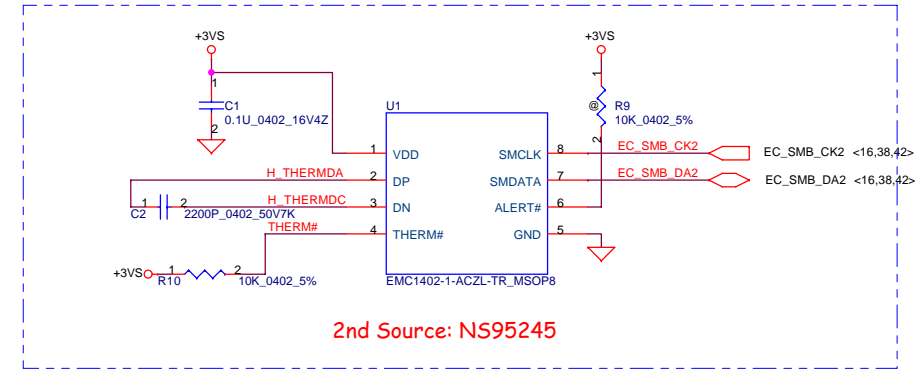
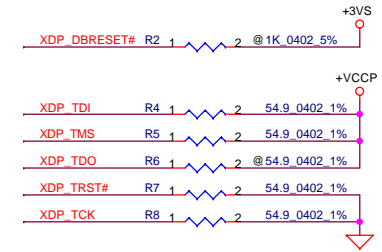


USE-->56Ω,NOT USE-->50Ω



USE-->68Ω,NOT USE-->56Ω

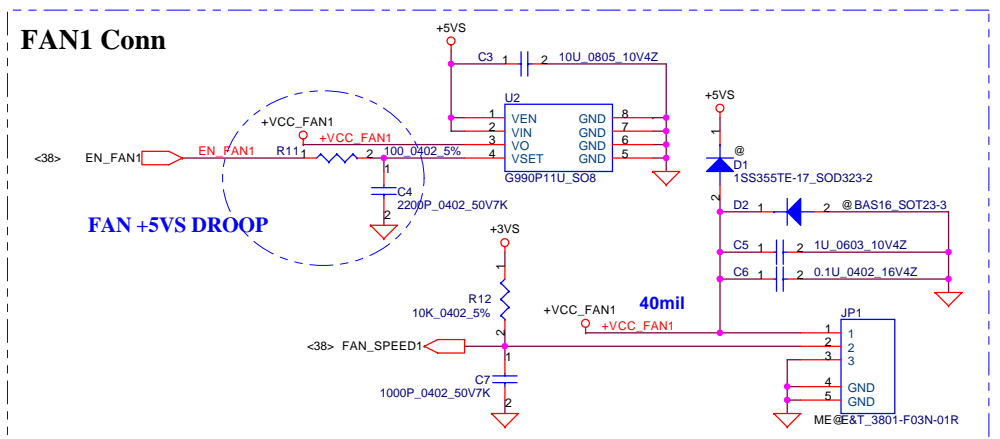
XDP Reserve



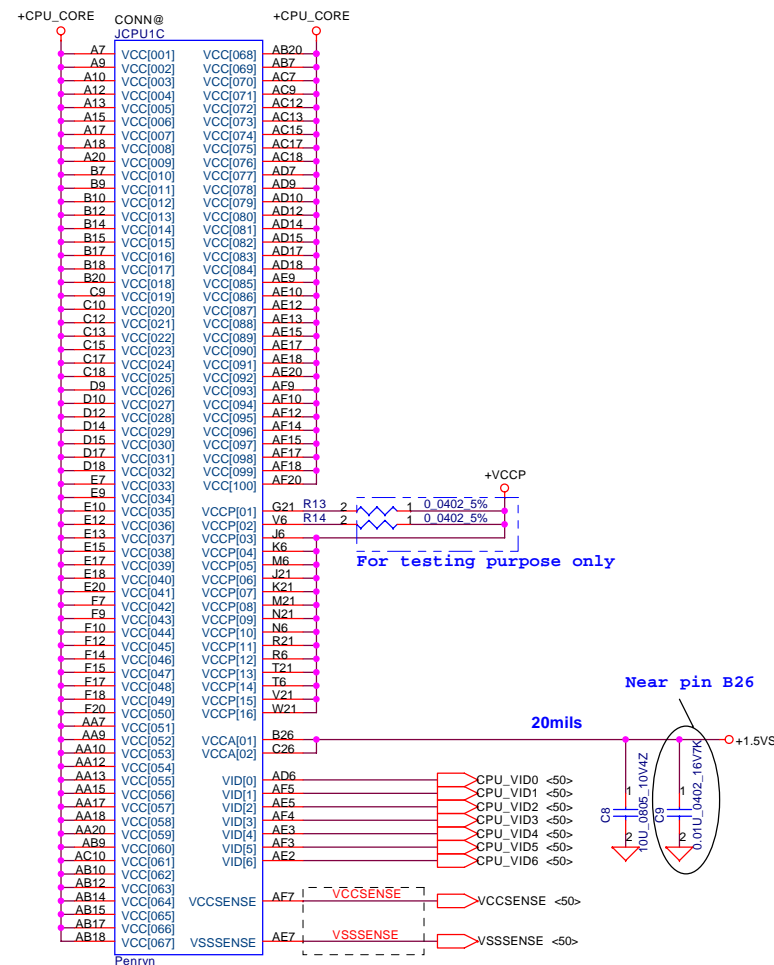
2nd Source: NS95245

Address:100_1100

FAN1 Conn



Security Classification		Compal Secret Data		Compal Electronics, Ltd.	
Issued Date	2008/03/25	Deciphered Date	2008/04/	Title	Penryn(1/3)
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
				Custom	KIWB3/B4 LA4551P
				Date: Monday, June 30, 2008	Rev 0.1
				Sheet 5 of 52	



Width=4 mil ,
Spacing: 15mil
(55Ohm)

TRACE CLOSELY CPU < 0.5'

COMP0, COMP2 layout : Width 18mils and Space 25mils (27.4Ohms)
COMP1, COMP3 layout : Width 5mils and Space 25mils (55Ohms)

FSB	BCLK	BSEL2	BSEL1	BSEL0
533	133	0	0	1
667	166	0	1	1
800	200	0	1	0
1067	266	0	0	0

Length match within 25 mils.
The trace width/space/other is
18/7/25.

Layout Note:
Route VCCSENSE and VSSSENSE traces at
27.4 Ohms with 50 mil spacing.
Place PU and PD within 1 inch of CPU.
Length matched to within 25 mils.

Close to CPU pin
within 500mils.

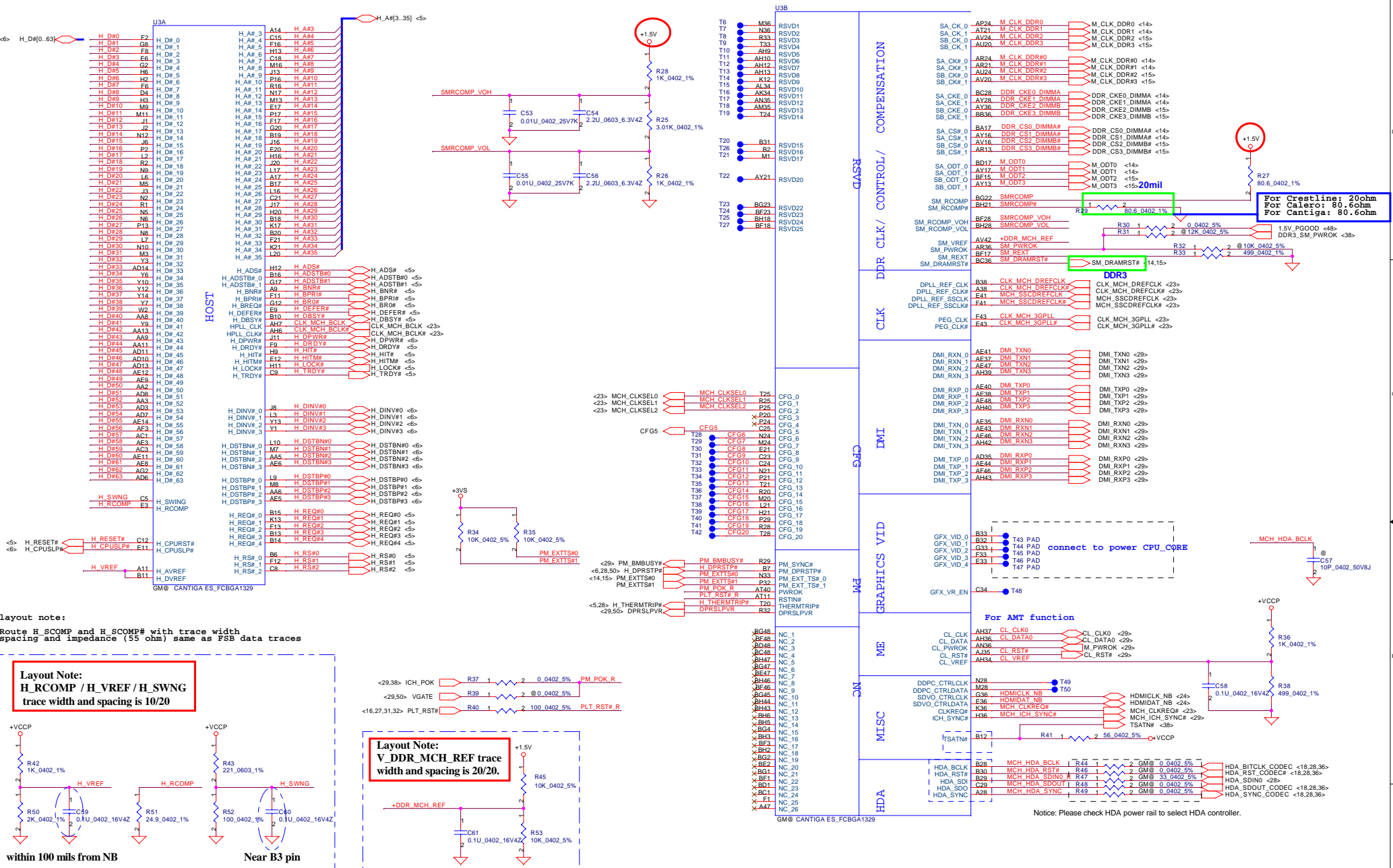
Compal Electronics, Inc.

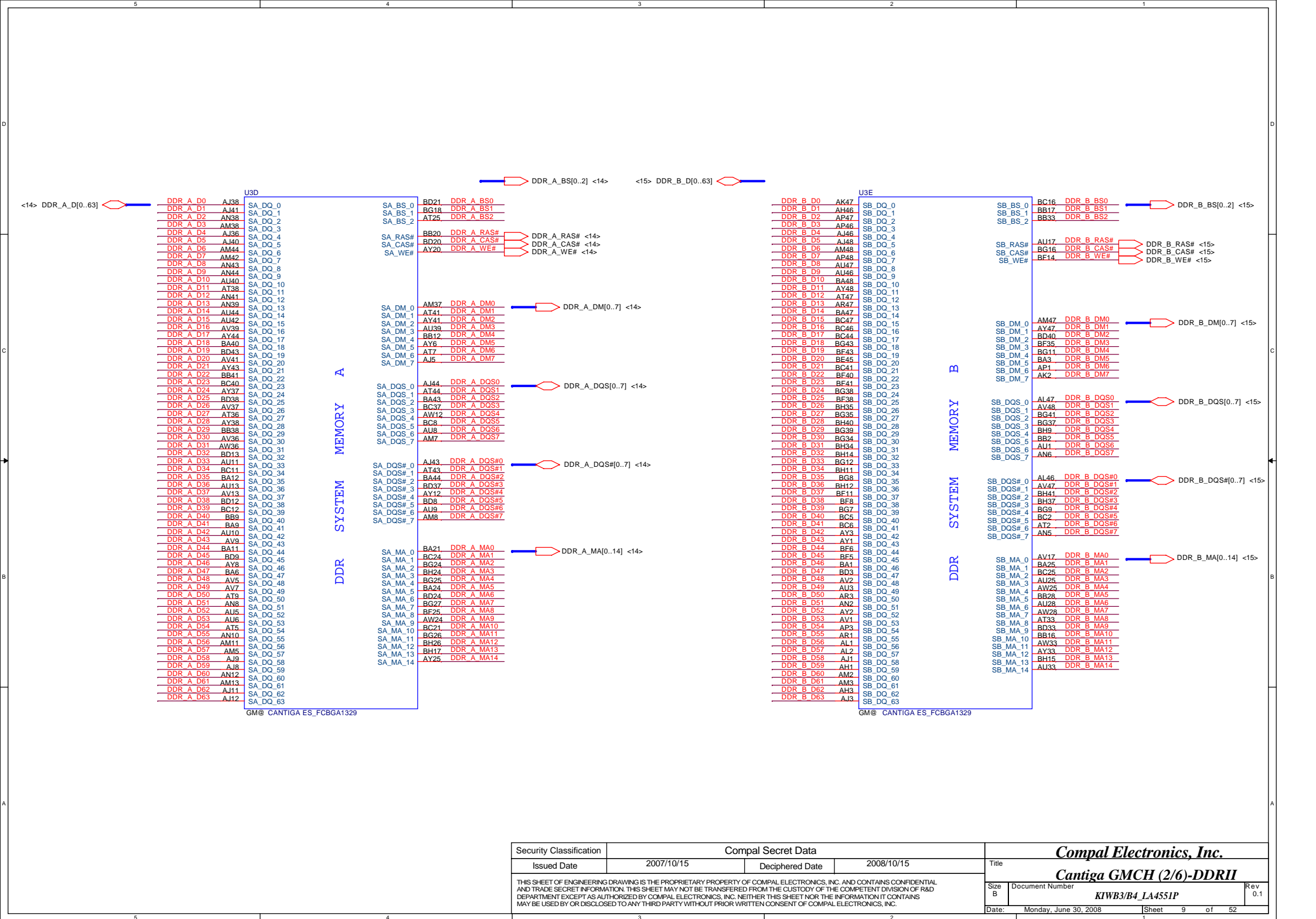
Penryn (2/3)

KIWB3/B4 LA4551P

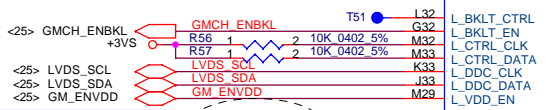
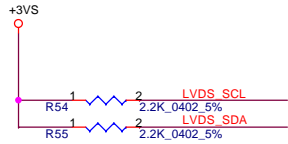
Rev	0,1
-----	-----

Date:	Monday, June 30, 2008	Sheet	6	of	52
-------	-----------------------	-------	---	----	----



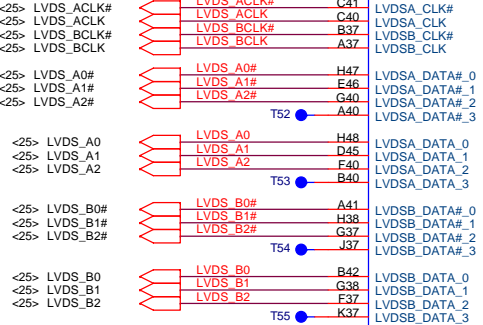


Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date		2007/10/15	Deciphered Date	2008/10/15	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Title	
				Cantiga GMCH (2/6)-DDRII	
Size		Document Number			Rev
B		KIWB3/B4_LA4551P			0.1
Date:		Monday, June 30, 2008		Sheet 9 of 52	

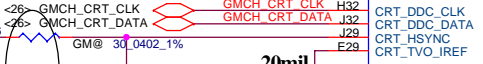
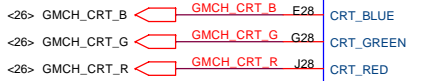
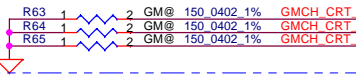


For Cantiga: 2.37kohm
For Crestline: 2.4kohm
For Calero: 1.5Kohm

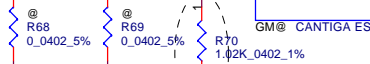
Note: All LVDS data signals and its compliments should be routed Differentially



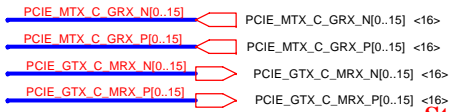
Layout Note: Place 150 Ω termination resistors close to GMCH



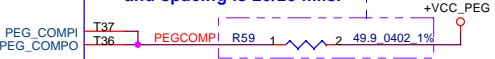
change R64,R65 from 33ohm to 30ohm by checklist2.0 & CRB1.0 05/08/08



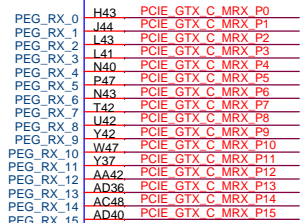
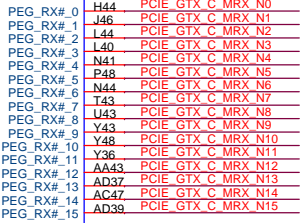
For Cantiga: 1.02kohm
For Crestline: 1.3kohm
For Calero: 255ohm



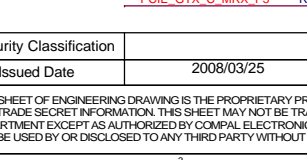
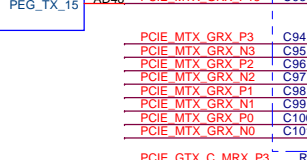
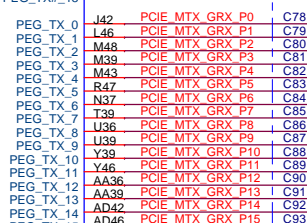
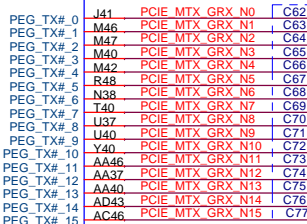
Place the resistor within 500mils (1.27mm) of the (G)MCH
PEGCOMP trace width and spacing is 20/25 mils.



Please check Power source if want support IAMT



CLOSE TO MCH



Strap Pin Table

CFG[2:0] FSB Freq select	000 = FSB 1066MHz 010 = FSB 800MHz 011 = FSB 667MHz Others = Reserved
CFG[4:3]	Reserved
CFG5 (DMI select)	0 = DMI x 2 1 = DMI x 4 *
CFG6	0 = (TLS)chipset suite with no confidentiality 1 = The iTPM Host Interface is disable *
CFG7 (Intel Management Engine Crypto strap)	0 = Reverse Lane, 15->0, 14->1 1 = Normal Operation, Lane Number in order *
CFG8	Reserved
CFG9 (PCIe Graphics Lane Reversal)	0 = Enable 1 = Disable *
CFG10 (PCIe Lookback enable)	0 = Reserved 01 = XOR Mode Enabled 10 = All Z Mode Enabled 11 = Normal Operation(Default) *
CFG11	Reserved
CFG[13:12] (XOR/ALLZ)	00 = Reserved 01 = XOR Mode Enabled 10 = All Z Mode Enabled 11 = Normal Operation(Default) *
CFG[15:14]	Reserved
CFG16 (FSB Dynamic ODT)	0 = Disabled 1 = Enabled *
CFG[18:17]	Reserved
CFG19 (DMI Lane Reversal)	0 = Normal Operation (Lane number in Order) 1 = Reverse Lane *
CFG20 (PCIe/SDVO concurrent)	0 = Only PCIe or SDVO is operational. * 1 = PCIe/SDVO are operating simu.

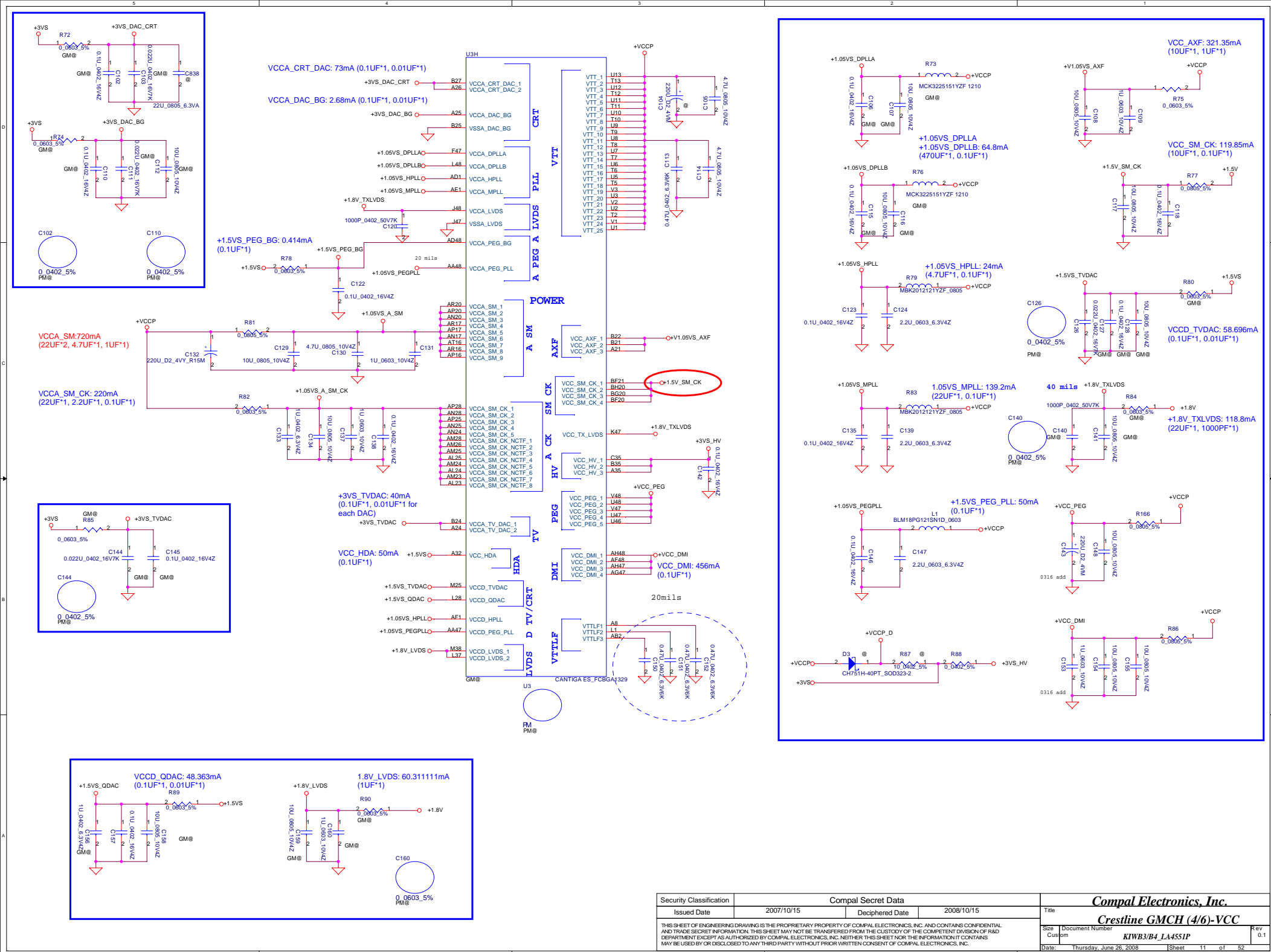
GRAPHICS

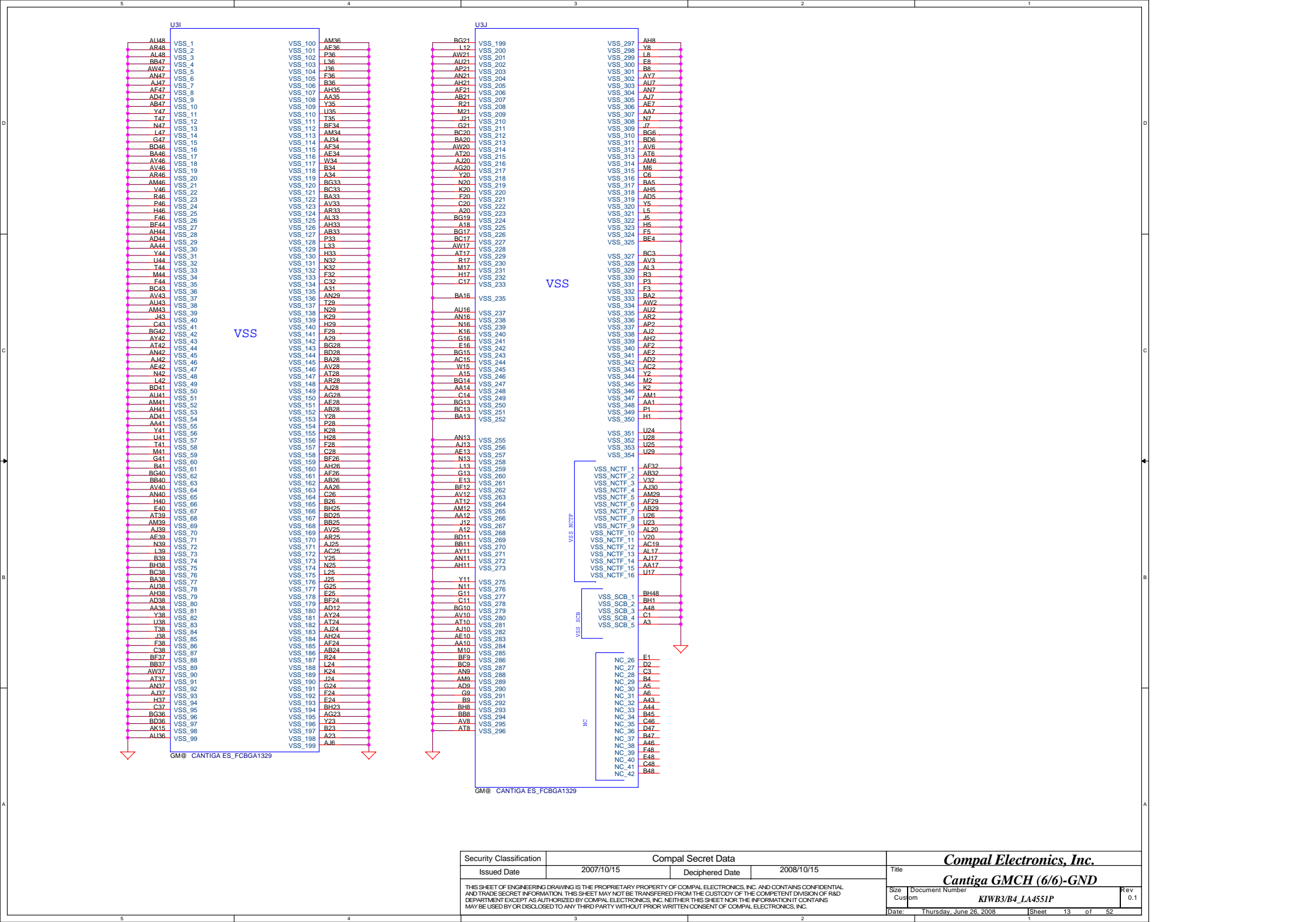
PCI-EXPRESS

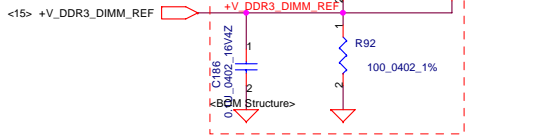
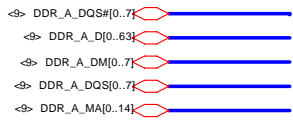
TV

VGA

Security Classification		Compal Secret Data		Compal Electronics, Ltd.	
Issued Date	2008/03/25	Deciphered Date	2008/04/	Title	Cantiga(3/6)-VGA/LVDS/TV
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
				Custom	KIWB3/B4 LA4551P
				Date	Monday, June 30, 2008
				Sheet	10 of 52

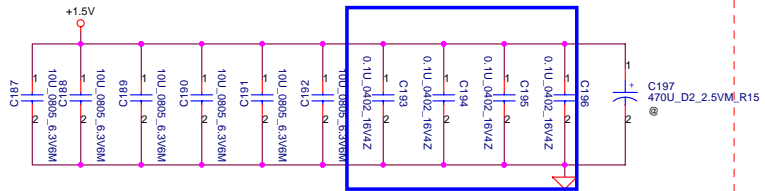




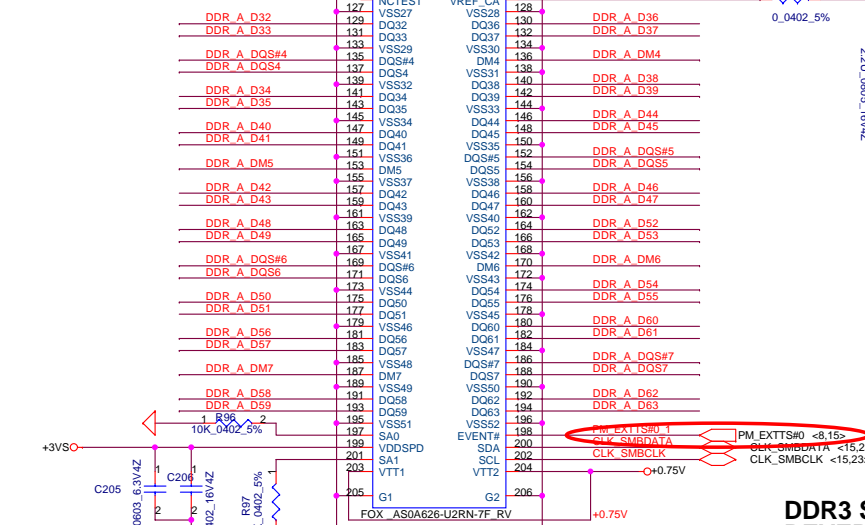
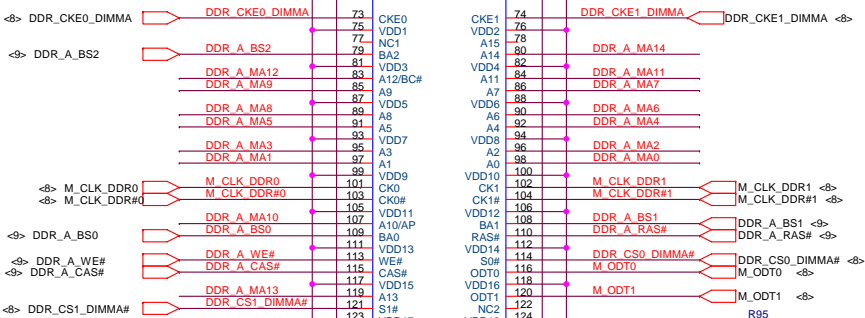
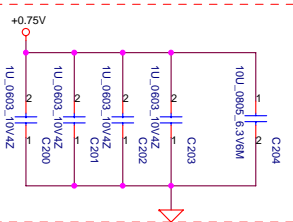


Layout Note:
Place near JP4

Layout Note: Place these 4 Caps near Command and Control signals of DIMMA



Layout Note:
Place near JP4.203 & JP4.204



DDR3 SO-DIMM A REVERSE

Security Classification		Compal Secret Data		Title	
Issued Date	2007/09/29	Deciphered Date	2007/09/29	Compal Electronics, Inc.	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSMITTED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				DDR3 SODIMM SLOT1	
Size	Custom	Document Number	KIWB1/B2_LA4601P		Rev
Date	Monday, June 30, 2008	Sheet	14	of	52

<9> DDR_B_DQS#[0..7]

<9> DDR_B_D[0..63]

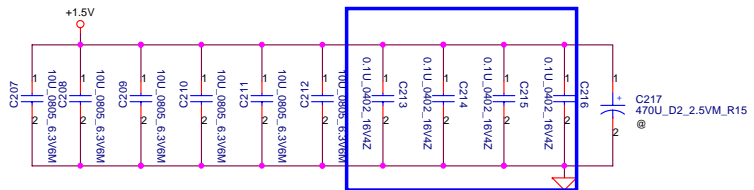
<9> DDR_B_DM[0..7]

<9> DDR_B_DQS[0..7]

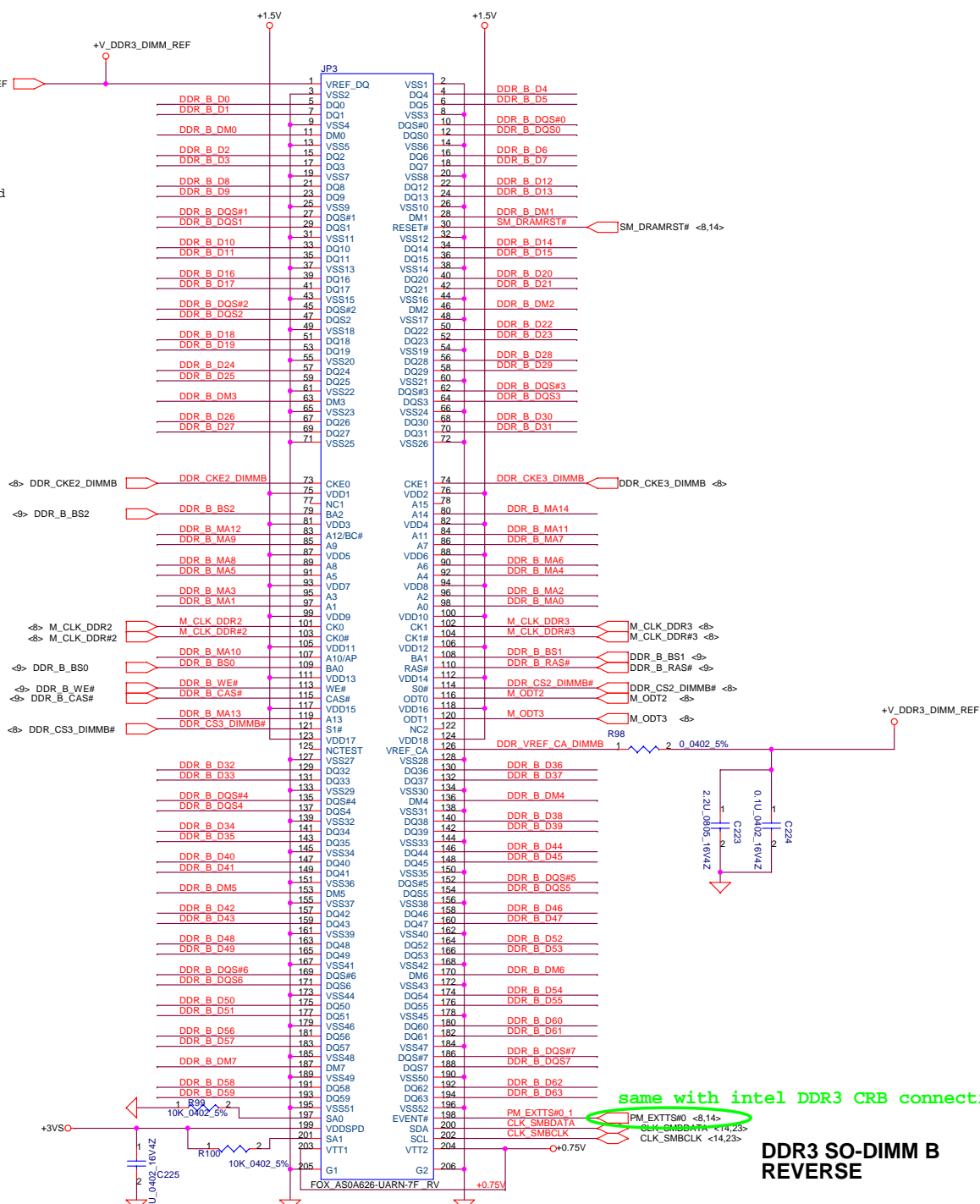
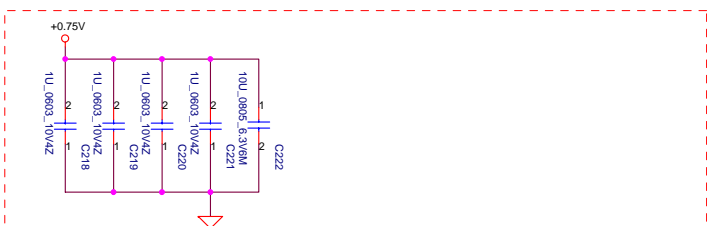
<9> DDR_B_MA[0..14]

Layout Note:
Place near JP5

Layout Note: Place these 4 Caps near Command
and Control signals of DIMMA



Layout Note:
Place near JP5.203 & JP5.204

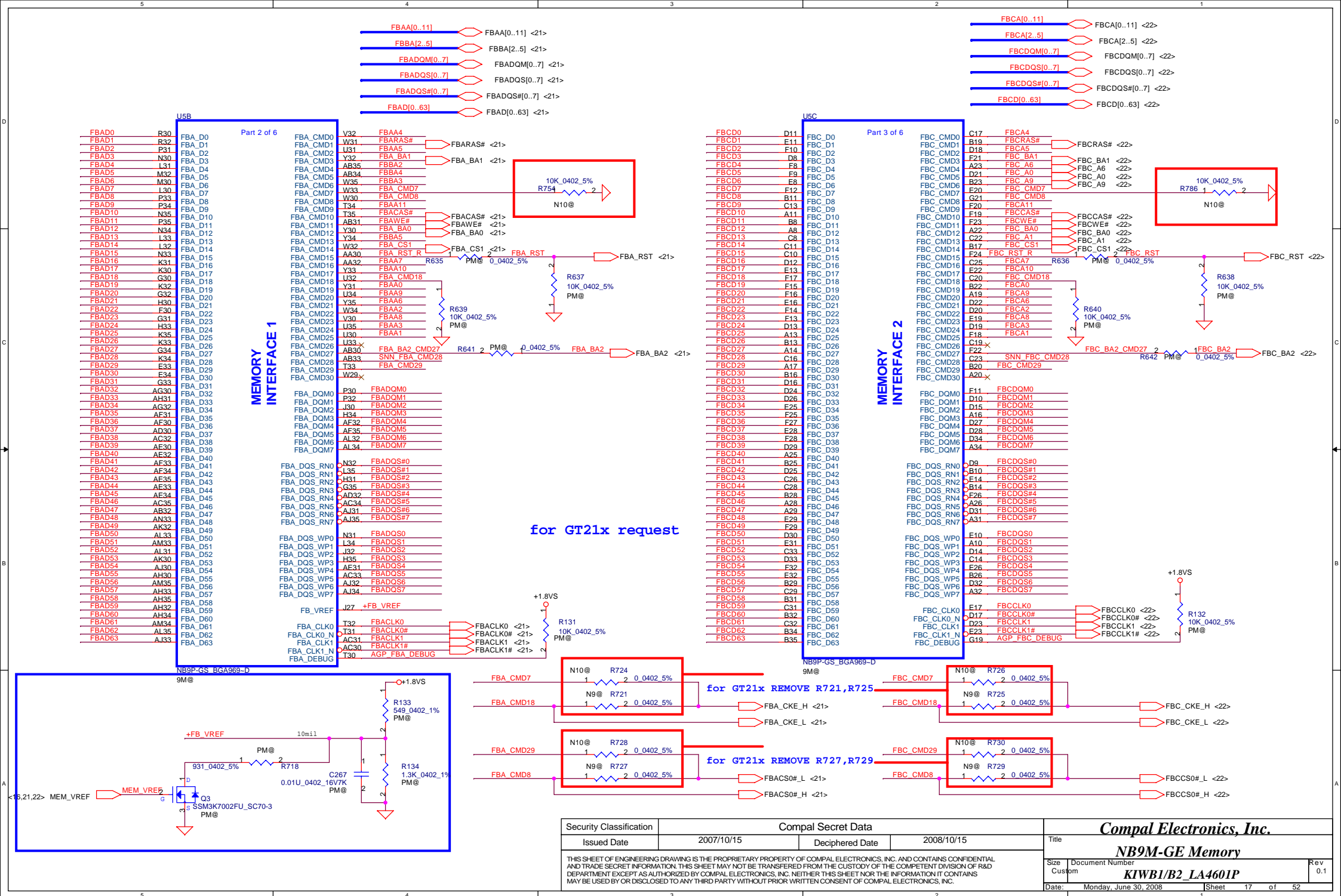


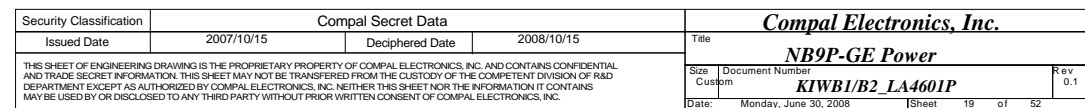
same with intel DDR3 CRB connection

DDR3 SO-DIMM B
REVERSE

Security Classification		Compal Secret Data		Title	
Issued Date	2007/09/29	Deciphered Date	2007/09/29	Compal Electronics, Inc.	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THE SHEET IS LOANED TO THE CUSTOMER BY THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				DDR3II-SODIMM SLOT2	
Size		Document Number		KIWB1/B2_LA4601P	
Date		Monday, June 30, 2008		Rev 1.0	
Sheet		15		of 52	







B3	GND_0	GND_100	V31
B6	GND_1	GND_101	Y11
B9	GND_2	GND_102	Y13
B12	GND_3	GND_103	Y15
B15	GND_4	GND_104	Y19
B21	GND_5	GND_105	Y21
B24	GND_6	GND_106	Y23
B27	GND_7	GND_107	Y25
B30	GND_8	GND_108	AA2
B33	GND_9	GND_109	AA5
C2	GND_10	GND_110	AA11
C34	GND_11	GND_111	AA12
E6	GND_12	GND_112	AA13
E9	GND_13	GND_113	AA14
E12	GND_14	GND_114	AA15
E15	GND_15	GND_115	AA16
E18	GND_16	GND_116	AA17
E24	GND_17	GND_117	AA18
E27	GND_18	GND_118	AA19
E30	GND_19	GND_119	AA20
F2	GND_20	GND_120	AA21
F5	GND_21	GND_121	AA22
F31	GND_22	GND_122	AA23
F34	GND_23	GND_123	AA24
J2	GND_24	GND_124	AA25
J5	GND_25	GND_125	AA34
J31	GND_26	GND_126	AB12
J34	GND_27	GND_127	AB14
L9	GND_28	GND_128	AB16
M2	GND_29	GND_129	AB18
M5	GND_30	GND_130	AB20
M11	GND_31	GND_131	AB22
M13	GND_32	GND_132	AB24
M15	GND_33	GND_133	AC9
M17	GND_34	GND_134	AD2
M19	GND_35	GND_135	AD5
M21	GND_36	GND_136	AD11
M25	GND_37	GND_137	AD13
M31	GND_38	GND_138	AD15
M34	GND_39	GND_139	AD17
N11	GND_40	GND_140	AD21
N12	GND_41	GND_141	AD23
N13	GND_42	GND_142	AD25
N14	GND_43	GND_143	AD31
N15	GND_44	GND_144	AD34
N16	GND_45	GND_145	AE11
N17	GND_46	GND_146	AE12
N18	GND_47	GND_147	AE13
N19	GND_48	GND_148	AE14
N20	GND_49	GND_149	AE15
N21	GND_50	GND_150	AE16
N22	GND_51	GND_151	AE17
N23	GND_52	GND_152	AE18
N24	GND_53	GND_153	AE19
N25	GND_54	GND_154	AE20
P12	GND_55	GND_155	AE21
P14	GND_56	GND_156	AE22
P16	GND_57	GND_157	AE23
P18	GND_58	GND_158	AE24
P20	GND_59	GND_159	AE25
P22	GND_60	GND_160	AG2
P24	GND_61	GND_161	AG5
R2	GND_62	GND_162	AG31
R5	GND_63	GND_163	AG34
R31	GND_64	GND_164	AK2
R34	GND_65	GND_165	AK5
T11	GND_66	GND_166	AK31
T13	GND_67	GND_167	AK34
T15	GND_68	GND_168	AL6
T17	GND_69	GND_169	AL9
T19	GND_70	GND_170	AL12
T21	GND_71	GND_171	AL15
T23	GND_72	GND_172	AL18
T25	GND_73	GND_173	AL21
U11	GND_74	GND_174	AL24
U12	GND_75	GND_175	AL27
U13	GND_76	GND_176	AL30
U14	GND_77	GND_177	AN2
U15	GND_78	GND_178	AN34
U16	GND_79	GND_179	AP3
U17	GND_80	GND_180	AP6
U18	GND_81	GND_181	AP9
U19	GND_82	GND_182	AP12
U20	GND_83	GND_183	AP15
U21	GND_84	GND_184	AP18
U22	GND_85	GND_185	AP21
U23	GND_86	GND_186	AP24
U24	GND_87	GND_187	AP27
U25	GND_88	GND_188	AP30
V2	GND_89	GND_189	AP33
V5	GND_90	GND_190	AK14
V9	GND_91	RFU_GND_0	K9
V12	GND_92	RFU_GND_1	AD19
V14	GND_93	GND_SENSE	
V16	GND_94		
V18	GND_95		
V20	GND_96		
V22	GND_97		
V24	GND_98		
V24	GND_99		

GND

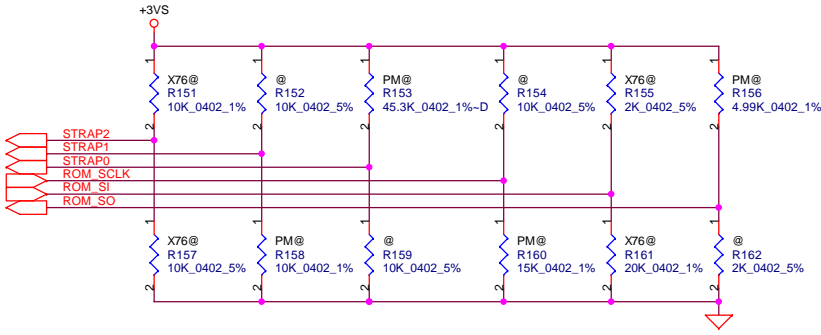
Part 6 of 6

FB_CAL_PU_GND
FB_CAL_TERM_GND
MIOA_CAL_PU_GND
MIOB_CAL_PU_GND



Place Components Close to BGA

<18> STRAP2
<18> STRAP1
<18> STRAP0
<18> ROM_SCLK
<18> ROM_SI
<18> ROM_SO



GB1 Family GPU Strap Options

GPU	FB Memory	ROM_SO	ROM_SCLK	ROM_SI	STRAP2	STRAP1	STRAP0
NB9M-GS 128bit	Samsung	16Mx32	PU 5K PD 15K	PD 20K	PU 10K	PD 10K	PU 45K
		32Mx32	PU 5K PD 15K	PD 45K	PU 10K	PD 10K	PU 45K
	Hynix	16Mx32	PU 5K PD 15K	PD 15K	PU 10K	PD 10K	PU 45K
		32Mx32	PU 5K PD 15K	PD 35K	PU 10K	PD 10K	PU 45K
	Qimonda	16Mx32	PU 5K PD 15K	PD 10K	PU 10K	PD 10K	PU 45K
		32Mx32	PU 5K PD 15K	PD 30K	PU 10K	PD 10K	PU 45K

GPU	FB Memory	ROM_SO	ROM_SCLK	ROM_SI	STRAP2	STRAP1	STRAP0
NB9P-GE2 128bit	Samsung	16Mx32	PU 5K PD 15K	PD 20K	PU 5K	PD 10K	PU 45K
		32Mx32	PU 5K PD 15K	PD 45K	PU 5K	PD 10K	PU 45K
	Hynix	16Mx32	PU 5K PD 15K	PD 15K	PU 5K	PD 10K	PU 45K
		32Mx32	PU 5K PD 15K	PD 35K	PU 5K	PD 10K	PU 45K
	Qimonda	16Mx32	PU 5K PD 15K	PD 10K	PU 5K	PD 10K	PU 45K
		32Mx32	PU 5K PD 15K	PD 30K	PU 5K	PD 10K	PU 45K

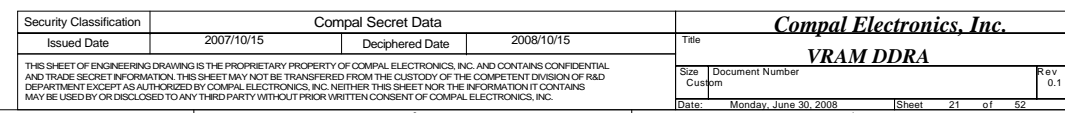
Component	Manufacturer	Compal PN	Compal X76 PN
GDDR3 VRAM (16M*32)	Hynix	X	X
	Qimonda	X	X
	Samsung	X	X
GDDR3 VRAM (32M*32)	Hynix	X	X
	Qimonda	SA000024N20	
	Samsung	SA00002R600	

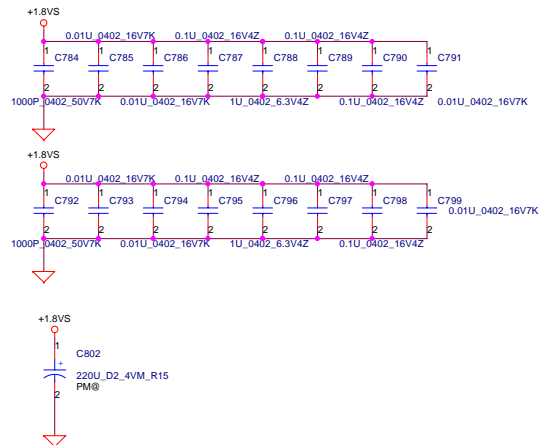
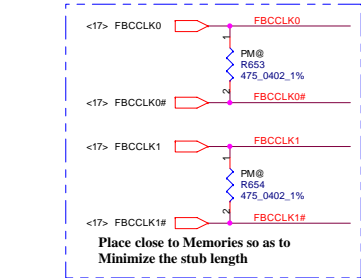
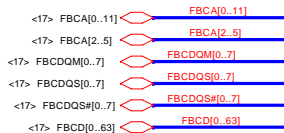
Memory/PKG	FBCAL_PU_GND	FBCAL_PD_VDDQ	FBCAL_TERM_GND
GDDR3	33.2ohm	44.2ohm	40.2ohm
GDDR3 BY N10			

To update for NV PUN-03304-001_V06 (2008/5/20)

Security Classification	Compal Secret Data			Compal Electronics, Ltd.	
Issued Date	2008/03/25	Deciphered Date	2008/04/	Title	
				NB9P-GE GND & STRAP	
				Size	Document Number
				Custom	Rev
				KIWB1/B2_LA4601P	
				Date: Monday, June 30, 2008	Sheet 20 of 52

THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

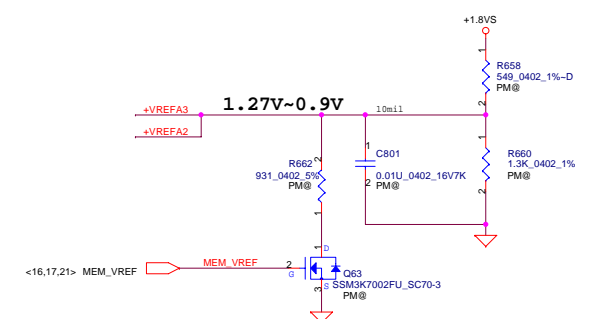
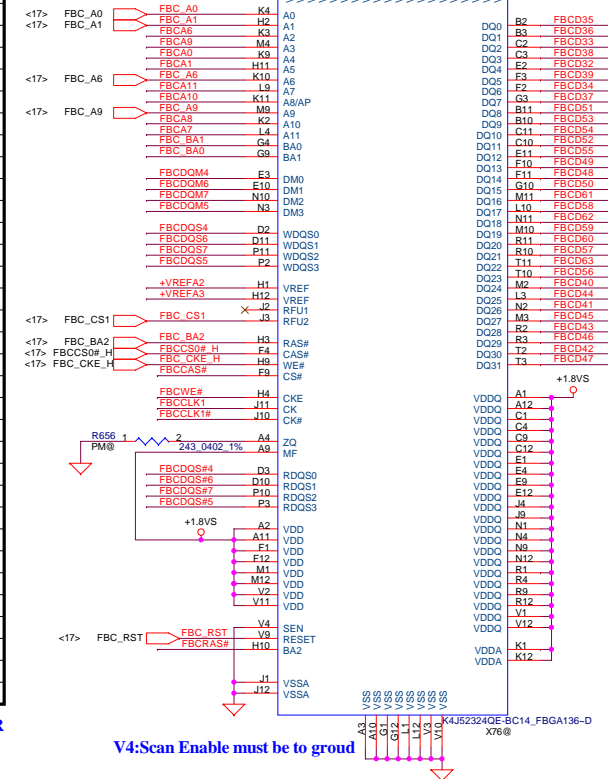




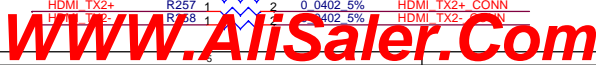
BGA 84 ADR/CMD MAPPING

Address	0..31	32..63
CMD0	A4	
CMD1	RAS*	BA2
CMD2	A5	
CMD3	BA1	BA0
CMD4		A6
CMD5		A0
CMD6		A9
CMD7	CS1* CKE	CS1*
CMD8	CS0*	CAS*
CMD9	A11	A11
CMD10	CAS*	CS*
CMD11	WE*	CKE*
CMD12	BA0	BA1
CMD13		A1
CMD14	A12	A12
CMD15	RST/ODT	RST/ODT
CMD16	A7	A7
CMD17	A10	A10
CMD18	CKE	WE#
CMD19	A0	A0
CMD20	A9	A9
CMD21	A6	A6
CMD22	A2	
CMD23	A8	A8
CMD24	A3	
CMD25	A1	A1
CMD26	A13	A13
CMD27	BA2	RAS#
CMD28	RFU0	RFU0
CMD29	RFU1 CS0*	RFU1
CMD30	RFU2	RFU2

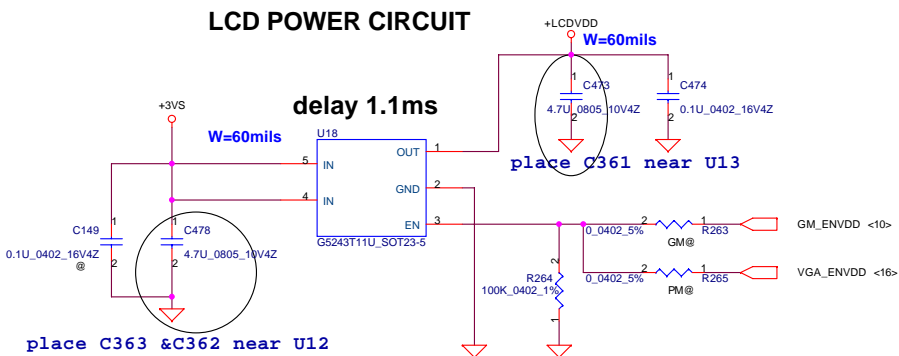
MIRROR



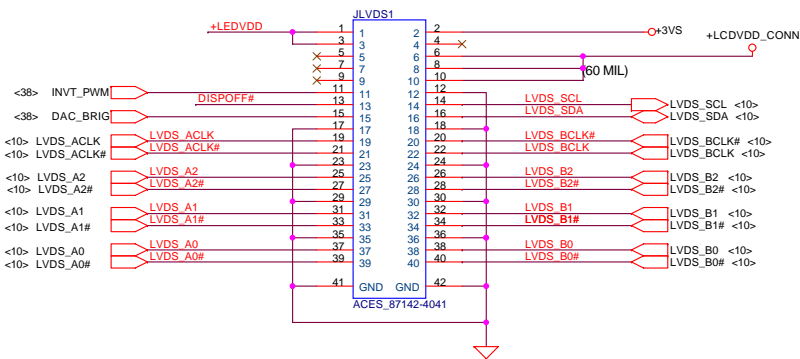
Security Classification	Compal Secret Data		Title		Compal Electronics, Inc.	
Issued Date	2007/10/15	Deciphered Date	2008/10/15	Size		Document Number
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Rev		0.1
Date: Monday, June 30, 2008				Sheet		22 of 52



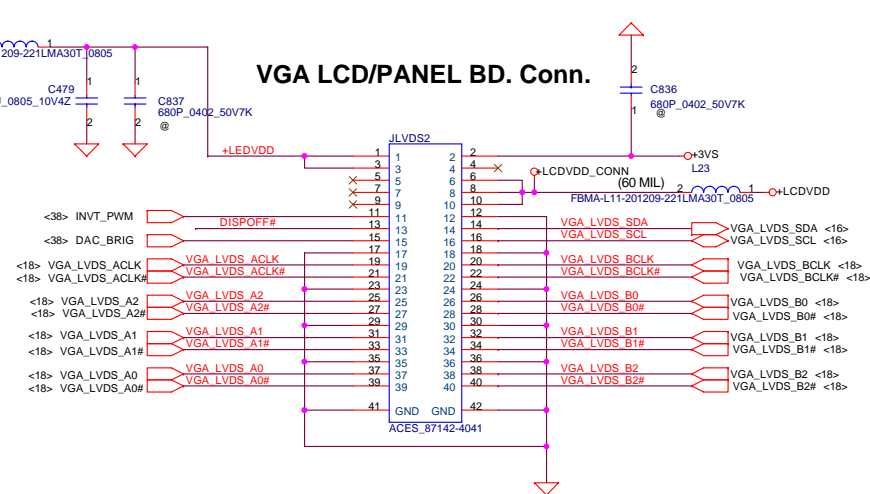
LCD POWER CIRCUIT



UMA LCD/PANEL BD. Conn.

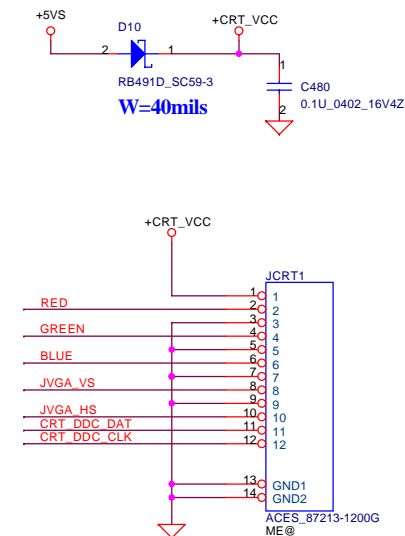
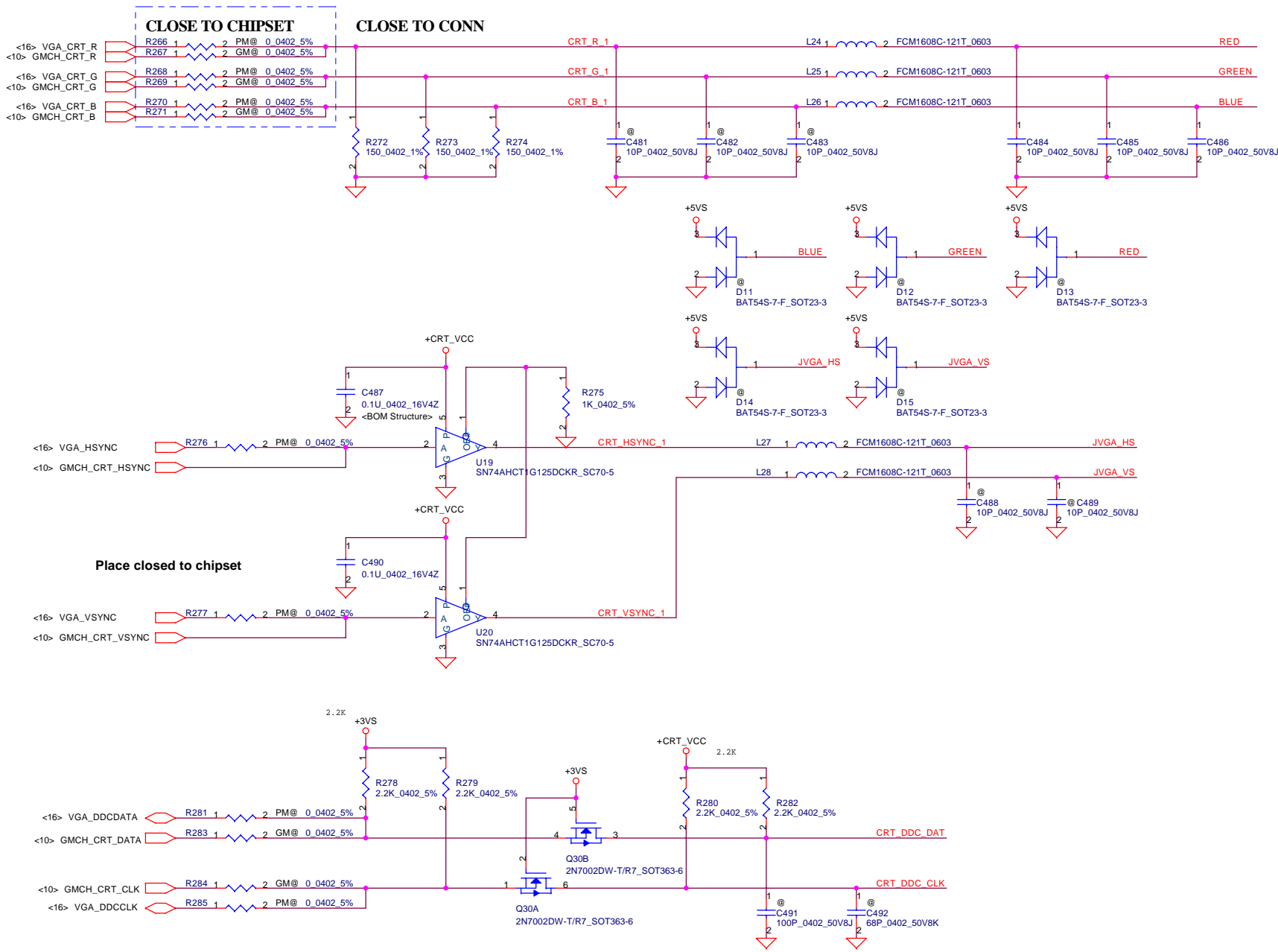


VGA LCD/PANEL BD. Conn.



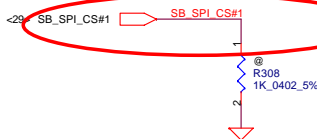
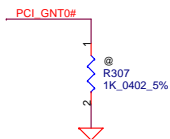
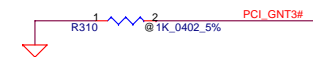
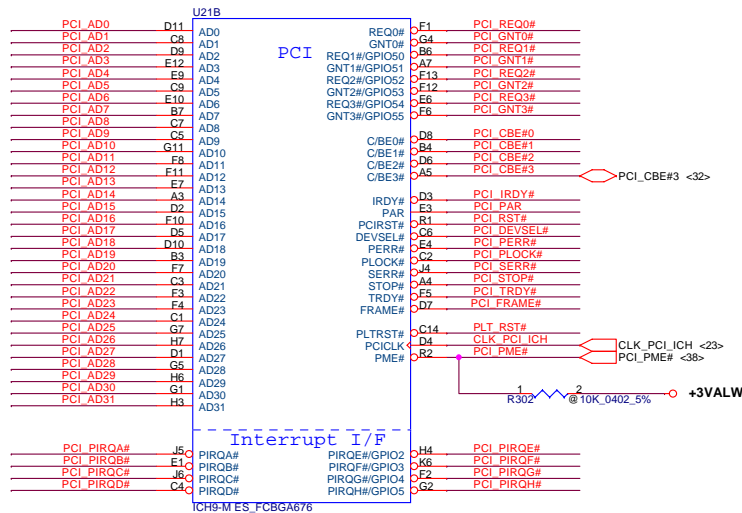
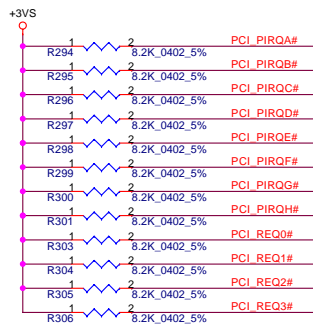
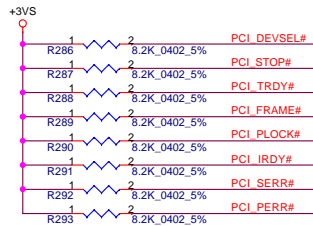
Security Classification				Compal Secret Data				Compal Electronics, Inc.			
Issued Date				Deciphered Date				Title			
2007/10/15				2008/10/15				LVDS & DVI Connector			
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size B				Document Number			
								KIWBI/B2_LA4601P			
								Rev 0.1			
								Date: Monday, June 30, 2008			
								Sheet 25 of 52			

CRT Connector

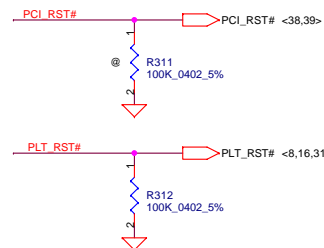
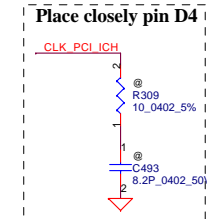


Security Classification				Compal Secret Data				Compal Electronics, Inc.			
Issued Date				2007/10/15		Deciphered Date		2008/10/15		Title	
										CRT & TV-OUT Connector	
										Size	
										Document Number	
										KIWB1/B2_LA4601P	
										Rev	
										0.1	
										Date	
										Monday, June 30, 2008	
										Sheet	
										26	
										of	
										52	

THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

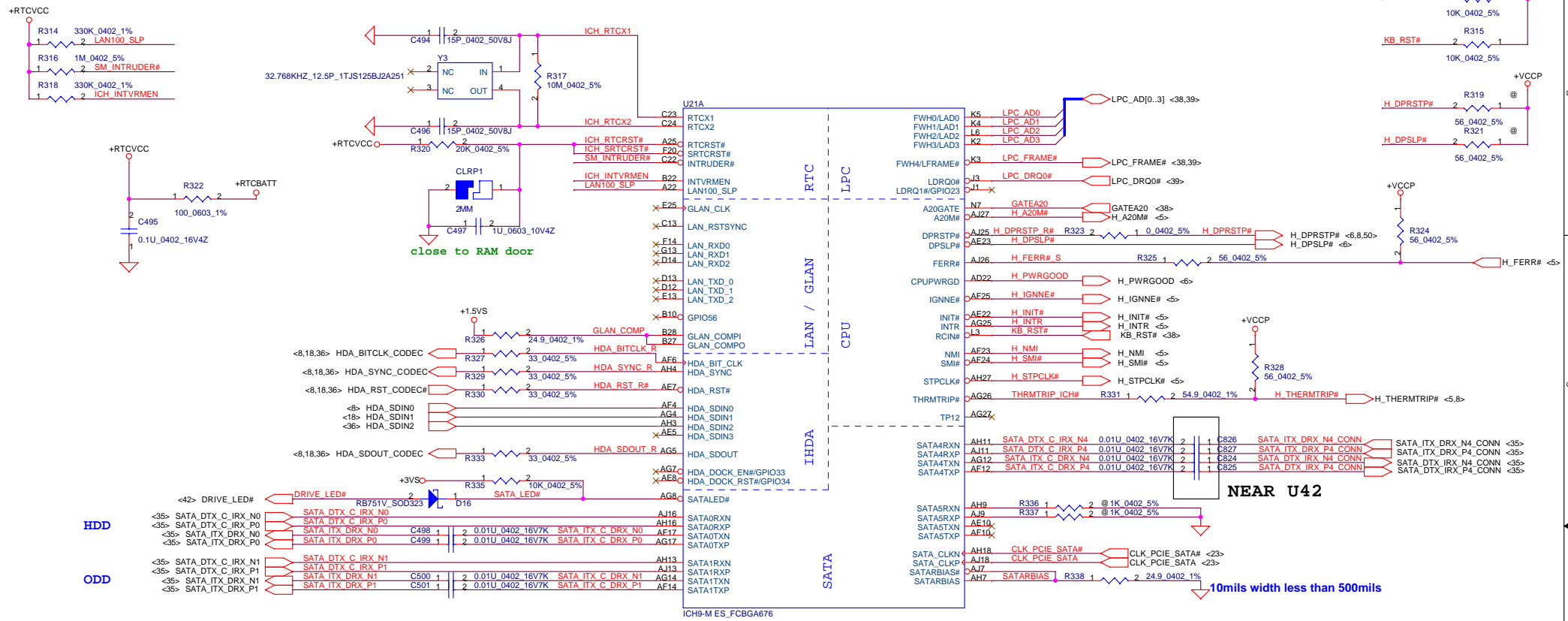


Pull high?



A16 Swap Override Strap	
PCI_GNT#3	Low= A16 swap override Enable High= Default*

Boot BIOS Strap		
PCI_GNT#0	SPI_CS#1	Boot BIOS Location
0	1	SPI
1	0	PCI
1	1	LPC*



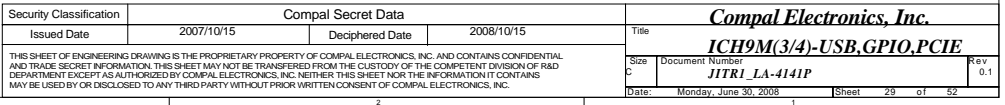
NEAR U42

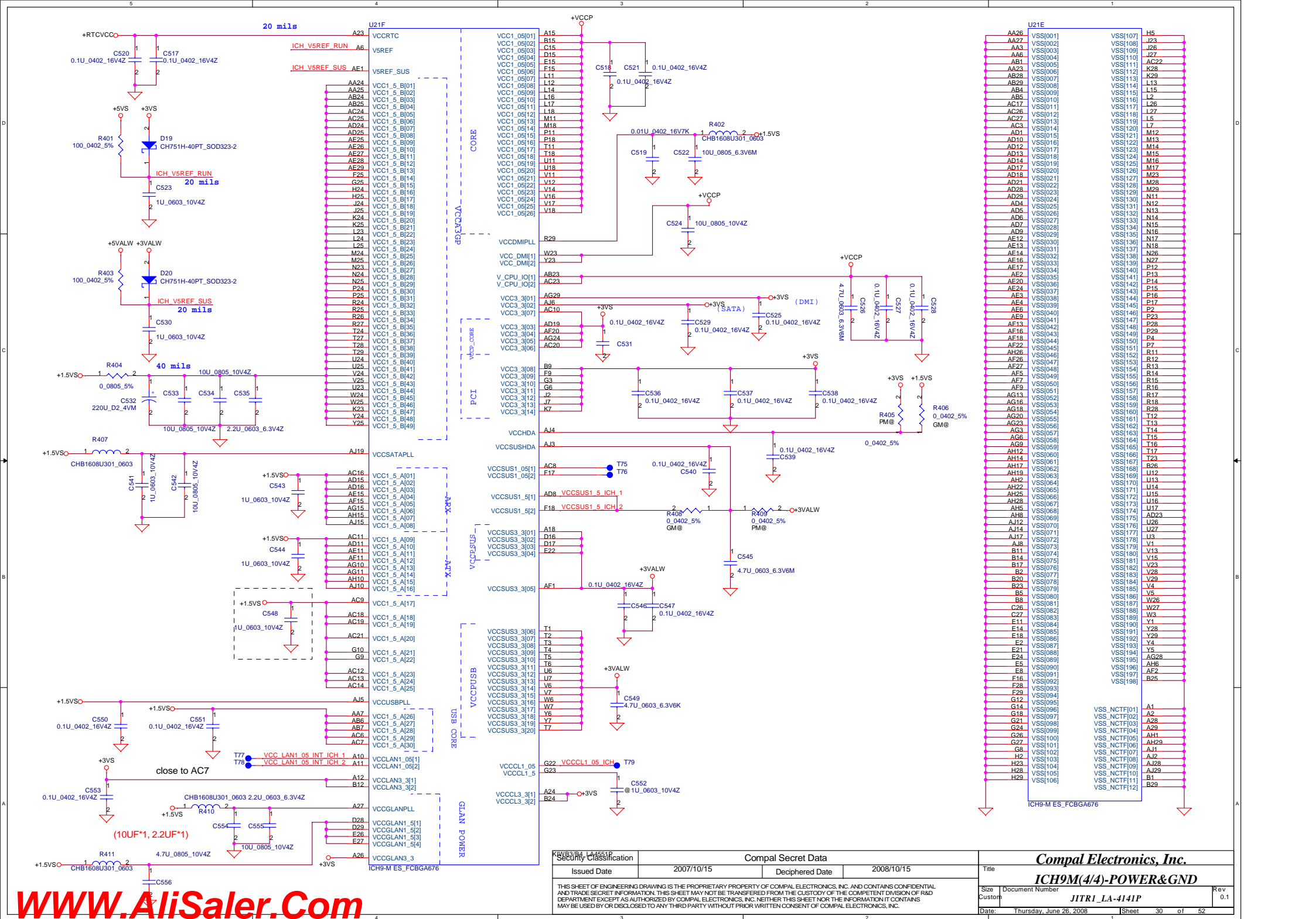
10mils width less than 500mils

Need check

XOR Chain Entrance Strap		
ICH_TP3	HDA_SDOUT	Description
0	0	RSVD
0	1	Enter XOR Chain
1	0	Normal Operation
1	1	Set PCIE port config bit 1

SATA PORT LIST	
PORT	DEVICE
0	HDD
1	ODD
2	X
3	X
4	ESATA
5	X



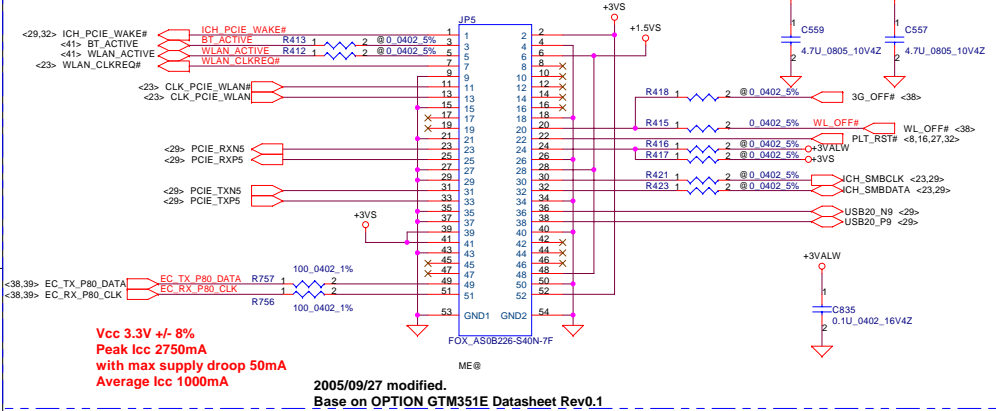


Mini-Express Card for 3G Or TV Tuner

Mini-Express Card for WLAN

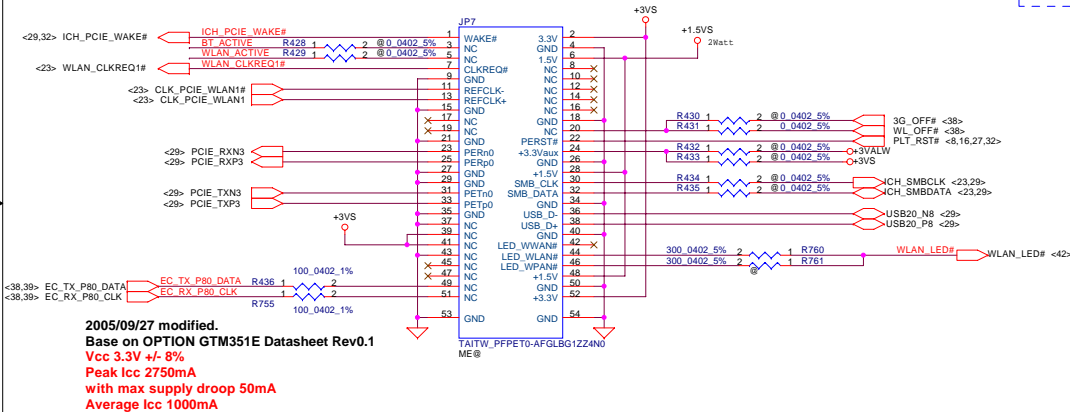
Mini-Express Card(Slot 1-TV TUNNER)

4.0mm high



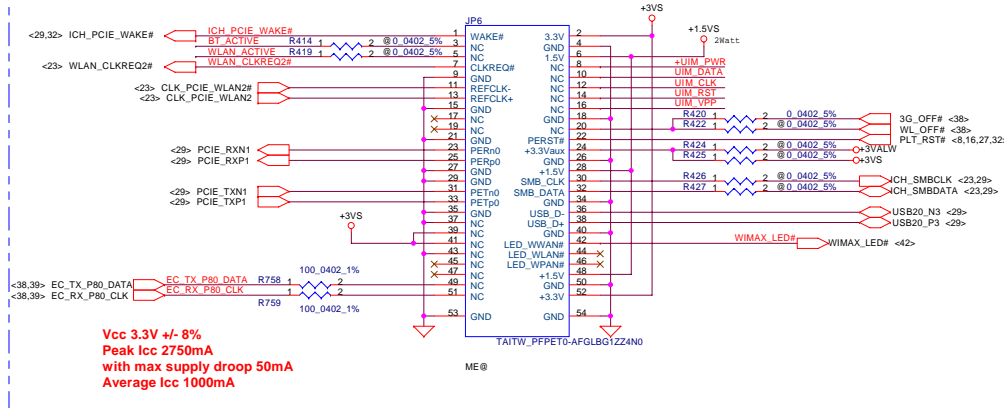
Mini-Express Card(Slot 2-WIRELESS)

5.6mm high

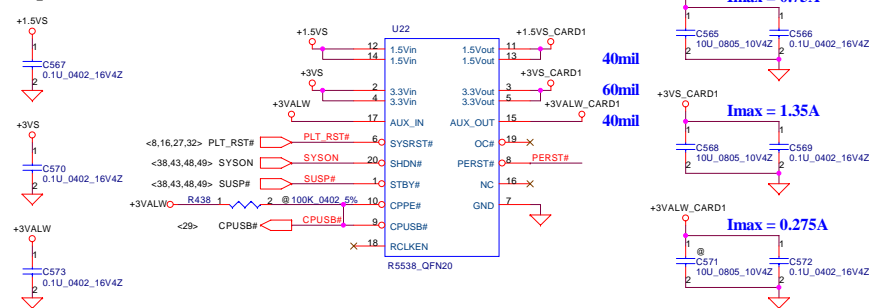


Mini-Express Card(Slot 3-WWAN 3G)

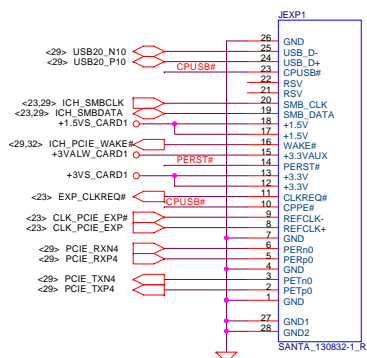
5.6mm high



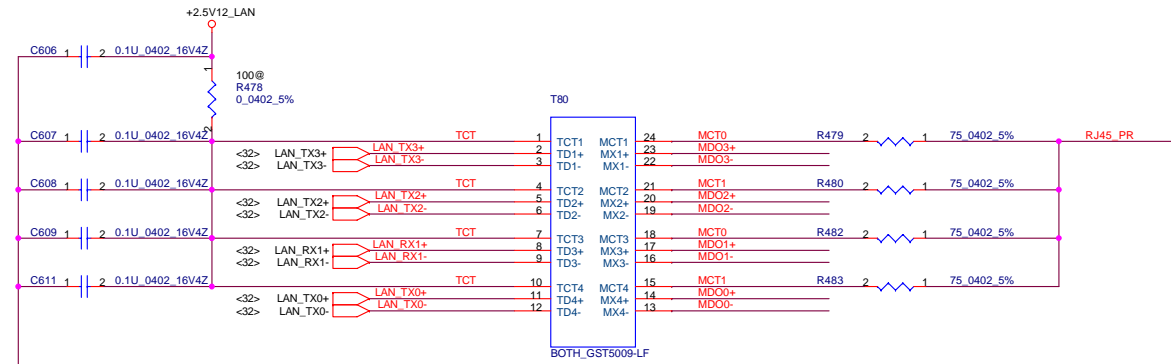
Express Card Power Switch



New Card 34mm Socket (Left/TOP)

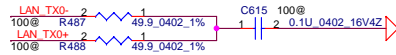
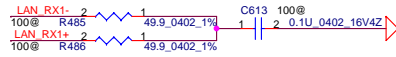




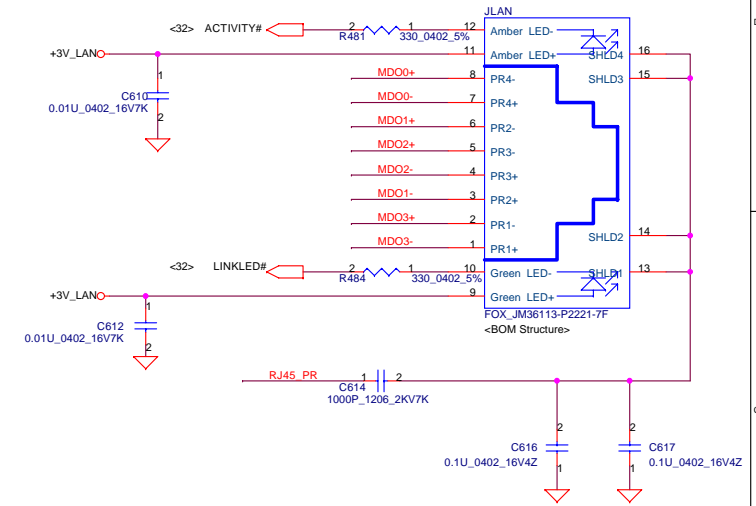


Change C468,C470,C473,C474,C475,C476 from 0.01uF to 0.1uF

near LAN controller

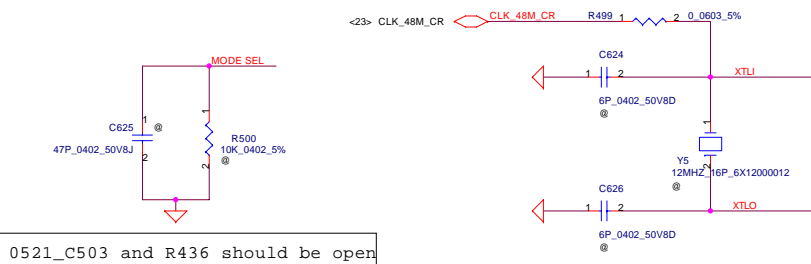
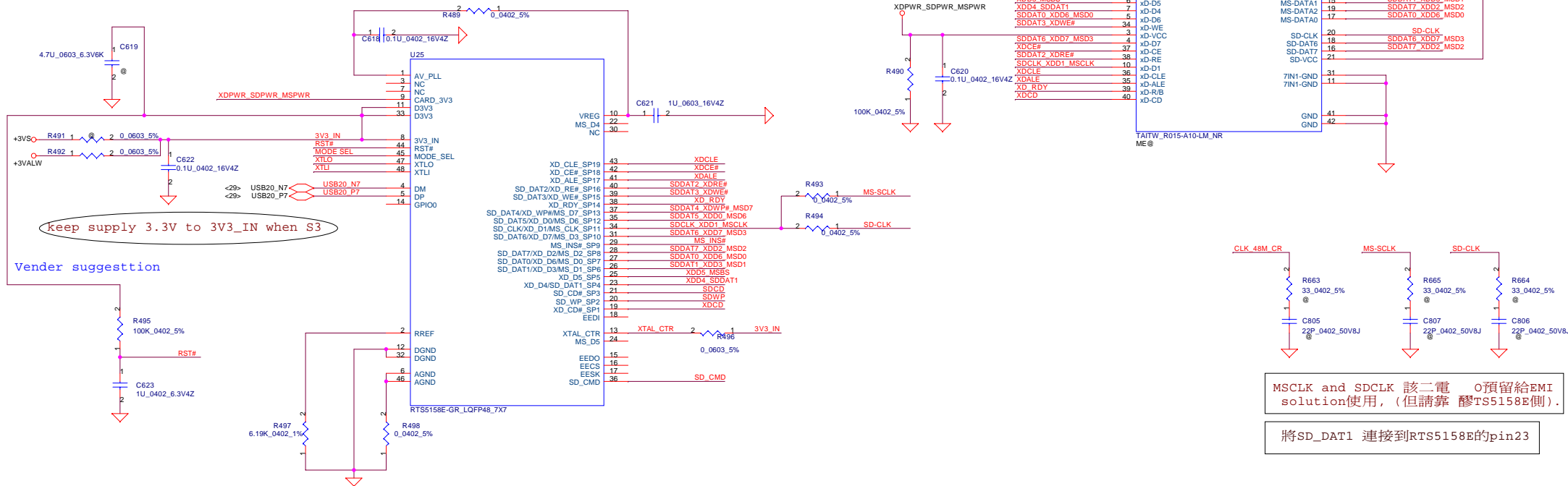


RJ11+RJ45 CONN



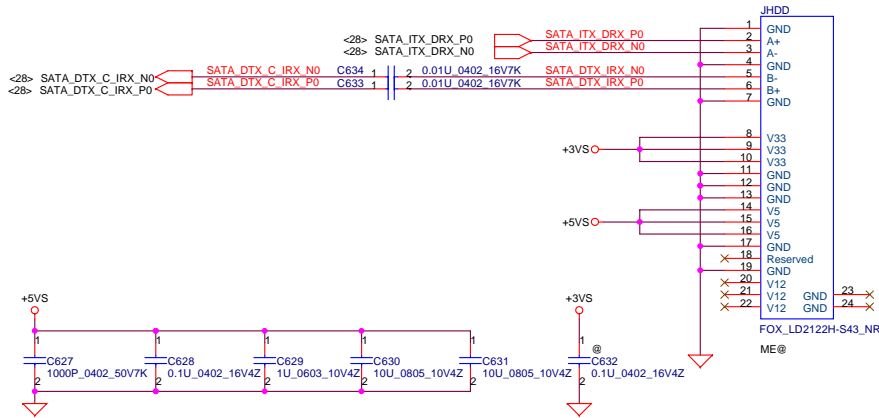
Security Classification		Compal Secret Data		Title	
Issued Date	2007/10/15	Deciphered Date	2008/10/15	LAN CONTROLLER	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
				Custom	KIWB1/B2_LA4601P
				Date:	Monday, June 30, 2008
				Sheet	33 of 52
				Rev	0.1

0513 : CARD_3V3 旁路電阻 100K change to 4.7u CAP==>預留
0521 : change C79 form 4.7u to 0.1u, add R47 100K ohm,
change C526 form 1u to 4.7u

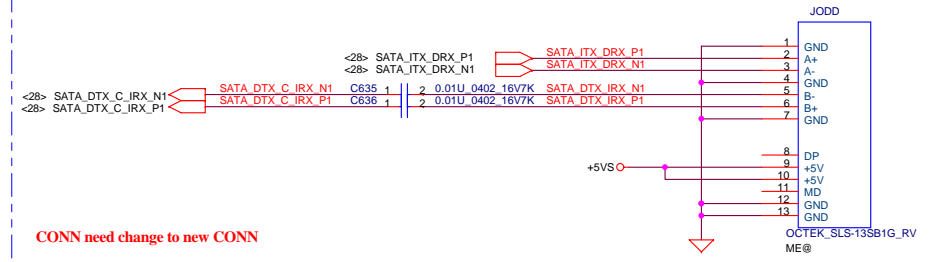


Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2006/08/04	Deciphered Date	2006/10/06	Title	1394+3 in 1 Card
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
				Custom	LA-3691P
				Date	Monday, June 30, 2008
				Sheet	34 of 52
				Rev	1.0

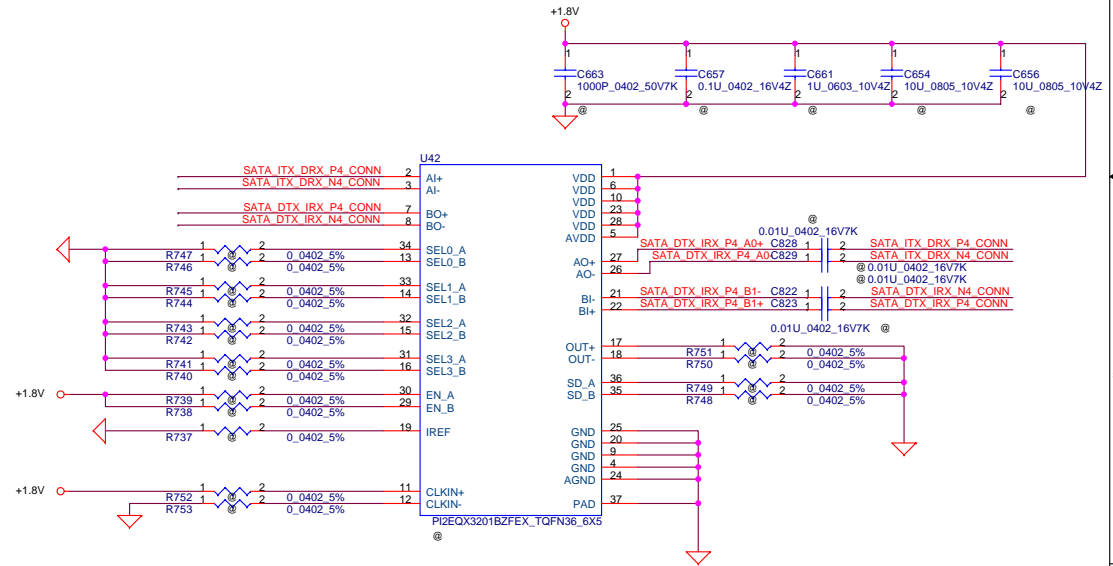
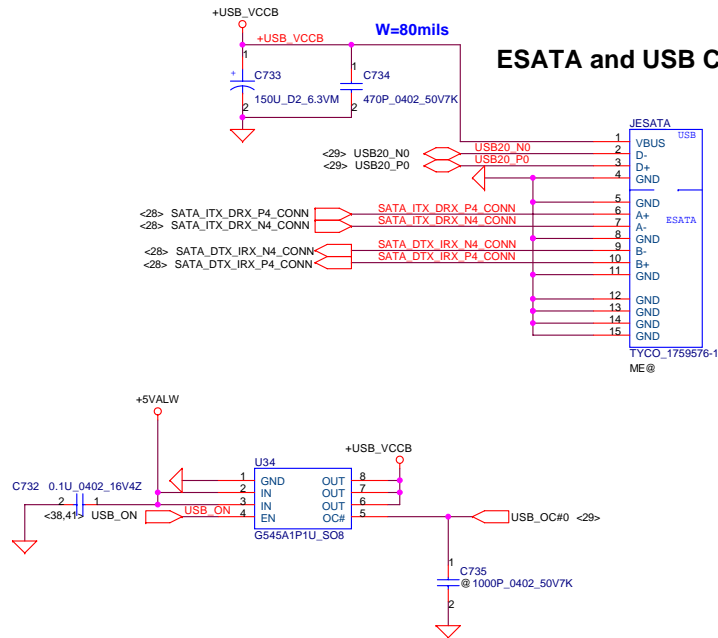
SATA HDD Conn.



SATA ODD Conn.



ESATA and USB Conn.



OUTPUT SWING CONTROL

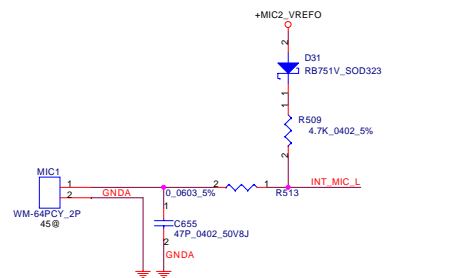
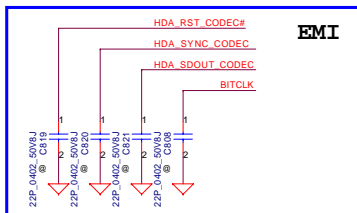
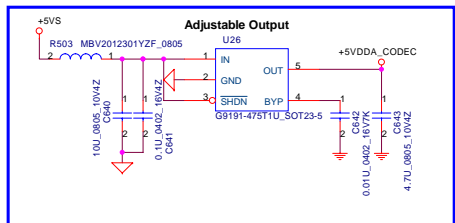
SEL2_[A:B]	SWING
0	1X
1	1.2X

OUTPUT DE-EMPHASIS ADJUSTMENT

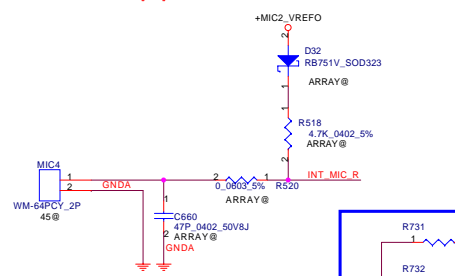
SEL3_[A:B]	DE-EMPHASIS
0	9dB
1	-3.5dB

EQUALIZER SELECTION

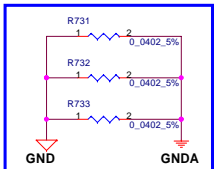
SEL3_[A:B]	DE-EMPHASIS	COMPLIANCE CHANNEL
0	0	NO EQUALIZATION
1	1	[0:2.5dB]@1.6GHz
1	0	[2.5:4.5dB]@1.6GHz
1	1	[4.5:6.5dB]@1.6GHz



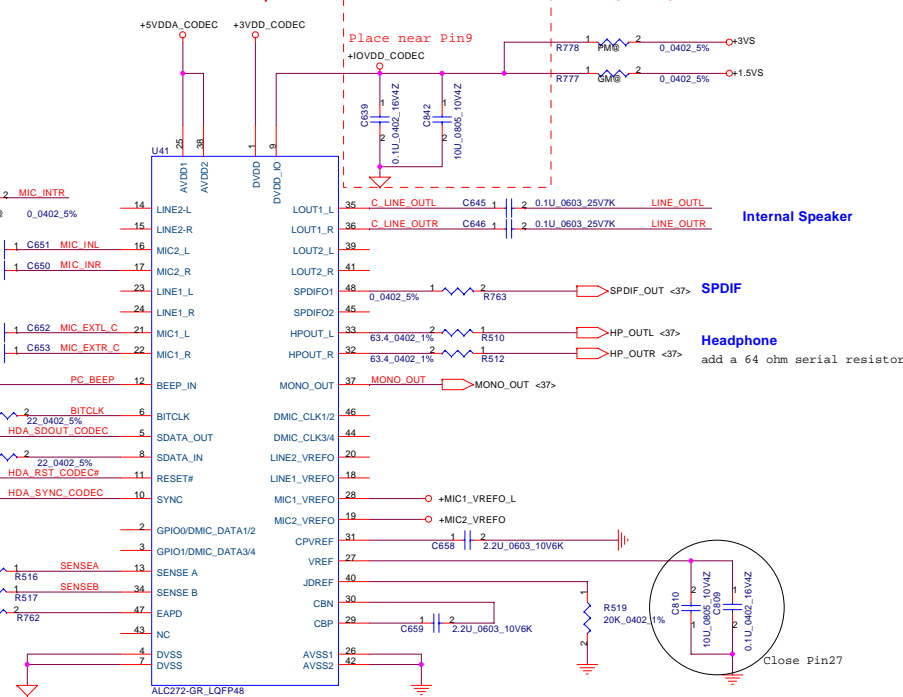
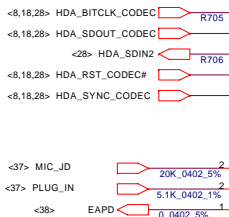
Internal MIC / Array MIC



external MIC



MIC Sense
R516 place near pin13
Capless HP Sense
R517 place near pin34



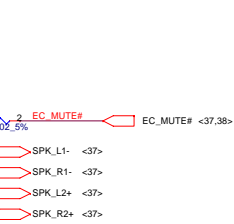
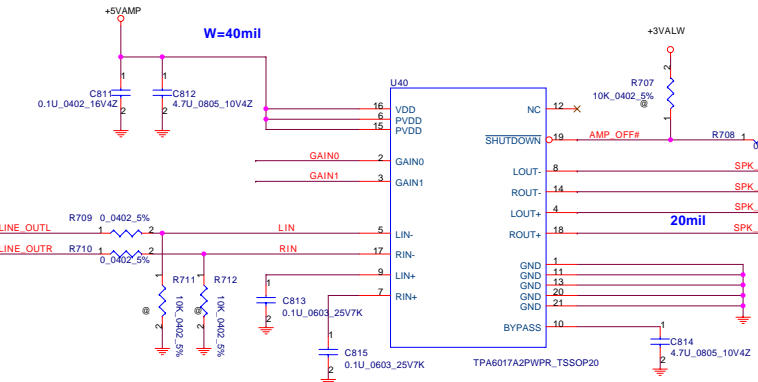
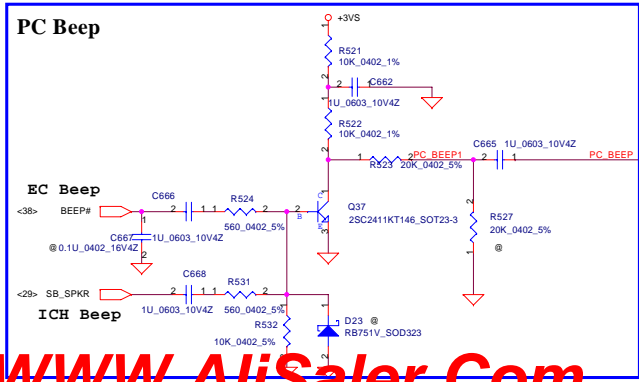
Internal Speaker

Headphone

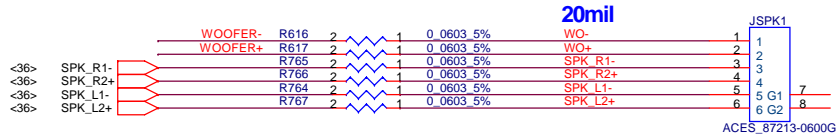
add a 64 ohm serial resistor

Close Pin27

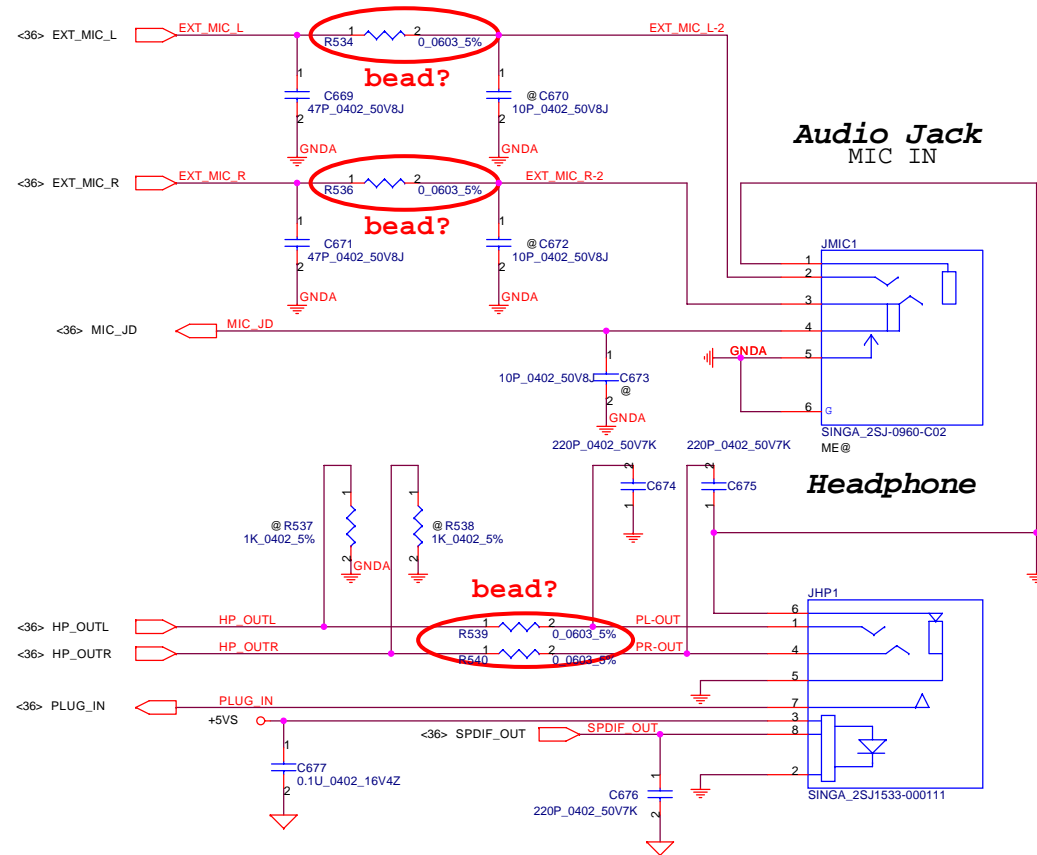
Pin Assignment	Location	Function
LINE-OUT (Pin35/36)	Internal	Int Speaker
Capless HP-OUT (Pin32/33)	External	Headphone out
LINE1 (Pin23/24)	External	Line in
MIC1 (Pin21/22)	External	Mic in
MONO-OUT (Pin37)	Internal	Internal Subwoofer
MIC2 (Pin16/17)	Internal	Internal Mic



SubWoofer Conn. Speaker Connector

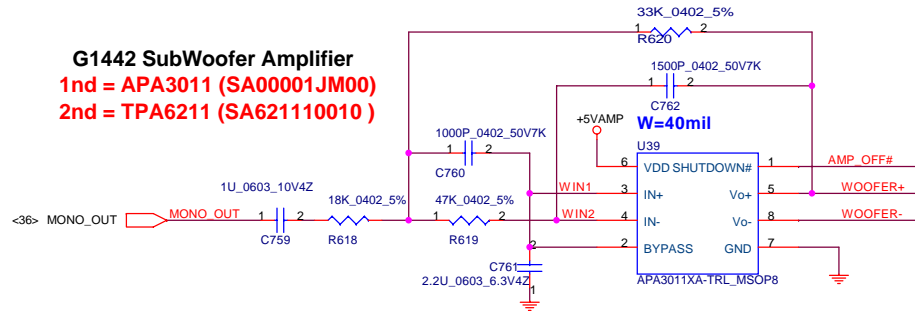


Audio Jack

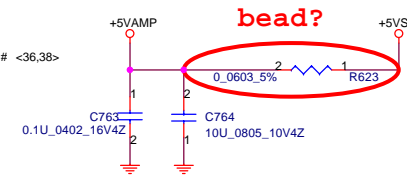


G1442 SubWoofer Amplifier

1nd = APA3011 (SA00001JM00)
2nd = TPA6211 (SA621110010)



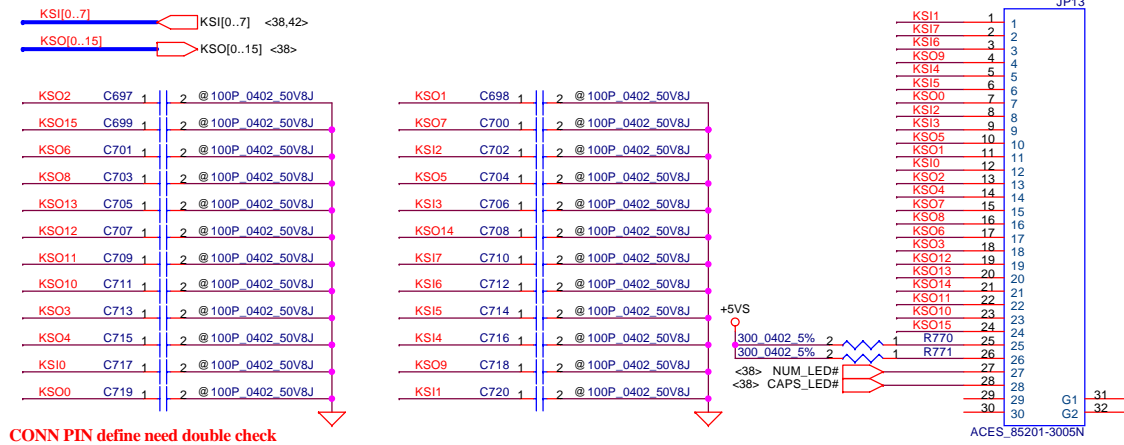
ONLY FOR 15.6W



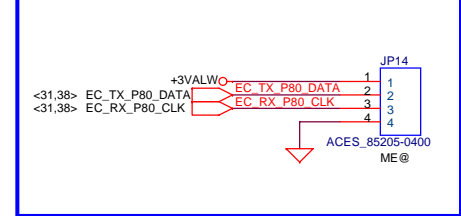
Security Classification		Compal Secret Data		Compal Electronics, Ltd.	
Issued Date	2008/03/25	Deciphered Date	2008/04/	Title	AMP Audio speaker CONN
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.					Size Custom
					Document Number KIWB1/B2_LA4601P
					Rev 0.1
Date: Monday, June 30, 2008					Sheet 37 of 52



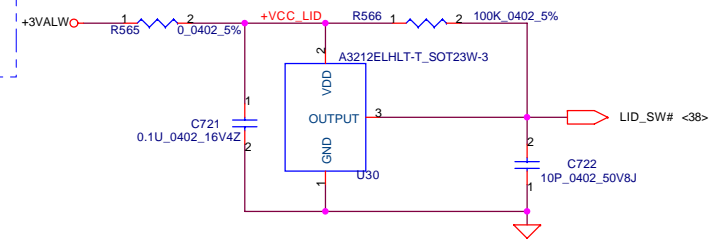
INT_KBD Conn.



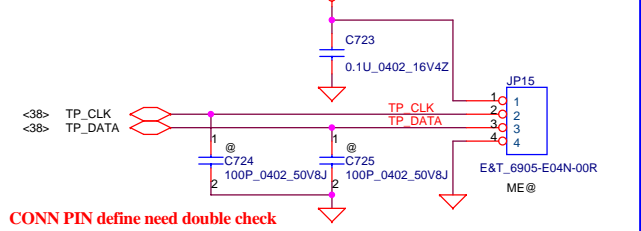
EC DEBUG PORT



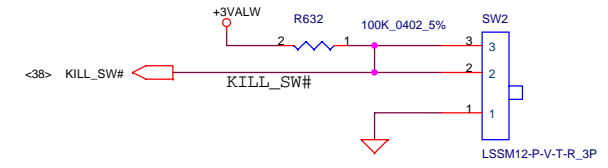
Lid Switch



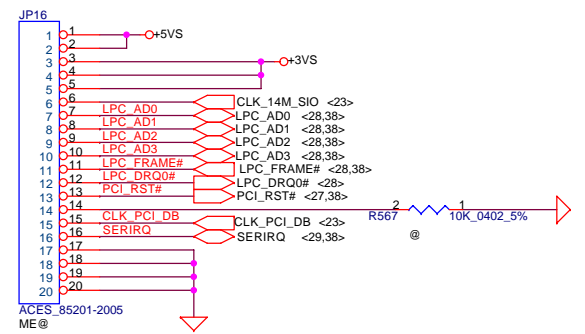
To TP/B Conn.



Kill Switch

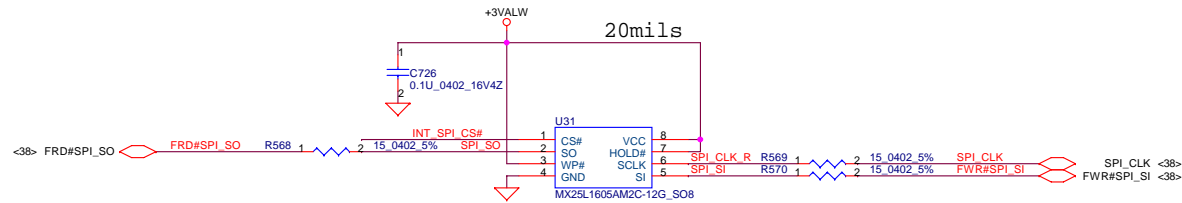


FOR LPC SIO DEBUG PORT

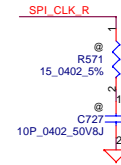
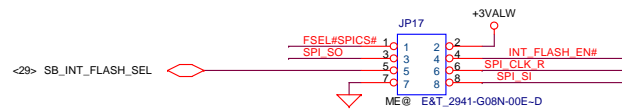
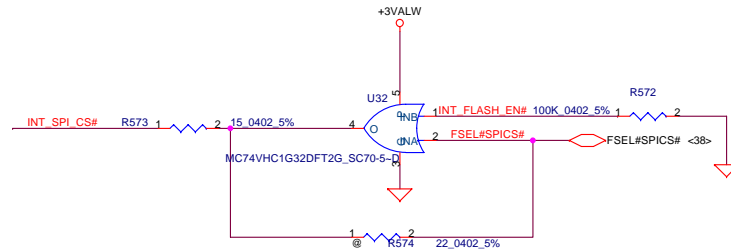


Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2007/10/15	Deciphered Date	2008/10/15	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
				B	Rev
				KIWB1/B2_LA4601P	
				Date:	Monday, June 30, 2008
				Sheet	39 of 52

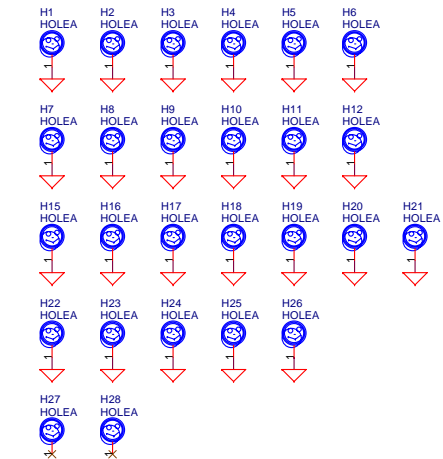
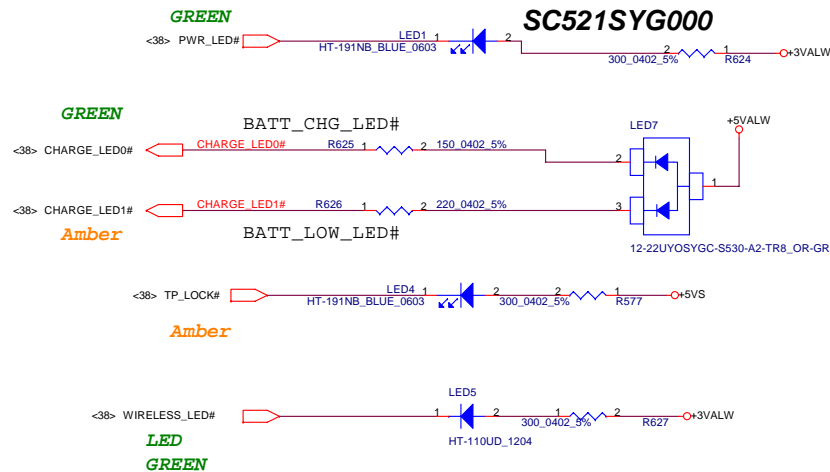
FOR EC 16M SPI ROM



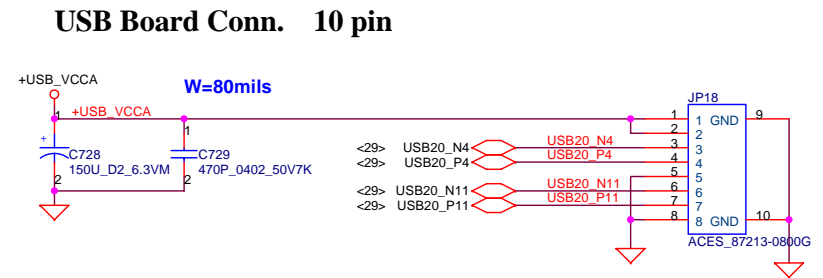
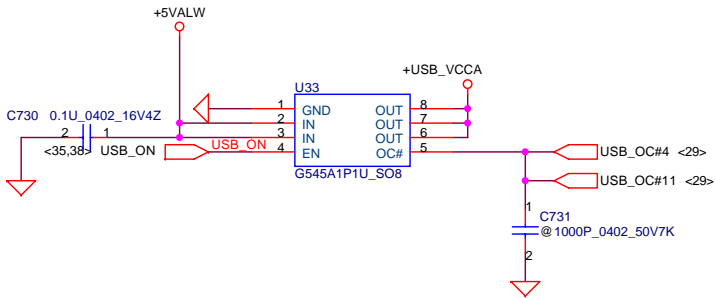
INPUT		OUTPUT
A	B	Y
L	L	L
H	L	H
L	H	H
H	H	H



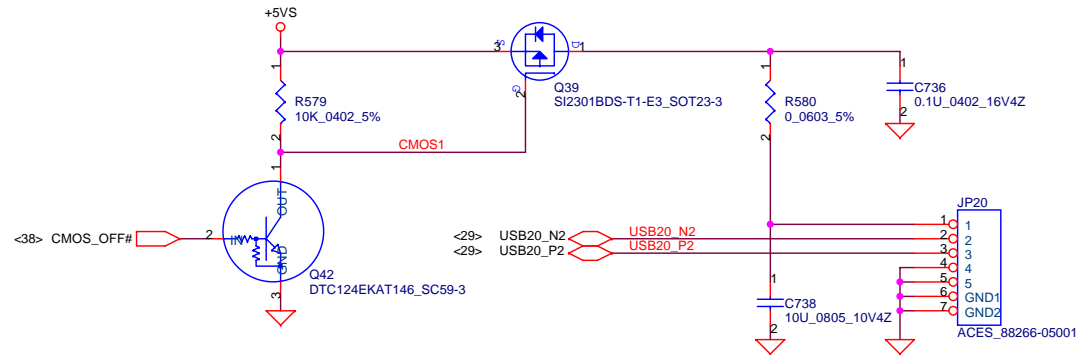
LED



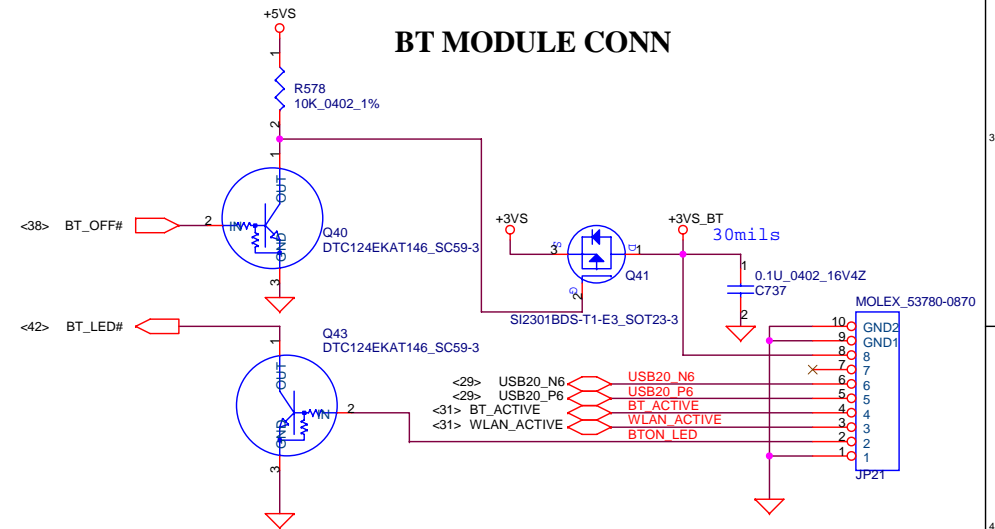
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2007/10/15	Deciphered Date	2008/10/15	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				LED/EC SPI ROM	
Size	B	Document Number	KIWB1/B2_LA4601P		Rev
Date:		Monday, June 30, 2008		Sheet	40 of 52



CMOS Camera Conn

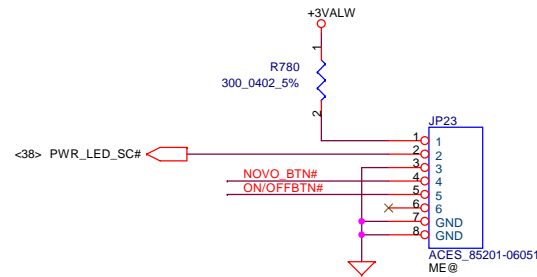


BT MODULE CONN

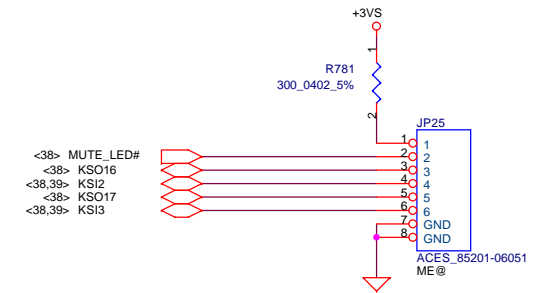


Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2006/08/18	Deciphered Date	2007/8/18	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Power OK, Reset and RTC Circuit, TP	
Size		Document Number		Rev	
Custom		KIWB1/B2_LA4601P		0.1	
Date:		Monday, June 30, 2008		Sheet	
				41 of 52	

Power Bottom Board Conn. 5 pin



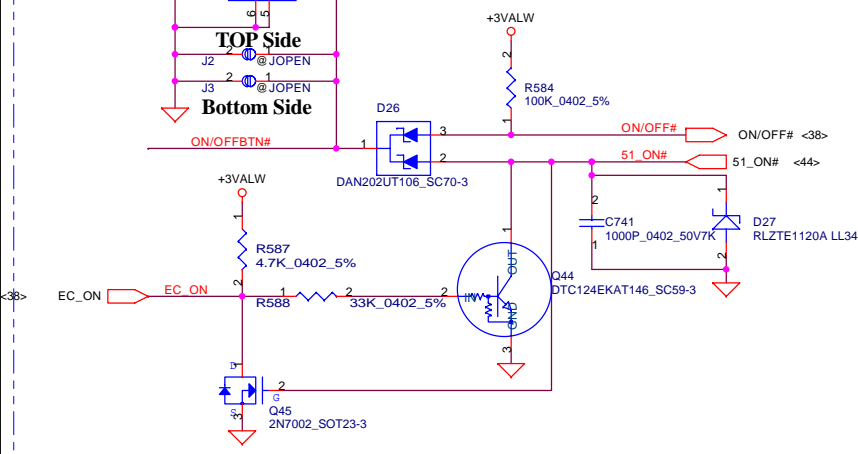
Bottom Board Conn. 6 pin



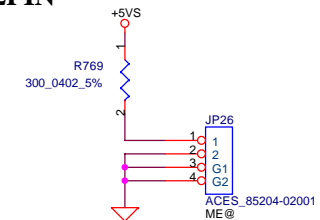
BTN FUNCTION	KEY MATRIX	
	IN	OUT
MUTE BTN	KSO17	KSI3
DOWN	KSO17	KSI2
UP	KSO16	KSI2

ON/OFF switch

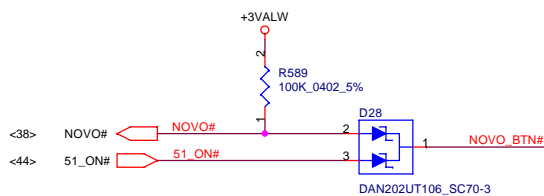
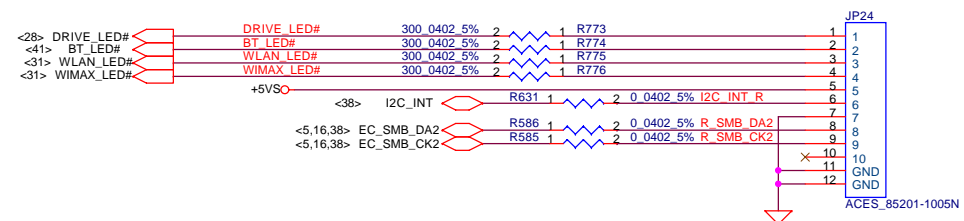
Power Button



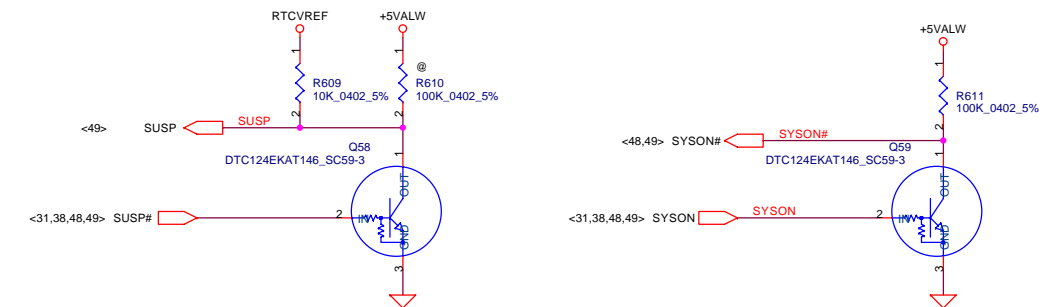
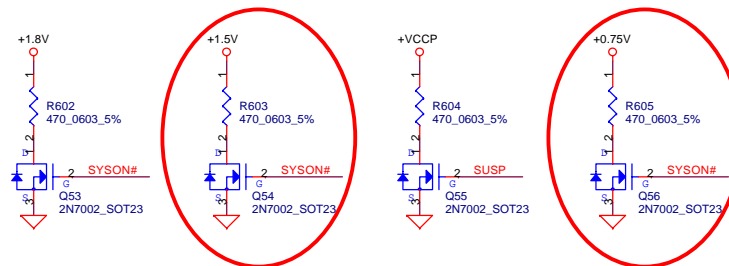
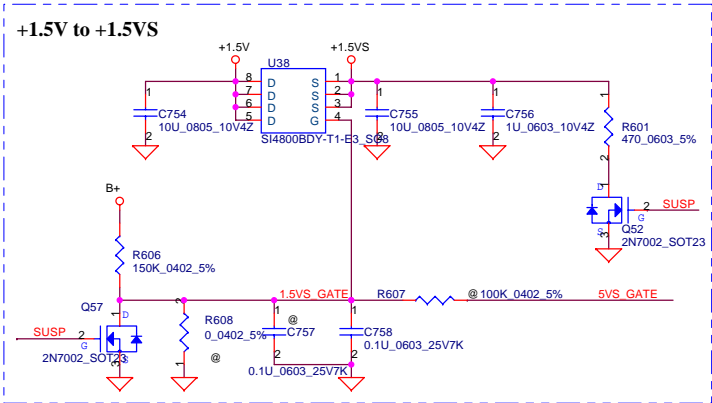
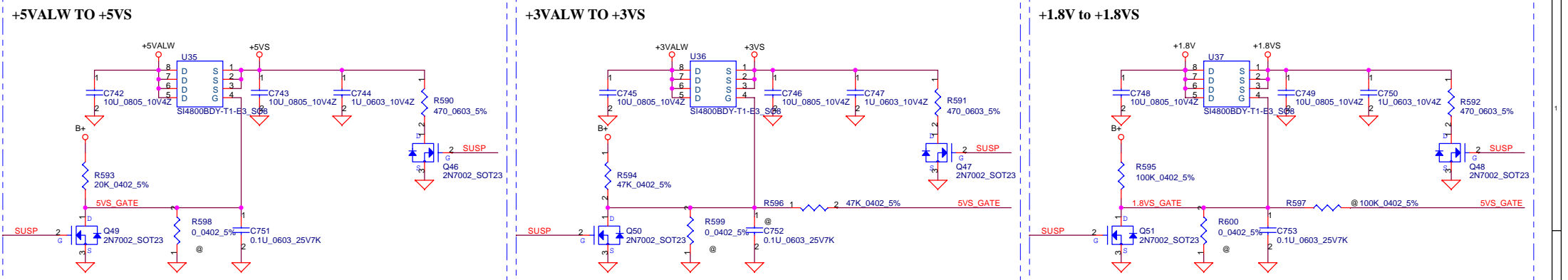
IDEAPAD BOARD 2PIN



Slide Board Conn. 12 pin

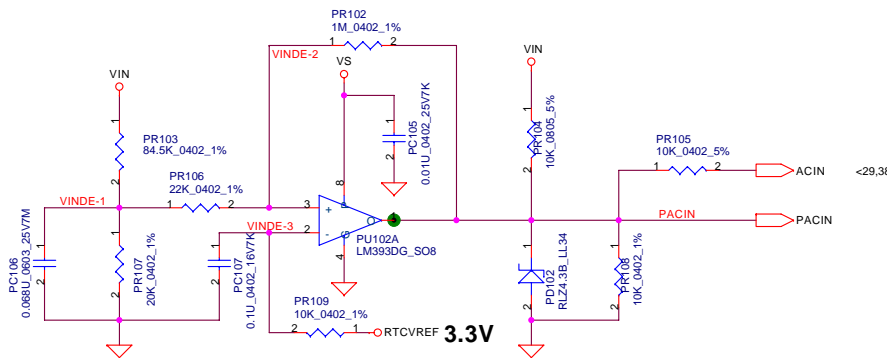
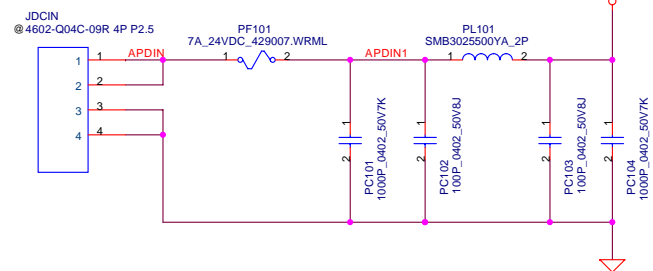


Security Classification	Compal Secret Data		Compal Electronics, Ltd.	
Issued Date	2008/03/25	Deciphered Date	2008/04/	Title
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Audio Jack & SW connector
Size	Document Number	KIWB1/B2_LA4601P		Rev 0.1
Custom				
Date: Monday, June 30, 2008	Sheet 42 of 52			



Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date		2006/08/18		Deciphered Date	
		2007/8/18		Title	
				DC Interface	
				Size	
				Document Number	
				KIWB1/B2_LA4601P	
				Rev	
				0.1	
				Date	
				Monday, June 30, 2008	
				Sheet	
				43 of 52	

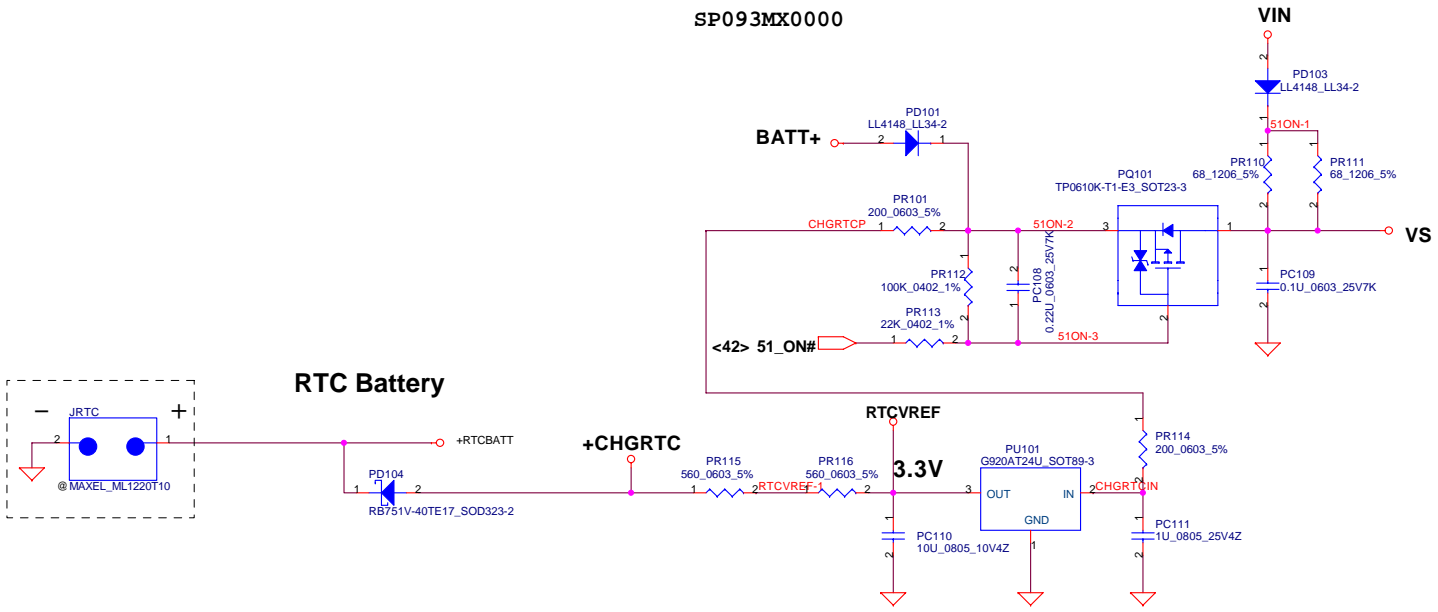
DC030006J00



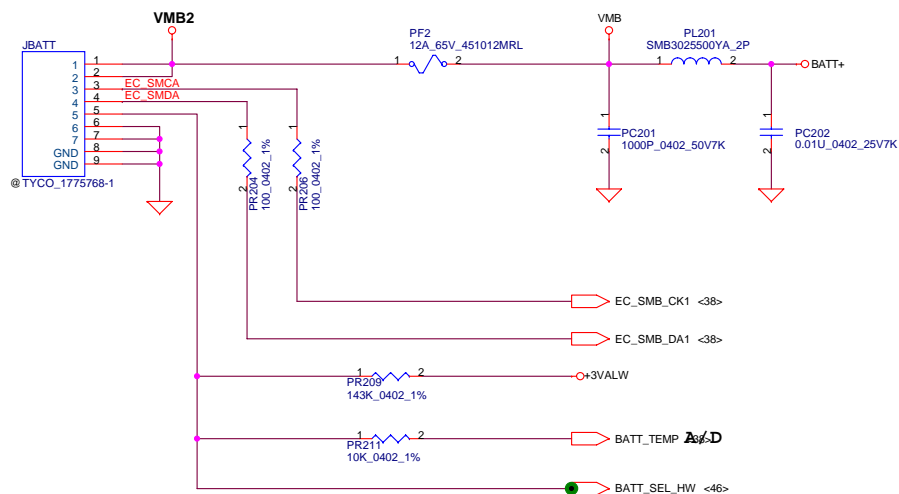
Vin Detector

High 18.384 17.901 17.430
Low 17.728 17.257 16.976

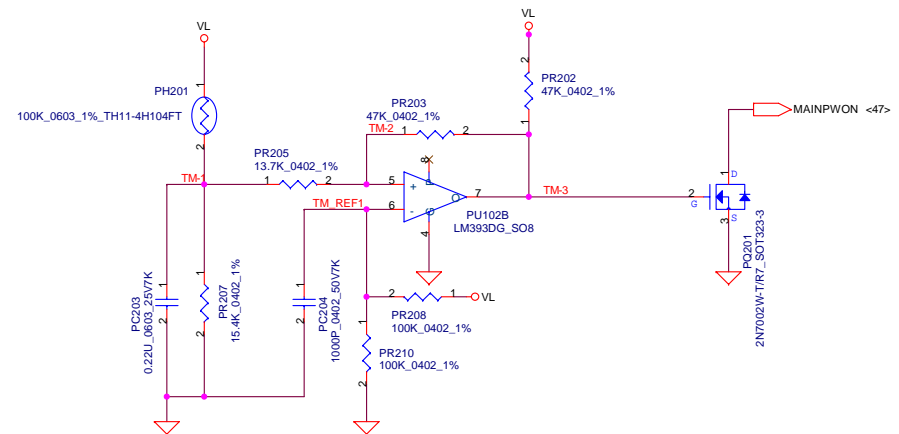
SP093MX0000



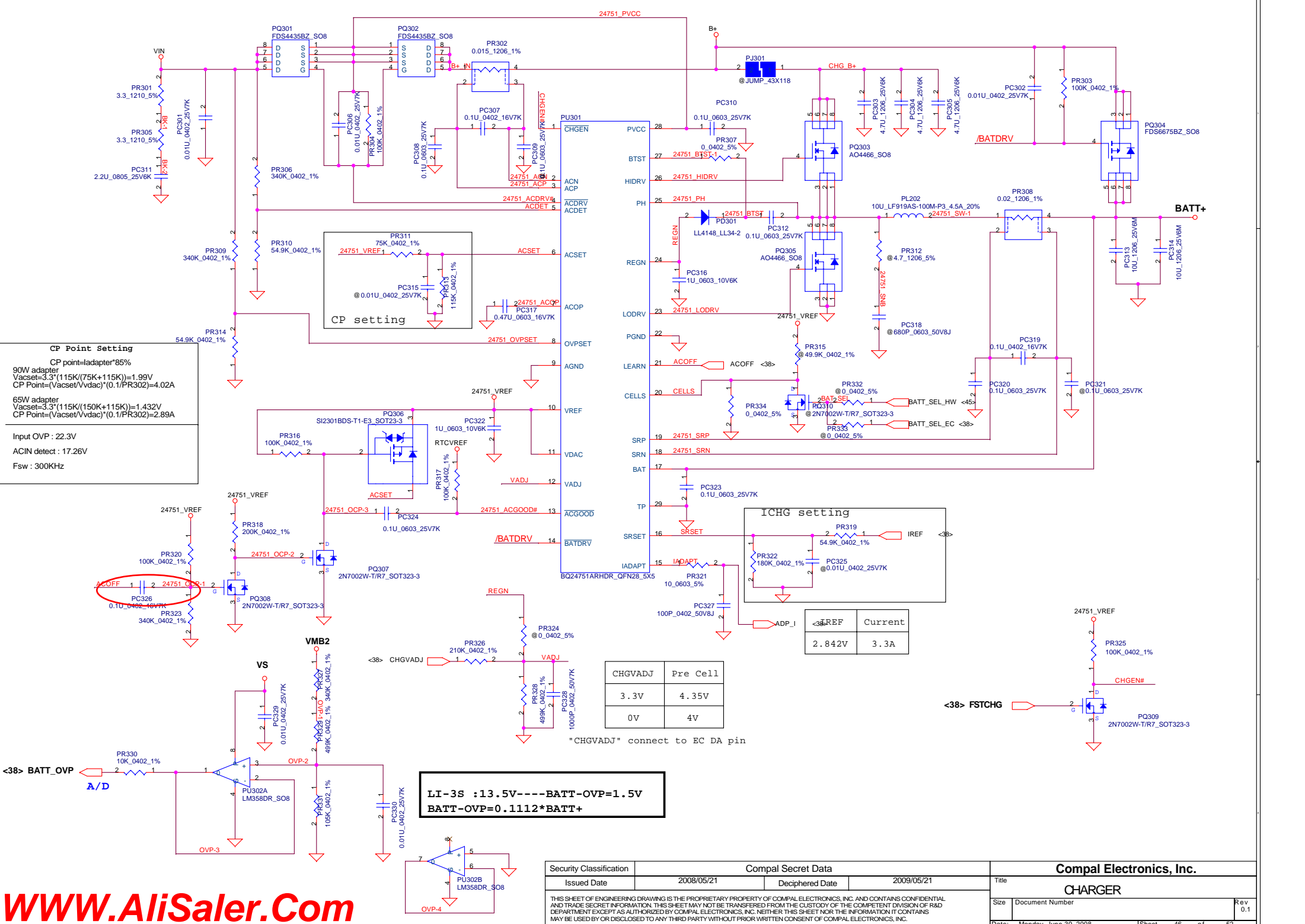
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2008/05/21	Deciphered Date	2009/05/21	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
				Custpm	Rev
				0.1	0.1
Date: Monday, June 30, 2008		Sheet		44	of 52

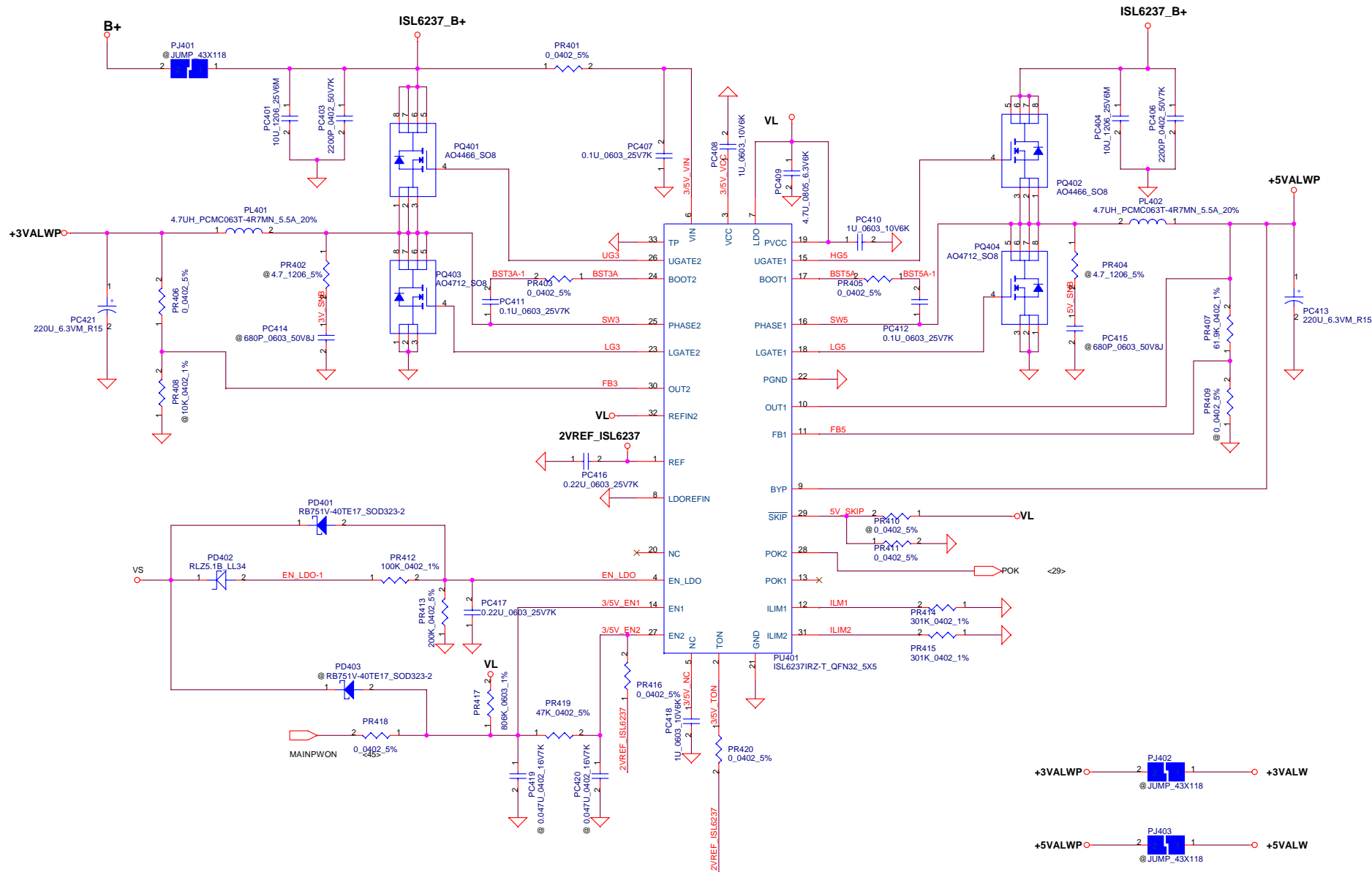


PH1 under CPU botten side :
CPU thermal protection at 92 degree C
Recovery at 56 degree C

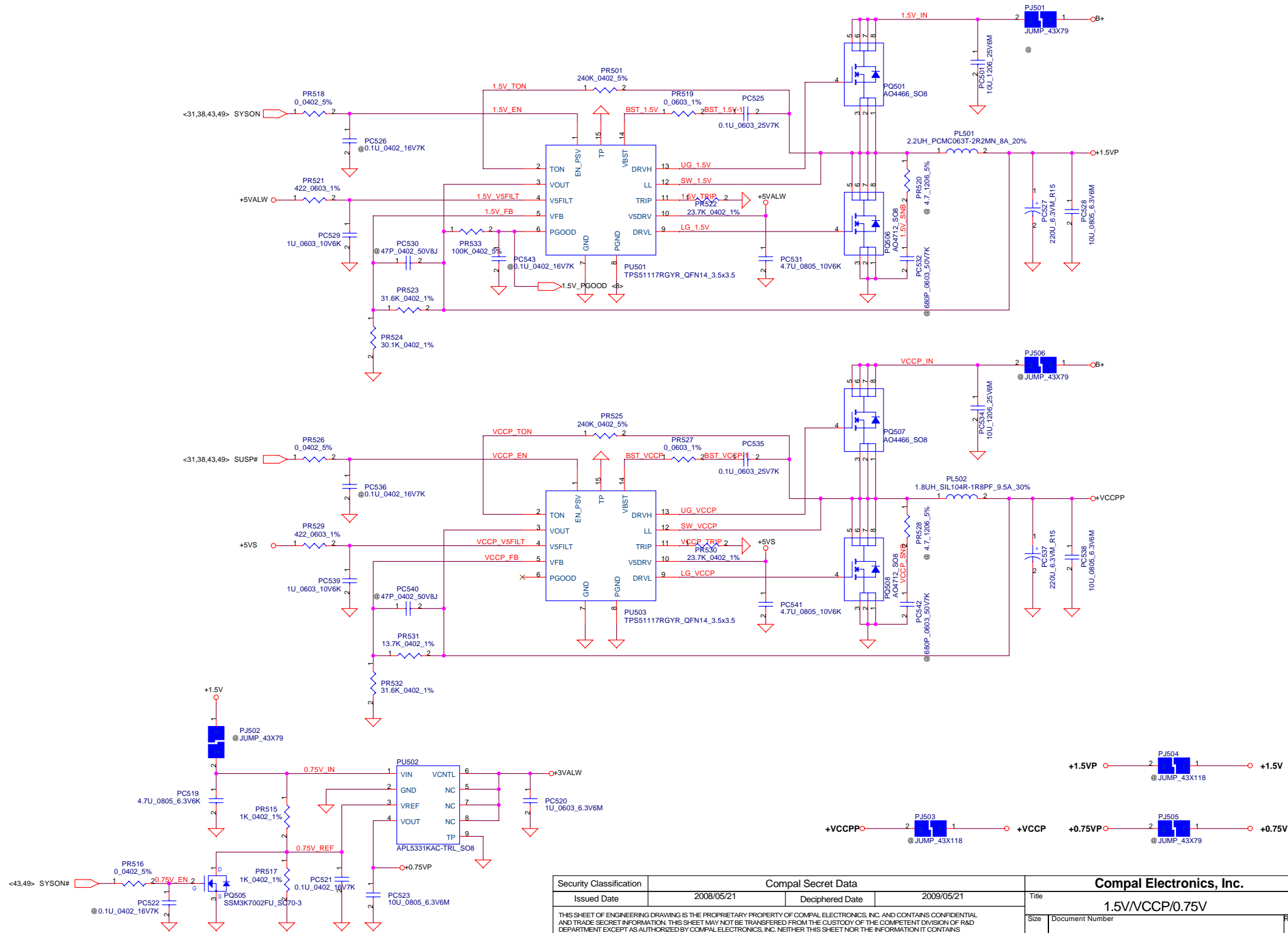


Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2008/05/21	Deciphered Date	2009/05/21	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				BATTERY CONN / OTP	
Size	Document Number			Rev	
				0.1	
Date: Monday, June 30, 2008		Sheet 45 of 52			





Security Classification	Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2008/05/21	Deciphered Date	2009/05/21	Title
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				3VALW / 5VALW
Size	Custom	Document Number	Rev 0.1	
Date	Monday, June 30, 2008	Sheet	47	of 52



Security Classification	Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2008/05/21	Deciphered Date	2009/05/21	Title
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				1.5V/VCCP/0.75V
Size	Document Number	Rev	0.1	
Date:	Monday, June 30, 2008	Sheet	48	of 52

Item	Fixed Issue	Reason for change	Rev.	PG#	Modify List	Date	Phase
1						20071031	EVT
2						20071115	DVT
3							
4							
5							
6							
7							
8							
9							
10							
11							
12							
13							
14							
15							
16							
17							
18							
19							
20							
21							
22							
23							

