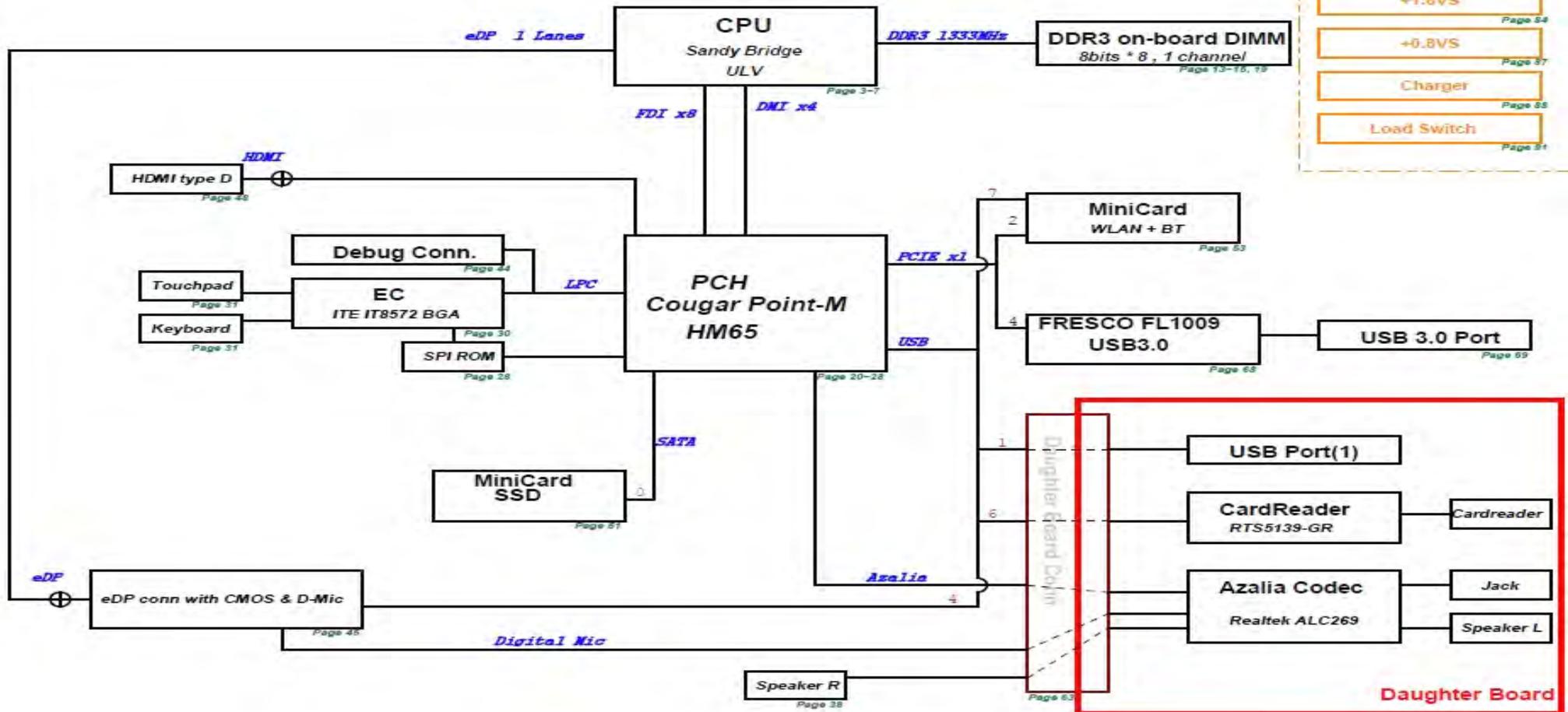


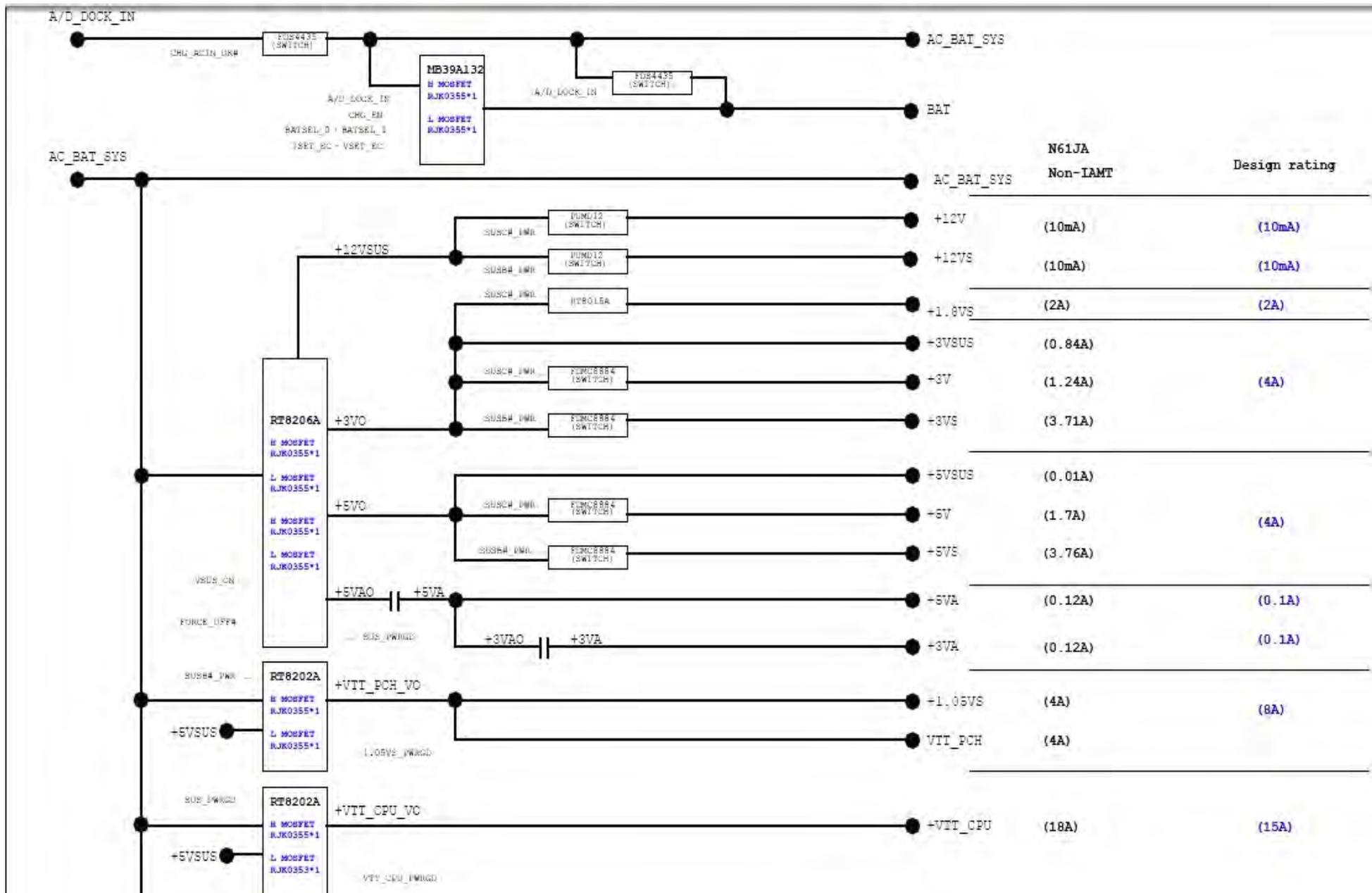
UX31E SCHEMATIC Revision R20

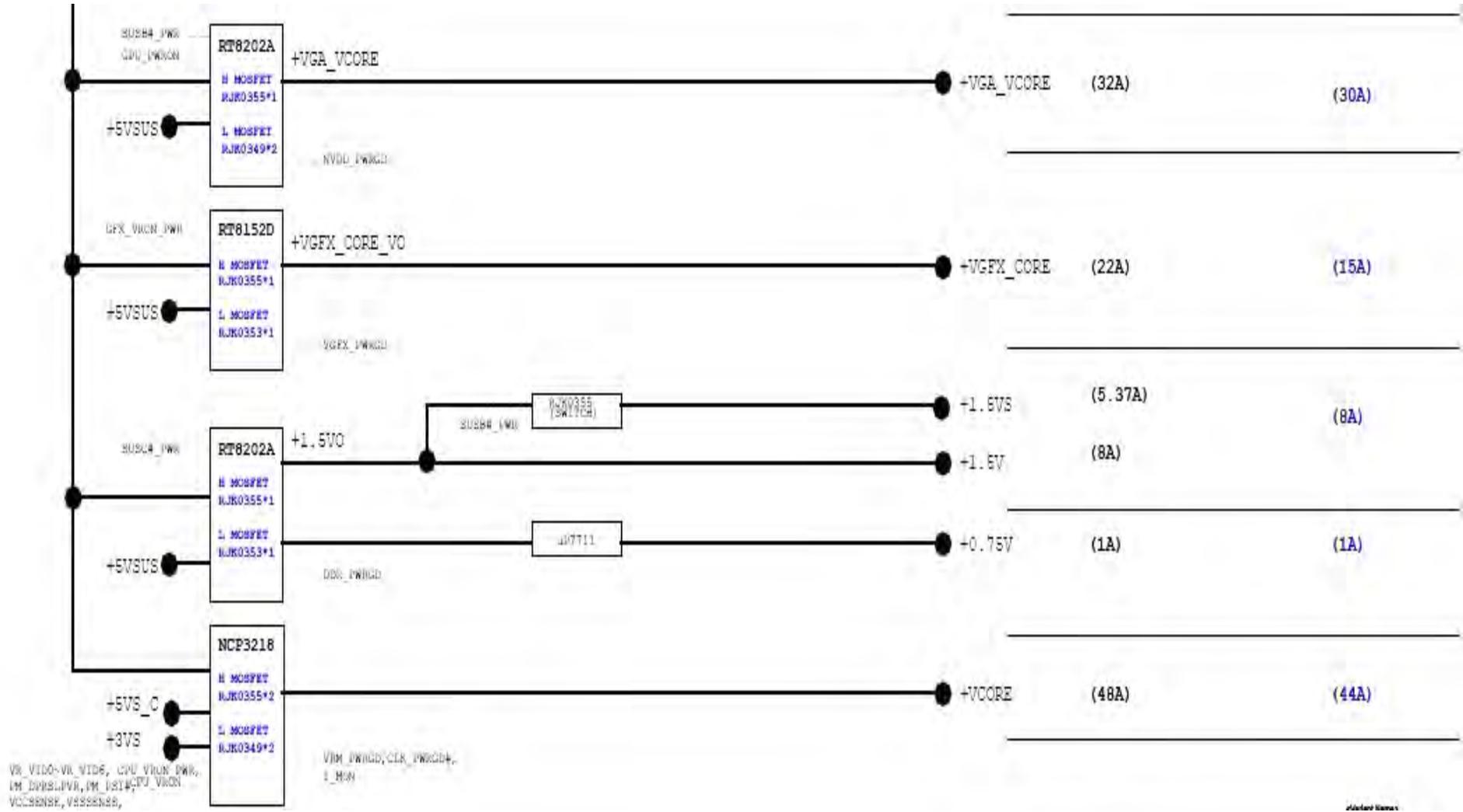
BLOCK DIAGRAM



Power	
VCORE+GFX CORE	Page 50
System	Page 51
+1.05VS	Page 52
+1.5V & +0.75V	Page 53
+1.8VS	Page 54
+0.8VS	Page 57
Charger	Page 58
Load Switch	Page 59

POWER FLOW



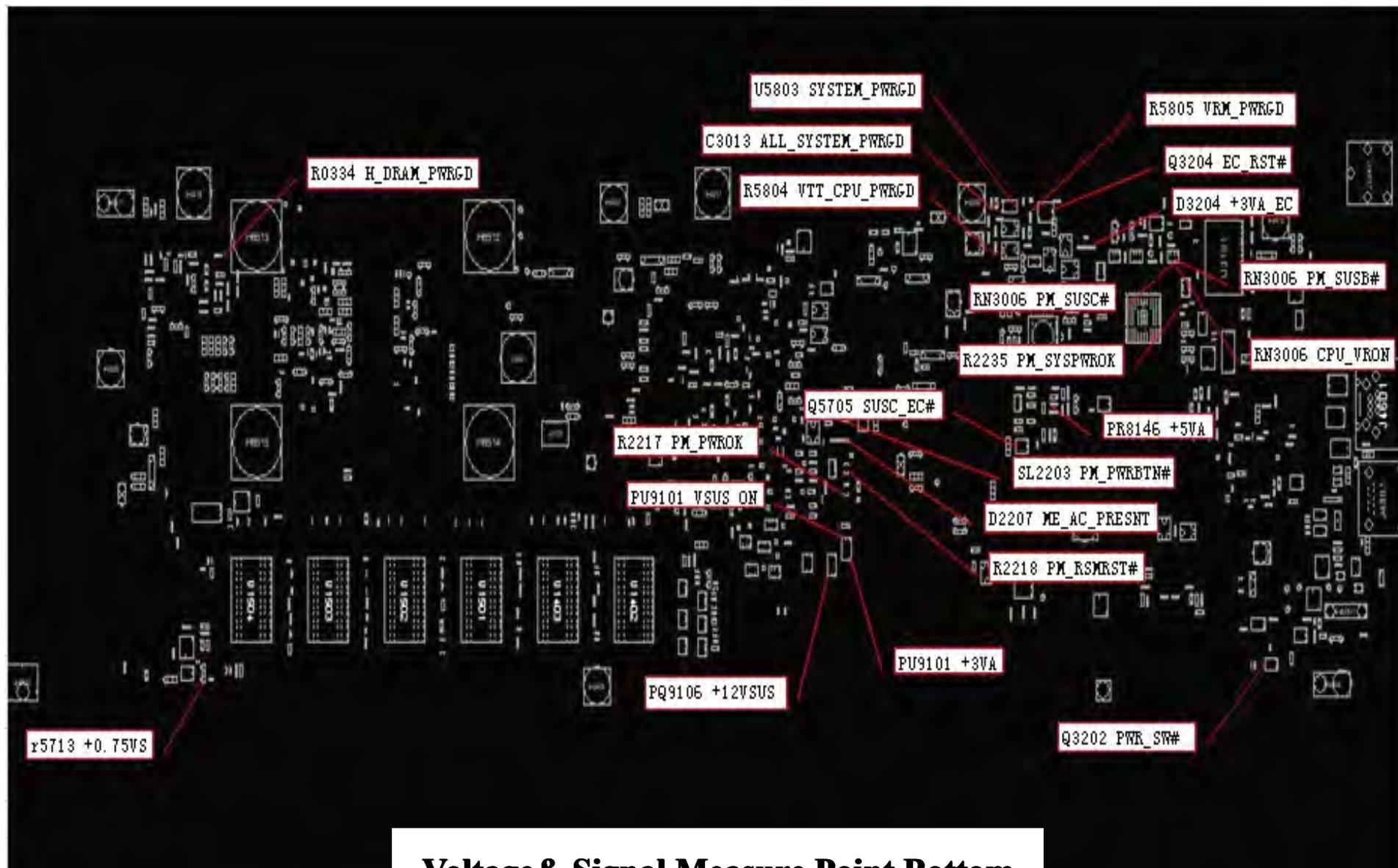


VR_VID0-VR_VID6, CPU_VTRN_PWR,
IM_DVBSLPWR, IM_DRTM_PU_VTRN
VCCSENSE, VESSENSE,

VIM_PWRGD/CLK_PWRGD+,
I_MON

Model Name:

Voltage & Signal Measure Point TOP



Voltage & Signal Measure Point Bottom

