

# Berry CRT Board Schematics Document

Arrandale

Intel PCH

2010-02-04

REV : A00

*DY : Nopop Component*

<Core Design>



**Wistron Corporation**  
21F, 88, Sec. 1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title

**Cover Page**

Size  
A3

Document Number

**Berry CRT Board**

Rev

**A00**

Date: Thursday, February 04, 2010

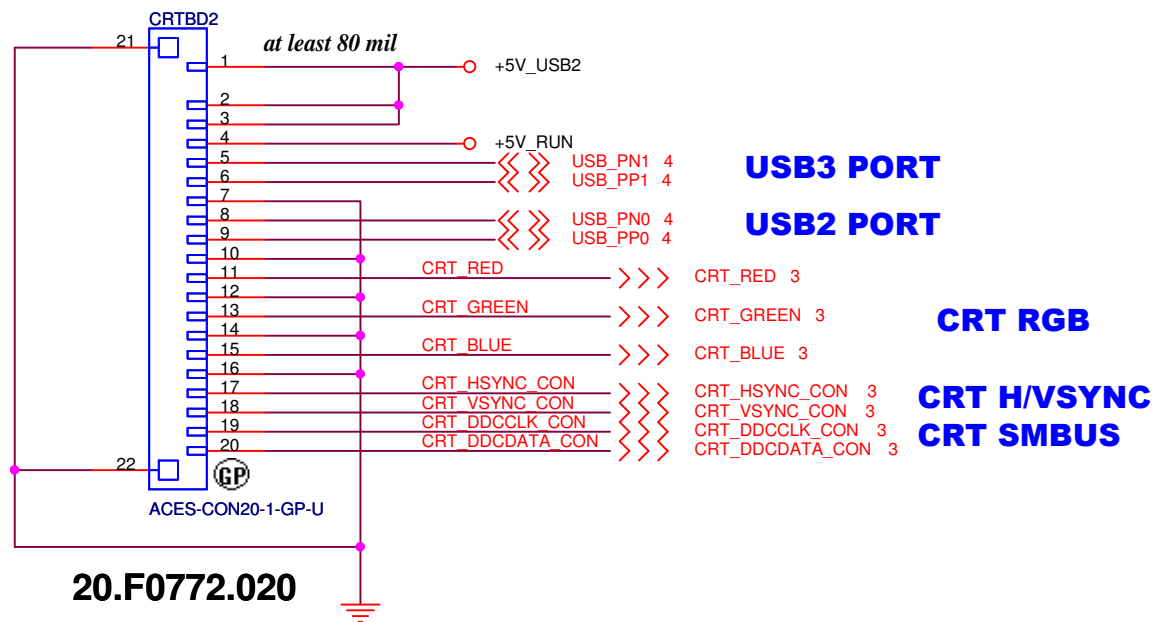
Sheet 1 of 5

# Berry CRT Board

Project code:91.4HH01.001

PCB P/N :09698-1

REVISION :-1



<Variant Name>



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Title

**IO Board CONN**

Size

Document Number

**A4**

**Berry CRT Board**

Rev

**A00**

Date: Thursday, February 04, 2010

Sheet 2 of 5

SSID = VIDEO

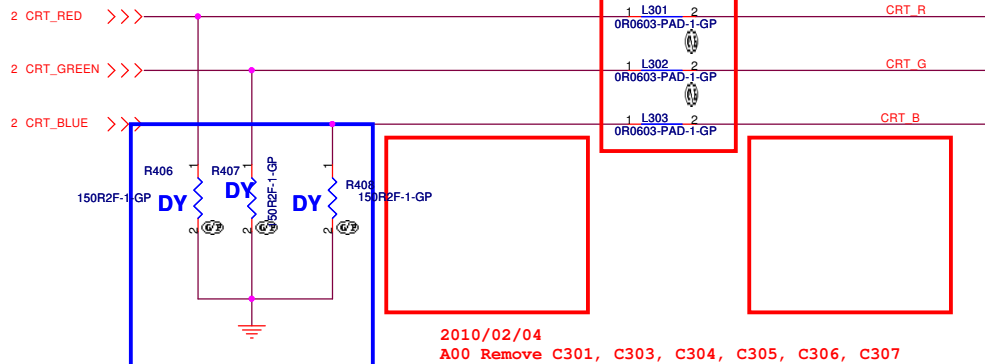
Layout Note:

\*Pi-filter & 150 Ohm pull-down  
resistors should be as close  
as to CRT CONN.

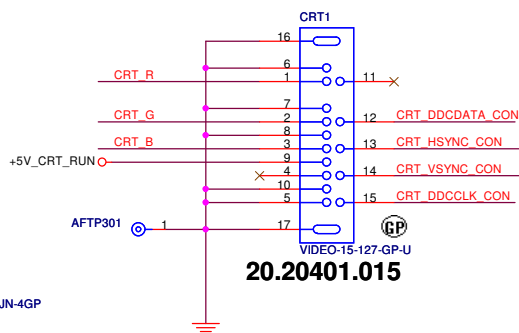
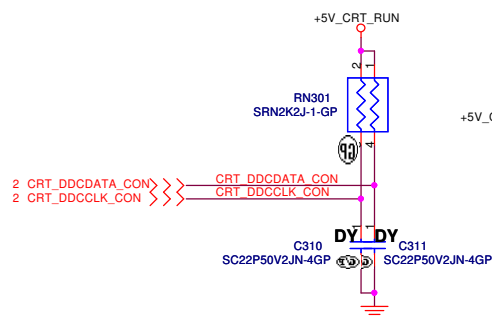
\* RGB signal will hit 75 Ohm  
first, then pi-filter, finally  
CRT CONN.

2010/01/19  
Change to 0603/0 ohm PAD

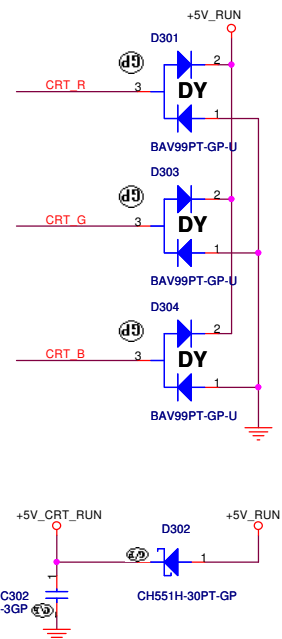
CRT RGB



2010/02/04  
A00 Remove C301, C303, C304, C305, C306, C307



20.20401.015



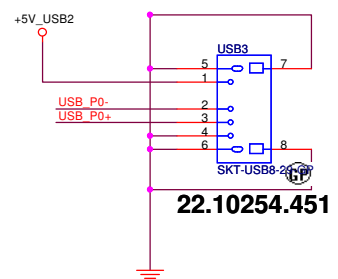
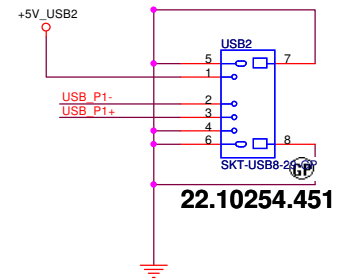
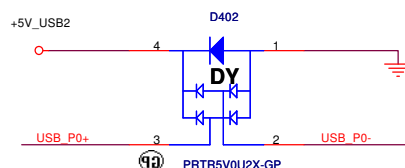
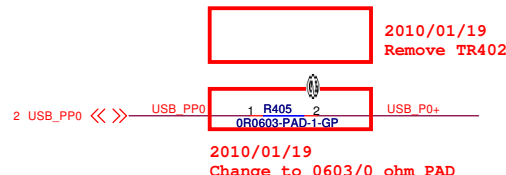
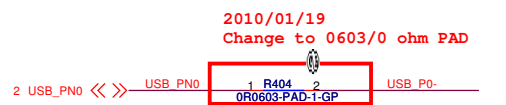
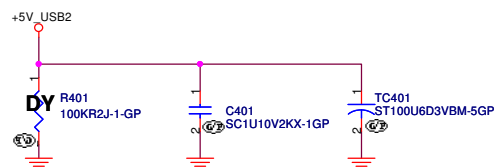
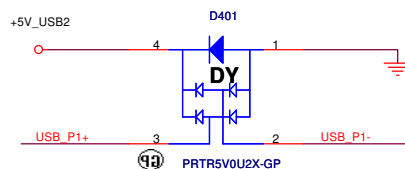
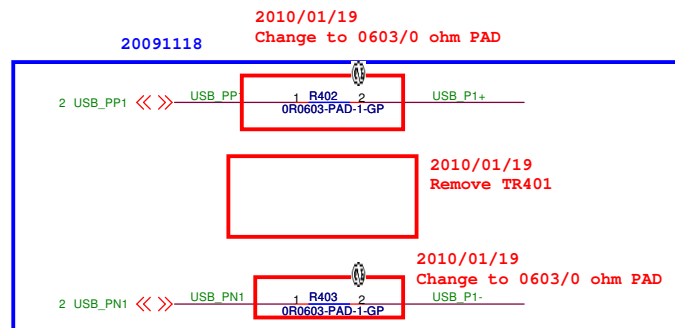
AFTP302	1	+5V_CRT_RUN
AFTP303	1	CRT_DDCDATA_CON
AFTP304	1	CRT_DDCLK_CON
AFTP305	1	CRT_R
AFTP306	1	CRT_G
AFTP307	1	CRT_B
AFTP308	1	CRT_HSYNC_CON
AFTP309	1	CRT_VSYNC_CON

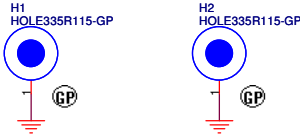
<Variant Name>

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Title	DCIN	Rev	A00
Size	Document Number		
A3	Berry CRT Board		
Date:	Thursday, February 04, 2010	Sheet	3 of 5

SSID = USB





<Variant Name>



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Title

**Change List**

Size

Document Number

Rev

**A3**

**Berry CRT Board**

**A00**

Date: Wednesday, January 20, 2010

Sheet 5 of 5