

EEE PC 701 PCB version

GPI37	GPI38	GPI39	PCB version
0	0	0	
0	0	0	
0	0	1	
0	0	1	
0	1	0	
0	1	0	
0	1	1	
0	1	1	
1	0	0	
1	0	0	
1	0	1	
1	0	1	
1	1	0	
1	1	0	
1	1	1	
1	1	1	

USB

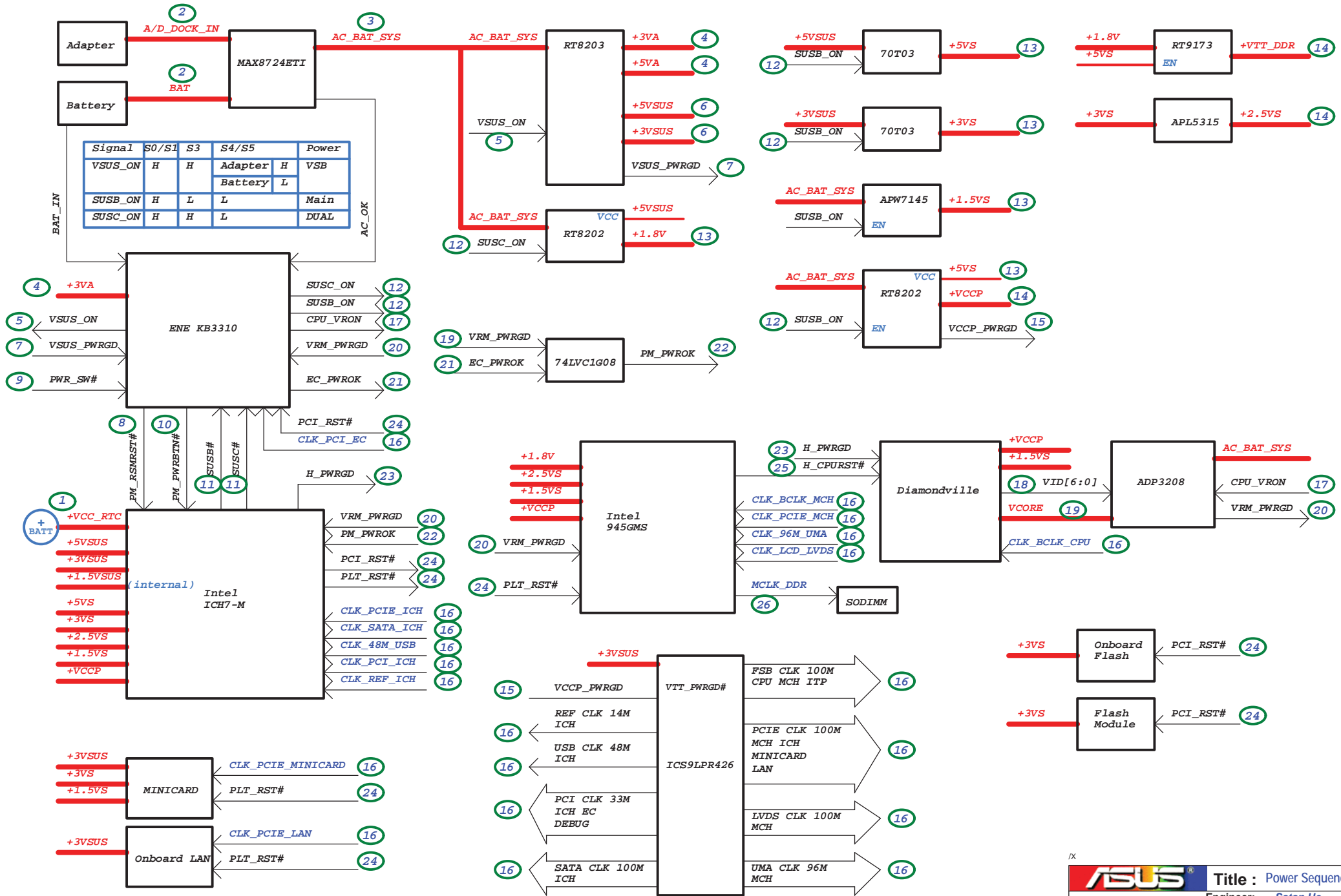
USB 0	Flash Conn
USB 1	USB Conn
USB 2	USB Conn
USB 3	USB Conn
USB 4	Card Reader
USB 5	Minicard
USB 6	NC
USB 7	Camera

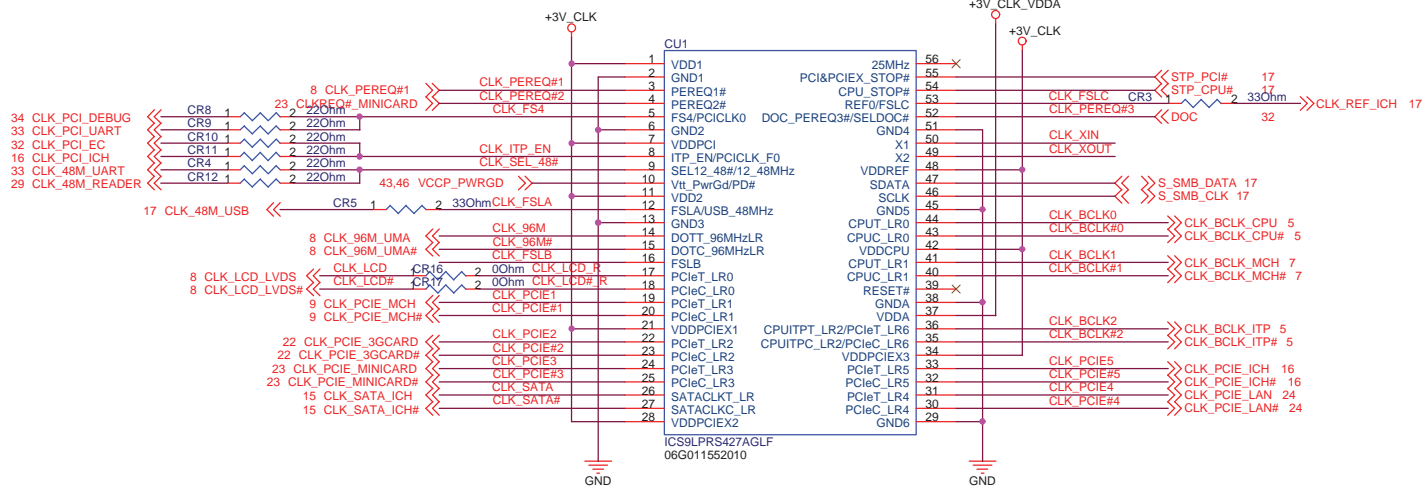
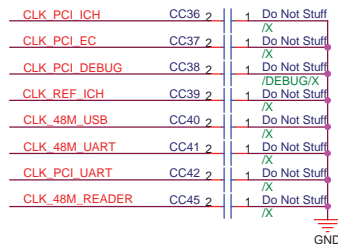
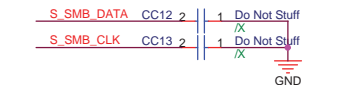
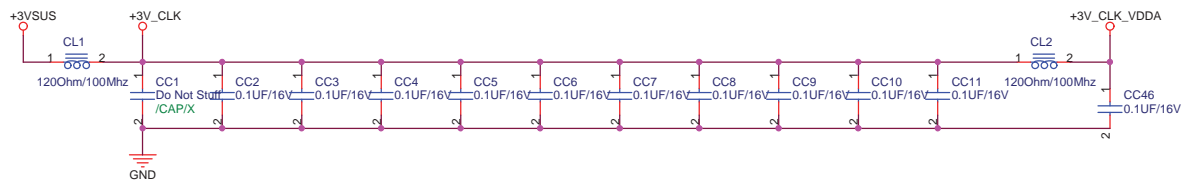
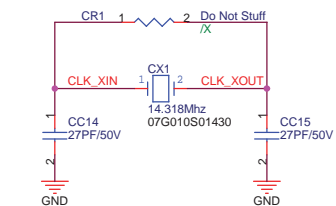
PCIE

PCIE 1	NC
PCIE 2	LAN
PCIE 3	Minicard
PCIE 4	NC

Azalia

ACZ_SDIN0	CODEC
ACZ_SDIN1	MODEM
ACZ_SDIN2	NC

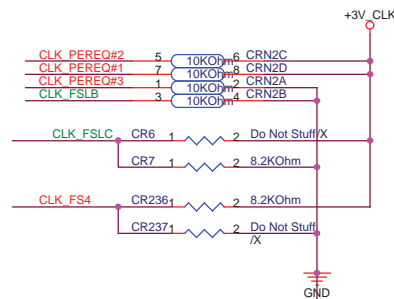
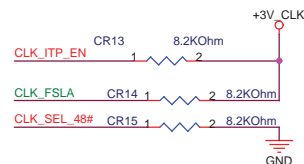


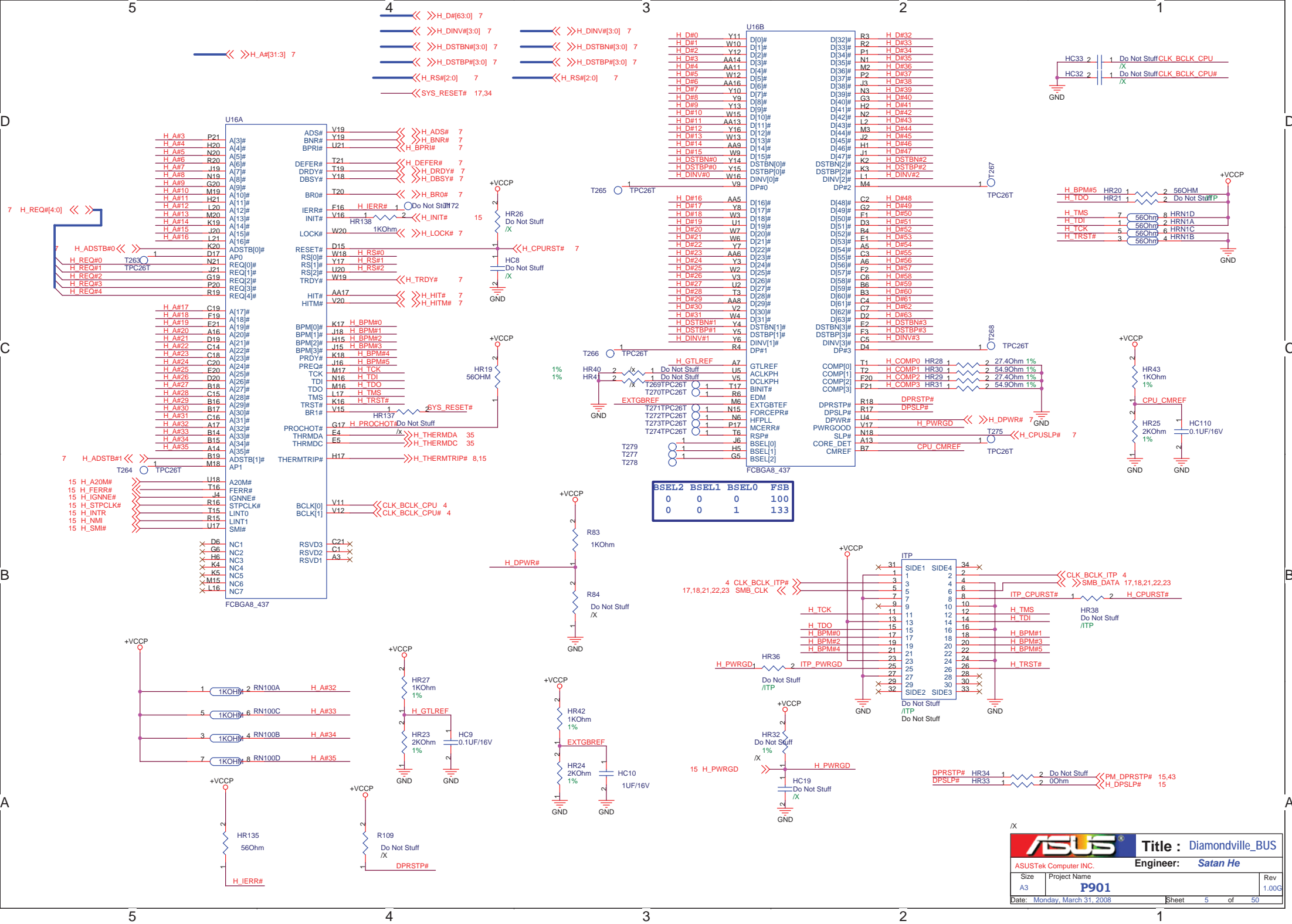


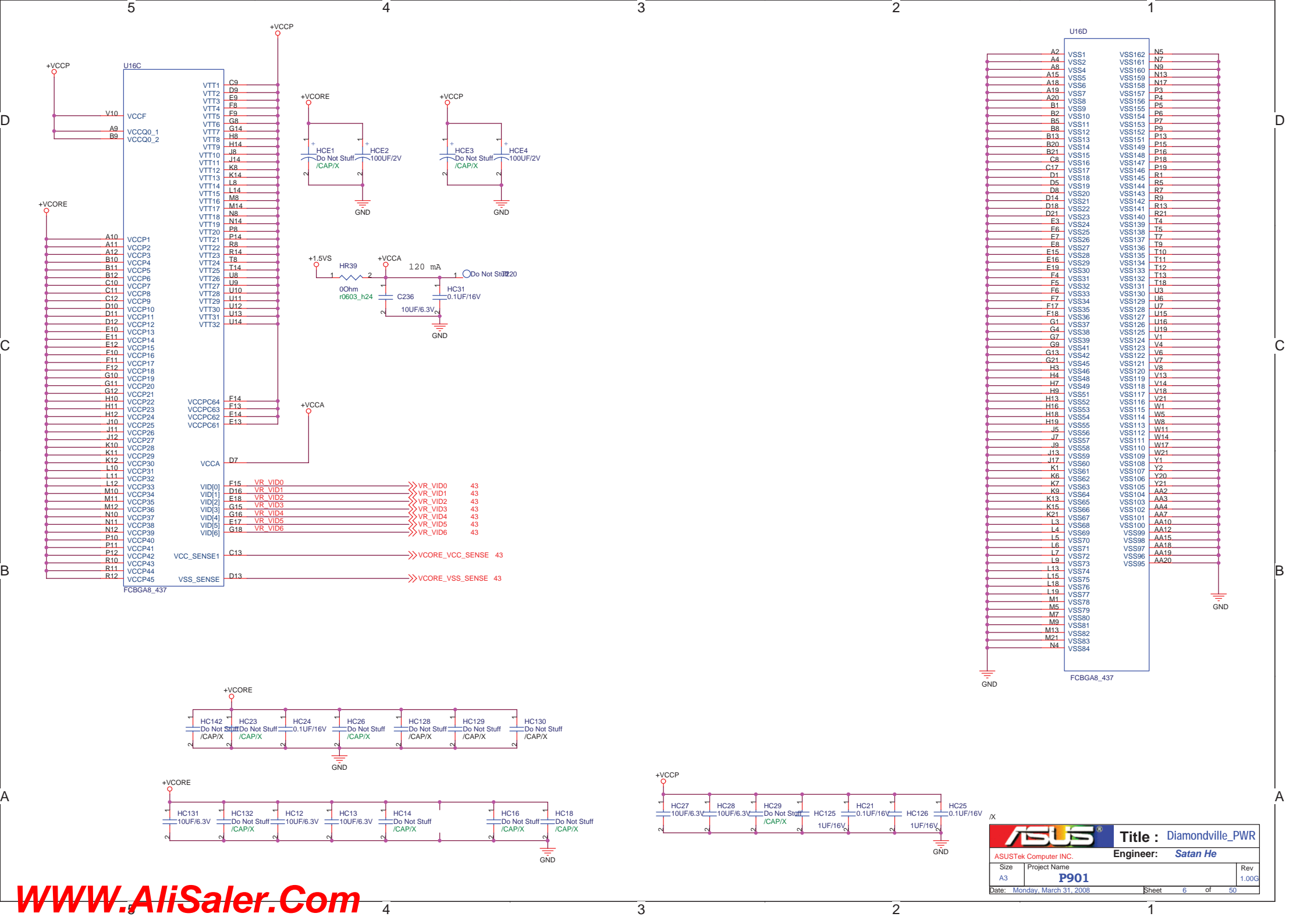
1:Disable
0:Enable

PEREQ1:PCIEx0 & PCIEx1
PEREQ2:PCIEx2 & PCIEx3 & SATA
PEREQ3:PCIEx4 & PCIEx5 & PCIEx6

FSC	FSB	FSA	CPU	PCIE	SATA
0	0	1	133	100	100
1	0	1	100	100	100



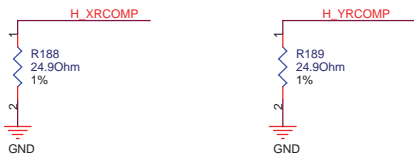




Power :
+VCCP

RCOMP

For Calibrating the FSB I/O Buffer



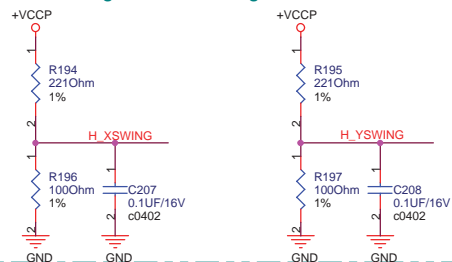
SCOMP

For Slew Rate Compensation on the FSB

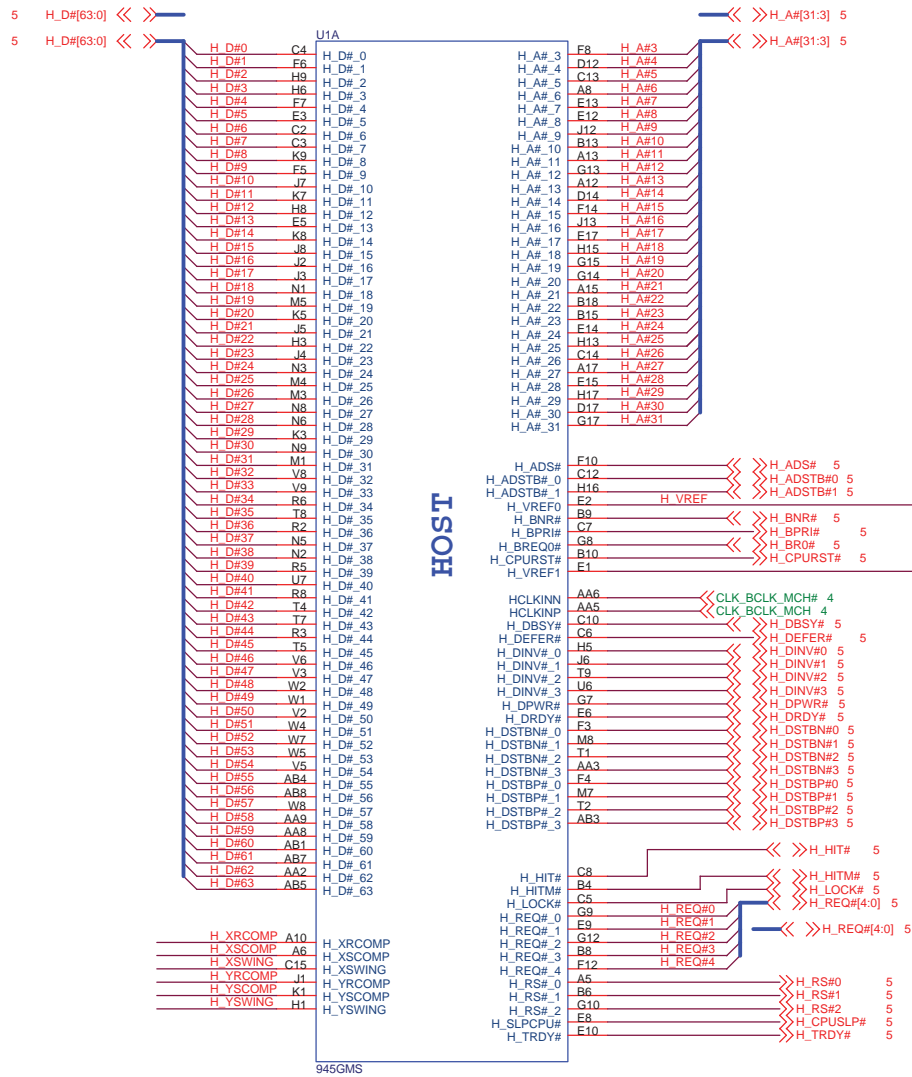


Voltage Swing

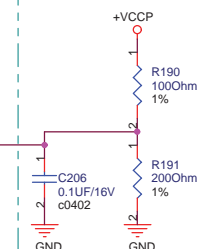
For Providing a Reference Voltage to The FSB RCOMP circuits



Signal voltage level =
0.3125*VCCP
Trace should be 10 mil wide
with 20 mil spacing

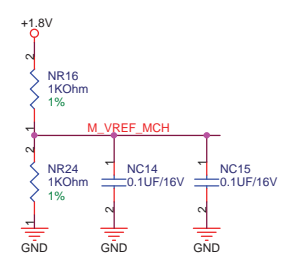
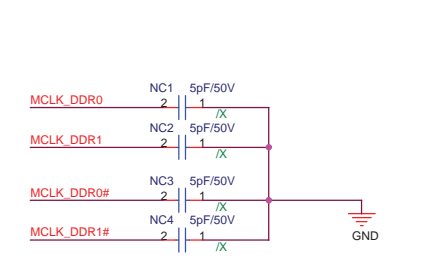
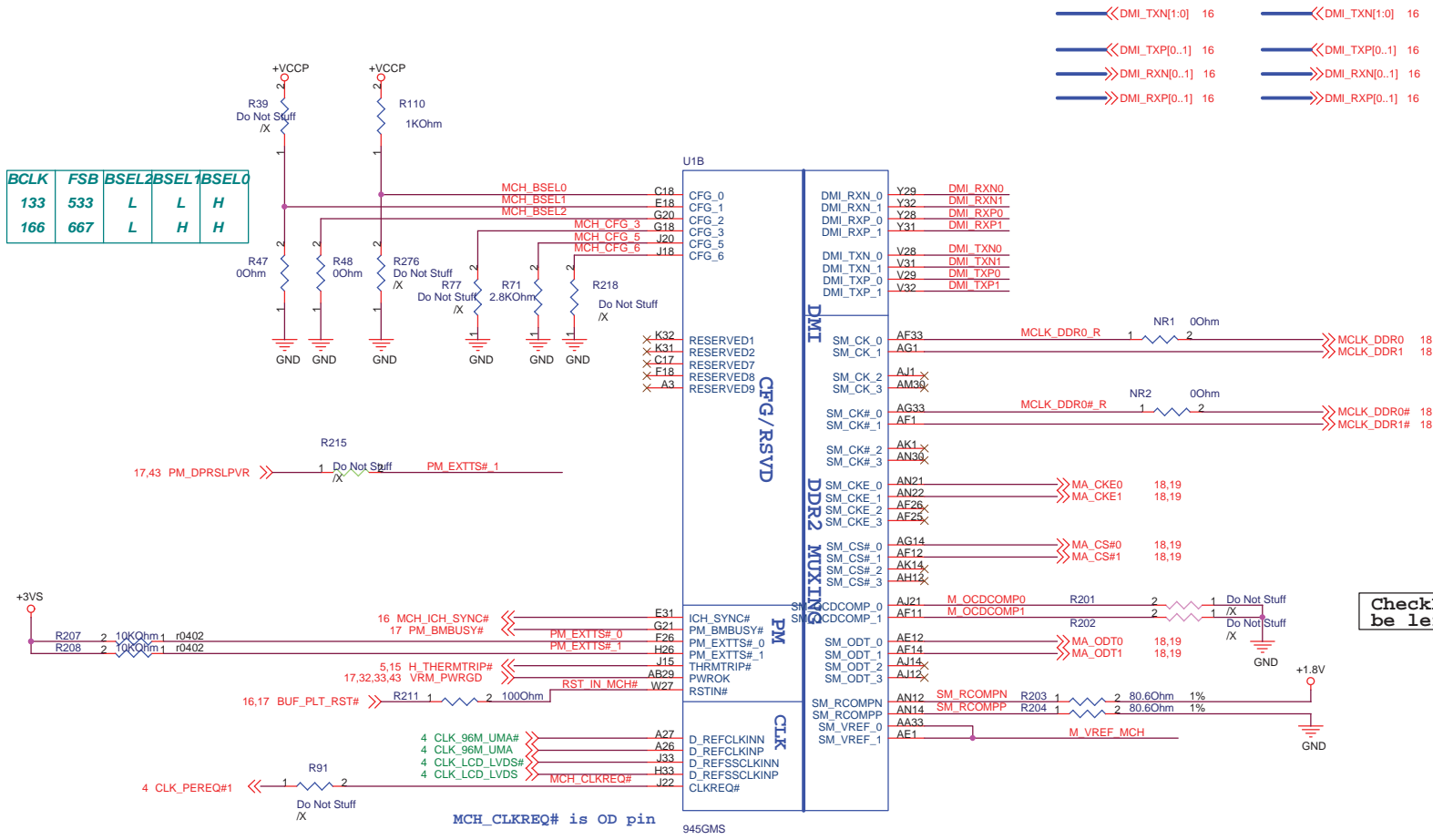


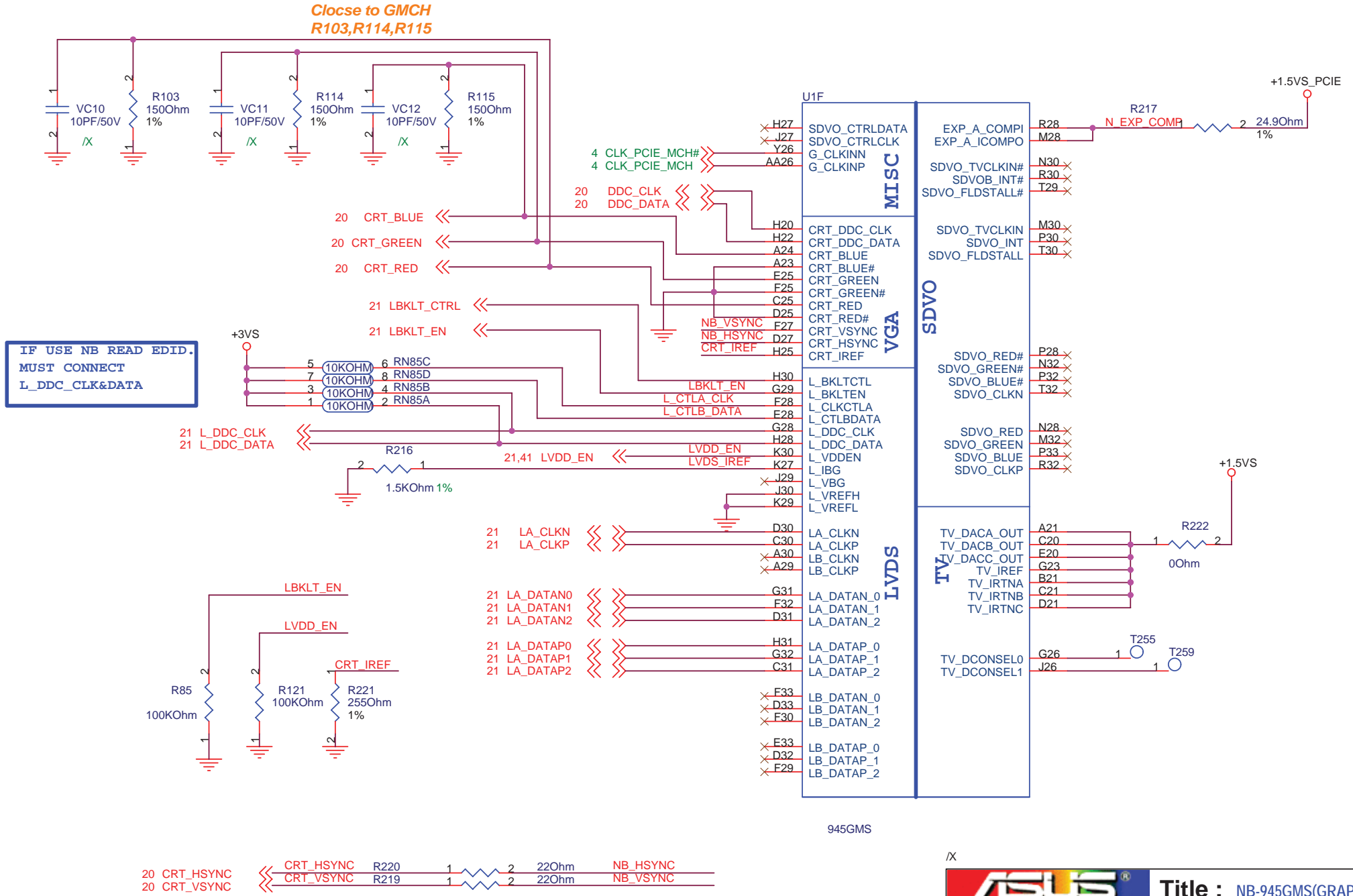
AGTL+ I/O Voltage Reference



Layout Note:
0.1uF should be placed 100mils or
less from GMCH pin.

BCLK	FSB	BSEL0	BSEL1	BSEL2
133	533	L	L	H
166	667	L	H	H



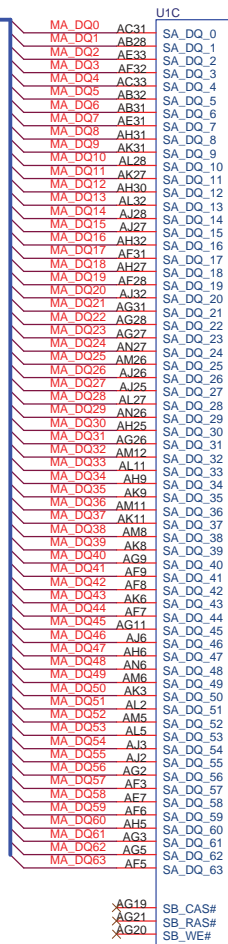


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ASUS		Title : NB-945GMS(GRAPHIC)	
ASUSTeK COMPUTER INC.		Engineer: Satan_He	
Size A4	Project Name P901		Rev 1.00G
Date: Monday, March 31, 2008		Sheet	9 of 50

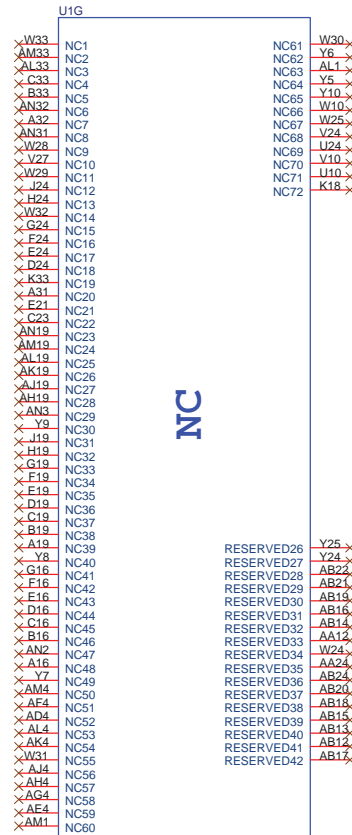
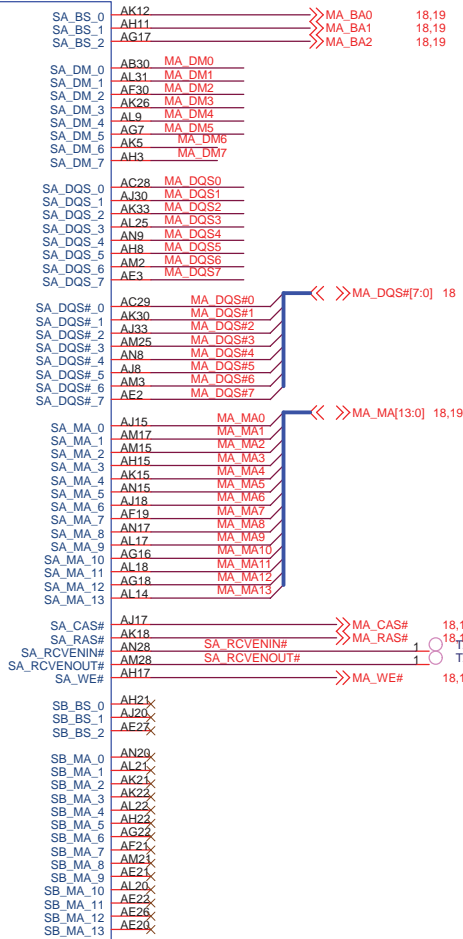
18 MA_DQS[7:0] << >> 18
18 MA_DM[7:0] << >> 18

18 MA_DQ[63:0] << >>
18 MA_DQ[63:0] << >>



945GMS

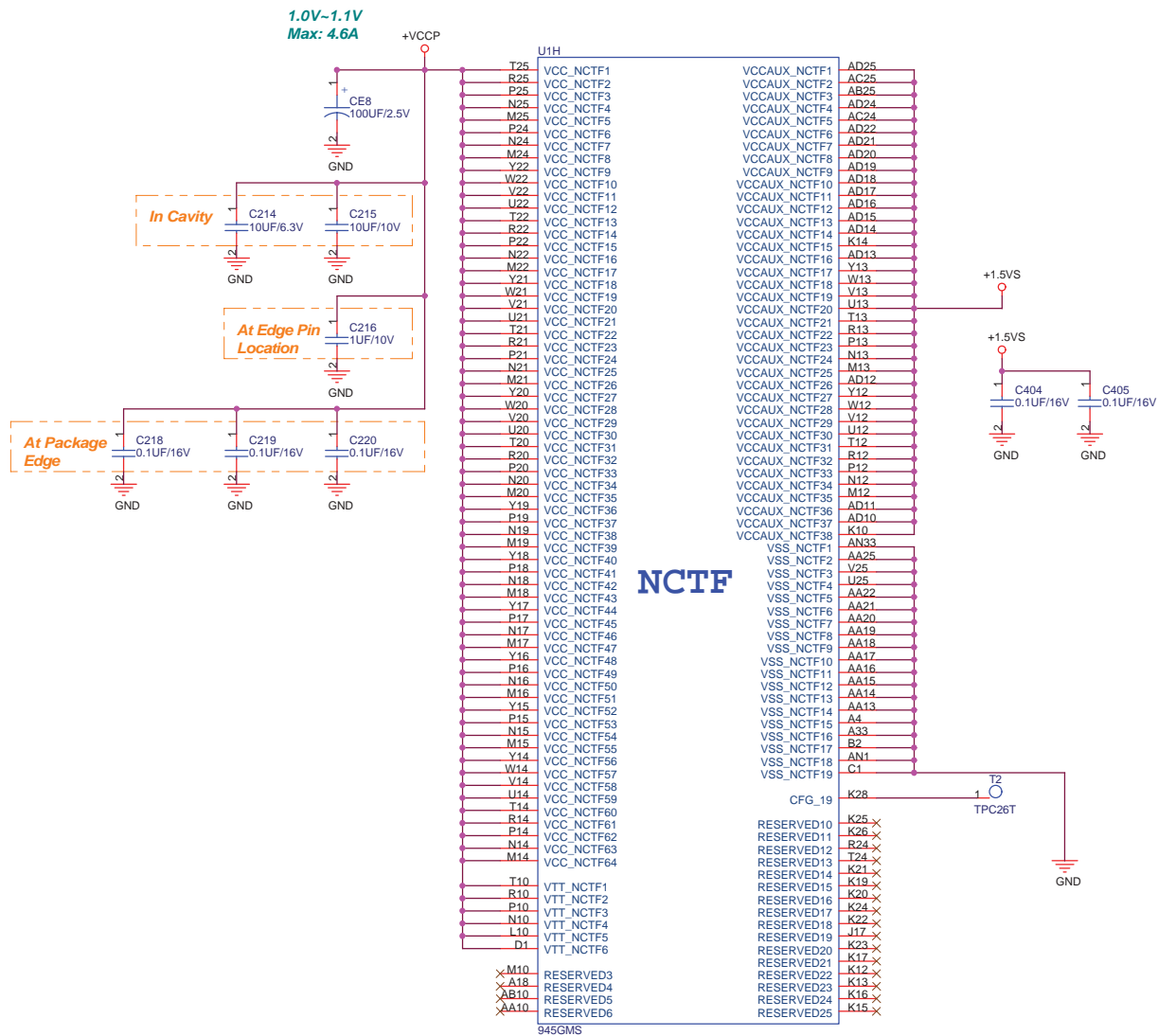
DDR2 SYSTEM MEMORY



945GMS

/X

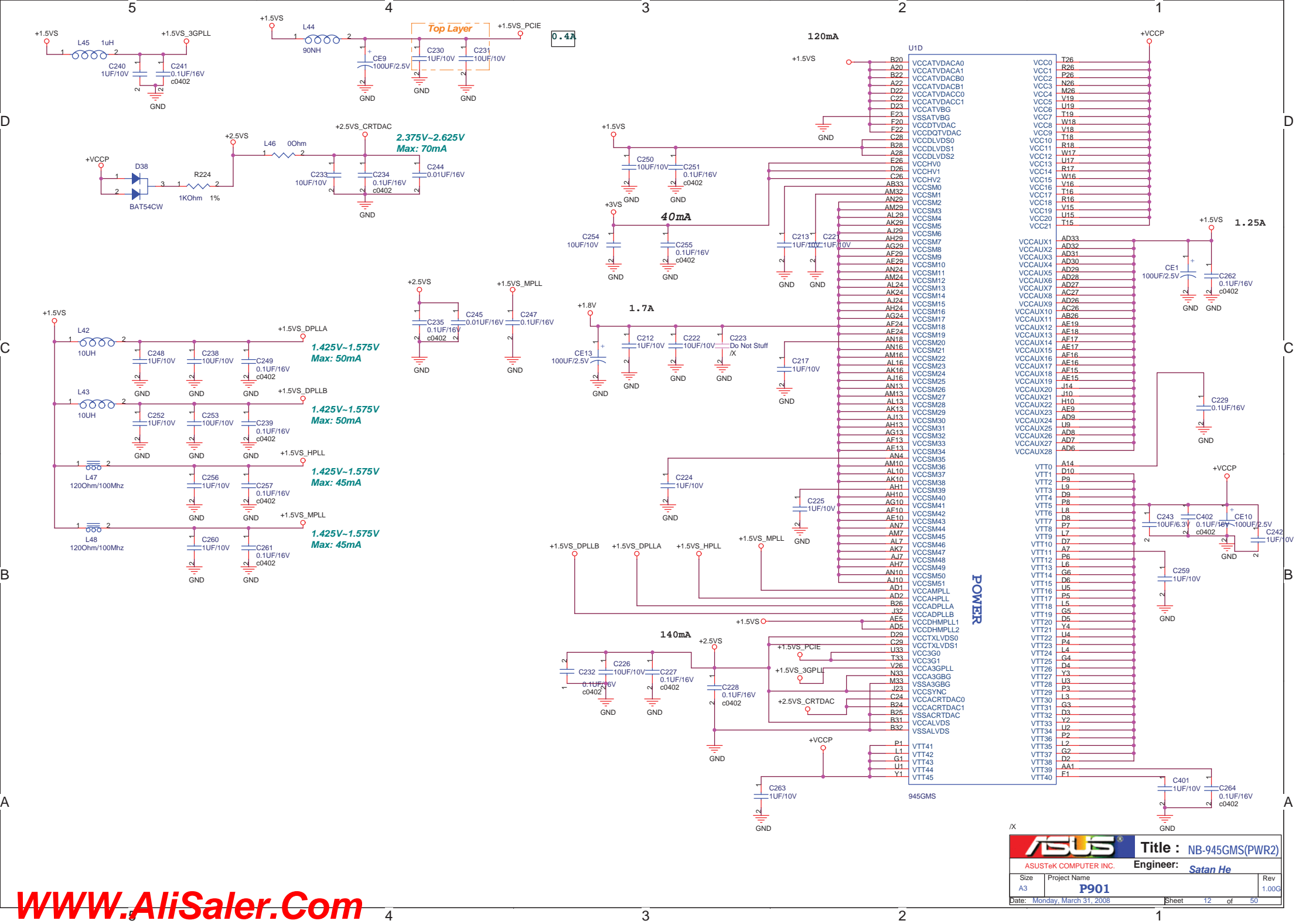
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ASUSTeK COMPUTER INC.		Engineer: Satan He	
Size A3	Project Name P901		Rev 1.00G
Date: Monday, March 31, 2008		Sheet 10 of 50	

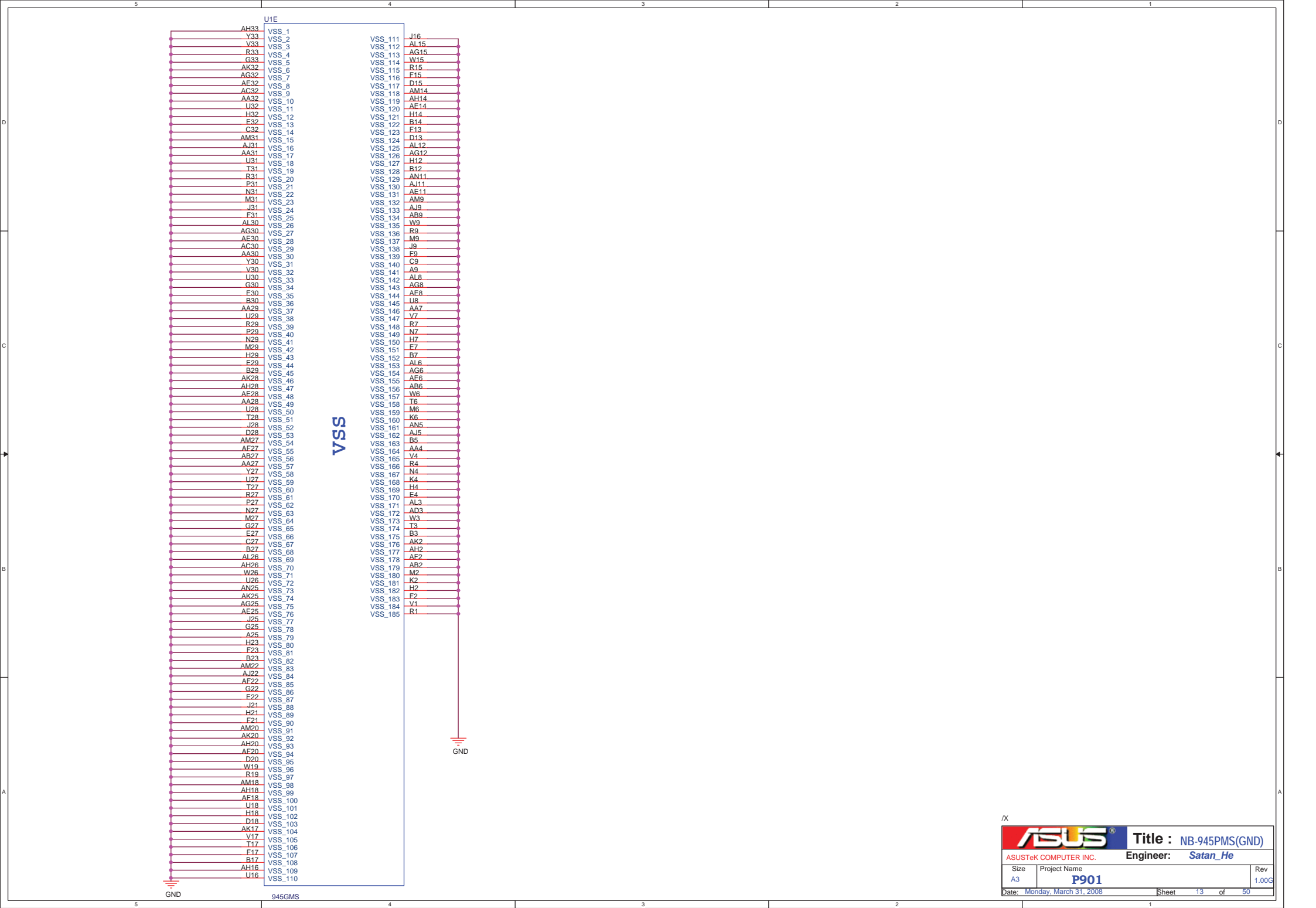


NCTF

CFG_19(K28) Strapping :
DMI LANE Reversal:
0:Normal Operation (Default)
1.:Reversal Lanes, 3->0,2->1..etc
Note:945GMS doesn't support DMI Lane Reversal


/X





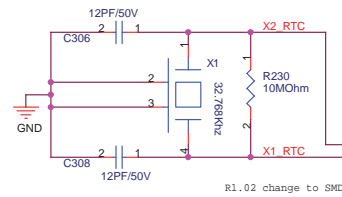
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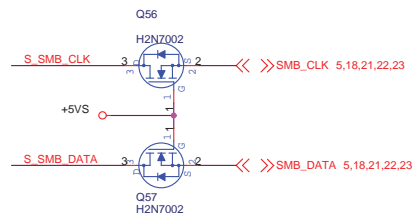
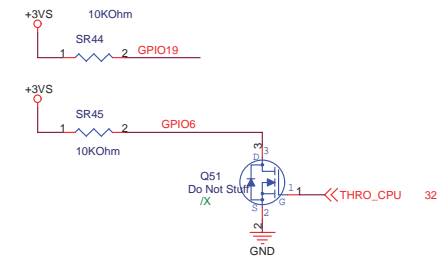
A4	Vss1	Vss98	P28
A23	Vss2	Vss99	R1
B1	Vss3	Vss100	R11
B8	Vss4	Vss101	R12
B11	Vss5	Vss102	R13
B14	Vss6	Vss103	R14
B17	Vss7	Vss104	R15
B20	Vss8	Vss105	R16
B26	Vss9	Vss106	R17
B28	Vss10	Vss107	R18
C2	Vss11	Vss108	T6
C6	Vss12	Vss109	T12
C27	Vss13	Vss110	T13
D10	Vss14	Vss111	T14
D13	Vss15	Vss112	T15
D18	Vss16	Vss113	T16
D21	Vss17	Vss114	T17
D24	Vss18	Vss115	U4
E1	Vss19	Vss116	U12
E2	Vss20	Vss117	U13
E4	Vss21	Vss118	U14
E8	Vss22	Vss119	U15
F15	Vss23	Vss120	U16
F4	Vss24	Vss121	U17
F5	Vss25	Vss122	U24
F12	Vss26	Vss123	U25
F27	Vss27	Vss124	U26
F28	Vss28	Vss125	V13
G1	Vss29	Vss126	V15
G2	Vss30	Vss127	V16
G5	Vss31	Vss128	V24
G6	Vss32	Vss129	V27
G9	Vss33	Vss130	V28
G14	Vss34	Vss131	W6
G18	Vss35	Vss132	W24
G21	Vss36	Vss133	W25
G24	Vss37	Vss134	W26
G25	Vss38	Vss135	Y24
H3	Vss39	Vss136	Y27
H4	Vss40	Vss137	Y28
H5	Vss41	Vss138	AA1
H24	Vss42	Vss139	AA24
H27	Vss43	Vss140	AA25
H28	Vss44	Vss141	AA26
J1	Vss45	Vss142	AB4
J2	Vss46	Vss143	AB6
J5	Vss47	Vss144	AB11
J24	Vss48	Vss145	AB14
J25	Vss49	Vss146	AB16
K24	Vss50	Vss147	AB19
K27	Vss51	Vss148	AB21
K28	Vss52	Vss149	AB24
L13	Vss53	Vss150	AB27
L15	Vss54	Vss151	AB28
L24	Vss55	Vss152	AC2
L25	Vss56	Vss153	AC5
L26	Vss57	Vss154	AC9
M3	Vss58	Vss155	AC11
M4	Vss59	Vss156	AD1
M5	Vss60	Vss157	AD3
M12	Vss61	Vss158	AD4
M13	Vss62	Vss159	AD7
M14	Vss63	Vss160	AD8
M15	Vss64	Vss161	AD11
M16	Vss65	Vss162	AD15
M17	Vss66	Vss163	AD19
M24	Vss67	Vss164	AD23
M28	Vss68	Vss165	AE2
N1	Vss69	Vss166	AE4
N2	Vss70	Vss167	AE8
N5	Vss71	Vss168	AE11
N6	Vss72	Vss169	AE13
N11	Vss73	Vss170	AE18
N12	Vss74	Vss171	AE21
N13	Vss75	Vss172	AE24
N14	Vss76	Vss173	AE25
N15	Vss77	Vss174	AF4
N16	Vss78	Vss175	AF7
N17	Vss79	Vss176	AF8
N18	Vss80	Vss177	AF11
N24	Vss81	Vss178	AG7
N25	Vss82	Vss179	AG11
N26	Vss83	Vss180	AG14
P3	Vss84	Vss181	AG17
P4	Vss85	Vss182	AG20
P12	Vss86	Vss183	AG25
P13	Vss87	Vss184	AH1
P14	Vss88	Vss185	AH3
P15	Vss89	Vss186	AH7
P16	Vss90	Vss187	AH12
P17	Vss91	Vss188	AH23
P24	Vss92	Vss189	AH27
P27	Vss93	Vss190	
P27	Vss94	Vss191	
P27	Vss95	Vss192	
P27	Vss96	Vss193	
P27	Vss97	Vss194	



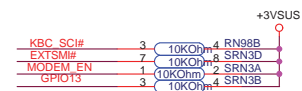
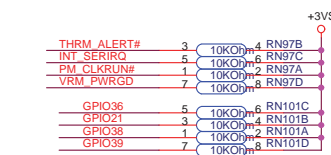
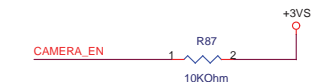
Title : SB-ICH7M(PWR)
Engineer: Satan He

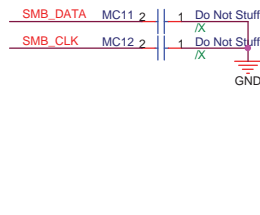
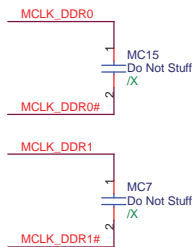
Size	Project Name	Rev
Custom	P901	1.00G
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WLAN_LED	WLAN	BT
High	v	v
High	v	x
High	x	v
Low	x	x

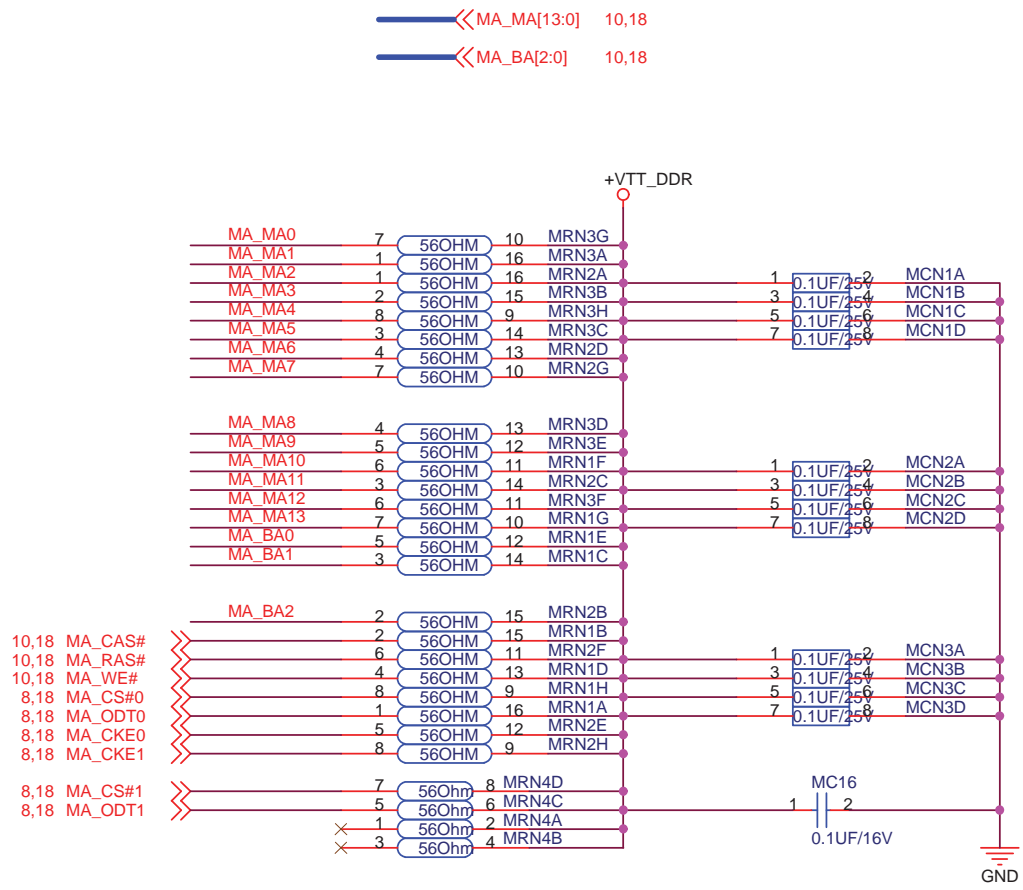


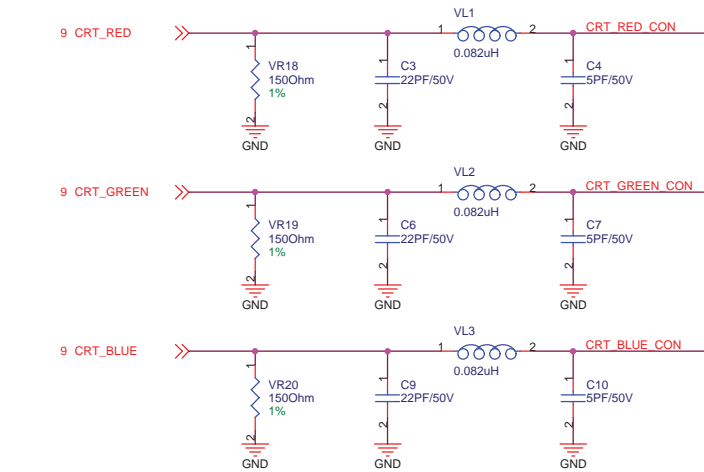


STD Type

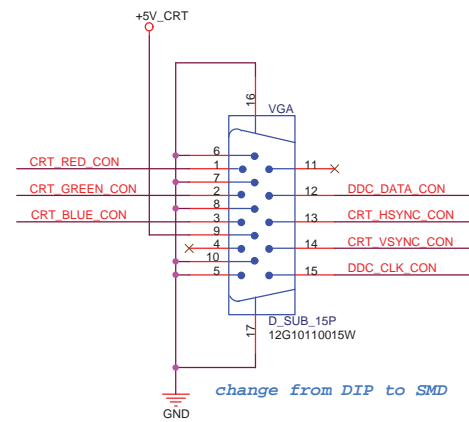
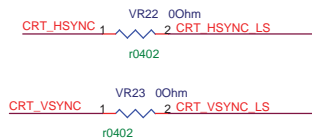
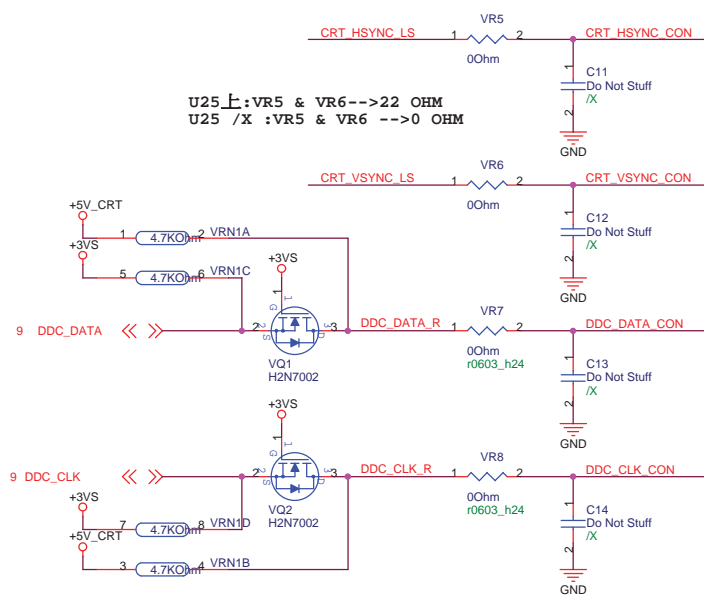
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MA_MA2	100	A2	DQ2	17	MA_DQ2	10
MA_MA3	99	A3	DQ3	19	MA_DQ3	10
MA_MA4	98	A4	DQ4	4	MA_DQ4	10
MA_MA5	97	A5	DQ5	6	MA_DQ5	10
MA_MA6	94	A6	DQ6	14	MA_DQ6	10,19
MA_MA7	92	A7	DQ7	16	MA_DQ7	10,19
MA_MA8	91	A8	DQ8	25	MA_DQ8	10
MA_MA9	90	A9	DQ9	35	MA_DQ9	10
MA_MA10	89	A10/AP	DQ10	37	MA_DQ10	10
MA_MA11	88	A11	DQ11	20	MA_DQ11	10
MA_MA12	87	A12	DQ12	22	MA_DQ12	10
MA_MA13	86	A13	DQ13	36	MA_DQ13	10
MA_MA14	85	A14	DQ14	38	MA_DQ14	10
MA_MA15	84	A15	DQ15	43	MA_DQ15	10
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MA_MA22	77	A22	DQ22	58	MA_DQ22	10
MA_MA23	76	A23	DQ23	61	MA_DQ23	10
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MA_MA43	56	A43	DQ43	140	MA_DQ43	10
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MA_MA89	10	A89	DQ89	225	MA_DQ89	10
MA_MA90	9	A90	DQ90	226	MA_DQ90	10
MA_MA91	8	A91	DQ91	227	MA_DQ91	10
MA_MA92	7	A92	DQ92	228	MA_DQ92	10
MA_MA93	6	A93	DQ93	229	MA_DQ93	10
MA_MA94	5	A94	DQ94	230	MA_DQ94	10
MA_MA95	4	A95	DQ95	231	MA_DQ95	10
MA_MA96	3	A96	DQ96	232	MA_DQ96	10
MA_MA97	2	A97	DQ97	233	MA_DQ97	10
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MA_MA109	0	A109	DQ109	245	MA_DQ109	10
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MA_MA112	0	A112	DQ112	248	MA_DQ112	10
MA_MA113	0	A113	DQ113	249	MA_DQ113	10
MA_MA114	0	A114	DQ114	250	MA_DQ114	10
MA_MA115	0	A115	DQ115	251	MA_DQ115	10
MA_MA116	0	A116	DQ116	252	MA_DQ116	10
MA_MA117	0	A117	DQ117	253	MA_DQ117	10
MA_MA118	0	A118	DQ118	254	MA_DQ118	10
MA_MA119	0	A119	DQ119	255	MA_DQ119	10
MA_MA120	0	A120	DQ120	256	MA_DQ120	10
MA_MA121	0	A121	DQ121	257	MA_DQ121	10
MA_MA122	0	A122	DQ122	258	MA_DQ122	10
MA_MA123	0	A123	DQ123	259	MA_DQ123	10
MA_MA124	0	A124	DQ124	260	MA_DQ124	10
MA_MA125	0	A125	DQ125	261	MA_DQ125	10
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MA_MA127	0	A127	DQ127	263	MA_DQ127	10
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MA_MA145	0	A145	DQ145	281	MA_DQ145	10
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MA_MA156	0	A156	DQ156	292	MA_DQ156	10
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MA_MA166	0	A166	DQ166	302	MA_DQ166	10
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MA_MA177	0	A177	DQ177	313	MA_DQ177	10
MA_MA178	0	A178	DQ178	314	MA_DQ178	10
MA_MA179	0	A179	DQ179	315	MA_DQ179	10
MA_MA180	0	A180	DQ180	316	MA_DQ180	10
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MA_MA182	0	A182	DQ182	318	MA_DQ182	10
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MA_MA184	0	A184	DQ184	320	MA_DQ184	10
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MA_MA186	0	A186	DQ186	322	MA_DQ186	10
MA_MA187	0	A187	DQ187	323	MA_DQ187	10
MA_MA188	0	A188	DQ188	324	MA_DQ188	10
MA_MA189	0	A189	DQ189	325	MA_DQ189	10
MA_MA190	0	A190	DQ190	326	MA_DQ190	10
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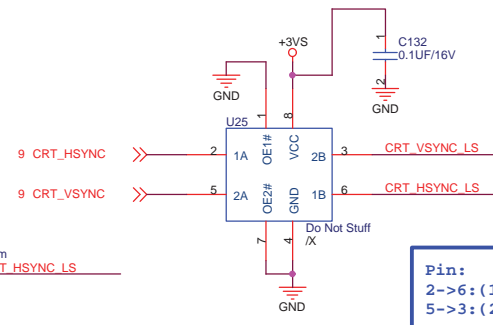




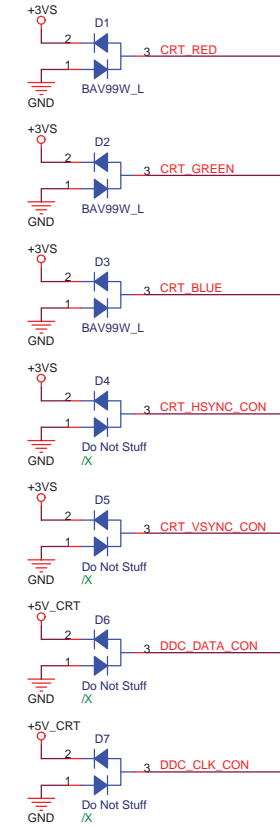
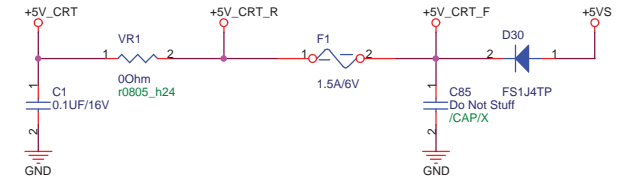
U25上:VR5 & VR6-->22 OHM
U25 /X :VR5 & VR6 -->0 OHM



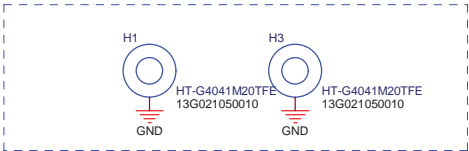
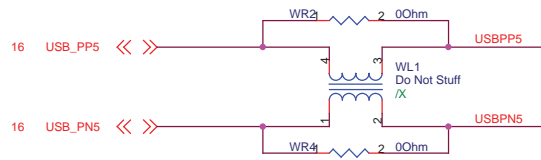
VGA use 12G10110015W & 12G10110015N



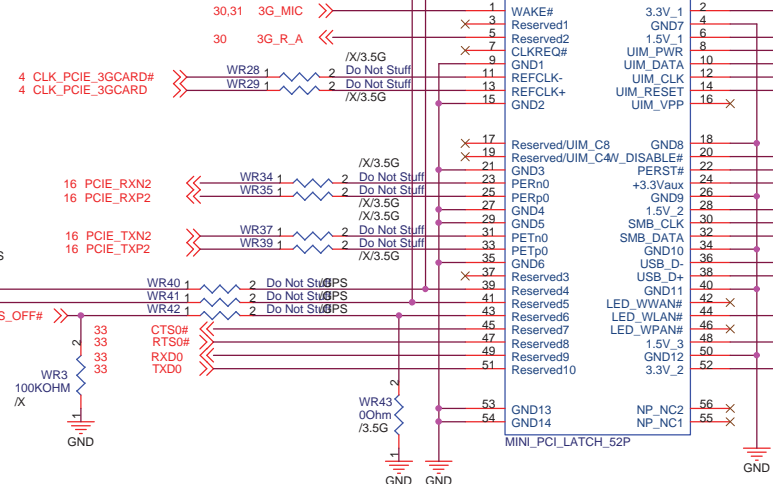
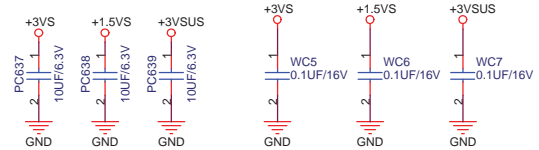
Pin:
2->6: (1A->1B)
5->3: (2A->2B)



ASUS		Title : Onboard VGA	
ASUSTek Computer INC.		Engineer: Kell_Huang	
Size	Project Name	Rev	
A3	P901	1.00G	
Date: Monday, March 31, 2008	Sheet	20	of 47

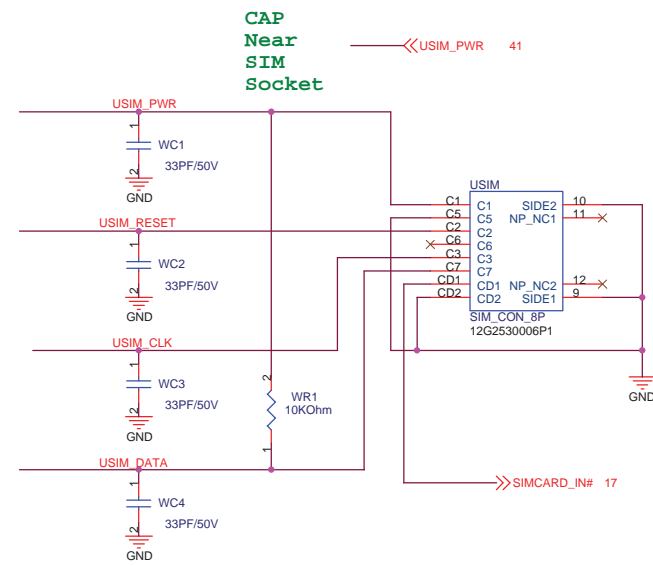
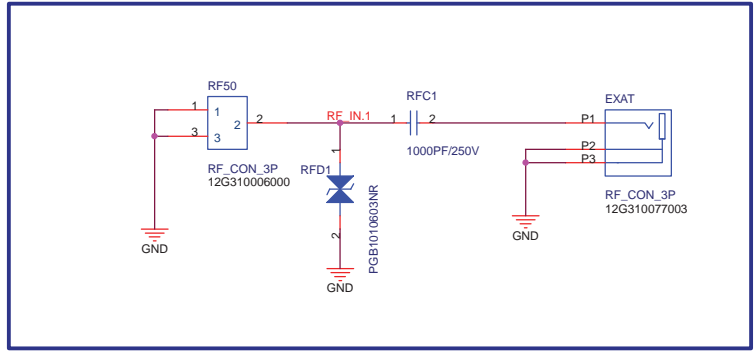


MINI CARD NUT(1.6mm) *2

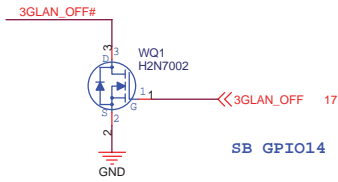


MINICARD use 12G03010052Q

External Antenna

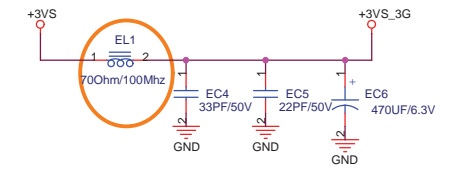


CAP Near SIM Socket

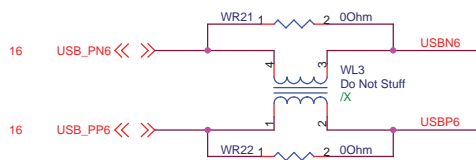
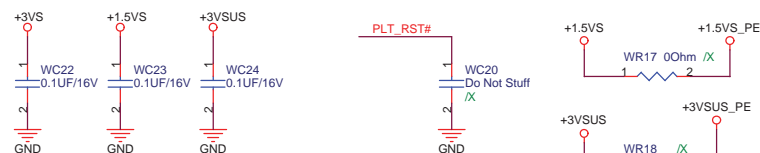
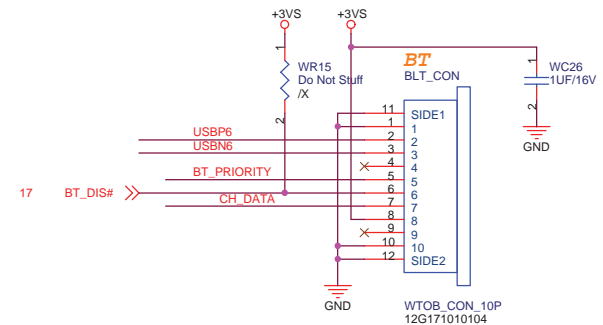
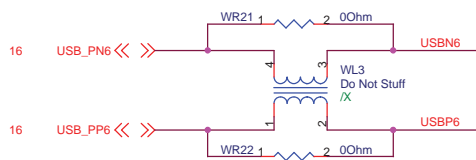
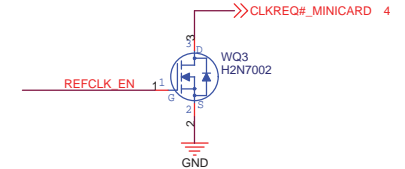
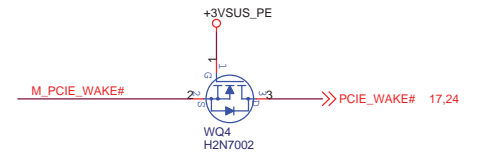
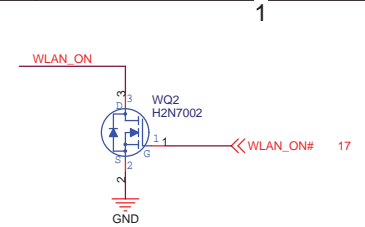
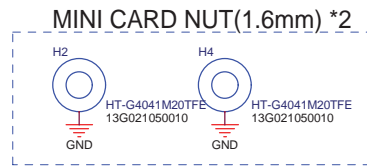


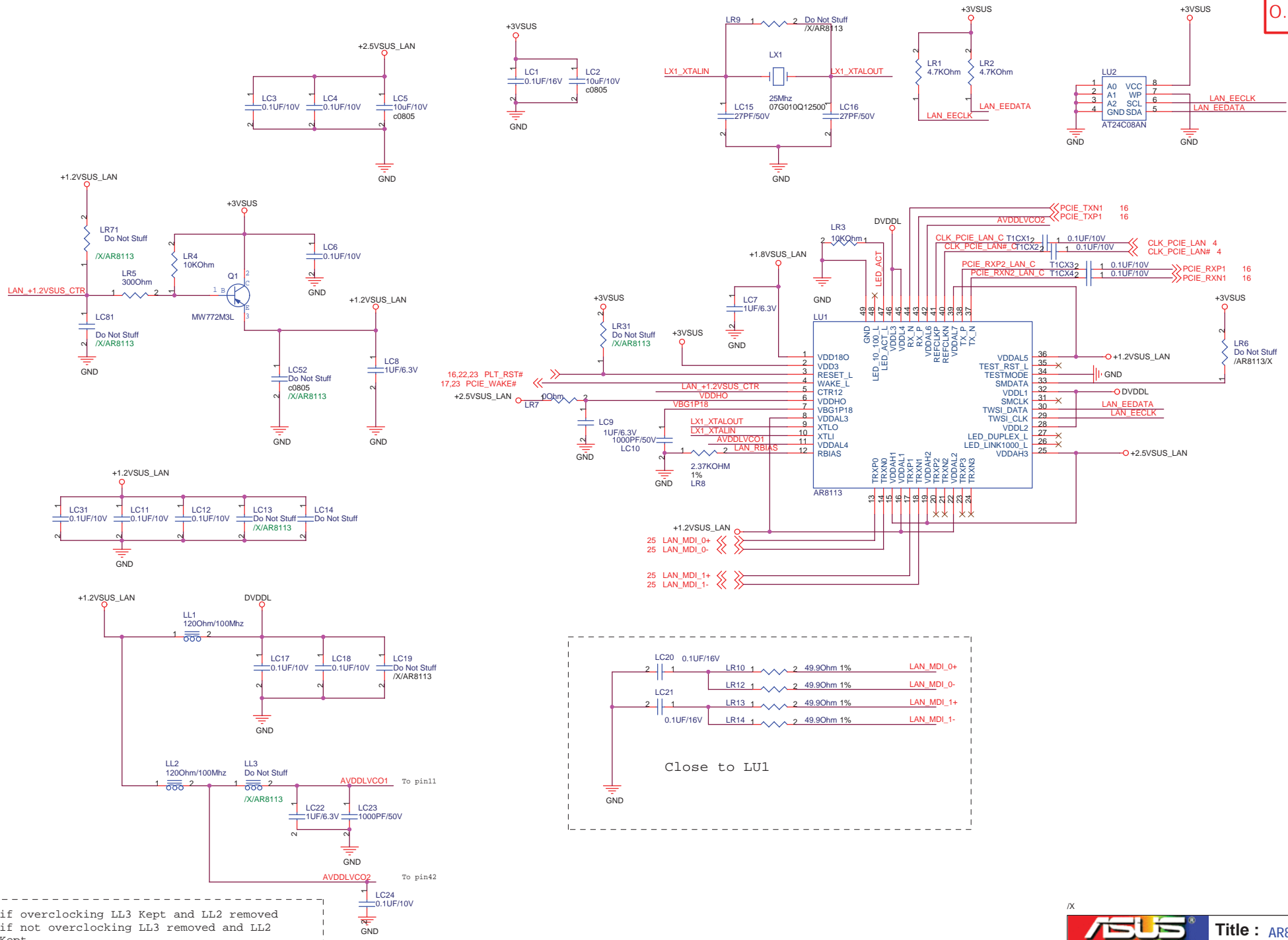
SB GPIO14

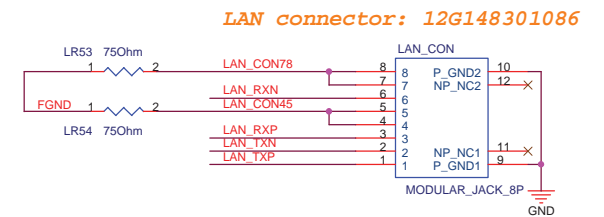
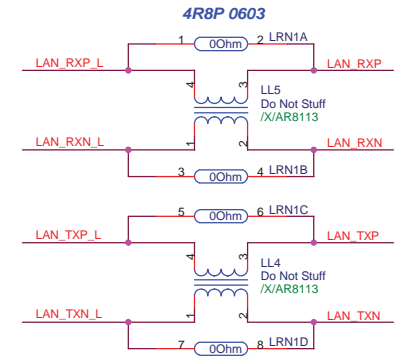
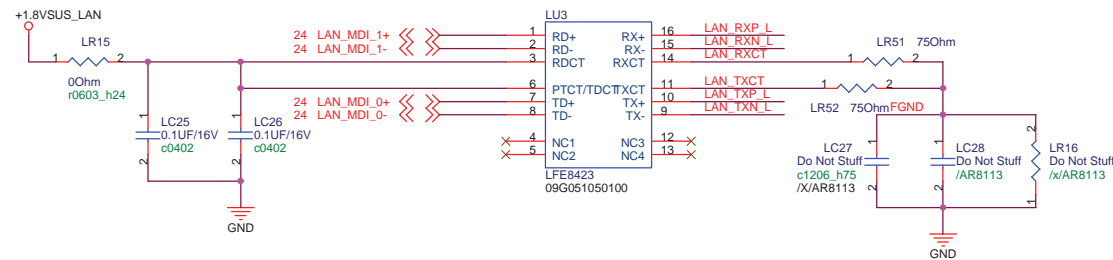
2008/03/11 change

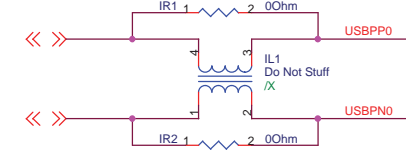
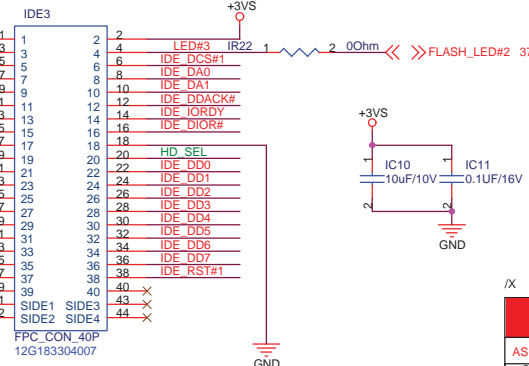
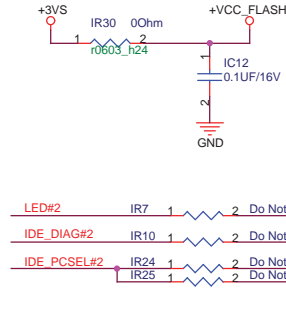
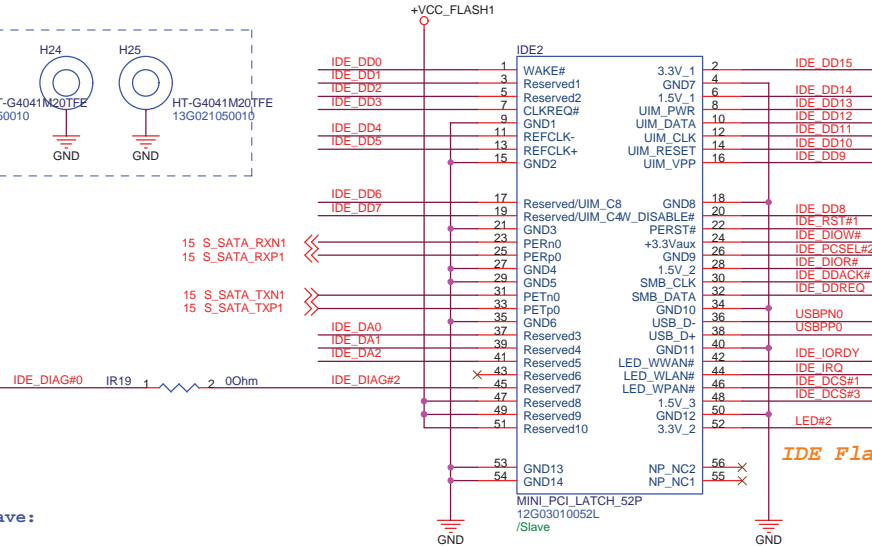
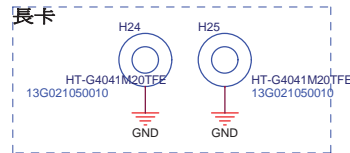



ASUS		3.5G Module & External Antenna	
ASUSTek Computer INC.		Title : Kell_Huang	
Size	Project Name	Rev	1.00G
A3	P901		
Date: Monday, March 31, 2008	Sheet 22 of 47		

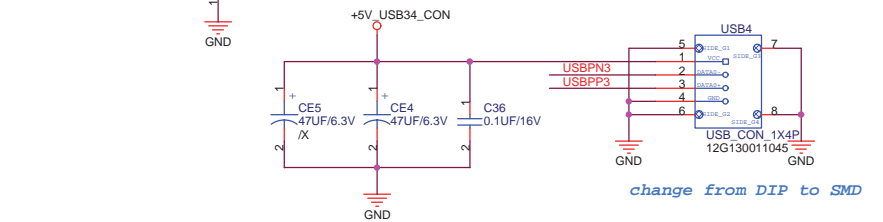
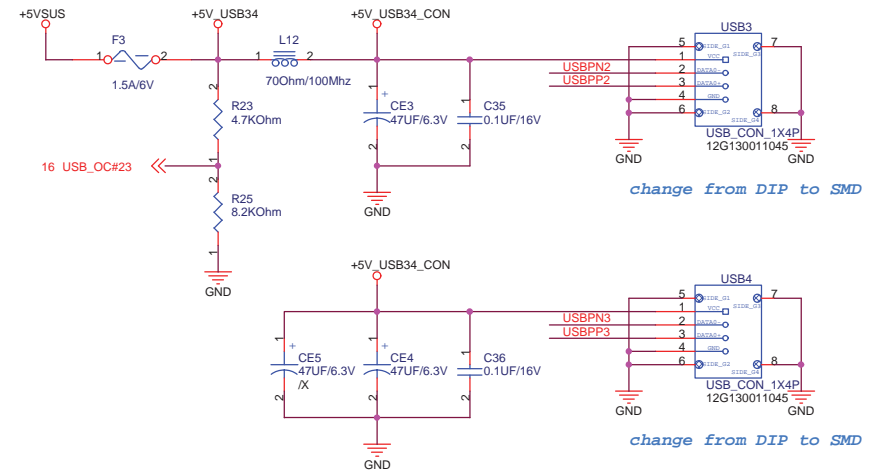
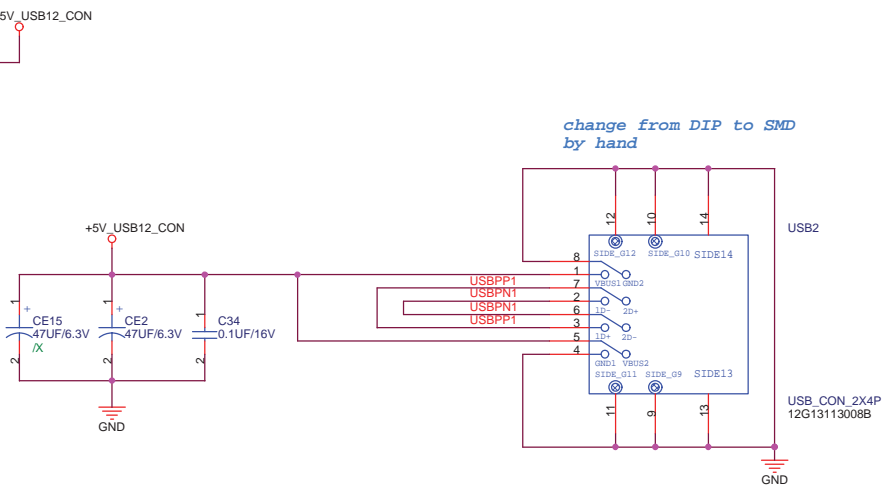
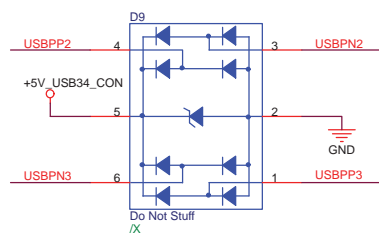
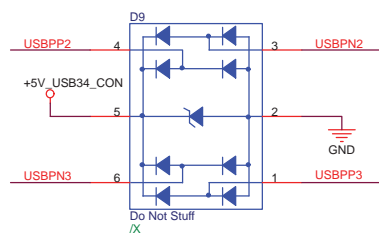
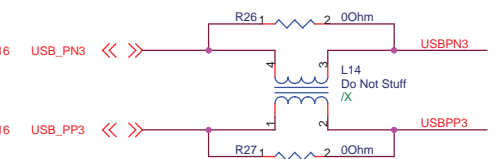
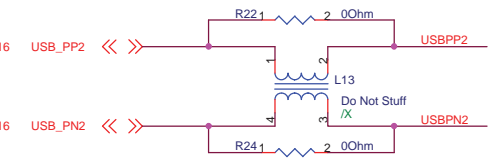
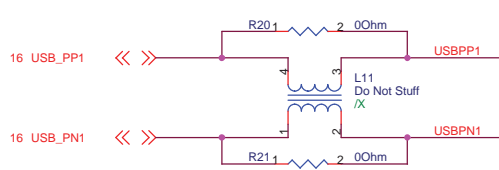
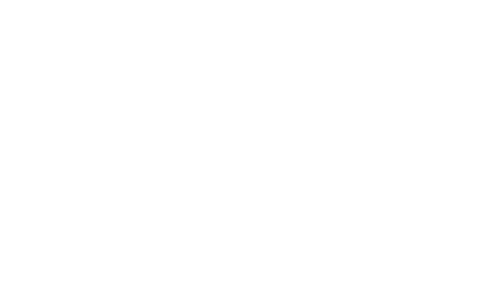




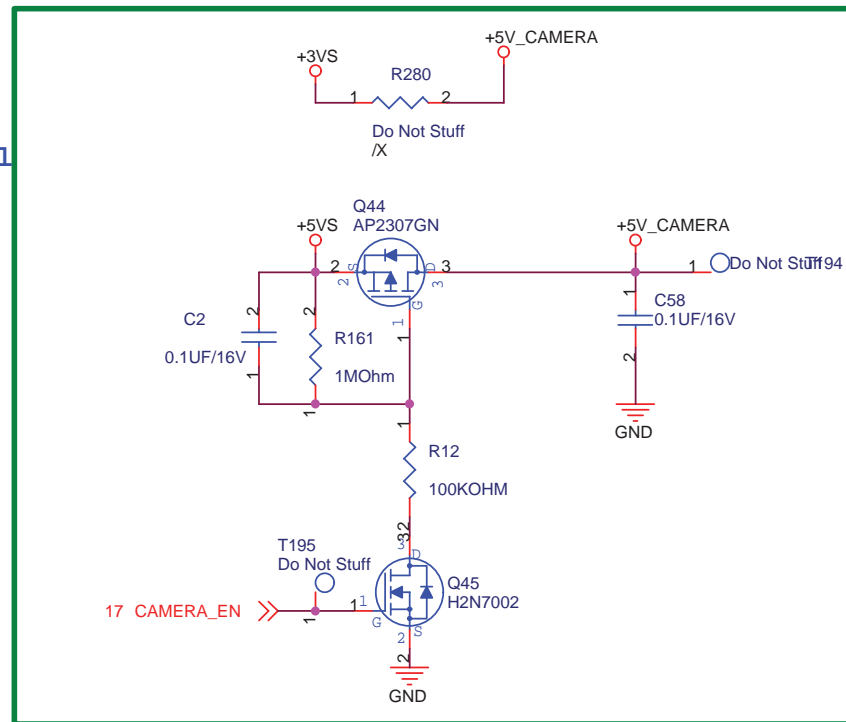




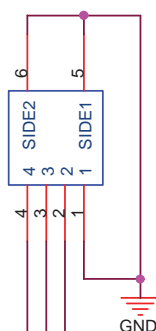
		Title : HD + Flash Conn	
ASUSTek Computer INC.		Engineer: <u>Kell Huang</u>	
Size A3	Project Name P901	Rev 1.00G	
Date: Monday, March 31, 2008		Sheet 26	of 47



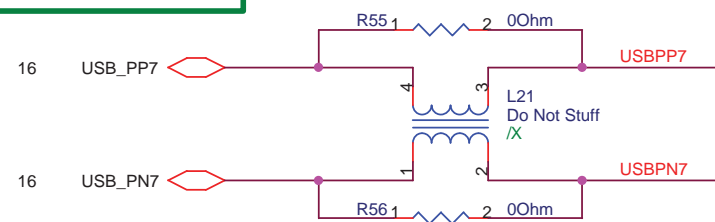
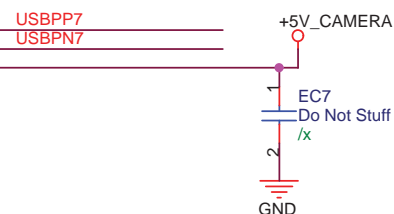
Power Control



CAMERA
WtoB_CON_4P

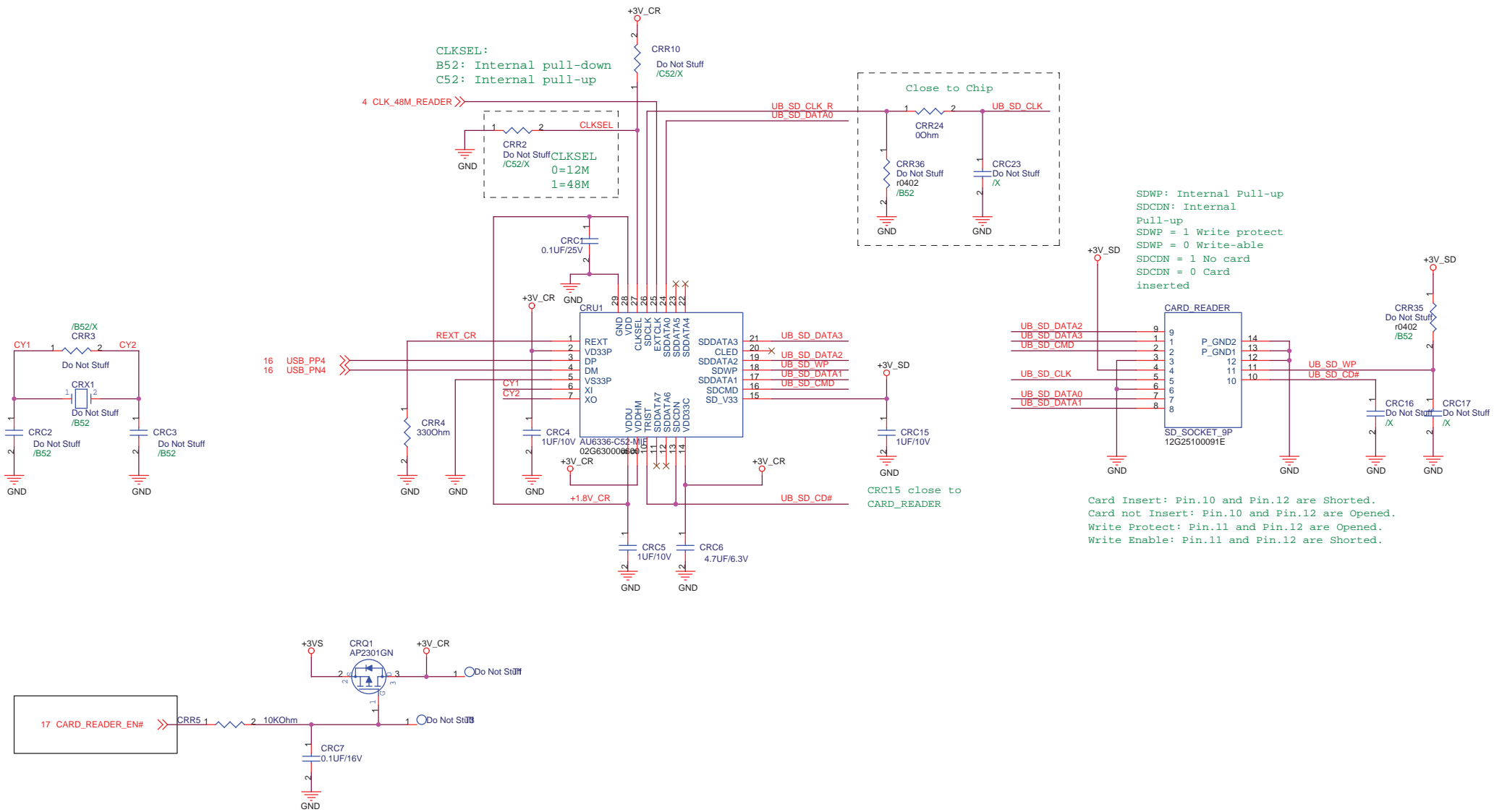


CAMERA USB Interface

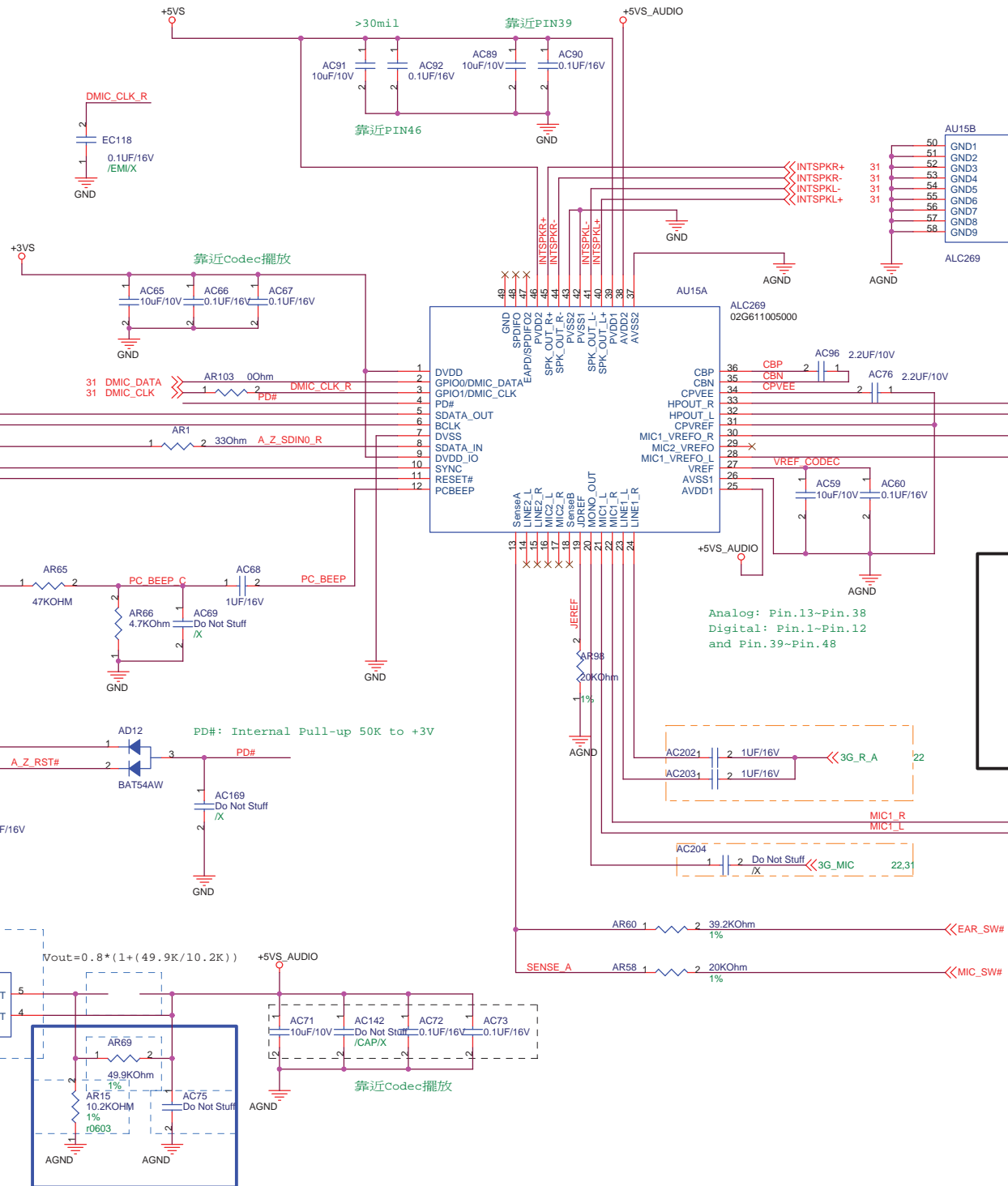
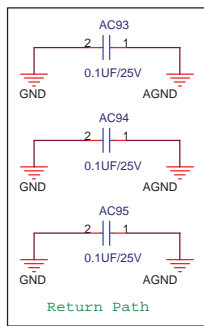


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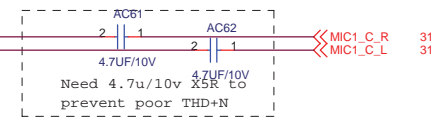
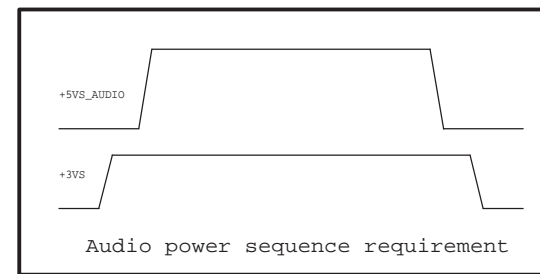
ASUS		Title : Camera Power	
ASUSTek Computer INC.		Engineer: Kell_Huang	
Size A4	Project Name P901	Rev 1.00G	
Date: Monday, March 31, 2008	Sheet 28 of 47		



Card Insert: Pin.10 and Pin.12 are Shorted.
Card not Insert: Pin.10 and Pin.12 are Opened.
Write Protect: Pin.11 and Pin.12 are Opened.
Write Enable: Pin.11 and Pin.12 are Shorted.

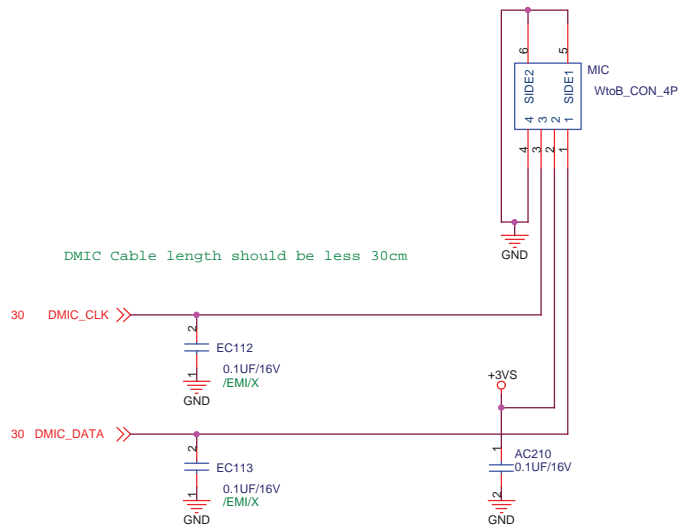


- Change Note
1. Remove AR34, AR63 for cost down
 2. Add +5VS_AUDIO shut_down control
 3. Remove net name +3VS_AUDIO
 4. Add diagram for power sequence requirement

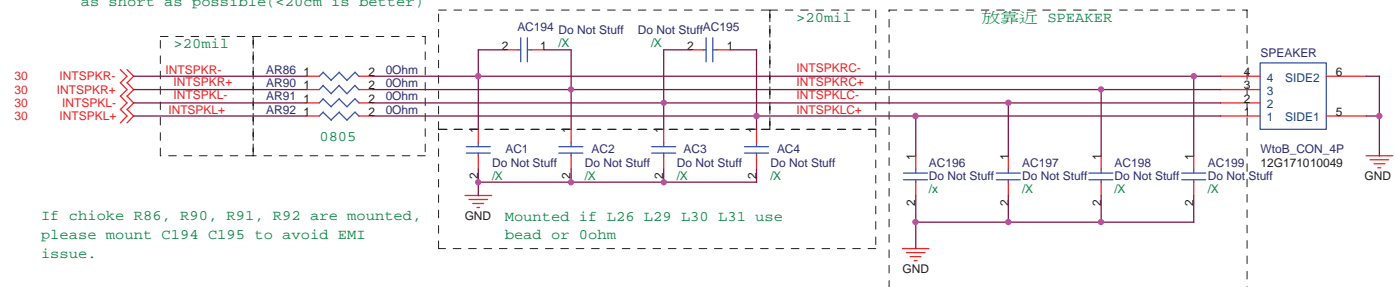


For Audio Noise Issue

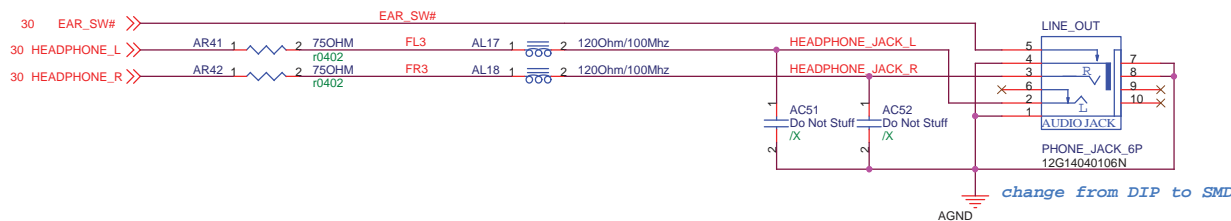
ASUS		Title : ALC269-1	
ASUSTek Computer Inc.		Engineer: Mick	
Size A3	Project Name P901	Rev 0.4A	
Date: Monday, March 31, 2008		Sheet	30 of 50



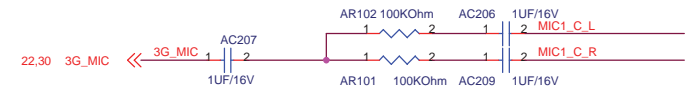
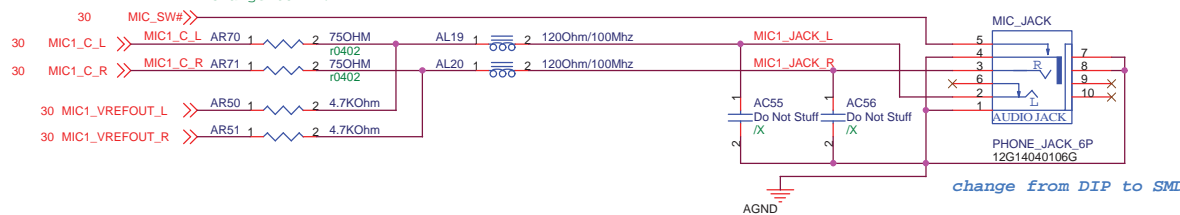
Total length from speakerR+- L+- (pin40 41 44 45) to internal speaker please as short as possible(<20cm is better)

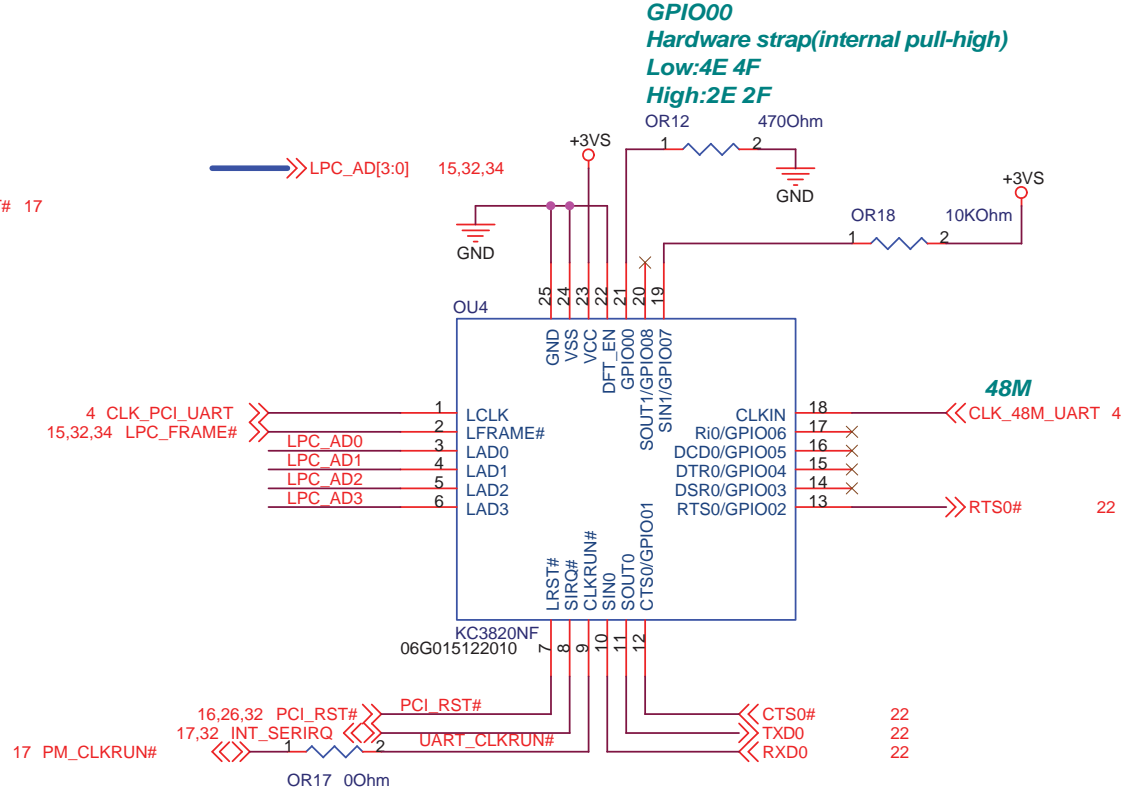
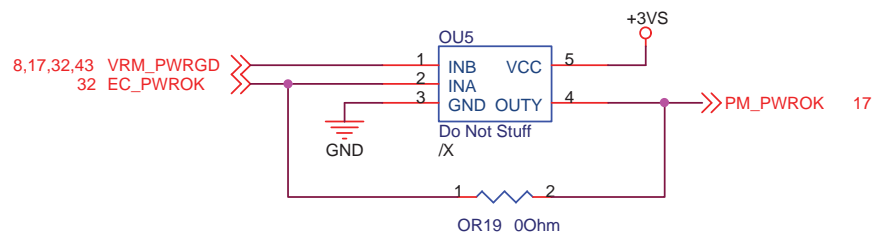
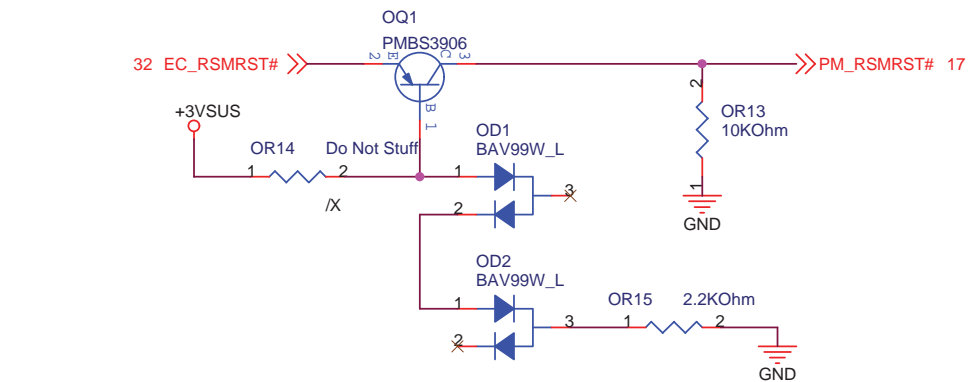


LINE_OUT use 12G14040106N



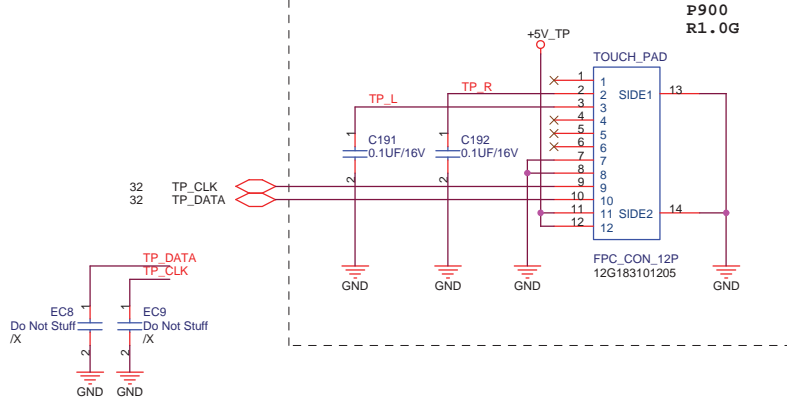
MIC_JACK use 12G14040106G



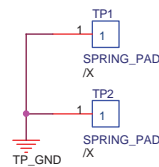
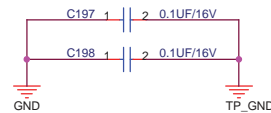
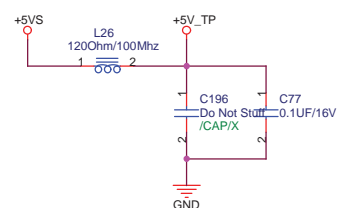
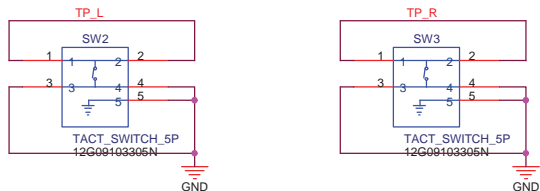


UART Control
IC for using
GPS module due
to no UART on
ENE EC

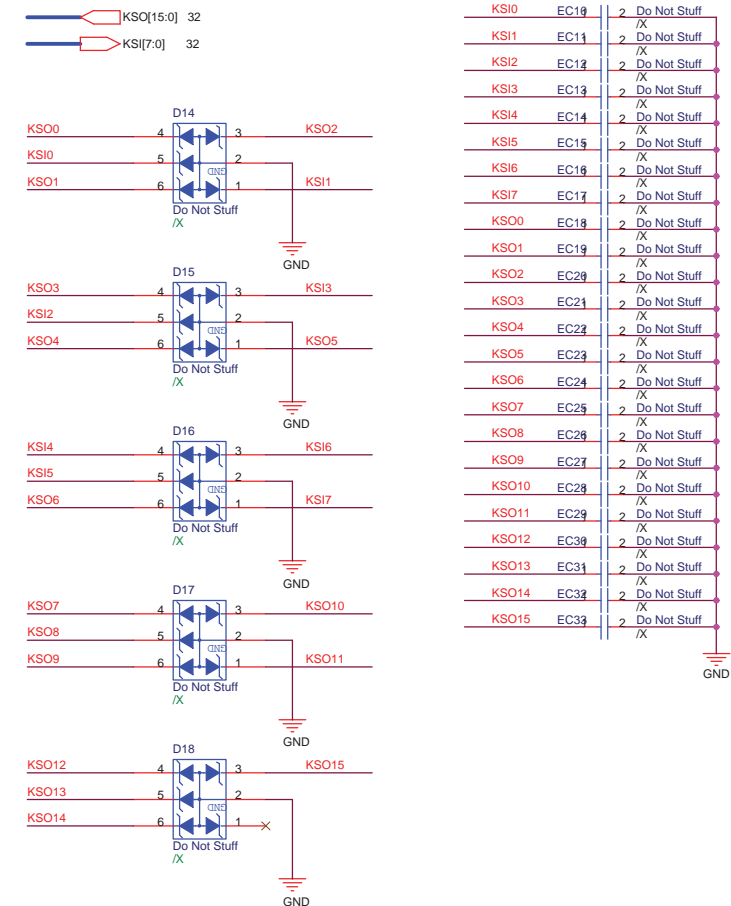
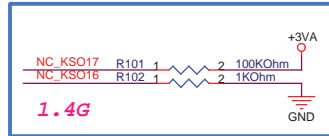
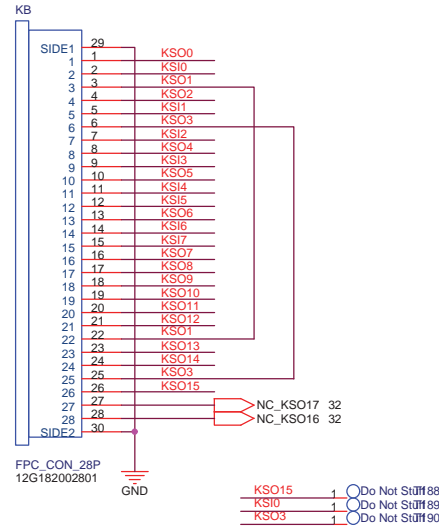
For Touch-Pad



SW2, SW3 use 12G09103305N

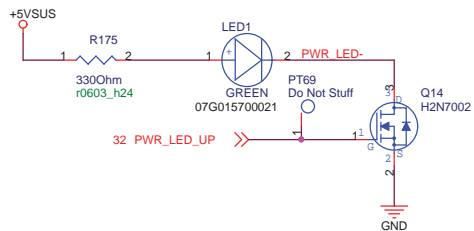


For Keyboard Connector

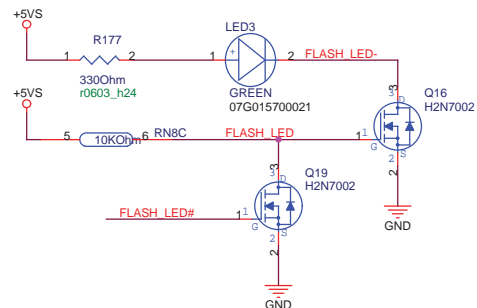


KSI0	EC19	2	Do Not Stuff
KSI1	EC11	2	Do Not Stuff
KSI2	EC12	2	Do Not Stuff
KSI3	EC13	2	Do Not Stuff
KSI4	EC14	2	Do Not Stuff
KSI5	EC15	2	Do Not Stuff
KSI6	EC16	2	Do Not Stuff
KSI7	EC17	2	Do Not Stuff
KSO0	EC18	2	Do Not Stuff
KSO1	EC19	2	Do Not Stuff
KSO2	EC20	2	Do Not Stuff
KSO3	EC21	2	Do Not Stuff
KSO4	EC22	2	Do Not Stuff
KSO5	EC23	2	Do Not Stuff
KSO6	EC24	2	Do Not Stuff
KSO7	EC25	2	Do Not Stuff
KSO8	EC26	2	Do Not Stuff
KSO9	EC27	2	Do Not Stuff
KSO10	EC28	2	Do Not Stuff
KSO11	EC29	2	Do Not Stuff
KSO12	EC30	2	Do Not Stuff
KSO13	EC31	2	Do Not Stuff
KSO14	EC32	2	Do Not Stuff
KSO15	EC33	2	Do Not Stuff

for POWER LED

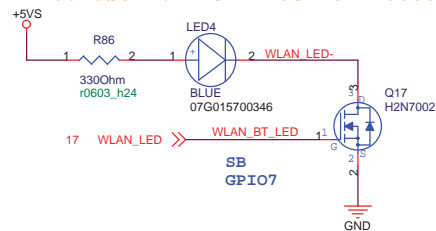


for FLASH LED

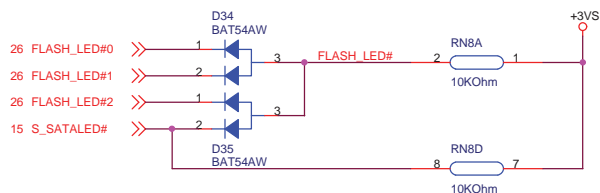
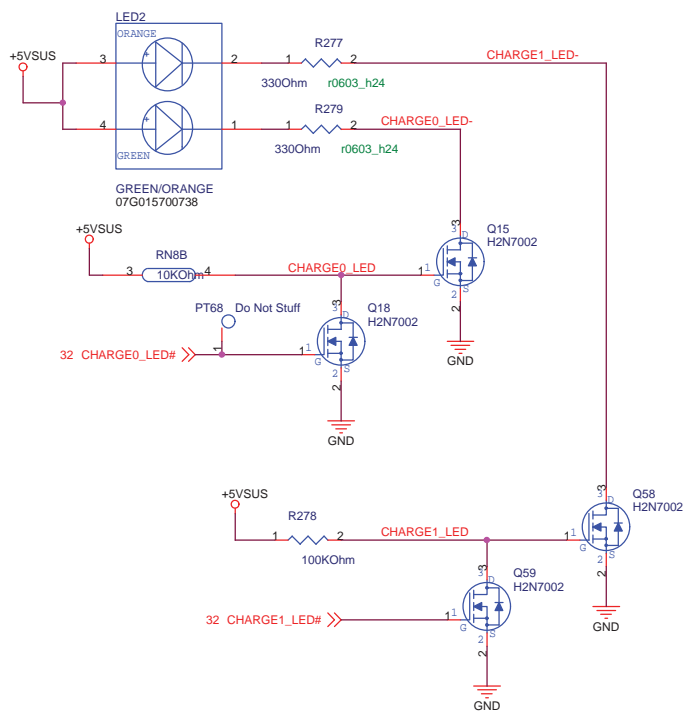


for WLAN/BlueTooth LED

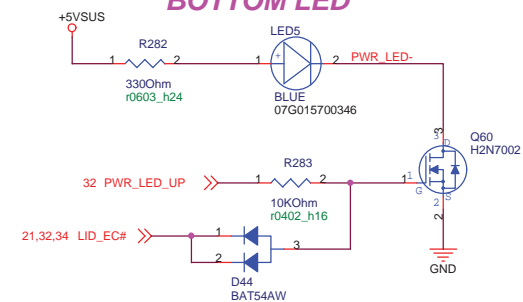
R86 use 4.7K OHm 10G213472003030

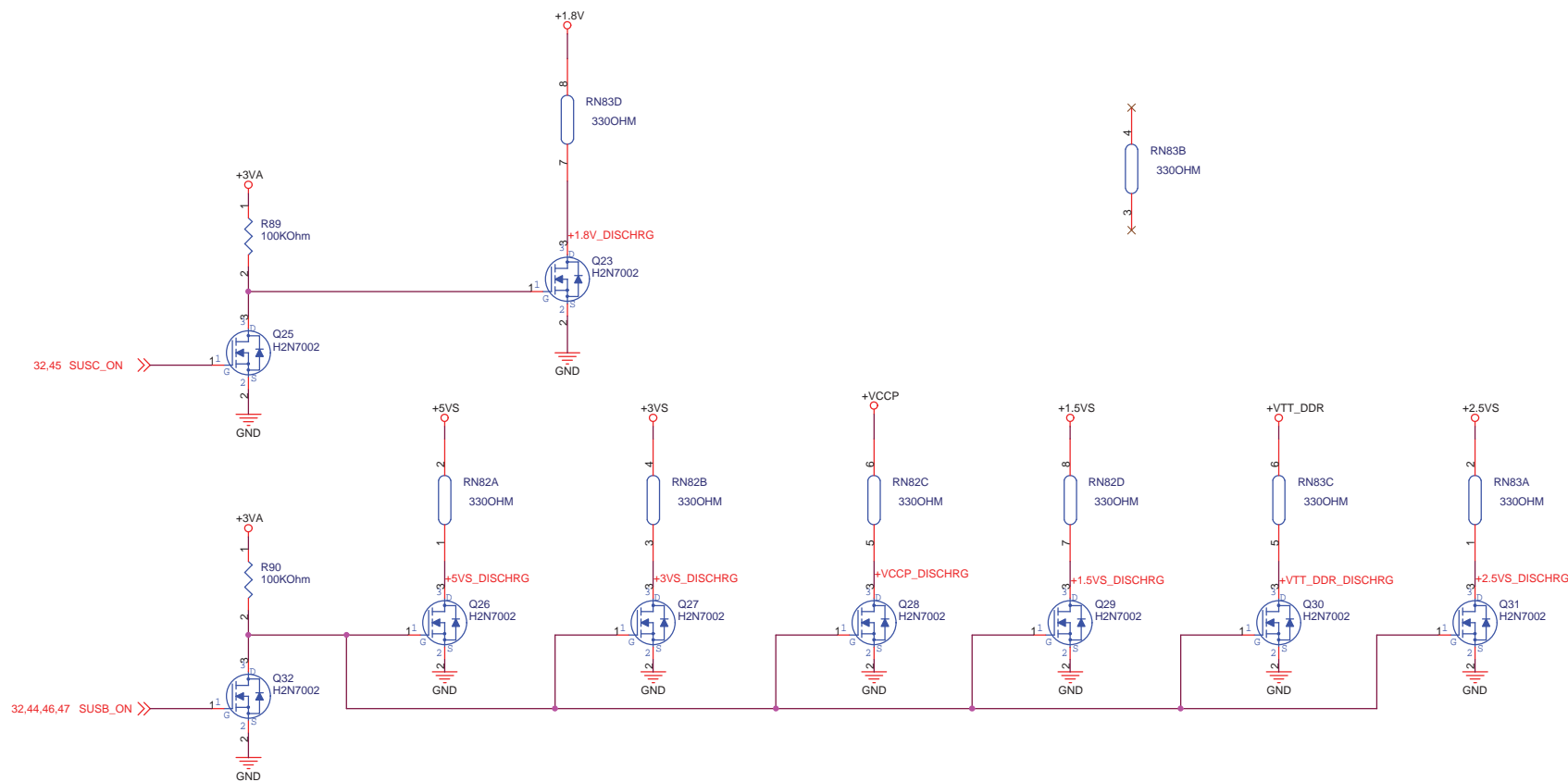


for CHARGE LED



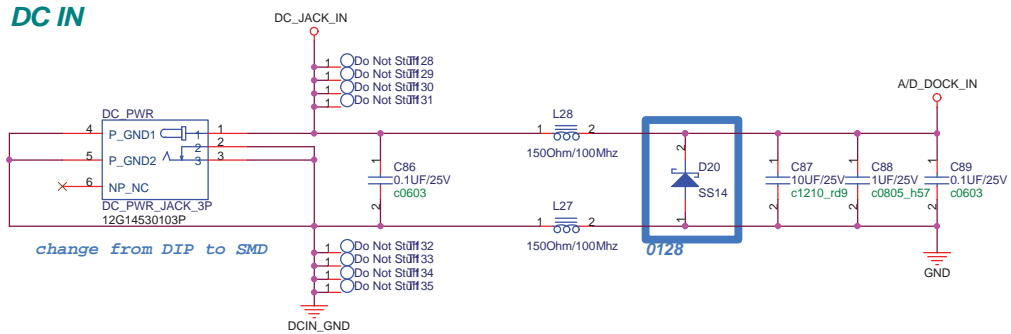
for POWER BOTTOM LED



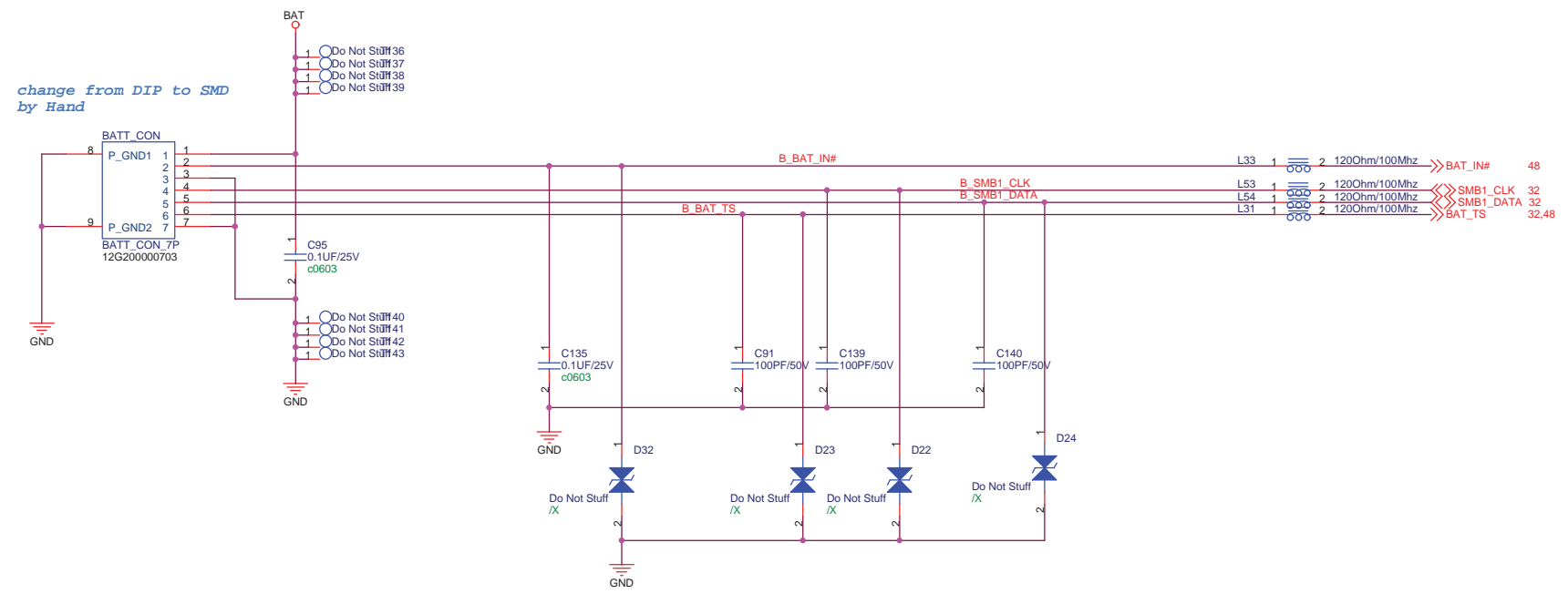


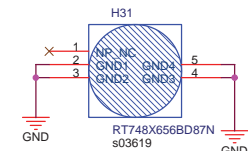
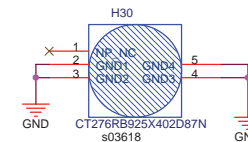
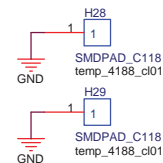
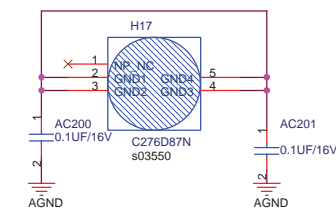
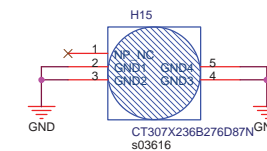
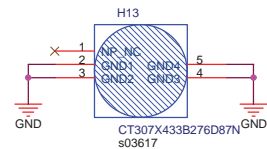
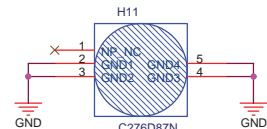
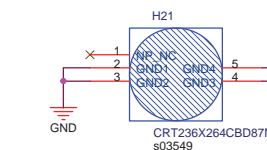
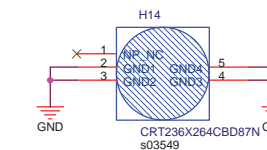
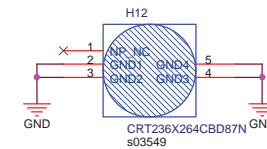
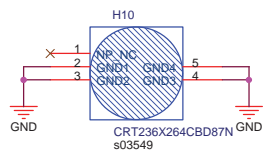
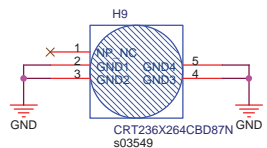
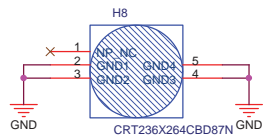
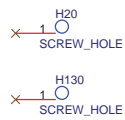
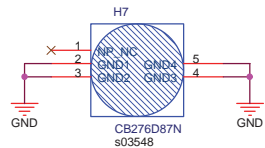
ASUS		Title : Discharge	
ASUSTek Computer INC.		Engineer: Kell_Huang	
Size	Project Name	Rev	
A3	P901	1.00G	
Date: Monday, March 31, 2008	Sheet 38 of 47		

DC IN

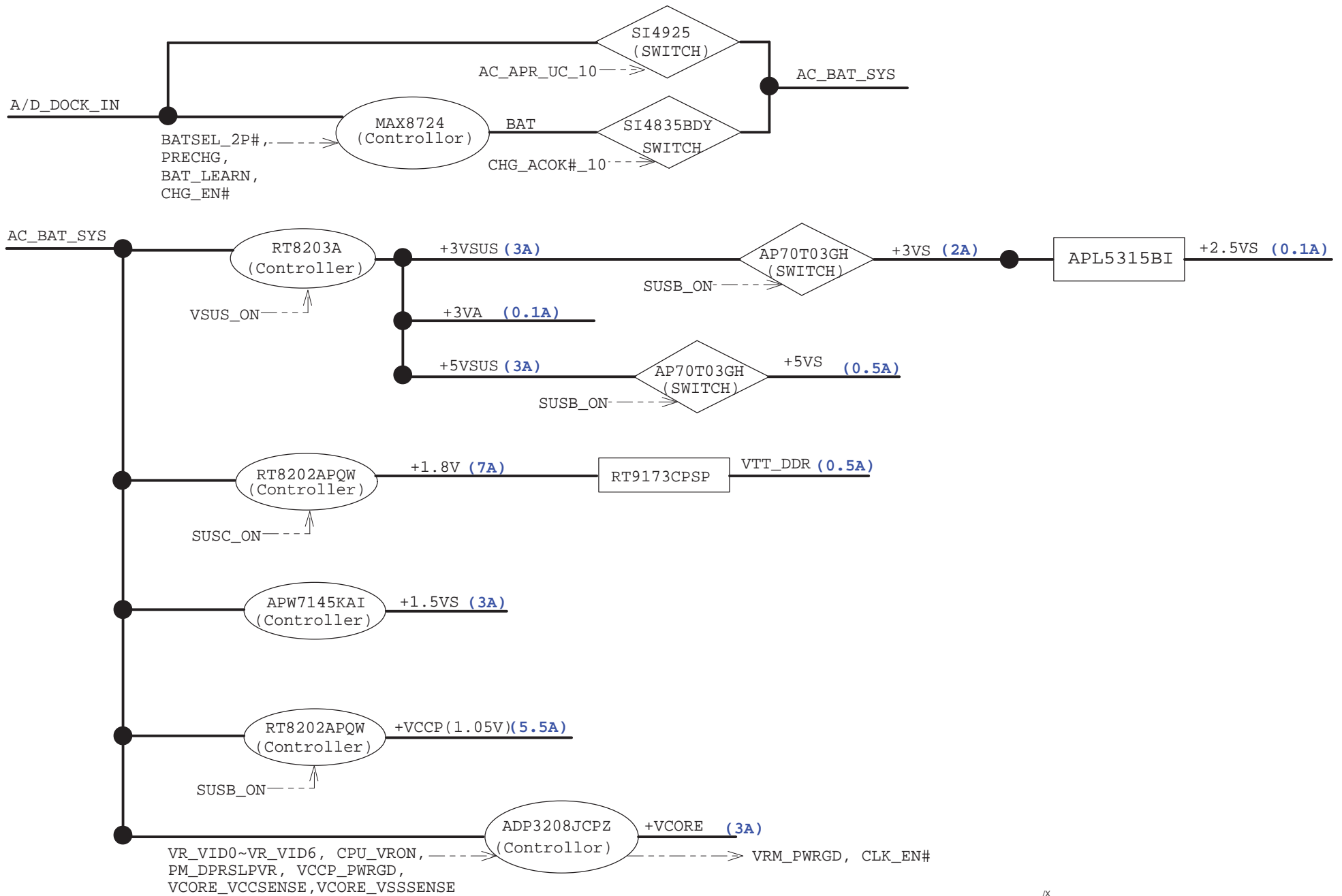


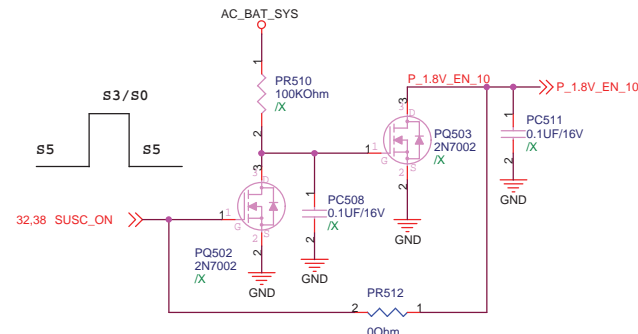
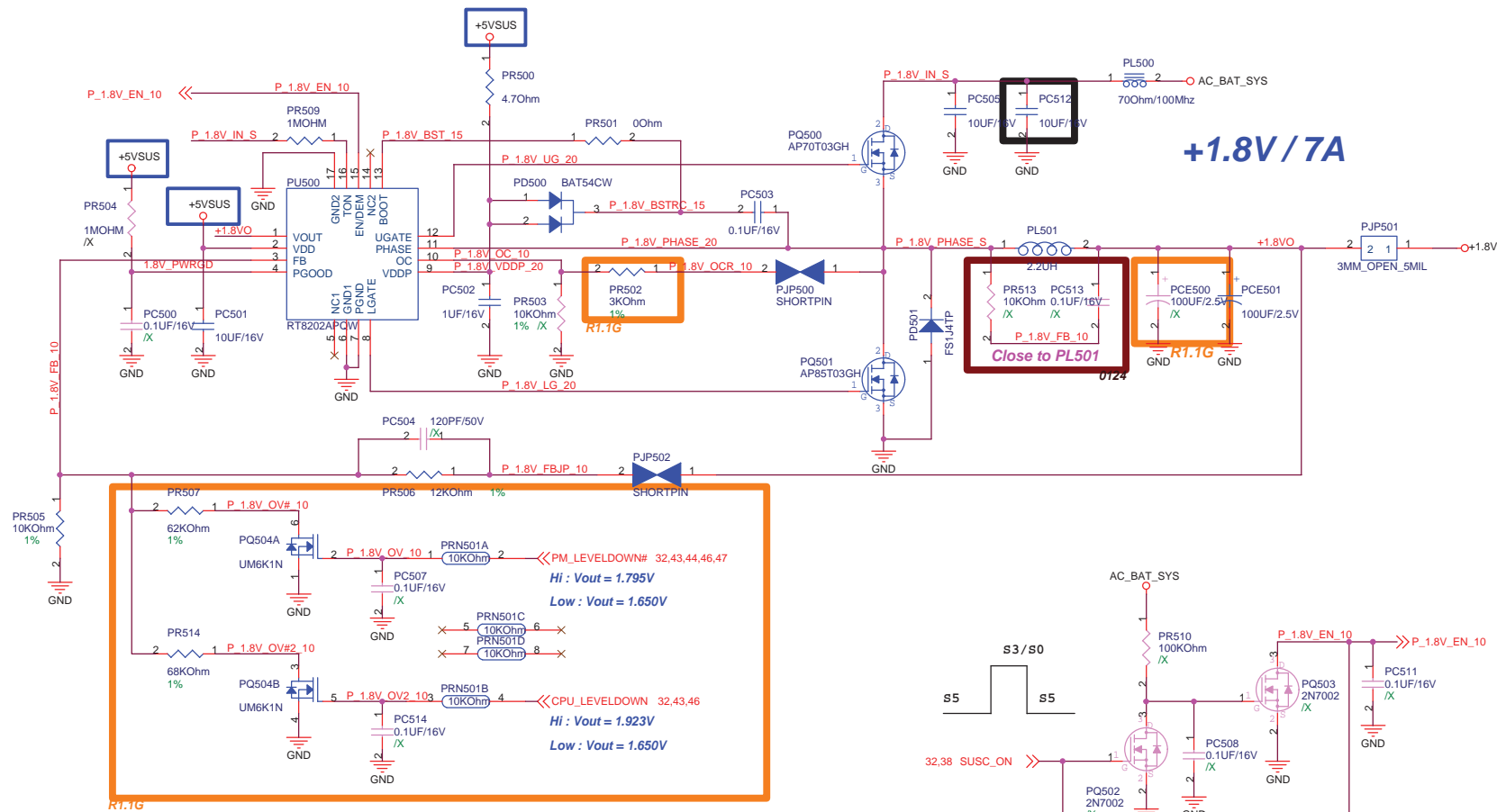
BAT IN



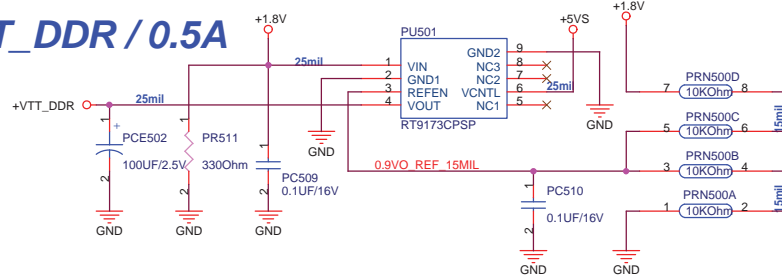


ASUS		Title : Srew Hole	
ASUSTek Computer INC.		Engineer: Kell_Huang	
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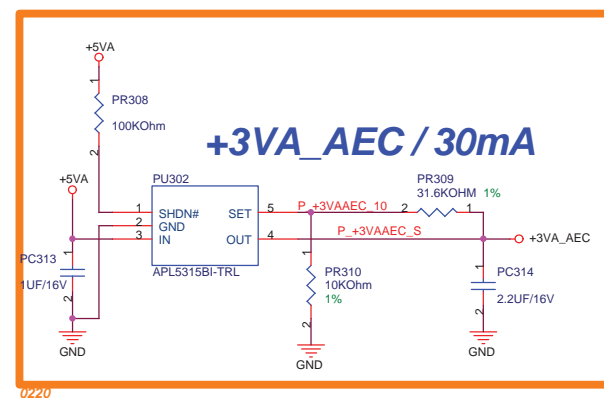
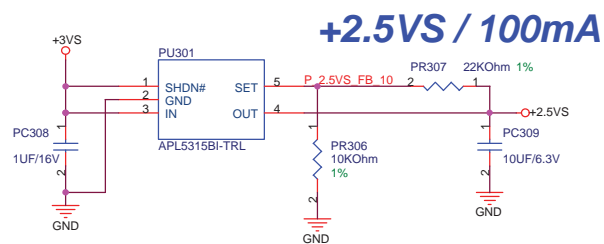
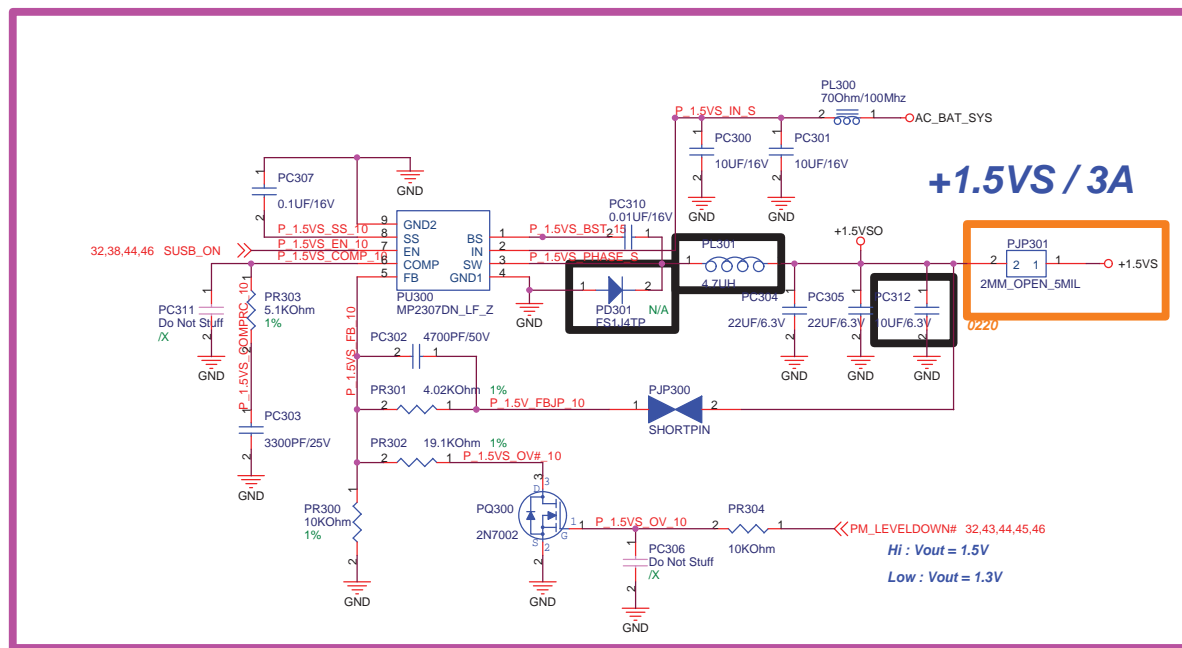




VTT_DDR / 0.5A



PM_LEVELDOWN#	CPU_LEVELDOWN	CPU_LEVELDOWN#	Voltage	Status
L	L	H	1.650V	Power Saving
H	L	H	1.795V	Normal
H	H	L	1.927V	Performance
L	H	L	1.782V	N/A



EC KB3310 GPIO SETTING

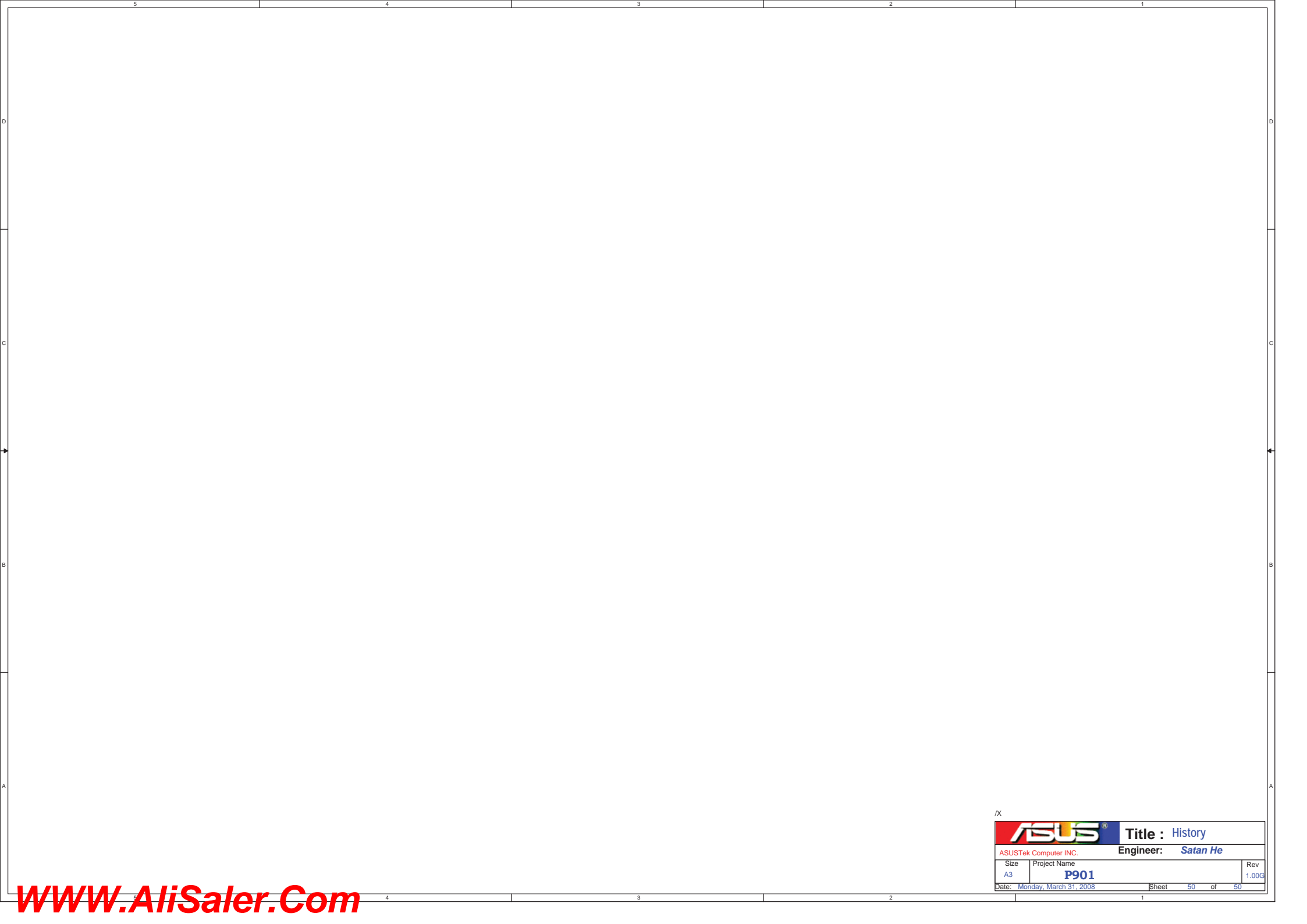
Pin	Pin Name	Signal Name	Type	Note
1	GPIO00/GA20	A20GATE	O	
2	GPIO01/KBRST#	RC_IN#	O	
6	GPIO04	EMAIL_SW#	I	Internal pull high
13	GPIO05/PCIRST#	PCI_RST#	I	
14	GPIO07	BAT_OTP	I	Battery over temperature
15	GPIO08	EXTSMH#	OD	10K pull high to +3VSB
16	GPIO0A	LID_EC#	I	Internal pull high
17	GPIO0B/ESB_CLK	NC	O	
18	GPIO0C/ESB_DAT	NC	O	
19	GPIO0D	DISTP_SW#	I	Internal pull high
20	GPIO0E/SC#	EXT_SC#	O	10K pull high to +3VSB
21	GPIO0F/PWM0	BL_PWM_DA	O	
23	GPIO10/PWM1	BAT_CRITICAL	I	Battery critical capacity
25	GPIO11/PWM2	PM_PWRBTN#	OD	Internal pull high in ICH
26	GPIO12/FANPWM1	FAN0_PWM	O	CPU Fan
27	GPIO13/FANPWM2	FAN1_PWM	O	VGA Fan
28	GPIO14/FANFB1	FAN0_TACH	I	CPU FanTach
29	GPIO15/FANFB2	FAN1_TACH	I	VGA FanTach
30	GPIO16/E51_TX	E51_TX	O	RS232 debug port
31	GPIO17/E51_RX	E51_RX	I	RS232 debug port
32	GPIO18	PWR_SW#	I	Internal pull high
34	GPIO19/PWM3	MAIL_LED#	O	
36	GPIO1A/NUMLED	NUM_LED#	O	
38	GPIO1D/CLKRUN#	NC	O	
39	GPIO20/KSO0/TP_TEST	KSO0	O	
40	GPIO21/KSO1/TP_PLL	KSO1	O	
41	GPIO22/KSO2	KSO2	O	
42	GPIO23/KSO3	KSO3	O	
43	GPIO24/KSO4	KSO4	O	
44	GPIO25/KSO5	KSO5	O	
45	GPIO26/KSO6	KSO6	O	
46	GPIO27/KSO7	KSO7	O	
47	GPIO28/KSO8	KSO8	O	
48	GPIO29/KSO9	KSO9	O	
49	GPIO2A/KSO10	KSO10	O	
50	GPIO2B/KSO11	KSO11	O	
51	GPIO2C/KSO12	KSO12	O	
52	GPIO2D/KSO13	KSO13	O	
53	GPIO2E/KSO14	KSO14	O	
54	GPIO2F/KSO15	KSO15	O	
55	GPIO30/KSI0	KSI0	I	Internal pull high
56	GPIO31/KSI1	KSI1	I	Internal pull high
57	GPIO32/KSI2	KSI2	I	Internal pull high
58	GPIO33/KSI3	KSI3	I	Internal pull high
59	GPIO34/KSI4	KSI4	I	Internal pull high
60	GPIO35/KSI5	KSI5	I	Internal pull high
61	GPIO36/KSI6	KSI6	I	Internal pull high
62	GPIO37/KSI7	KSI7	I	Internal pull high
63	GPI38/AD0	BAT_ICHG	I	
64	GPI39/AD1	BAT_CONFIG	I	Battery configuration
65	GPIO3A/AD2	BAT_SENSE	I	Battery Voltage Sensor
66	GPIO3B/AD3	BAT_TS	I	Battery Thermal Sensor
68	GPO3C/DA0	DOC	O	Trigger Clock Gen

EC KB3310 Other Pin SETTING

Pin	Pin Name	Signal Name	Type	Note
3	SERIRQ	INT_SERIRQ	I/O	10K pull high to +3V
4	LFRAME#	LPC_FRAME#	I	
5	LAD3	LPC_AD3	I/O	
7	LAD2	LPC_AD2	I/O	
8	LAD1	LPC_AD1	I/O	
9	VCC	+3VA_EC	P	
10	LAD0	LPC_AD0	I/O	
11	GND	GND	P	
12	PCICLK	CLK_PCL_EC	I	
22	VCC	+3VA_EC	P	
24	GND	GND	P	
33	VCC	+3VA_EC	P	
35	GND	GND	P	
37	ECRST#	EC_RST#	I	100K pull high to +3VA_EC
67	AVCC	+3VACC	P	
69	AGND	AGND	P	
94	GND	GND	P	
96	VCC	+3VA_EC	P	
111	VCC	+3VA_EC	P	
113	GND	GND	P	
119	RD#/SPIDI	SPI_SO	I	
120	WR#/SPIDO	SPI_SI	O	
112	XCLKI	32KXCLKI	I	
123	XCLKO	32KXCLKO	O	
124	V18R	V18R	P	Reserved 1uF to GND
125	VCC	+3VA_EC	P	
128	SPICS#/SELMEM#	SPI_CE#	O	

Pin	Pin Name	Signal Name	Type	Note
70	GPO3D/DA1	LCD_BACKOFF#	O	
71	GPO3E/DA2	CLK_PWRSERVE#	O	
72	GPO3F/DA3	BAT_LL#	O	Battery Low Low
73	GPIO40	AC_OK	I	AC Adaptor Plug in
74	GPIO41	PM_RSMRST#	O	10K pull down to GND
75	GPI42	BAT_IN	I	
76	GPI43	CLRTC_EC	I	
77	GPIO44/SCL1	SMB0_CLK	I/O	4.7K pull high to +3VA_EC
78	GPIO45/SDA1	SMB0_DAT	I/O	4.7K pull high to +3VA_EC
79	GPIO46/SCL2	SMB1_CLK	I/O	10K pull high to +3V
80	GPIO47/SDA2	SMB1_DAT	I/O	10K pull high to +3V
81	GPIO48/KSO16	KB pin 28	I	for KB type detection
82	GPIO49/KSO17	KB pin 27	I	for KB type detection
83	GPIO4A/PSCLK1	AUO_SCL	O	for AUO, default H at S0
84	GPIO4B/PSDAT1	AUO_SDA	O	for AUO, default L at S0
85	GPIO4C/PSCLK2	AUO_CSB	O	for AUO, default H at S0
86	GPIO4D/PSDAT2	LVDD_EN	I	for AUO 7" Panel
87	GPIO4E/PSCLK3	TP_CLK	I/O	10K pull high to +3V
88	GPIO4F/PSDAT3	TP_DAT	I/O	10K pull high to +3V
89	GPIO50/SELIO#	BATSEL_3S	O	Battery series, H:3S, L:4S
90	GPIO52/E51_CS#	CHG_LED_UP#	O	
91	GPIO53/CAPLED	CAP_LED#	O	
92	GPIO54	PWR_LED_UP	O	
93	GPIO55/SCRLED	SCRLED	O	
95	GPIO56	PWR4G_SW#	I	Internal pull high
97	GPXOA00/SDICS#	SPI_MODE#	O	4.7K pull down to GND
98	GPXOA01/SDICLK	SUSC_ON	O	
99	GPXOA02/SDIDO	VSUS_ON	O	
100	GPXOA03	CPU_VRON	O	
101	GPXOA04	SUSB_ON	O	
102	GPXOA05	ICH_PWROK	O	
103	GPXOA06	VOLT_CTRL	O	
104	GPXOA07	CHG_EN#	O	Battery charging enabled
105	GPXOA08	PRECHG	O	
106	GPXOA09	SPI_WP#	O	
107	GPXOA10	OP_SD#	O	Audio OP
108	GPXOA11	BAT_LEARN	O	
109	GPXID0/SDIDI	BATSEL_2P#	O	Battery parallel, H:1P, L:2P~3P
110	GPXID1	NC	O	
112	GPXID2	THRO_CPU	O	Active if CPU temperature over spec
114	GPXID3	SUSB#	I	100K pull down to GND
115	GPXID4	SUSC#	I	100K pull down to GND
116	GPXID5	CPUPWR_GD	I	Pull high to +3V
117	GPXID6	VSUS_GD	I	
118	GPXID7	NC	O	
121	GPIO57	INTERNET#	I	Internal pull high
126	GPIO57/SPICLK	SPI_CLK	O	
127	GPIO59/TEST_CLK	NC	O	

		Title : EC Pin Define	
ASUSTek Computer INC.		Engineer: Satan He	
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ASUS

Title : History

ASUSTek Computer INC.

Engineer: Satan He

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