

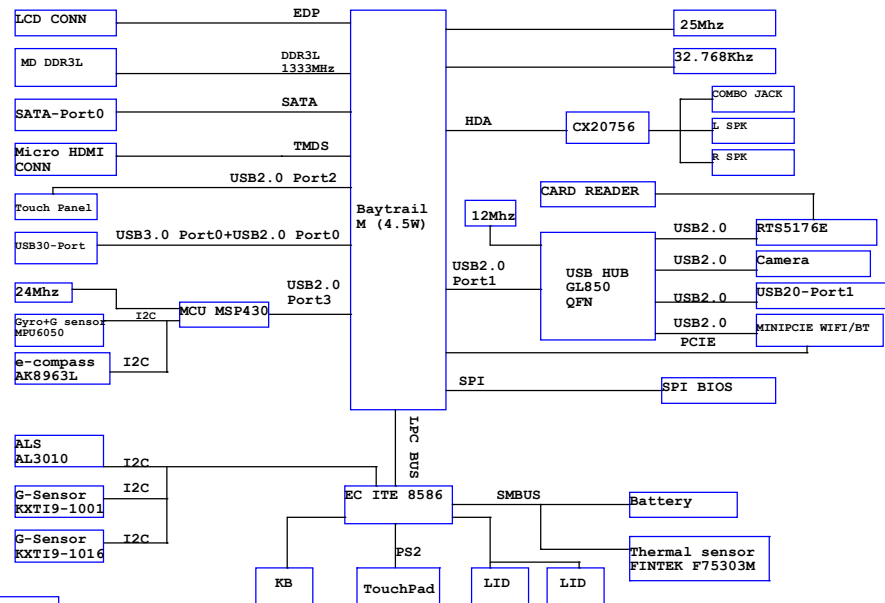
LCFC Confidential

VIENNA M/B Schematics Document INTEL Bay Trail-M Platform

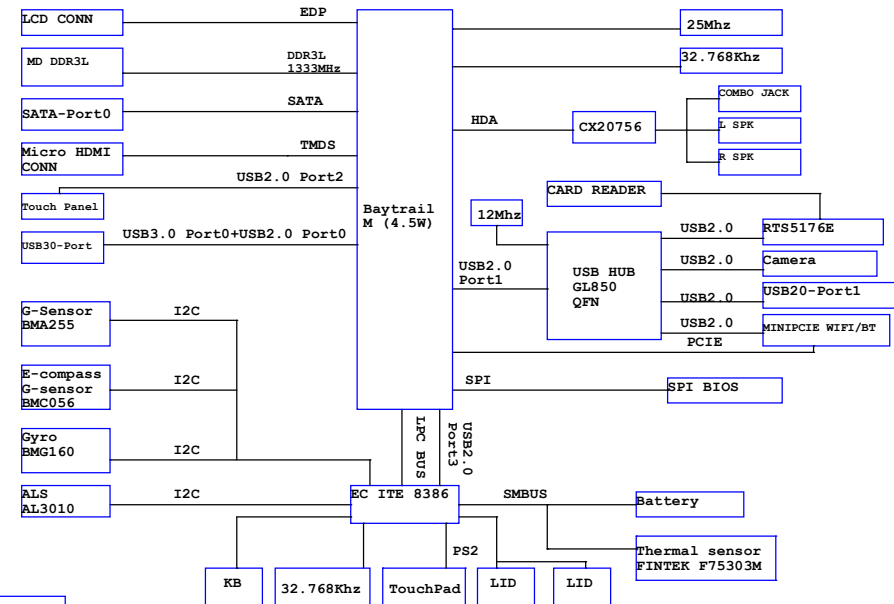
2013-10-22

REV:1.0

VIENNA M Block diagram EC 8586



VIENNA M Block diagram EC 8386



Security Classification	LC Future Center Secret Data	Title	Block Diagram
Version	20130205	Doc phase Date	20130201
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Author	VIENNA M	Page	10

Power Plane State	B+	+3VALW +3VL	+3VALW_SOC +1.0VALW +1.8VALW	+1.35V	+5VS +3VS +1.5VS +1.05VS +0.68VS CPU_CORE GFX_CORE
S0	O	O	O	O	O
S3	O	O	O	O	X
S5 S4/AC Only	O	O	O	X	X
S5 S4 Battery only	O	X	X	X	X
S5 S4 AC & Battery don't exist	X	X	X	X	X

STATE \ SIGNAL	SLP_S1#	SLP_S3#	SLP_S4#	+VALW	+VALW_PCH	+V	+VS	Clock
Full ON	HIGH	HIGH	HIGH	ON	ON	ON	ON	ON
S1 (Power On Suspend)	LOW	HIGH	HIGH	ON	ON	ON	ON	LOW
S3 (Suspend to RAM)	LOW	LOW	HIGH	ON	ON	ON	OFF	OFF
S4 (Suspend to Disk)	LOW	LOW	LOW	ON	ON	OFF	OFF	OFF
S5 (Soft OFF)	LOW	LOW	LOW	ON	ON	OFF	OFF	OFF

[illegible]





	Device	address
EC1	Battery	0001 011X b
	Charger	
	ALS	
	G-sensor	
EC2	Thermal Sensor	1001_101xb
EC3	G-sensor1	
	G-sensor2	
	E-compass	
	GYRO	
SOC	TP	


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Port	Device
0	WLAN
1	X
2	X
3	X

	USB20		USB30	
PORT / TYPE	0	Left USB	1	Left USB
	1	USB HUB		
	2	TOUCH PANEL		
	3	MCU		
USB / HW	1	Right USB		
	2	CAMERA		
	3	CARD READER		
	4	WLAN		

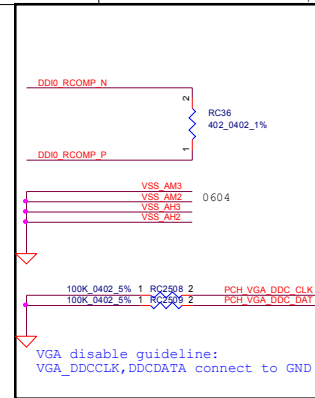
[illegible][illegible][illegible]

	CPU	PCB	LOGO
NPI DA8xxxxxx    	ZZ5 N29 10@ ZZ4 N35 10@ ZZ6 N28 10@	ZZ N48@	
MP DAZxxxxxx			

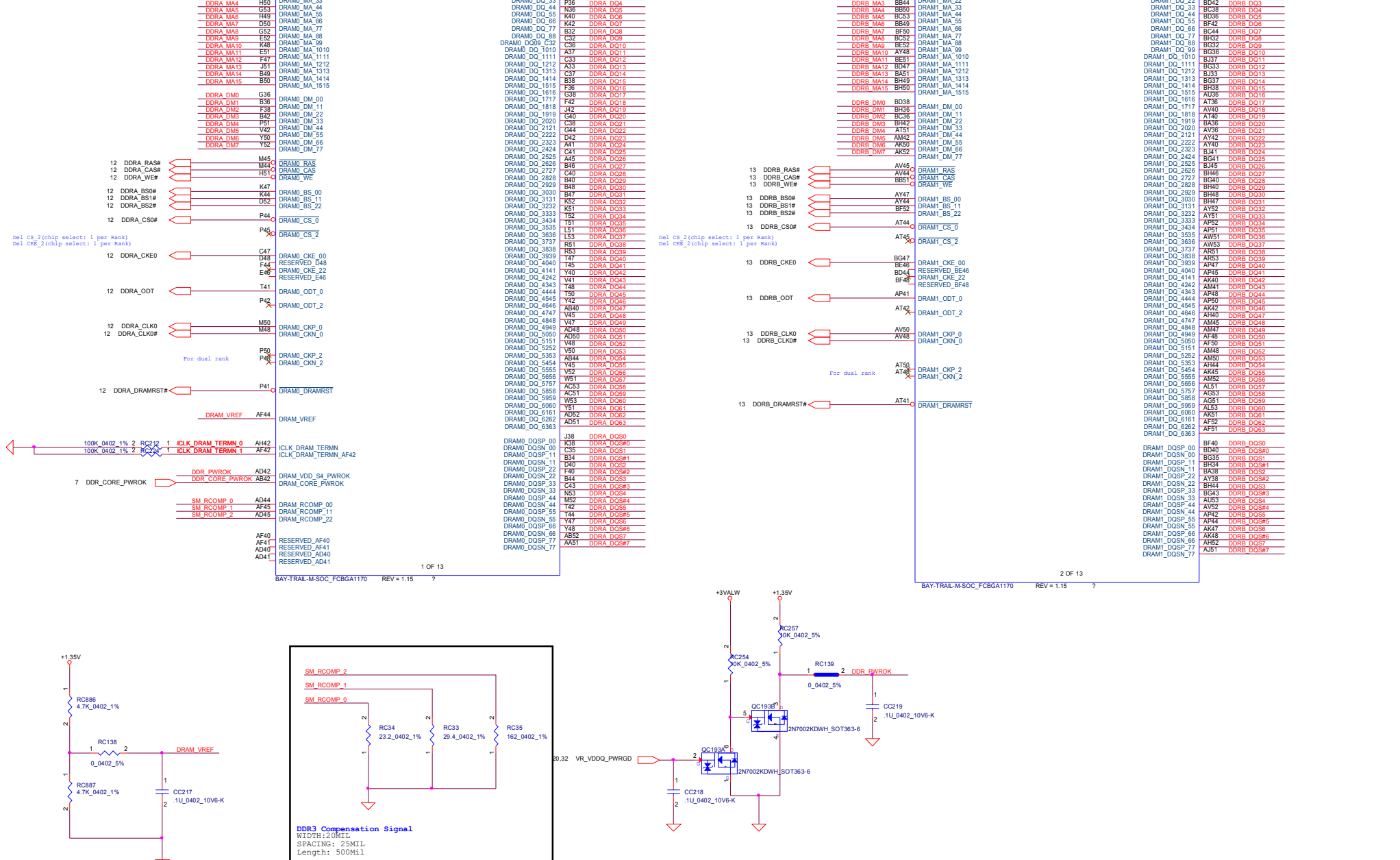
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Issued Date	2013/03/26	Deciphered Date	2013/02/01	Notes List	
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Date: Friday, November 16, 2013				Sheet 3 of 36	Rev 1

HDMI

EDP



Port	DDI PROCESSOR Pin Names	
Port 0	DDIO_TXP_0	HDMI* Mapping
	DDIO_TXN_0	PCH_HDMI_TX2+
	DDIO_TXP_1	PCH_HDMI_TX2-
	DDIO_TXN_1	PCH_HDMI_TX1+
	DDIO_TXP_2	PCH_HDMI_TX1-
	DDIO_TXN_2	PCH_HDMI_TX0+
	DDIO_TXP_3	PCH_HDMI_TX0-
	DDIO_TXP_3	PCH_HDMI_CLK+
	DDIO_TXP_3	PCH_HDMI_CLK-
	DDIO_HPD	PCH_HDMI_HPD
	DDIO_DDCDATA	PCH_HDMI_DDC_DAT
	DDIO_DDCCLK	PCH_HDMI_DDC_CLK
Port 1	DDI1_TXP_0	EDP* Mapping
	DDI1_TXN_0	PCH_EDP_TX0+
	DDI1_TXP_1	PCH_EDP_TX0-
	DDI1_TXN_1	PCH_EDP_TX1+
	DDI1_AUXP	PCH_EDP_TX1-
	DDI1_AUXN	PCH_EDP_AUX+
	DDI1_HPD	PCH_EDP_AUX-
	DDI1_HPD	



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	VIENNA M				1.0
Date:	Friday, November 15, 2013				Sheet
	5				of 35

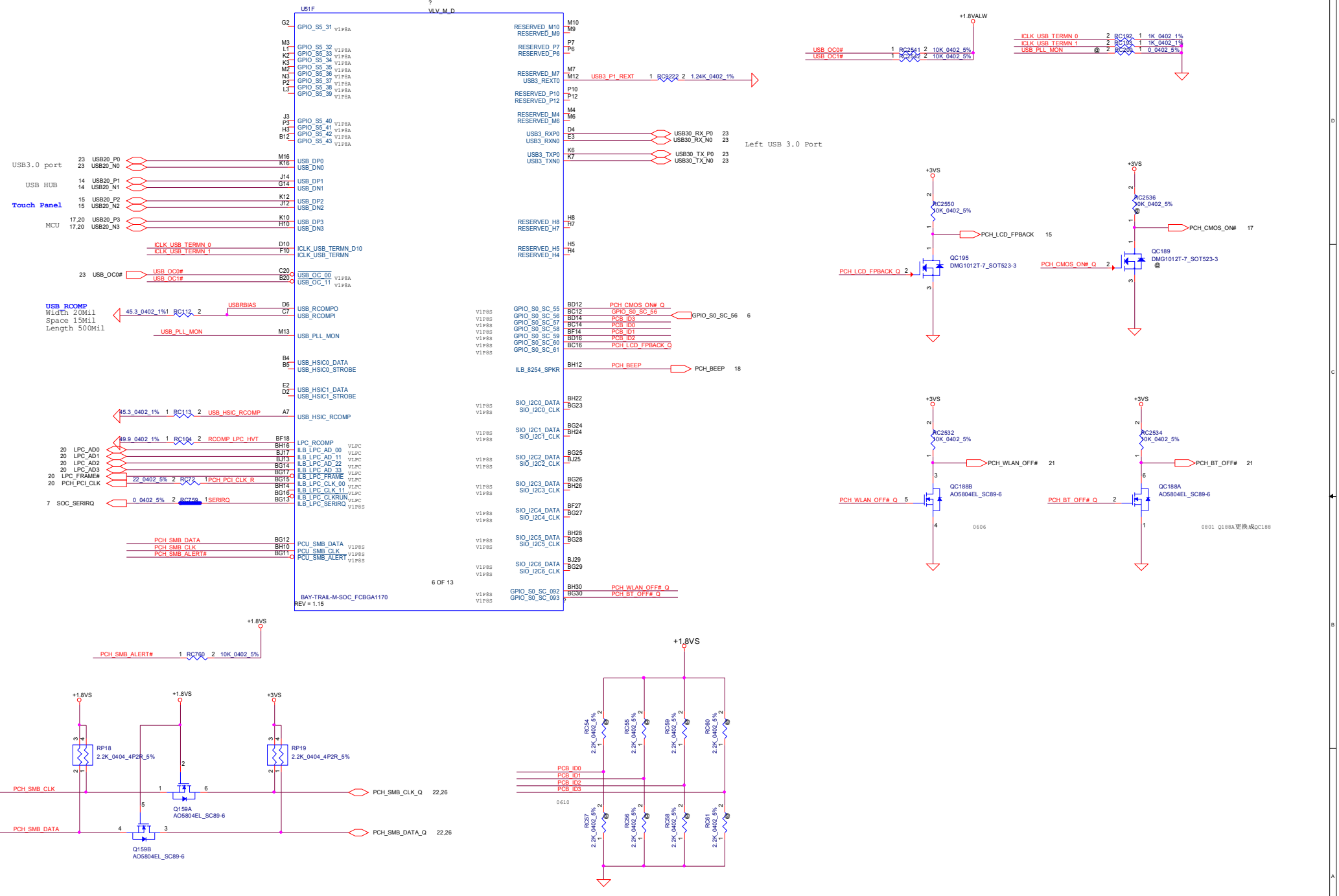


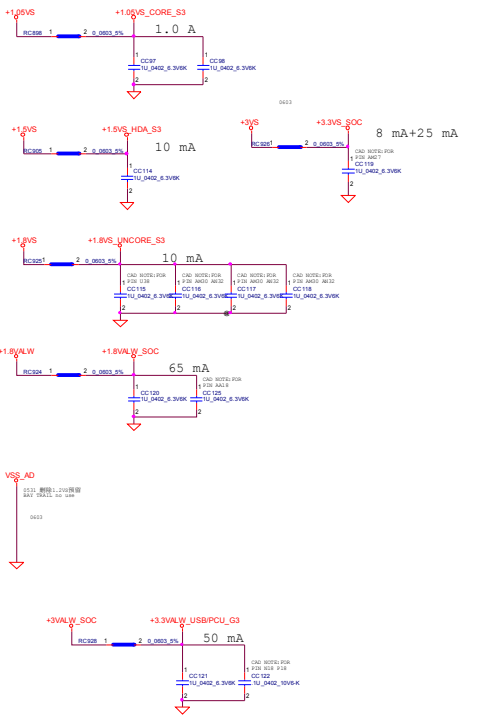
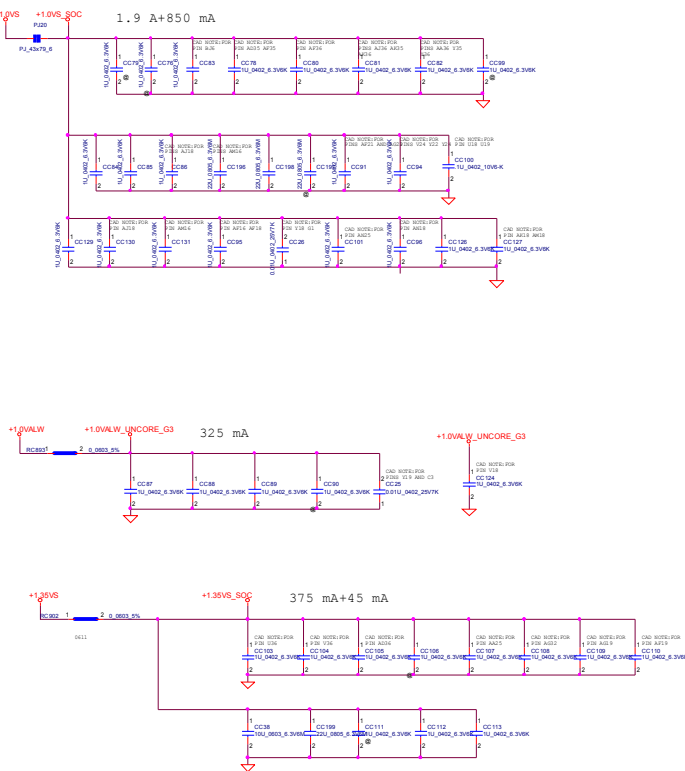
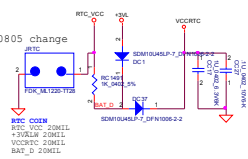
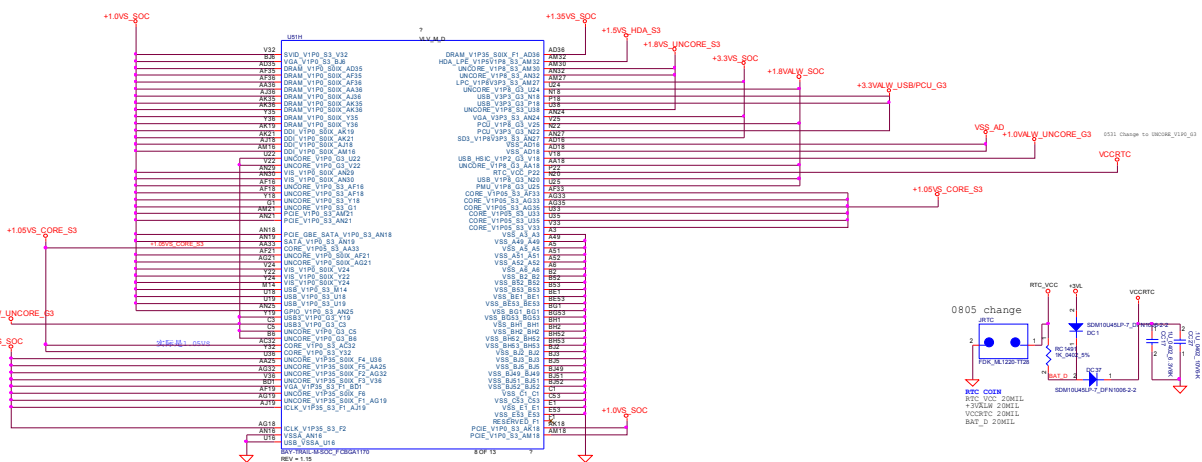
PCB ID table					
PCB ID3	PCB ID2	PCB ID1	PCB ID0	DRAM	
0	0	0	0	2G	8G HYUNIX
0	0	0	1	2G	8G MICRON
0	0	1	0	2G	8G SAMSUNG
0	0	1	1	2G	8G HYUNIX
0	1	0	0	4G	8G HYUNIX
0	1	0	1	4G	8G MICRON
0	1	1	0	4G	8G SAMSUNG
0	1	1	1	4G	8G HYUNIX

Security Classification LC Future Center Secret Data

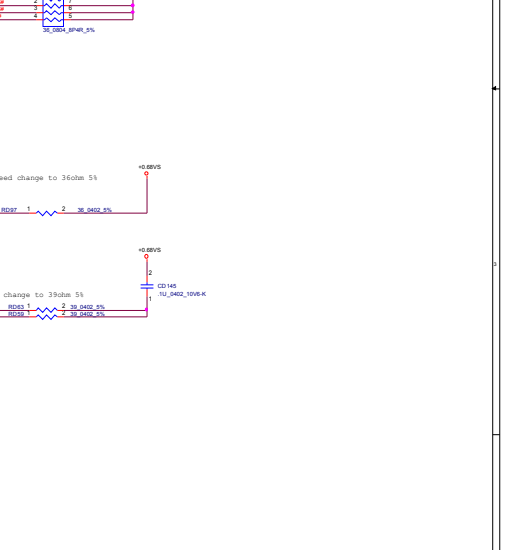
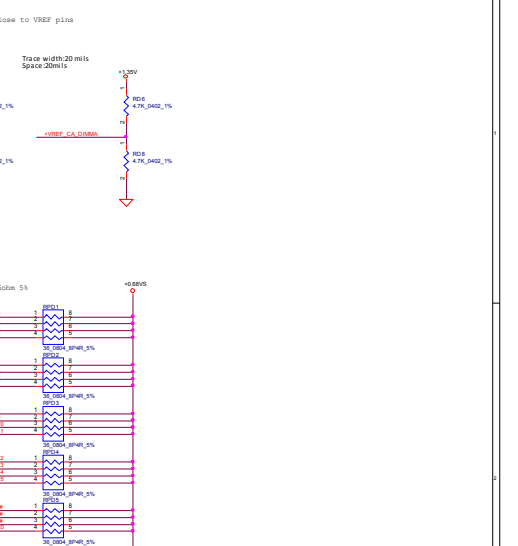
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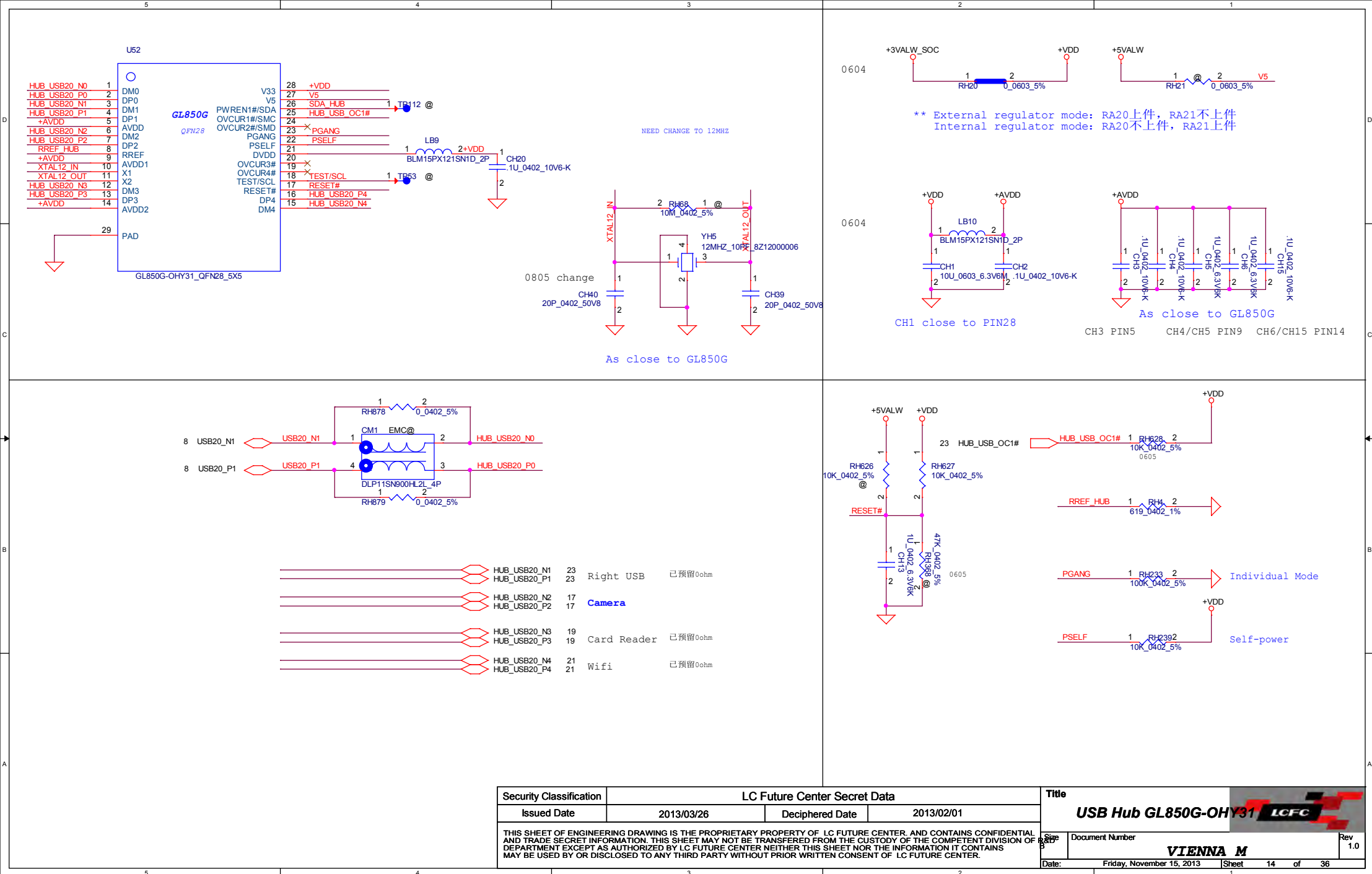
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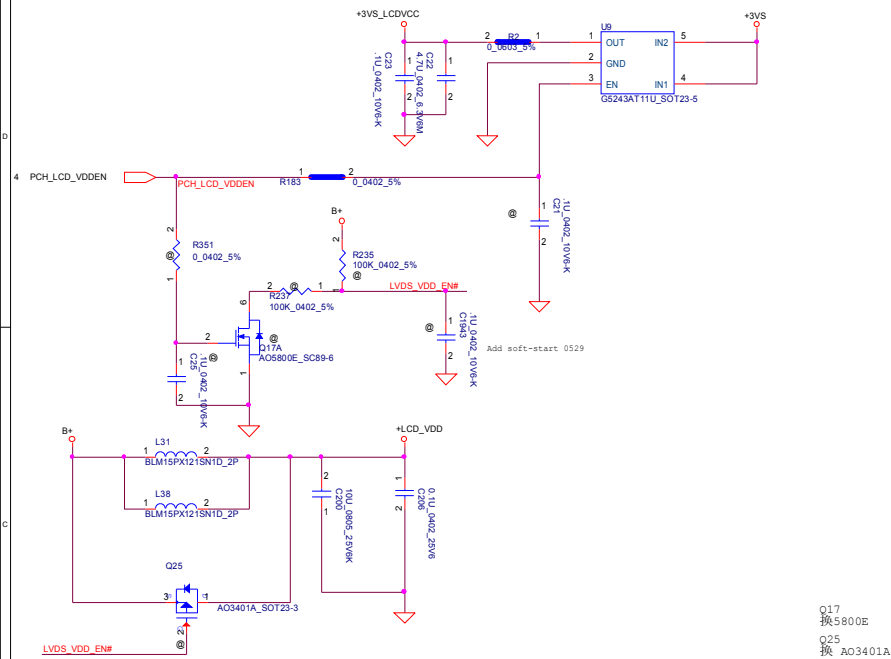


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				Rev	1.0
				Rev	1.0

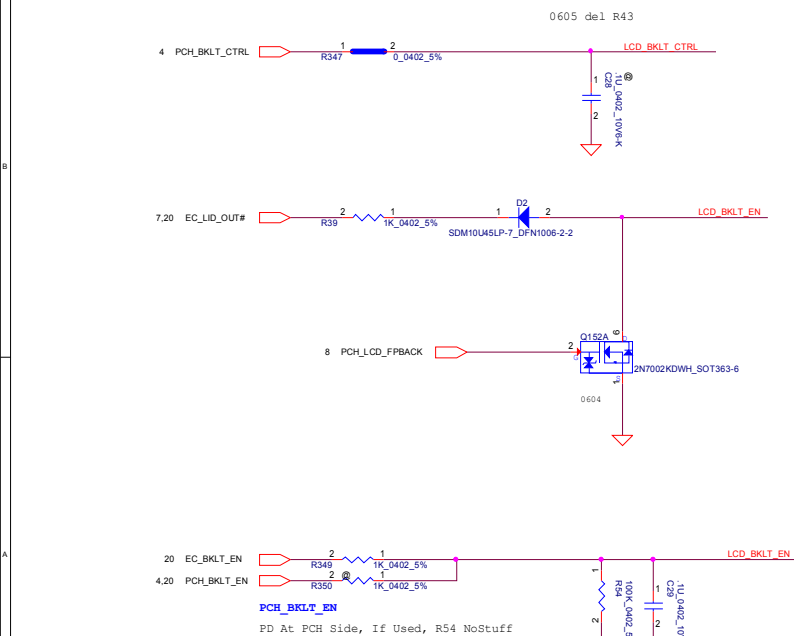




LCD PWR

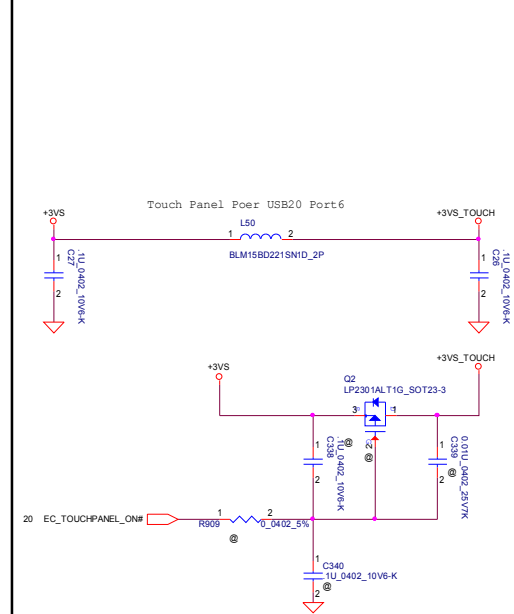


BKLT CNTL



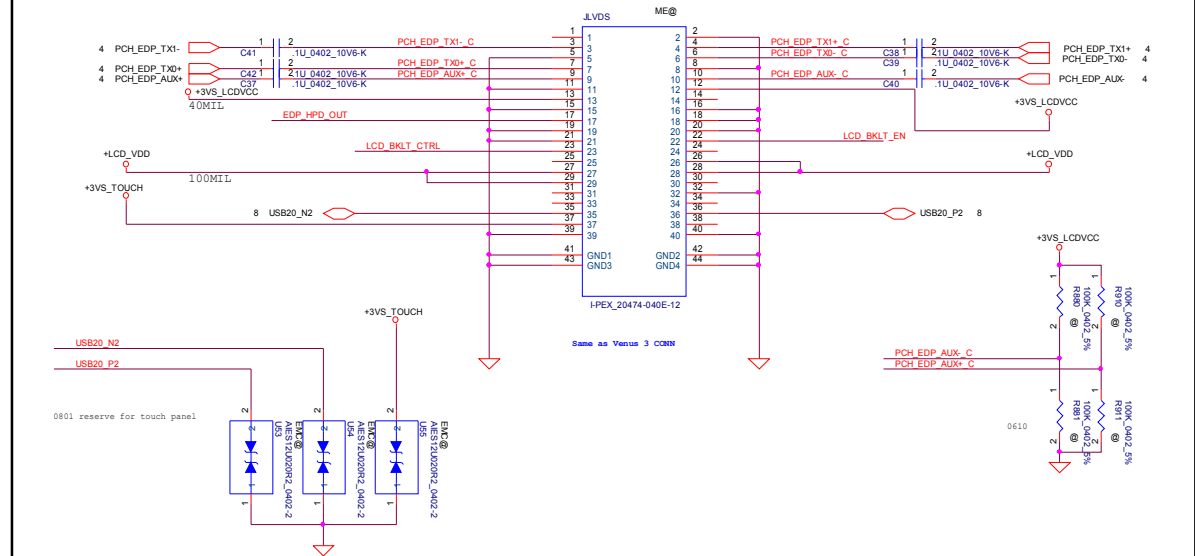
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TOUCH PANEL



CONN

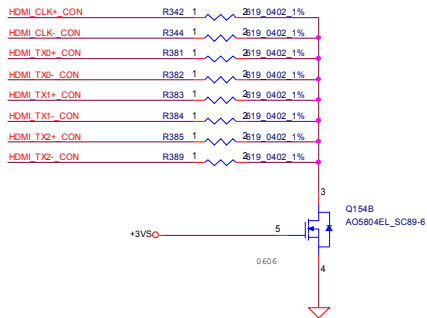
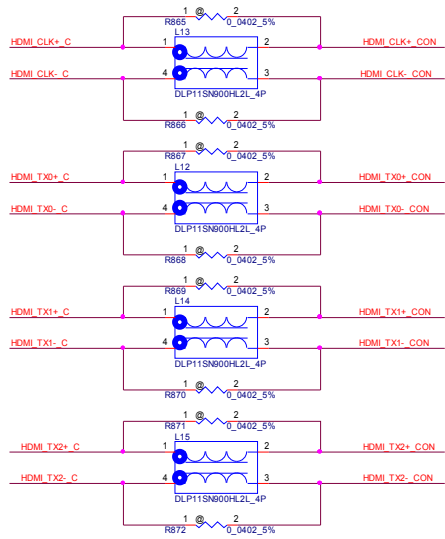
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LCVDD
Width 100mil



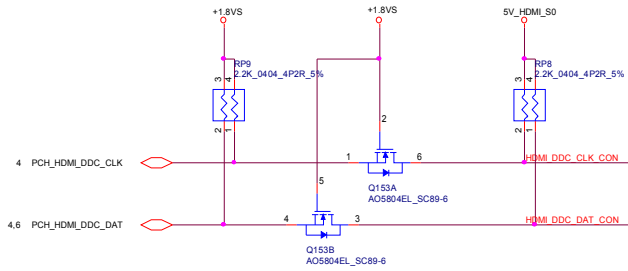
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			Date:	Friday, November 15, 2013	Sheet 15 of 35

TMDS

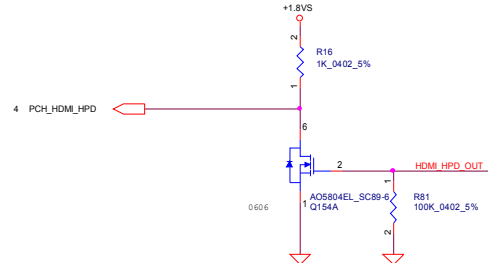
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4	PCH_HDMI_TX0-	CV256 1	2	0.1u_0201_10V6K	HDMI_TX0+_C
4	PCH_HDMI_TX1+	CV257 1	2	0.1u_0201_10V6K	HDMI_TX1+_C
4	PCH_HDMI_TX1-	CV258 1	2	0.1u_0201_10V6K	HDMI_TX1+_C
4	PCH_HDMI_TX2+	CV259 1	2	0.1u_0201_10V6K	HDMI_TX2+_C
4	PCH_HDMI_TX2-	CV260 1	2	0.1u_0201_10V6K	HDMI_TX2+_C
4	PCH_HDMI_CLK+	CV262 1	2	0.1u_0201_10V6K	HDMI_CLK+_C
4	PCH_HDMI_CLK-	CV263 1	2	0.1u_0201_10V6K	HDMI_CLK+_C



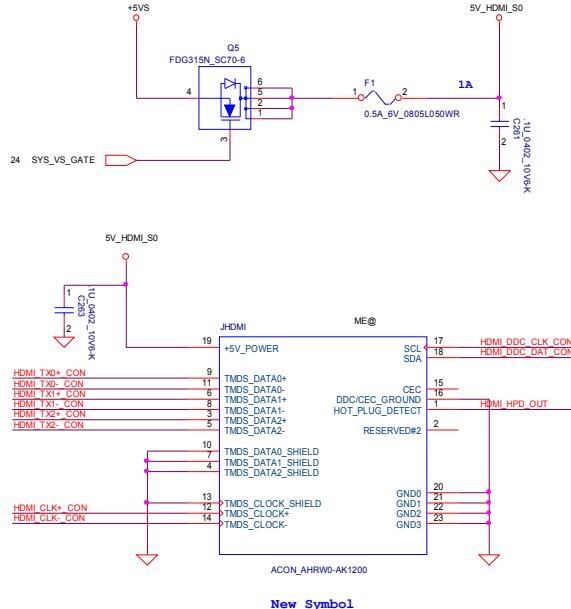
DDC



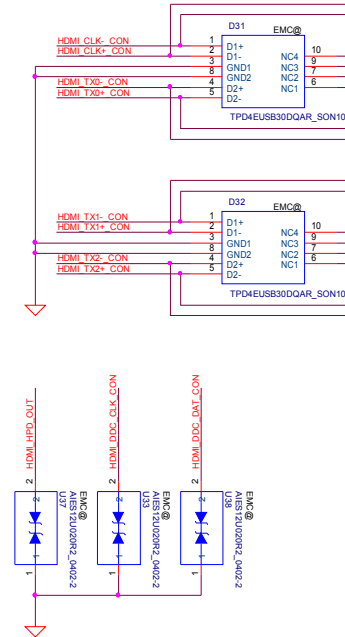
H-PLUG



CONN

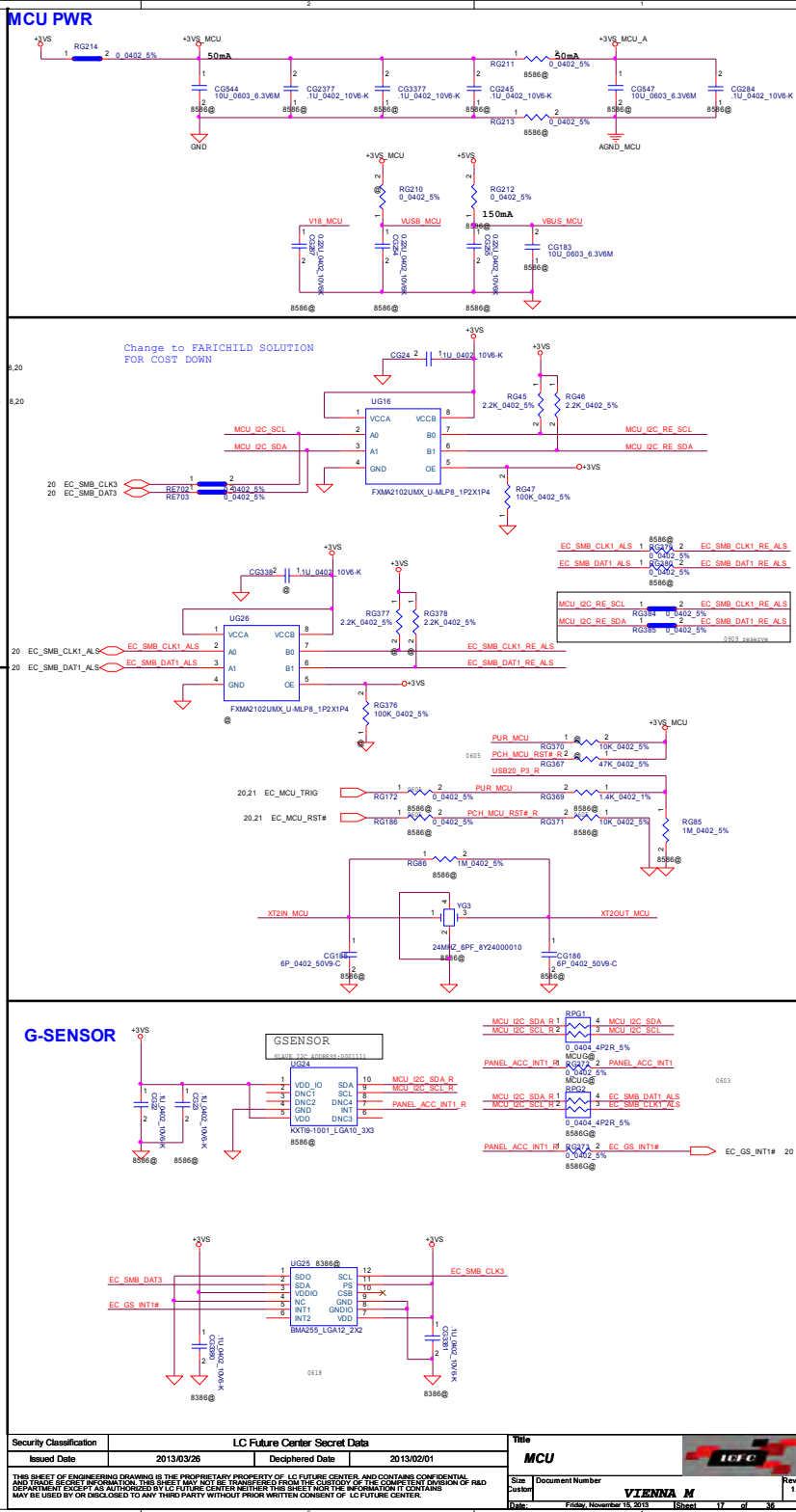
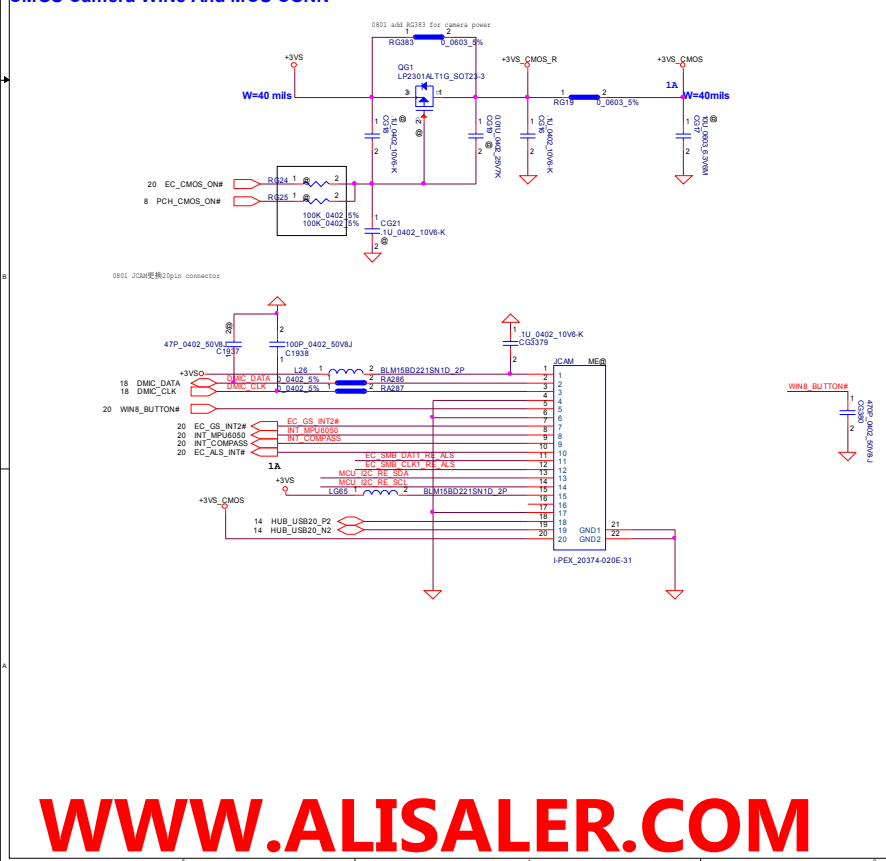
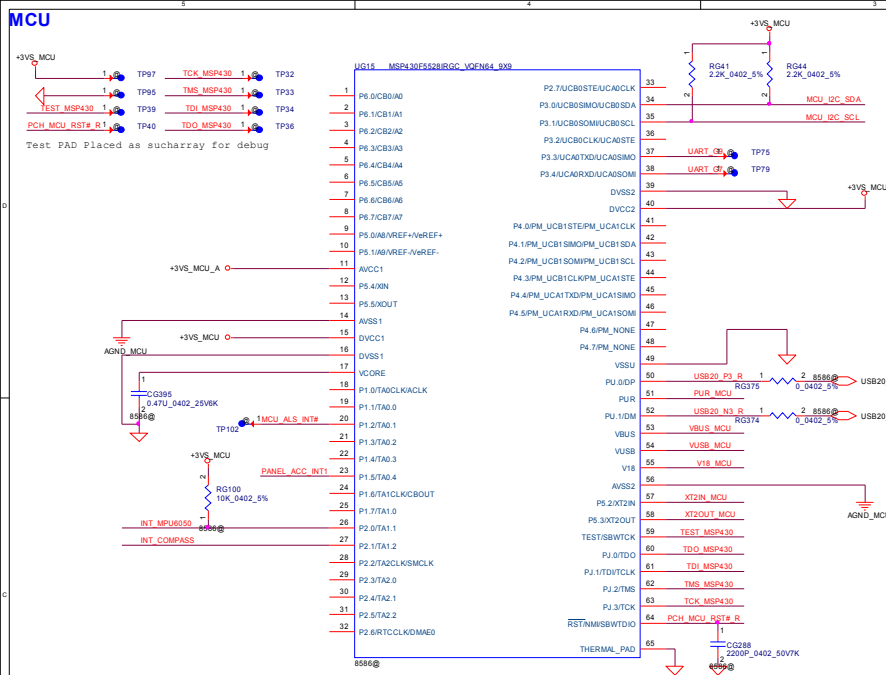


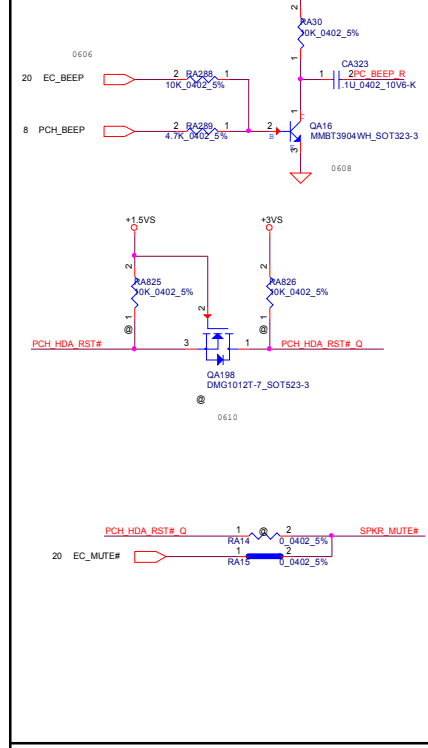
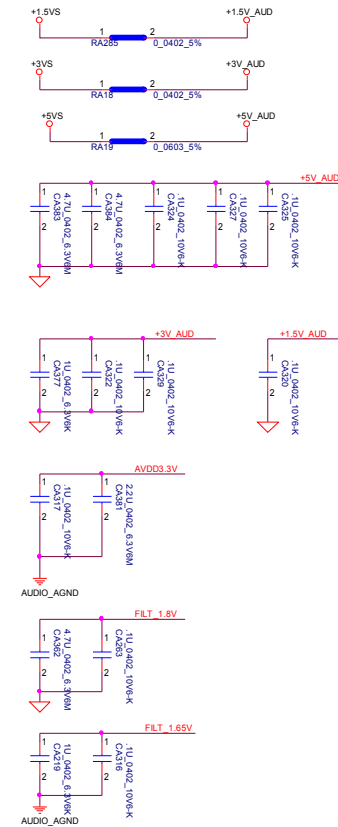
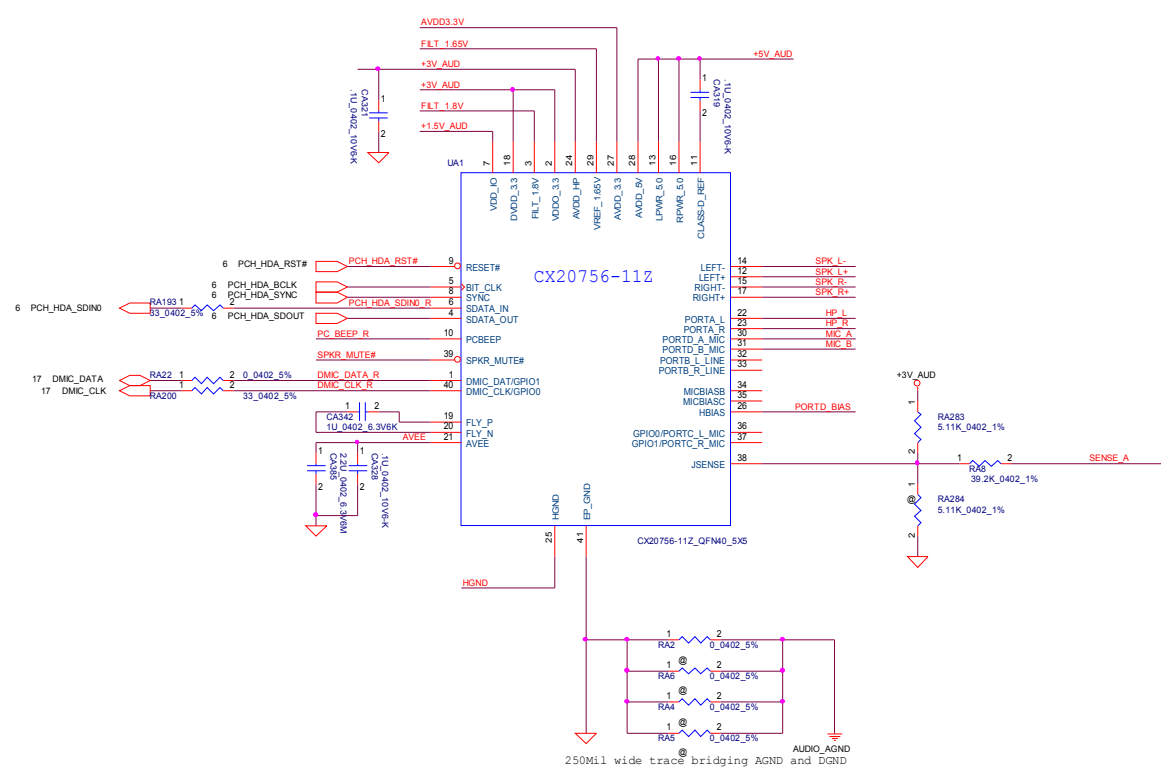
ESD



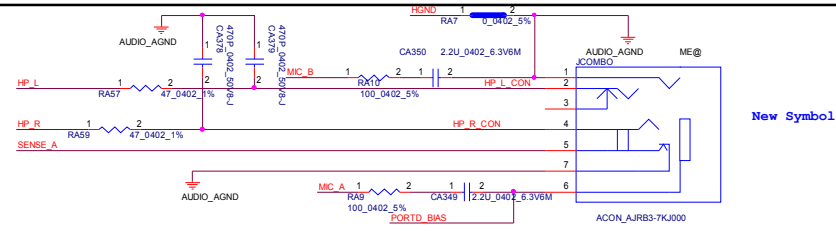
Note

1. Mount 0ohm resistor, Reserve choke



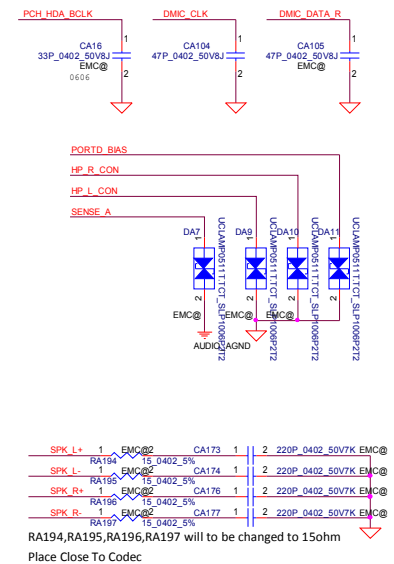
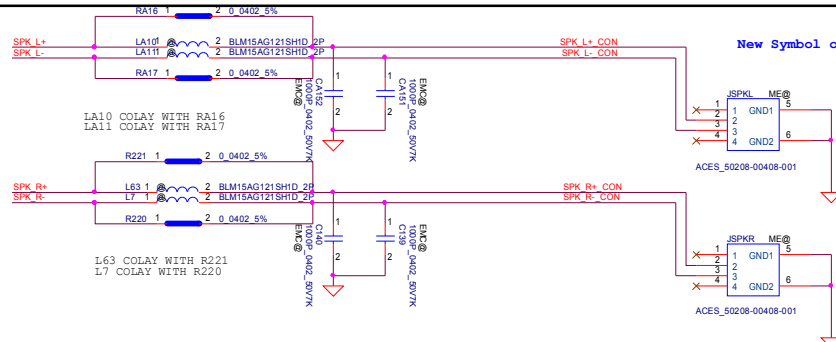


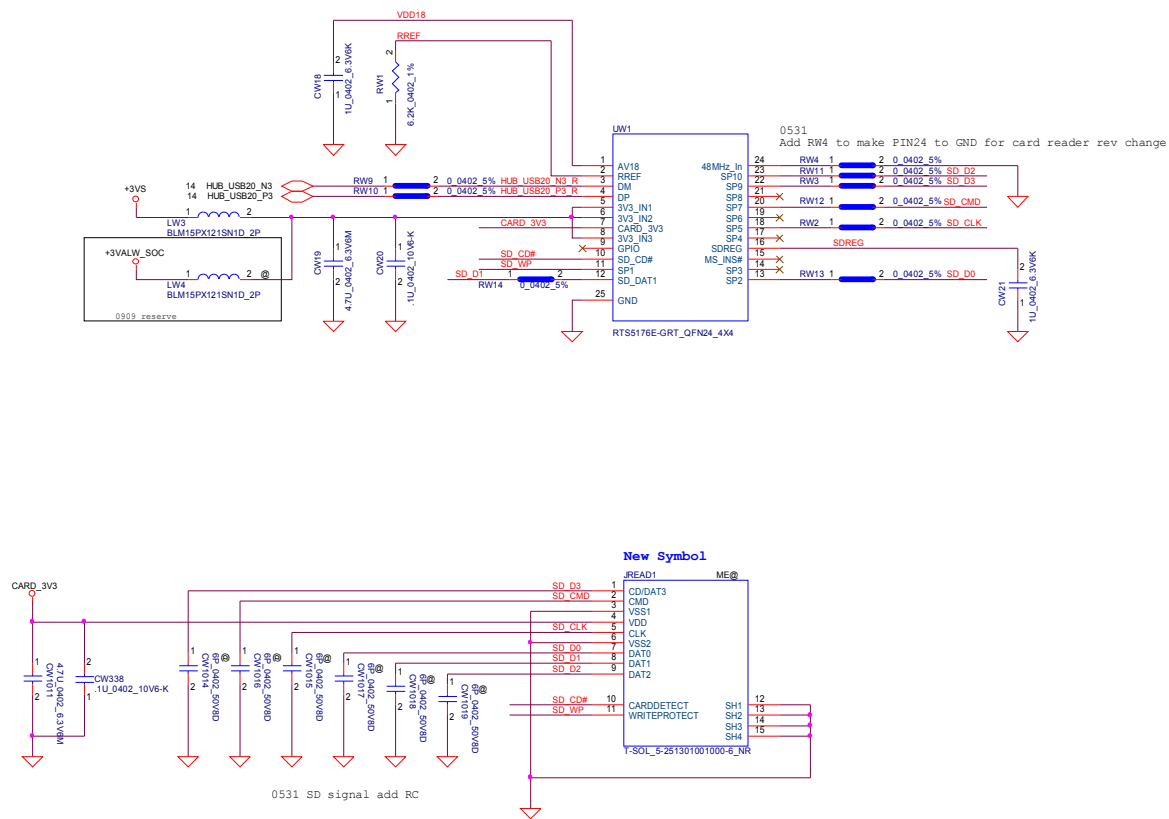
COMBO Jack



Left

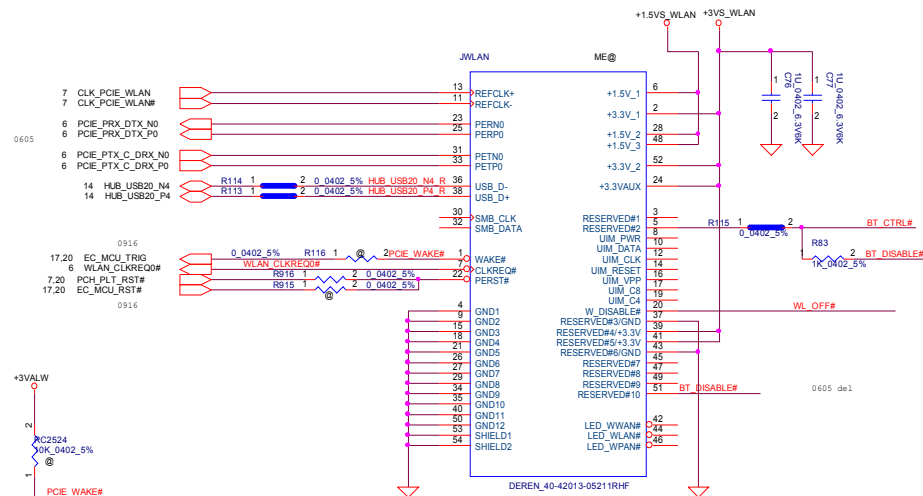
Right





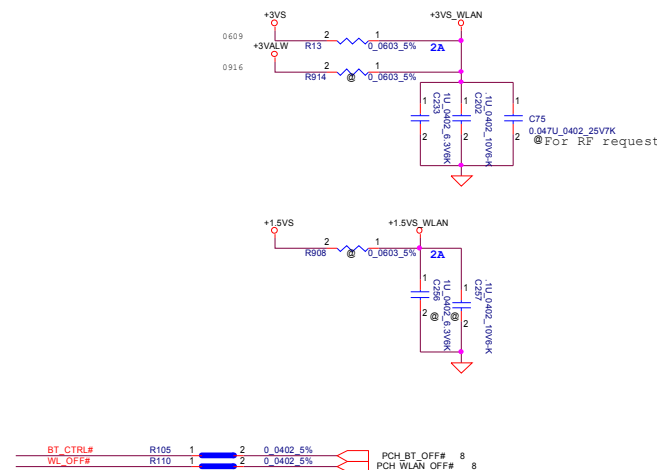
WiFi&BT Board Connector

Mini Card(WLAN/WiMAX)



New Symbol

Mini Card
Connector Pin define确认
Current: Half Card???

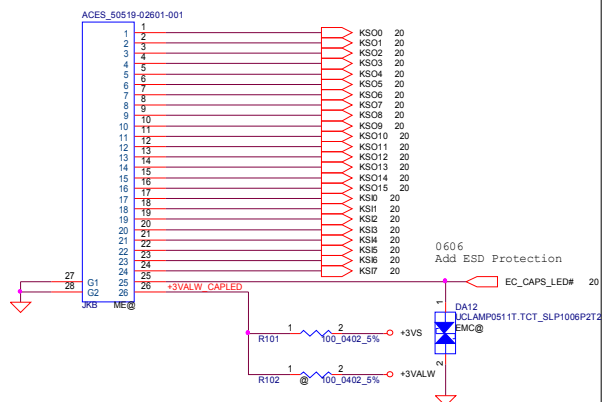


WLAN&BT Combo module circuits

	BT on module Enable	BT on module Disable
* BT_CRTL (GPIO22)	H	L
PCH_BT_ON#	L	H

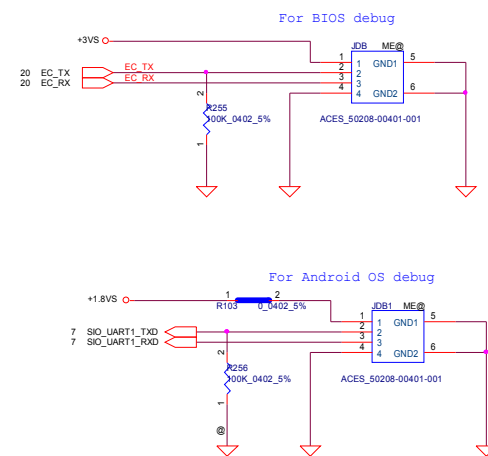
Keyboard Connector

Inverter

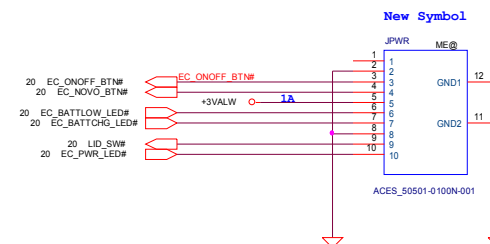


need add 4pin connector for 背光键盘

UART Debug Connector

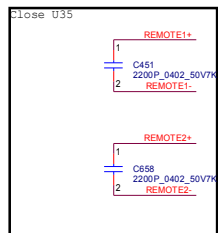
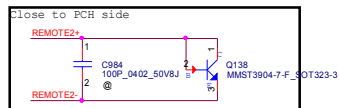
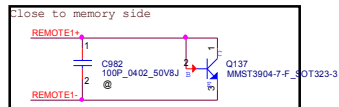
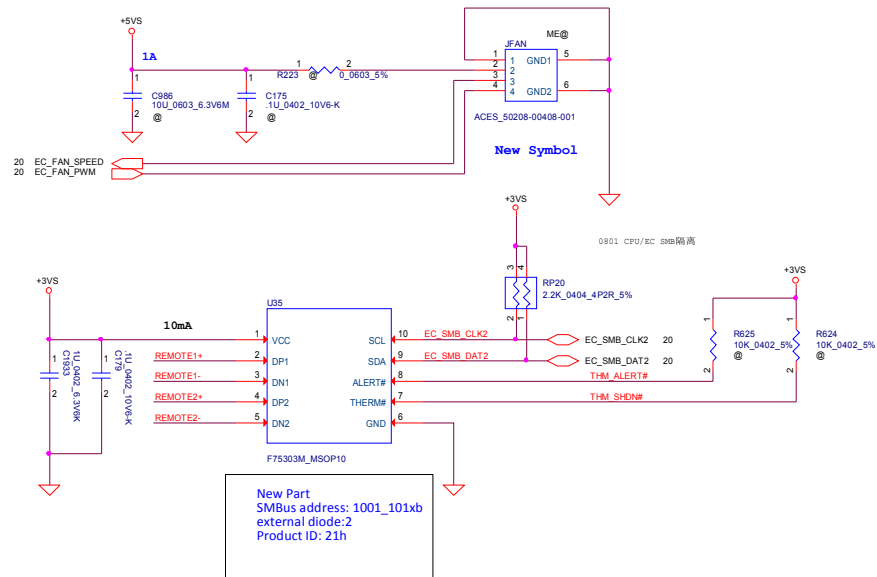


Power&Volume Board Connector

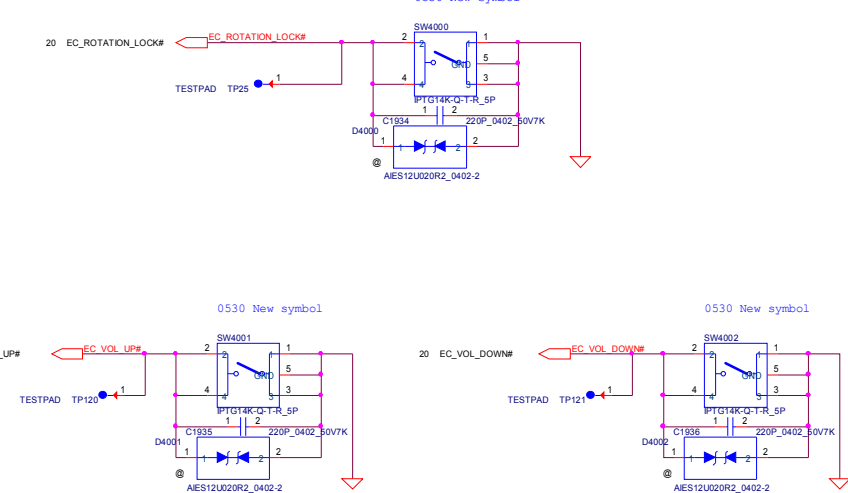


New Symbol

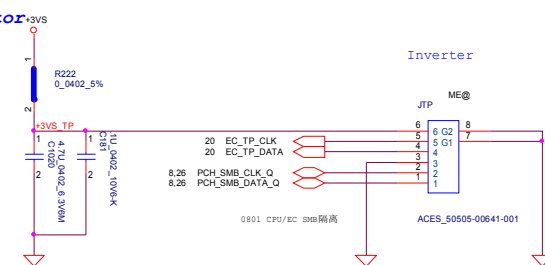
THERMAL SENSOR AND FAN



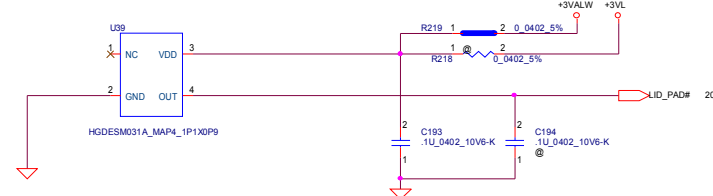
Rotation Vol up/down button



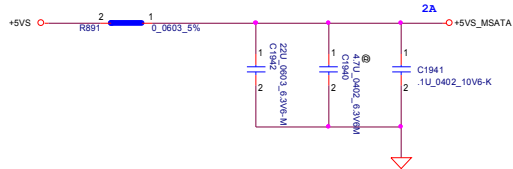
Touch Pad Connector



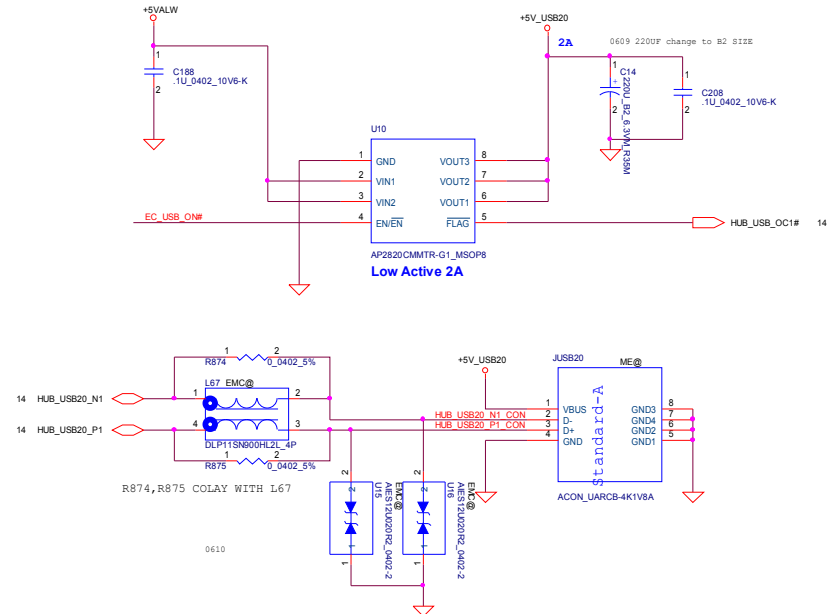
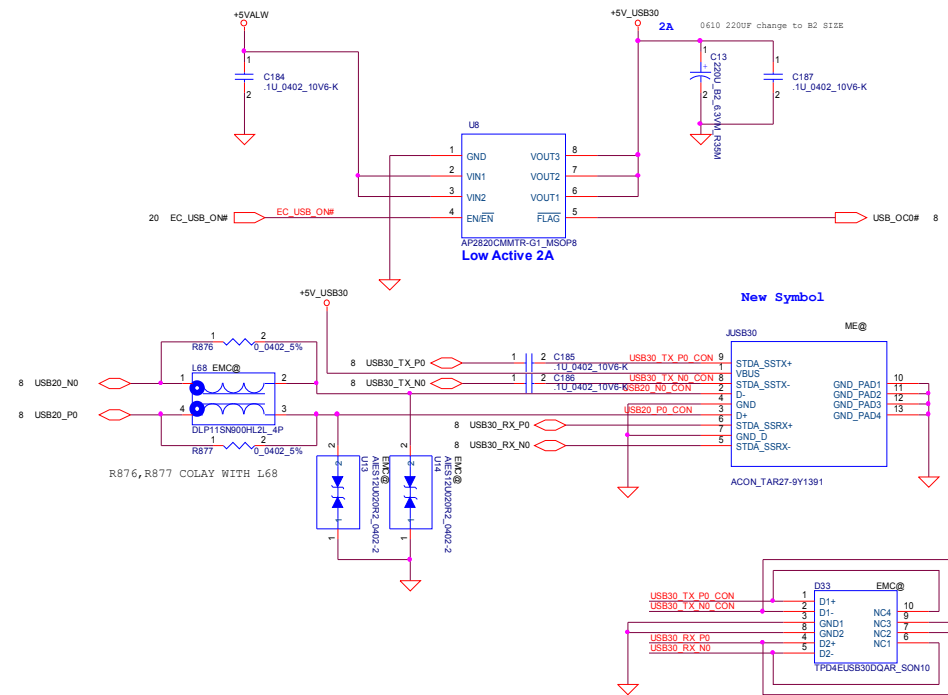
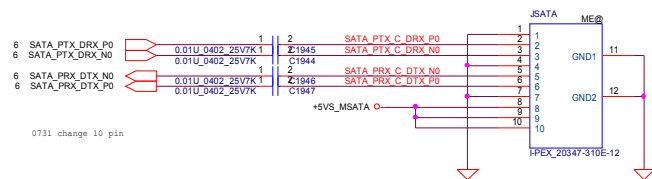
LCD ROTATION LID

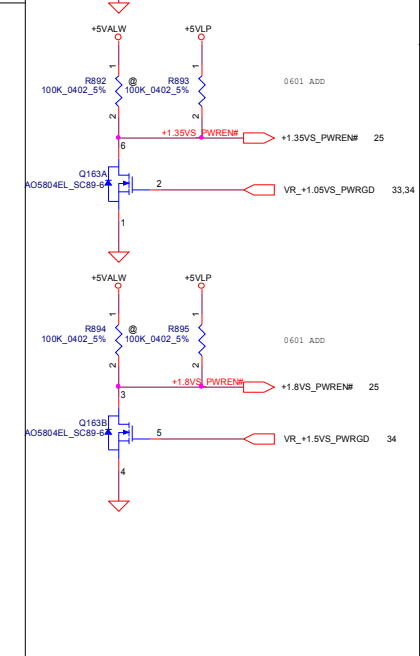
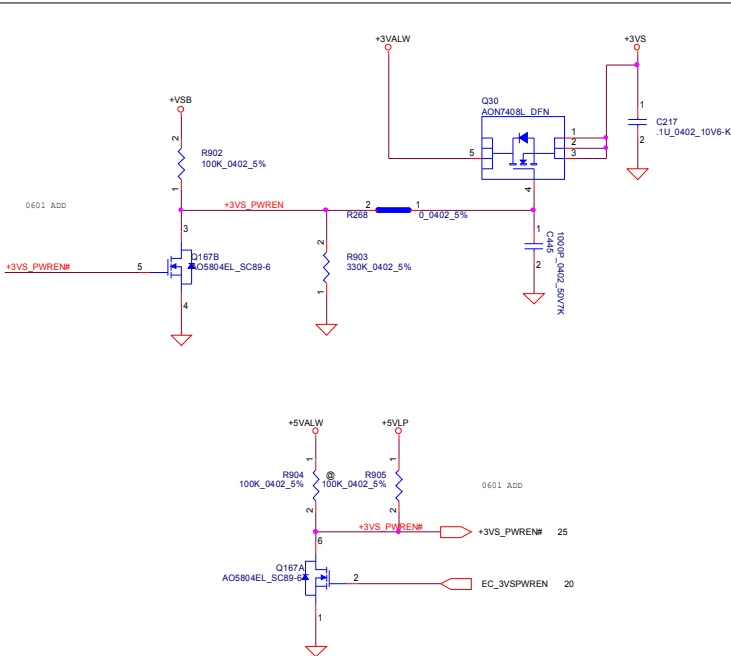
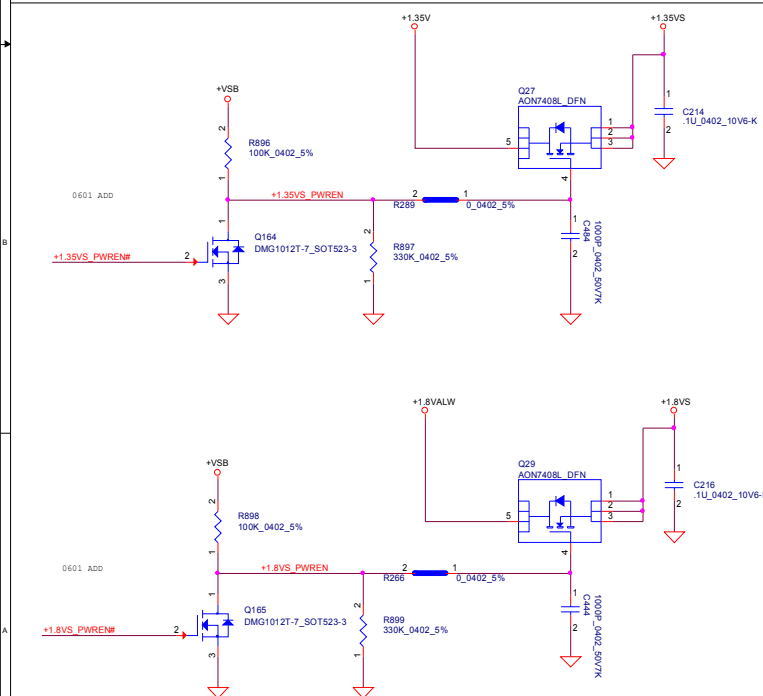
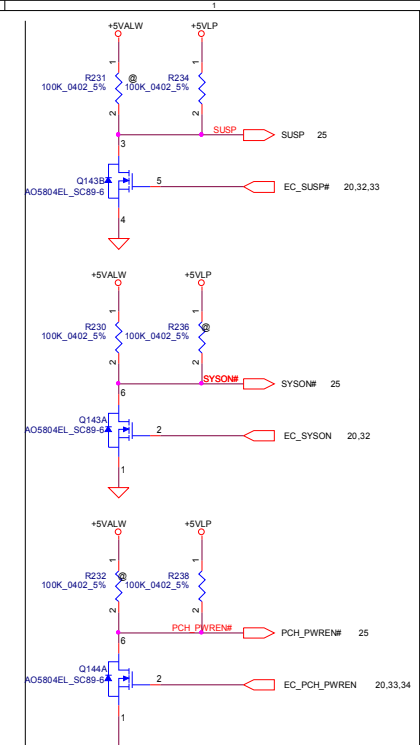
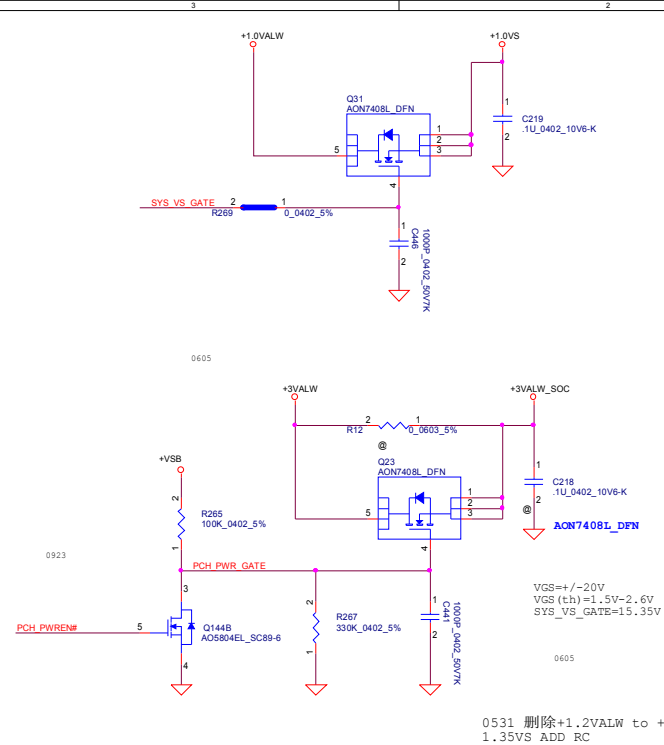
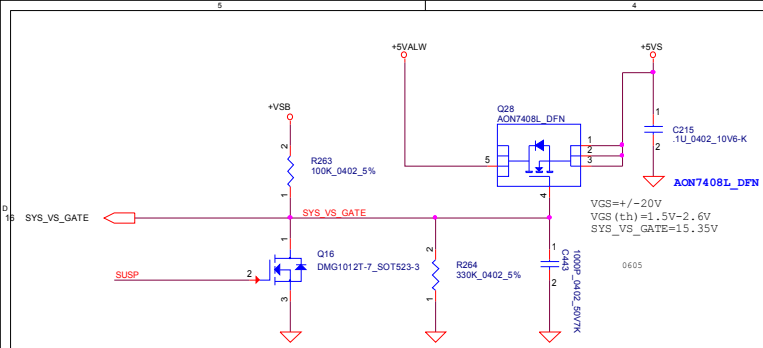


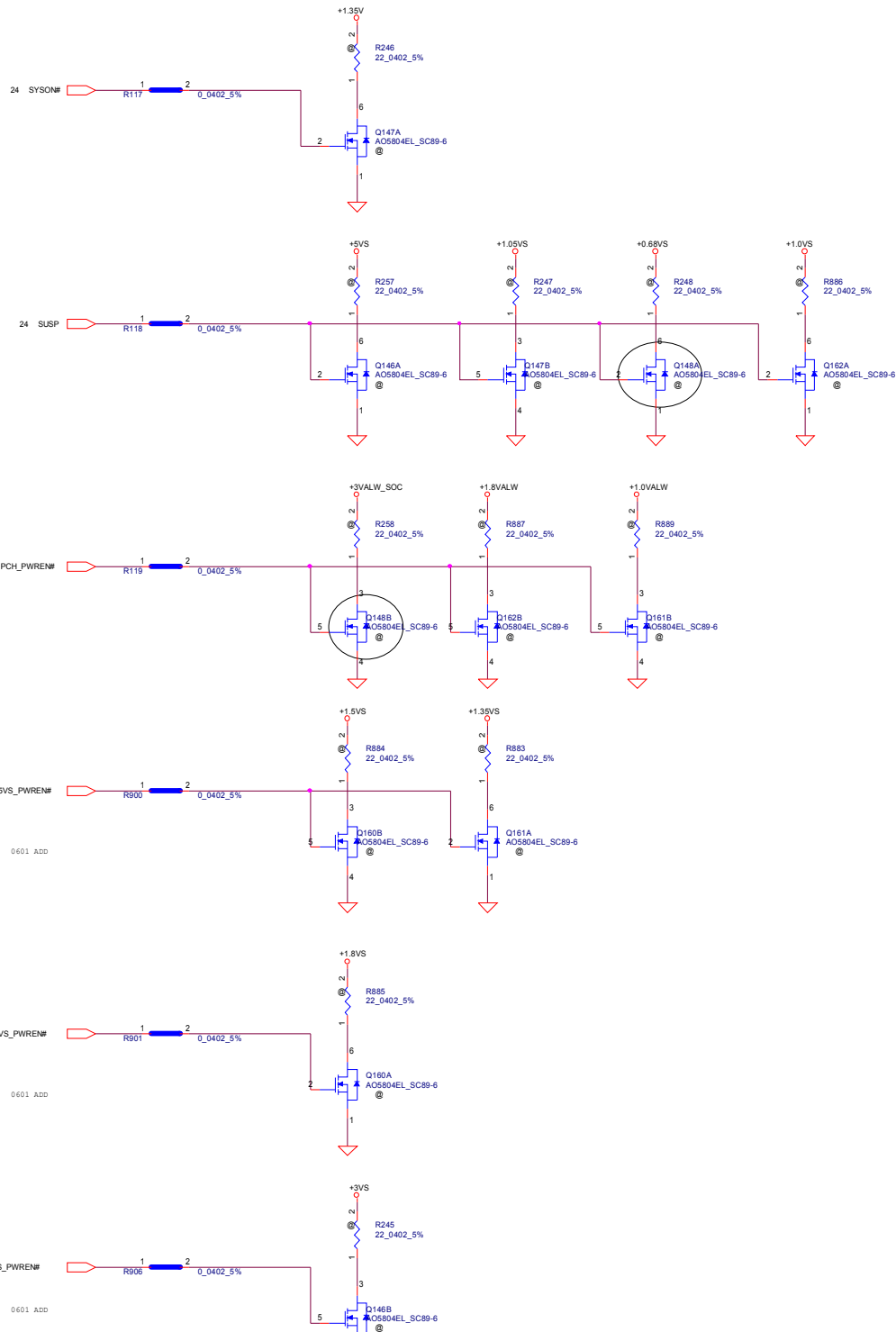
0731 删除+3VS_MSATA线路



New Symbol




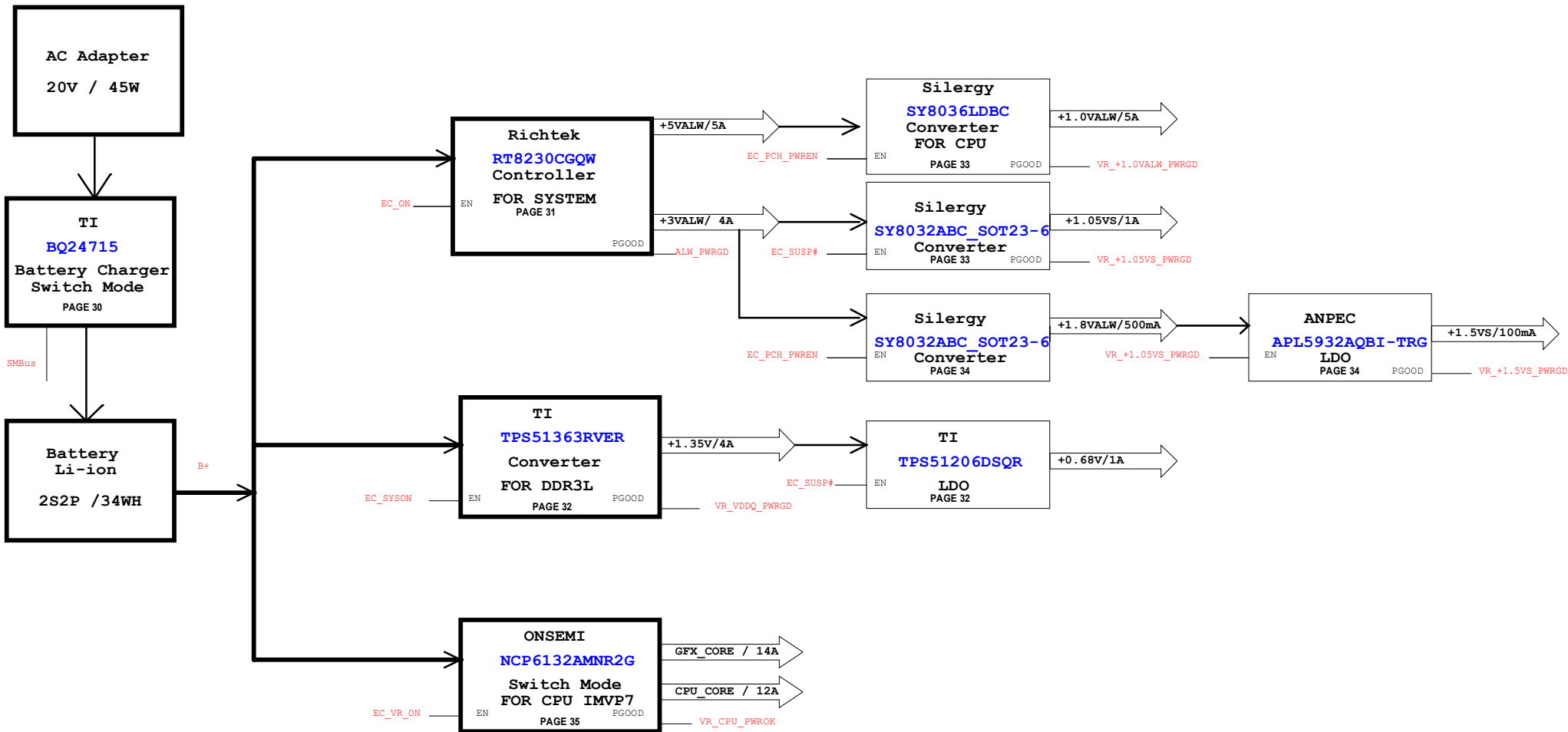


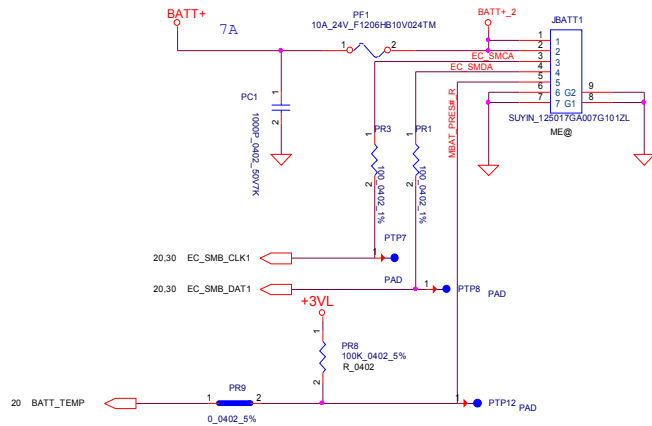


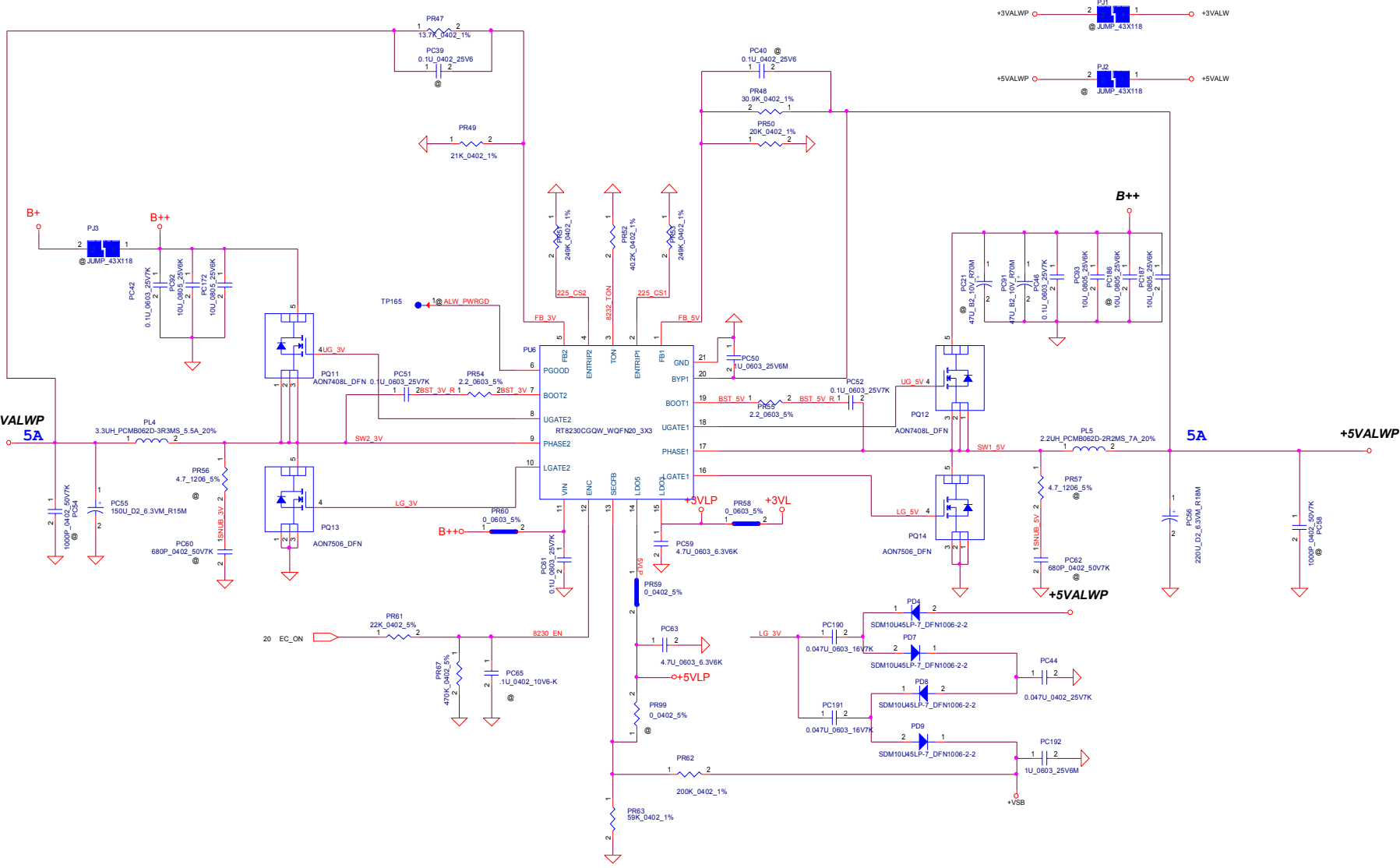
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Date: Friday, November 15, 2013				Sheet 25	of 35

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Date:				Friday, November 15, 2013		Sheet	27 of 35



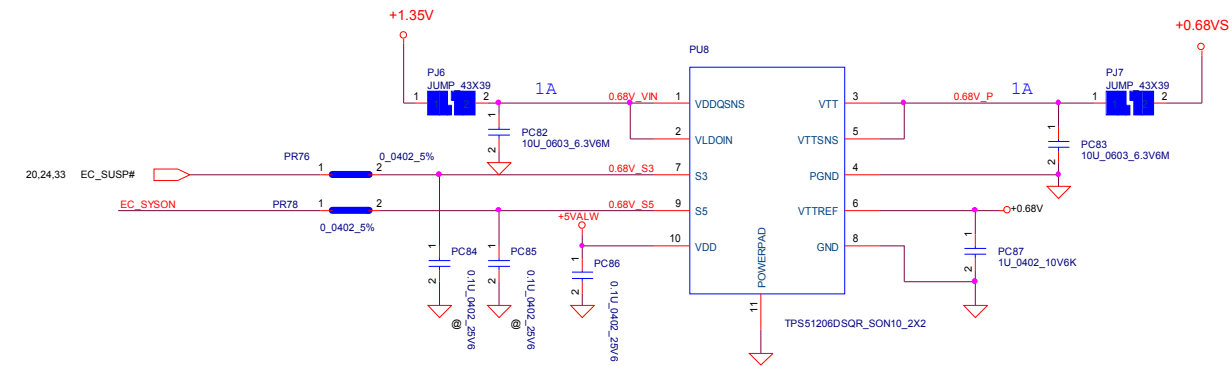
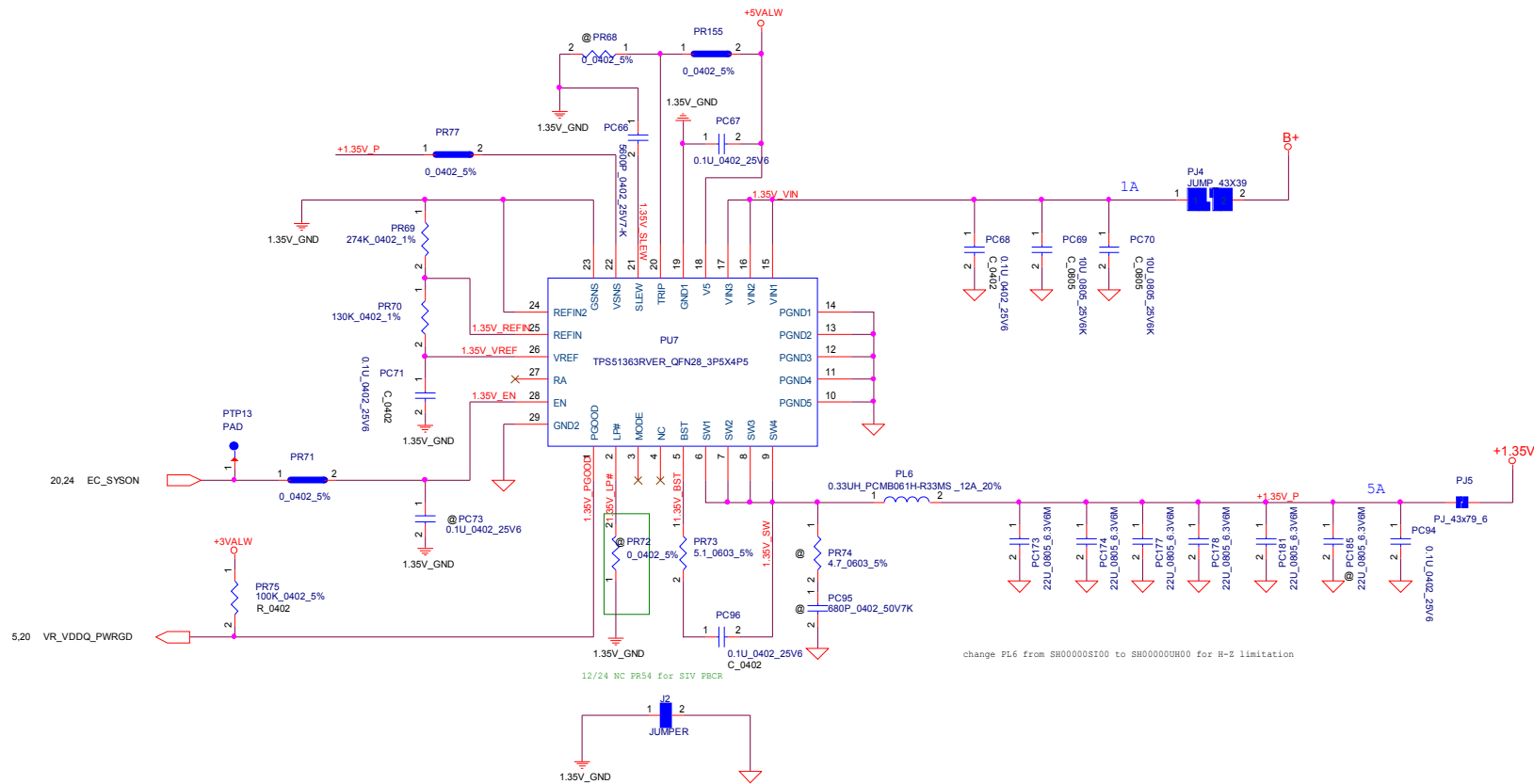




3VALWP VFB=2V TDC 5A Fsw=350KHZ OCP: 7.8A~9.5A	5VALWP TDC 5A Fsw=300KHZ OCP: 7.8A~9.5A
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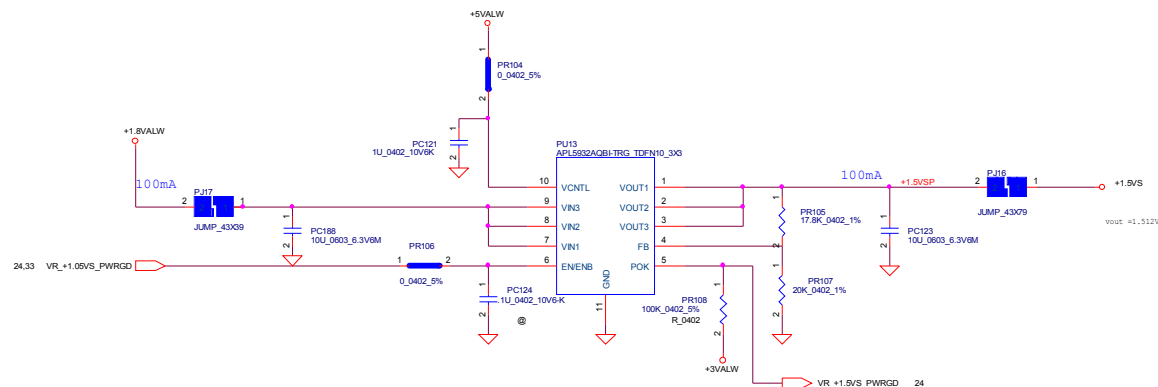
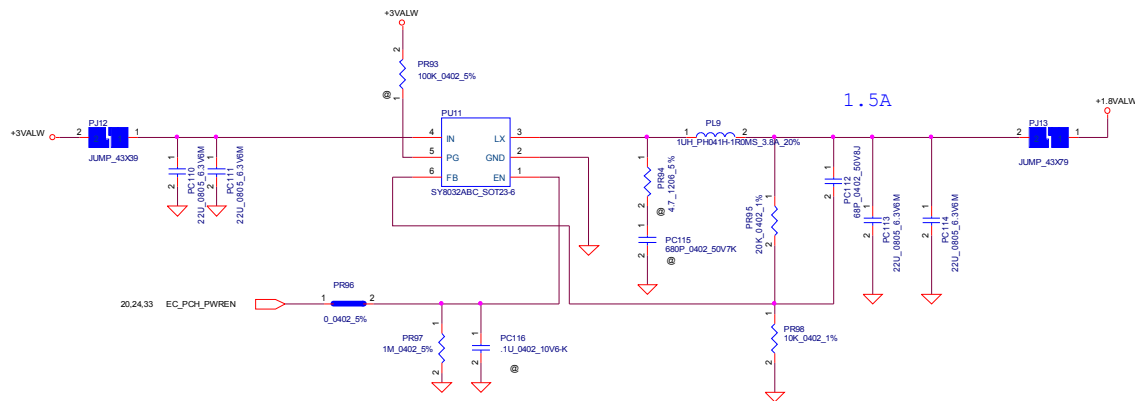
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