

Zenith UMA

Cycle 1

MV BUILD

2009.03.18

DATE	CHANGE NO.	REV

DRAWER	EE	DATE	POWER	DATE	INVENTEC
DESIGN					
CHECK					
					TITLE Zenith UMA C1
					SIZE A3 CODE CS DOC. NUMBER 1310A22526-0-MTR REV A03
XXXX-XXXXXX-XX					SHEET 1 OF 52

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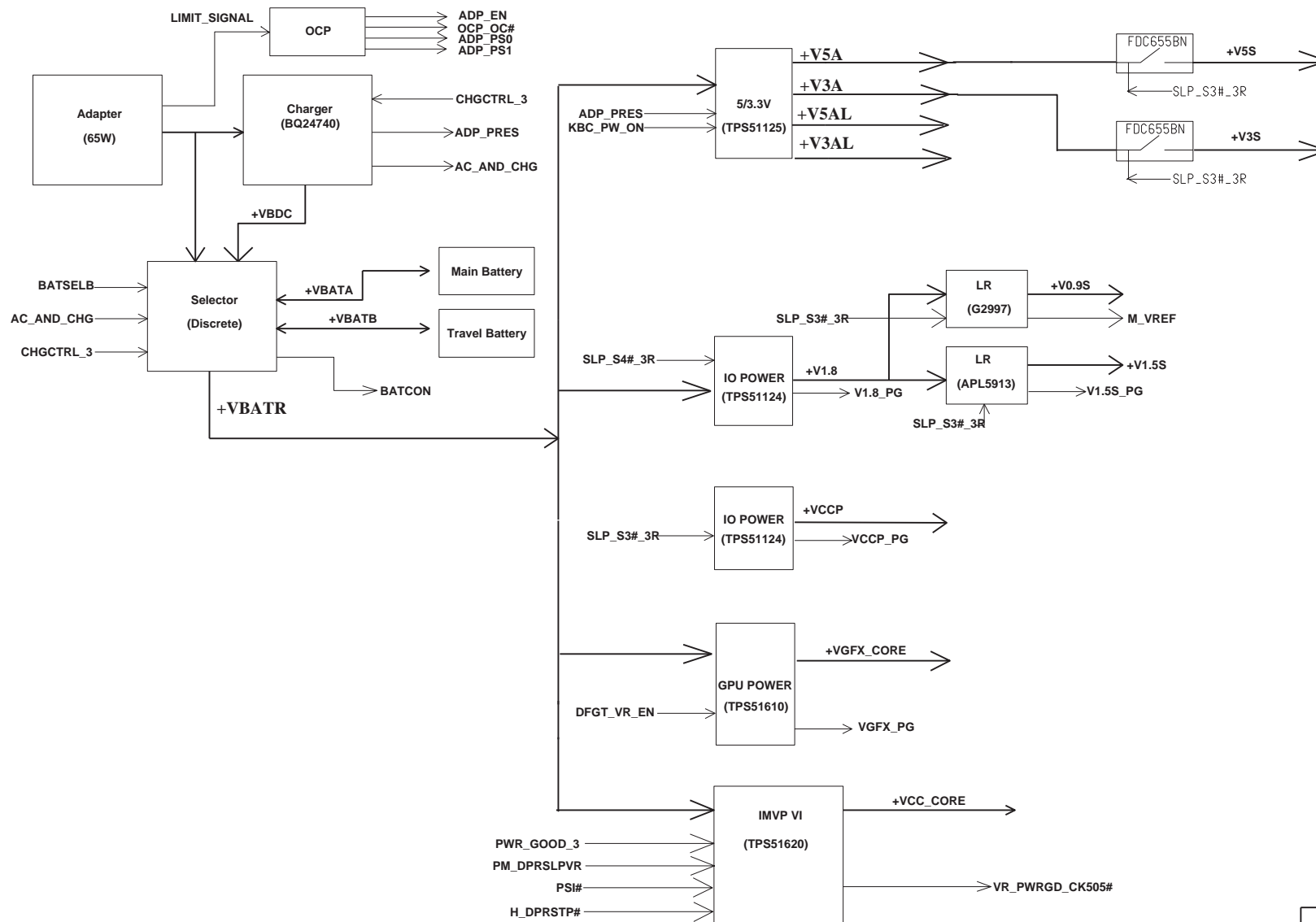
INVENTEC

TITLE **Zenith UMA C1**

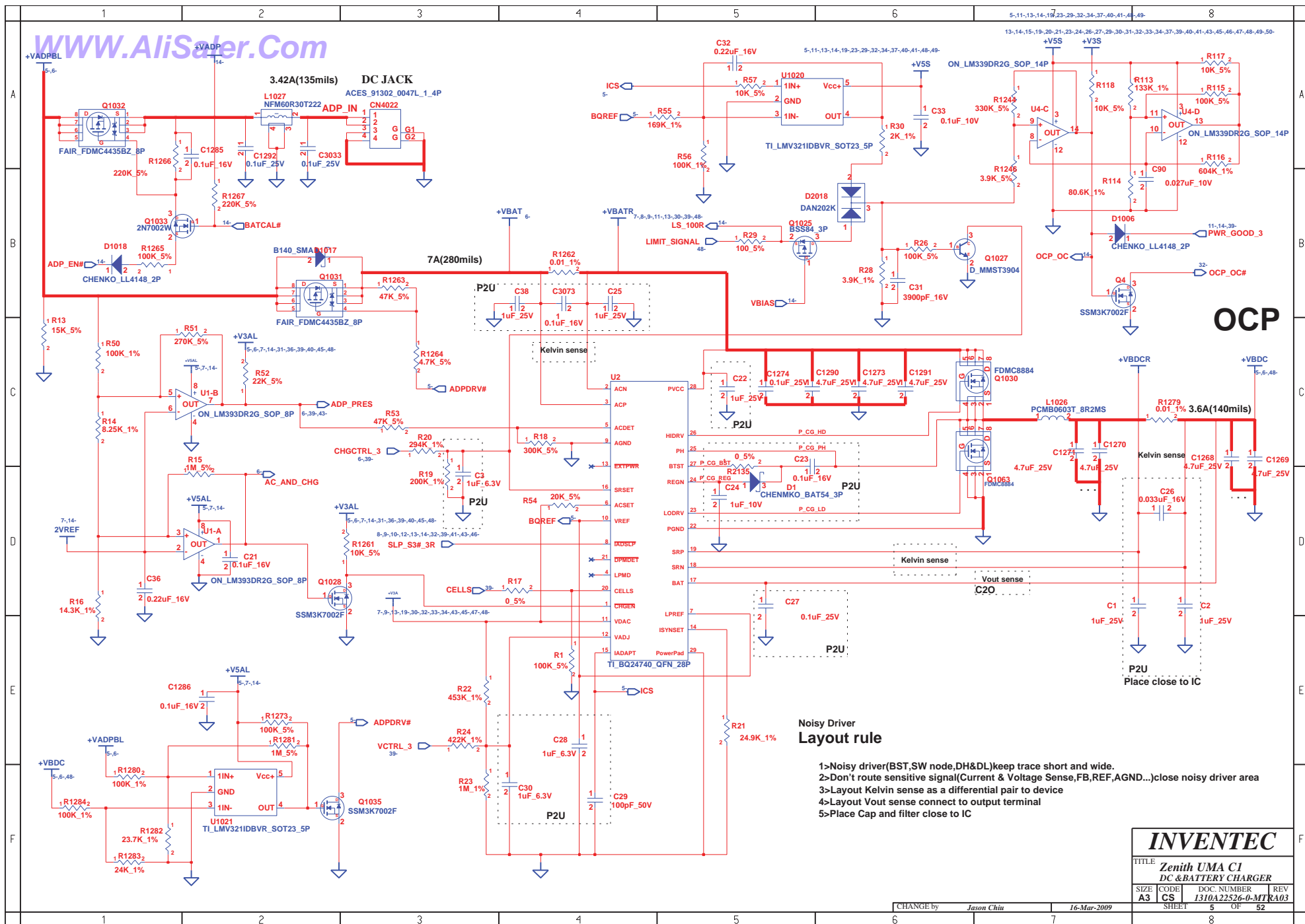
SIZE A3	CODE CS	DOC. NUMBER 1310A22526-0-MTR	REV A03
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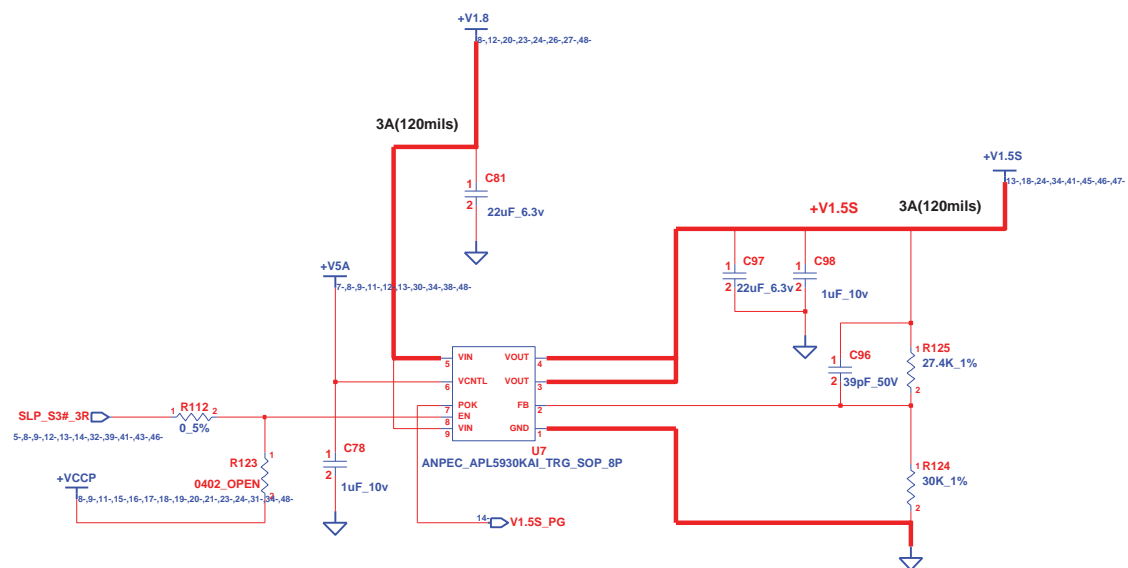
CHANGE by Jason Chiu 16-Mar-2009

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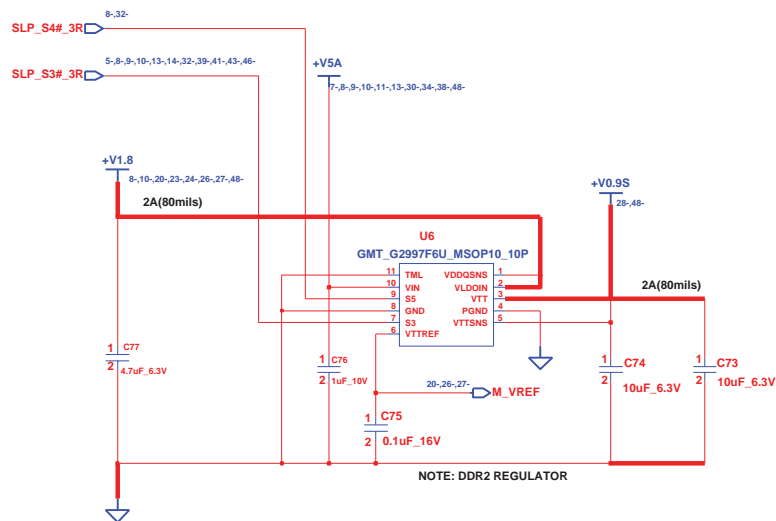


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TITLE Zenith UMA C1			
SIZE A3	CODE CS	DOC. NUMBER 1310A22526-0-MTR	REV A03
CHANGE by Jason Chiu		16-Mar-2009	
SHEET 4		OF 52	

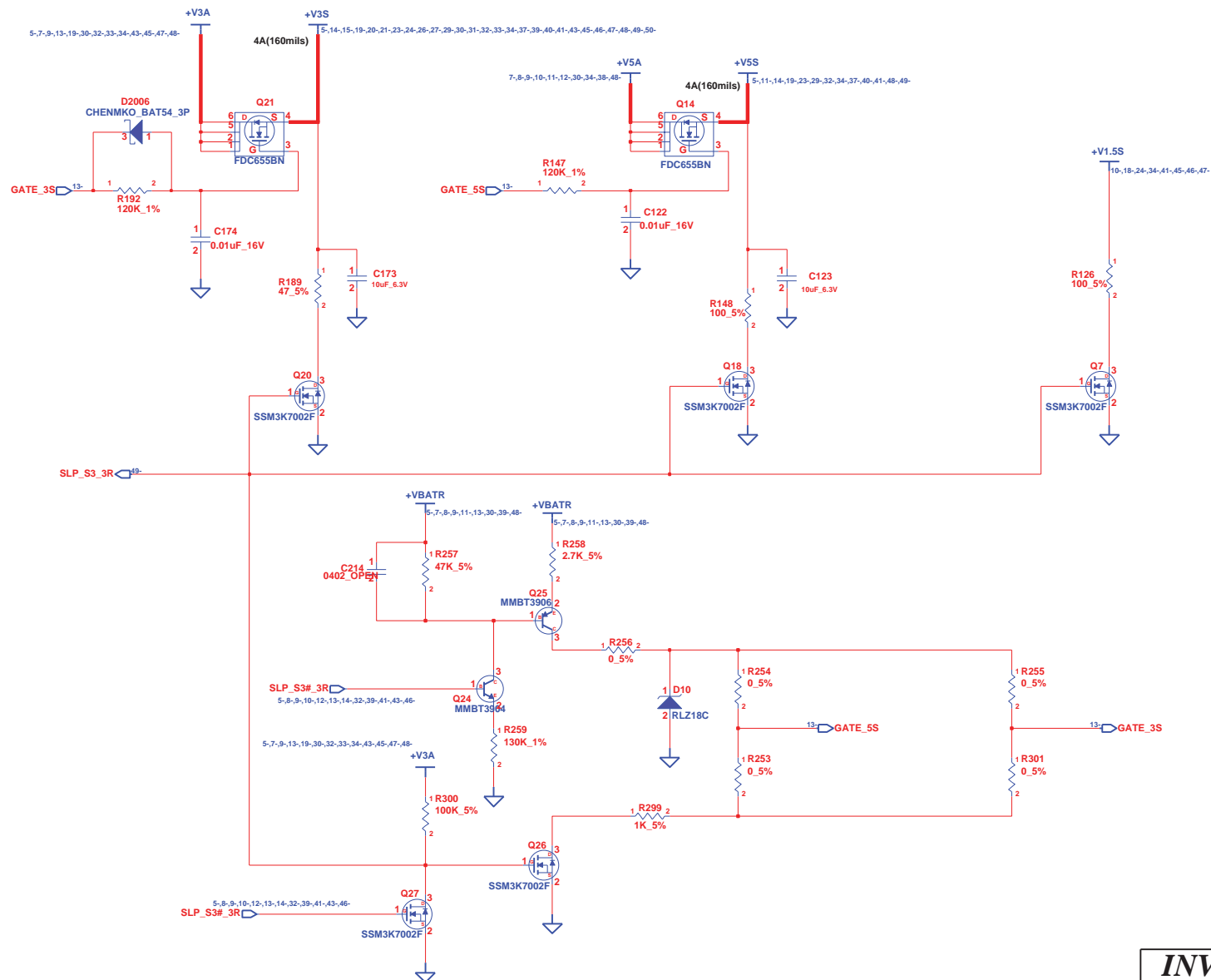




INVENTEC			
TITLE Zenith UMA C1 SYSTEM POWER(+VCCP/+V1.5S)			
SIZE A3	CODE CS	DOC. NUMBER 1310A22526-0-MTR	REV A03
CHANGE by Jason Chiu		16-Mar-2009	
SHEET 10		OF 52	



INVENTEC			
TITLE Zenith UMA C1 DDR TERMINATION VOLTAGE			
SIZE A3	CODE CS	DOC. NUMBER 1310A22526-0-MTR	REV A03
CHANGE by Jason Chiu		16-Mar-2009	
SHEET 12		OF 52	



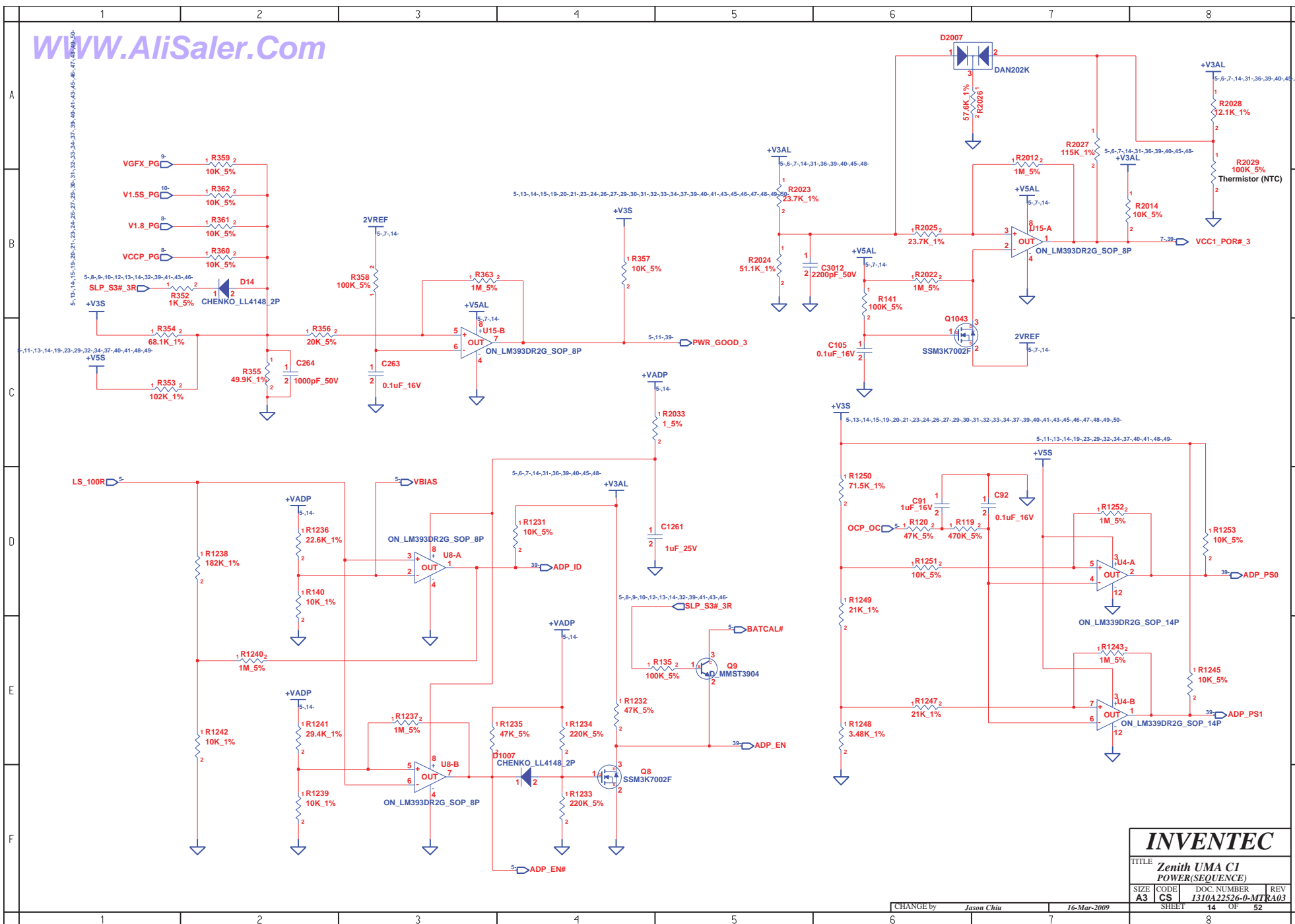
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TITLE
**Zenith UMA C1
POWER(SLEEP)**

SIZE A3	CODE CS	DOC. NUMBER 1310A22526-0-MTR	REV A03
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CHANGE by *Jason Chiu* 16-Mar-2009

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INVENTEC

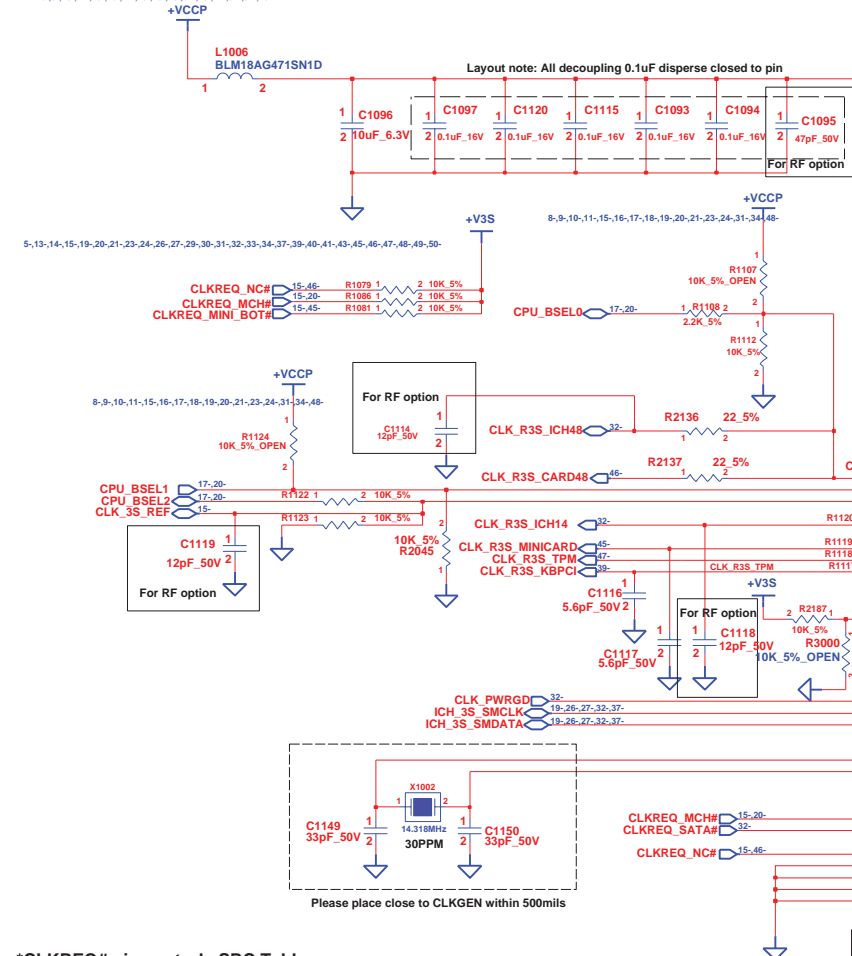
TITLE
**Zenith UMA C1
POWER(SEQUENCE)**

SIZE A3	CODE CS	DOC NUMBER 1310A22526-0-MTR	REV A03
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CHANGE by Jason Chiu

16-Mar-2009

SHEET 14 OF 52

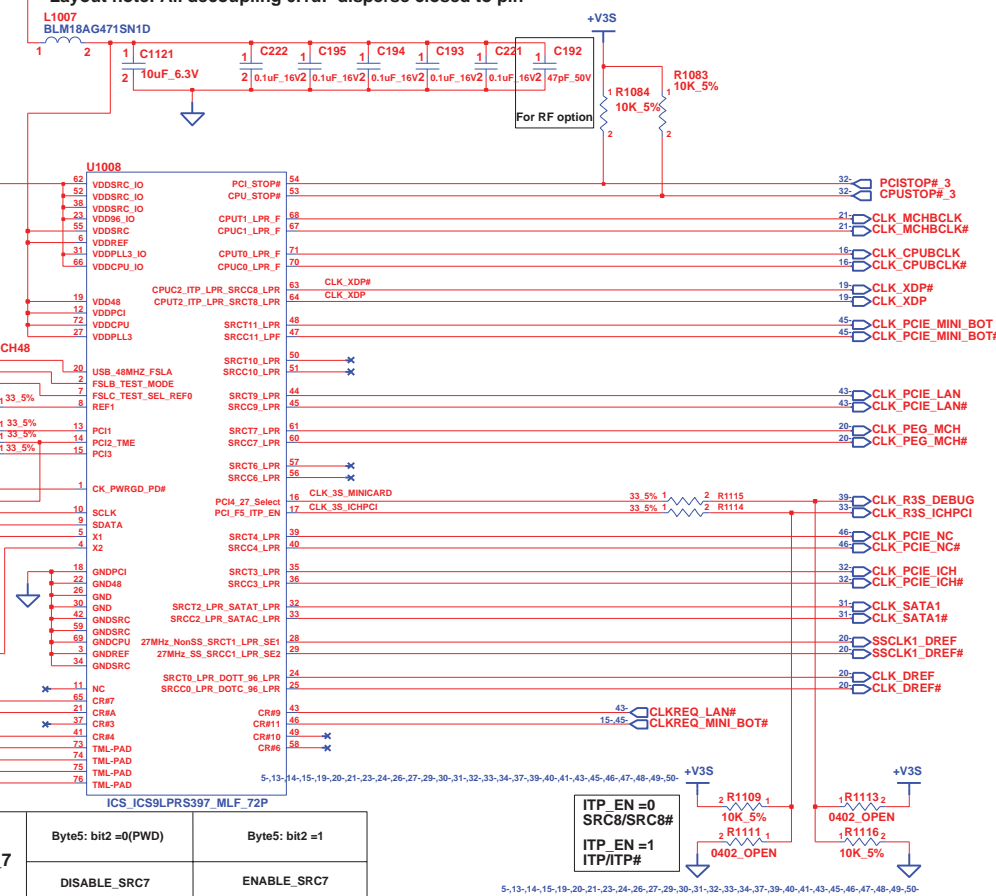
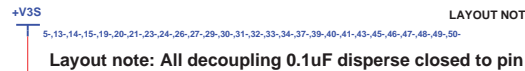
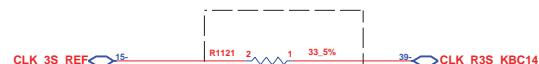


*CLKREQ# pin controls SRC Table.

CR#_A	Byte5: bit6 =0(PWD)	Byte5: bit6 =1	CR#_3	Byte5: bit5 =0(PWD)	Byte5: bit5 =1
	ENABLE_SRC0	ENABLE_SRC2		DISABLE_SRC3	ENABLE_SRC3

CR#_4	Byte5: bit4 =0(PWD)	Byte5: bit4 =1	CR#_6	Byte5: bit3 =0(PWD)	Byte5: bit3 =1
	DISABLE_SRC4	ENABLE_SRC4		DISABLE_SRC6	ENABLE_SRC6

CR#_7	Byte5: bit2 =0(PWD)	Byte5: bit2 =1
	DISABLE_SRC7	ENABLE_SRC7
CR#_9	Byte5: bit1 =0(PWD)	Byte5: bit1 =1
	DISABLE_SRC9	ENABLE_SRC9
CR#_10	Byte5: bit0 =0(PWD)	Byte5: bit0 =1
	DISABLE_SRC10	ENABLE_SRC10
CR#_11	Byte6: bit7 =0(PWD)	Byte6: bit7 =1
	DISABLE_SRC11	ENABLE_SRC11



5-13,14,15,19,20,21,23,24,26,27,29,30,31,32,33,34,37,39,40,41,43,45,46,47,48,49,50-



5-13,14,15,19,20,21,23,24,26,27,29,30,31,32,33,34,37,39,40,41,43,45,46,47,48,49,50-

27_Selet =0
LCD_SST 100MHz
27_Selet =1
27MHz non-spread clock

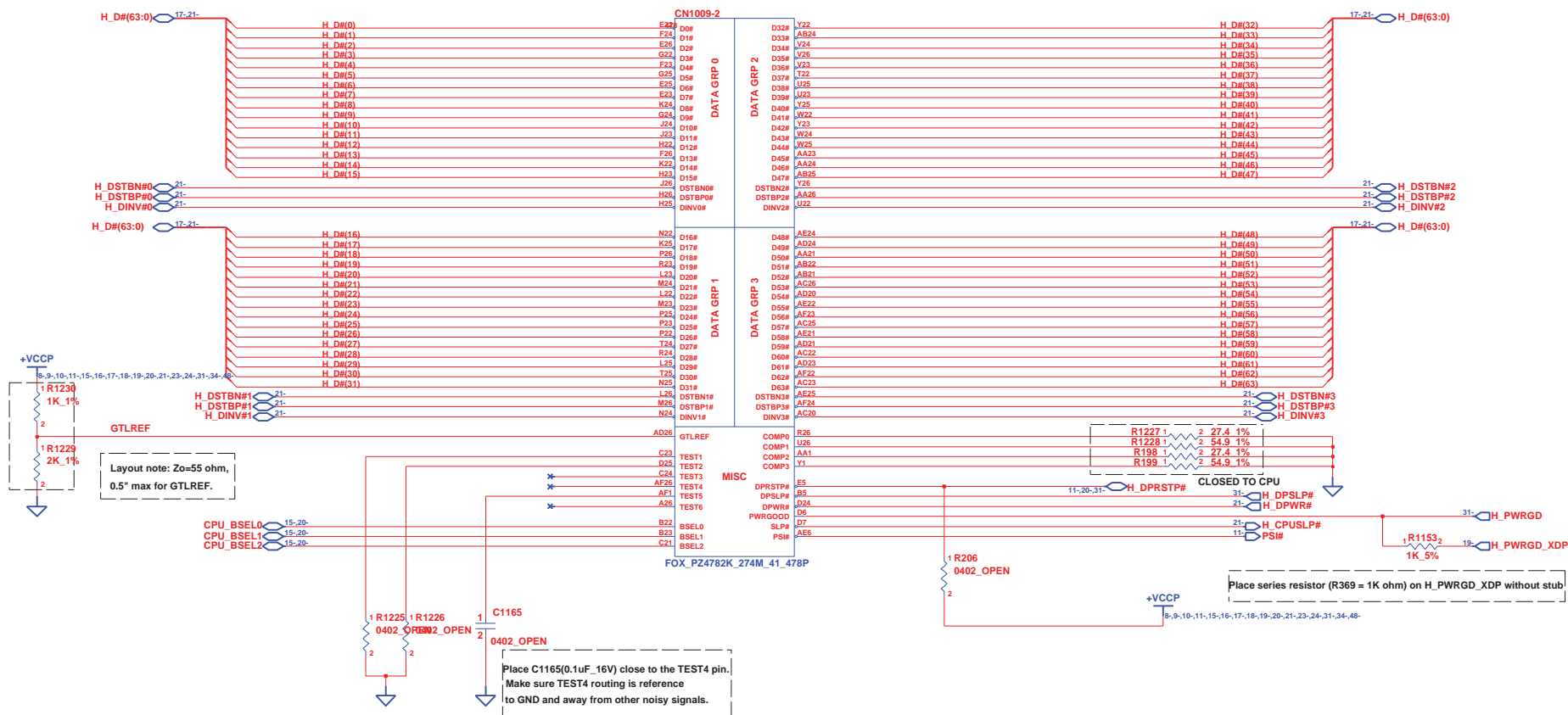
FSA	FSB	FSC	FSB CLOCK FREQUENCY	HOST CLOCK FREQUENCY
1	1	0	667	166
0	1	0	800	200
0	0	0	1066	266

INVENTEC			
Zenith UMA C1 CLOCK GENERATOR			
SIZE A3	CODE CS	DOC NUMBER 1310A22526-0-MTR	REV A03

CHANGE by Jason Chiu

16-Mar-2009

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INVENTEC

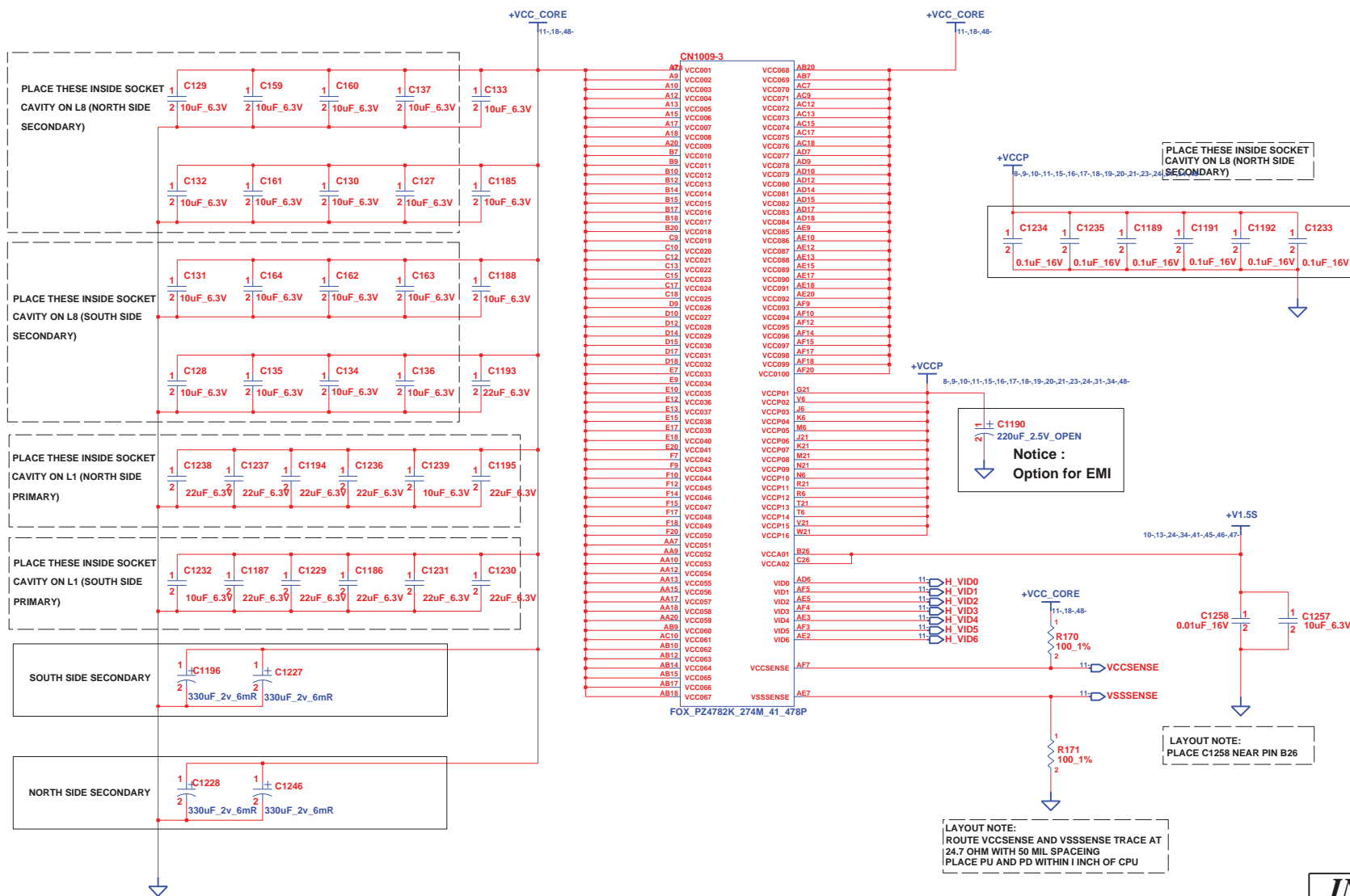
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**Zenith UMA C1
PENRYN-2**

SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A22526-0-MT	RA03

CHANGE by Jason Chiu

16-Mar-2009

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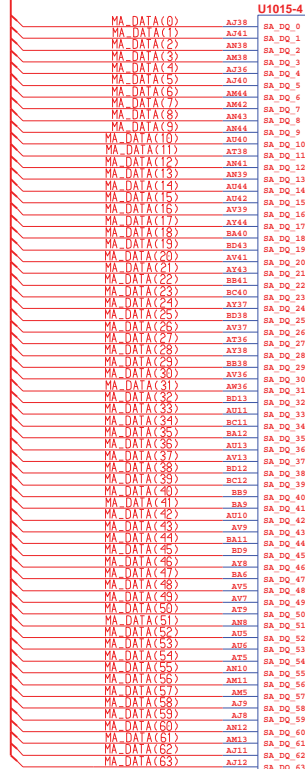
INVENTEC

TITLE
**Zenith UMA C1
PENRYN-3**

SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A22526-0-MT	RA03

CHANGE by	Jason Chiu	16-Mar-2009	SHEET 18 OF 52
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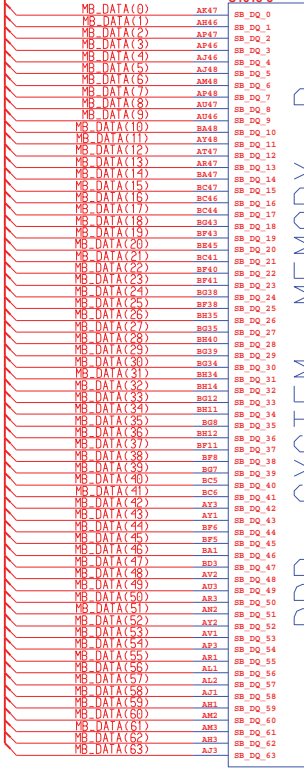
MA_DATA(63:0)



ITL_CANTIGA_FCBGA_1329P

DDR SYSTEM MEMORY A

MB_DATA(63:0)



ITL_CANTIGA_FCBGA_1329P

DDR SYSTEM MEMORY B

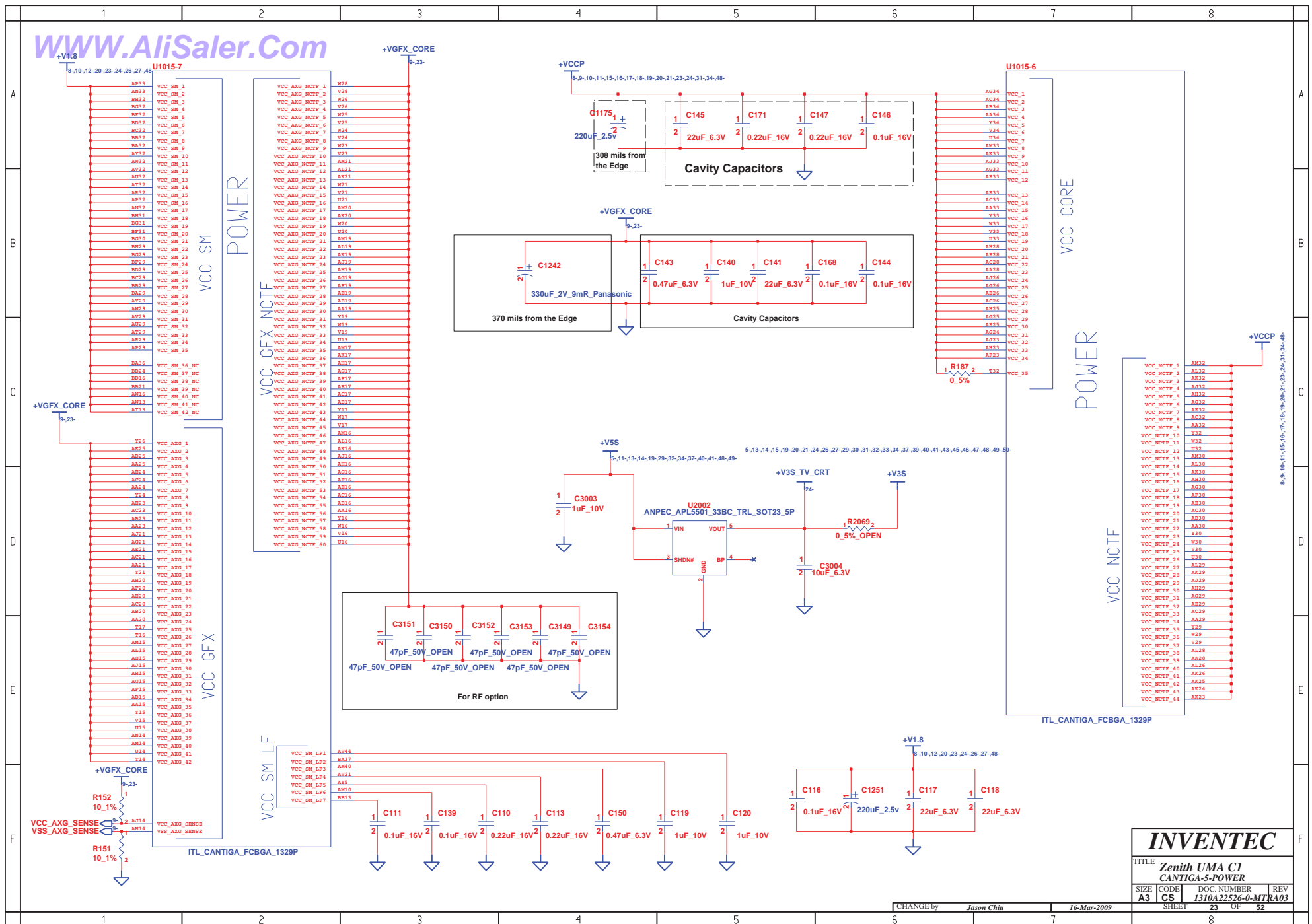
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TITLE
Zenith UMA C1
CANTIGA-4-DDR2

SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A22526-0-MTR	A03

CHANGE by Jason Chiu 16-Mar-2009

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SO DIMM0_9.2mm

INVENTEC

TITLE
Zenith UMA C1
DDR2-DIMM-0

SIZE CODE DOC. NUMBER REV
A3 CS 1310A22526-0-MTR

SHEET 26 OF 52

CHANGE by Jason Chiu 16-Mar-2009

8

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SO DIMM0_9.2mm

INVENTEC
TITLE Zenith UMA C1
DDR2-DIMM-0
SIZE CODE DOC NUMBER REV
A3 CS 1310A22526-0-MTR A03
CHANGE by Jason Chiu 16-Mar-2009 SHEET 26 OF 52

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SO DIMM0_9.2mm

INVENTEC
TITLE Zenith UMA C1
DDR2-DIMM-0
SIZE CODE DOC NUMBER REV
A3 CS 1310A22526-0-MTR A03
CHANGE by Jason Chiu 16-Mar-2009 SHEET 26 OF 52

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SO DIMM0_9.2mm

INVENTEC

TITLE
Zenith UMA C1
DDR2-DIMM-0

SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A22526-0-MTR	A03

CHANGE by Jason Chiu 16-Mar-2009

SHEET 26 OF 52

8

1 2 3 4 5 6 7 8

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A

B

C

D

E

F

MA_A(14:0) 22-28- CN1012-1 MA_DATA(63:0) 22- +V1.8 8-10-12-20-23-24-27-48- MA_BS2# 22-28- MA_BS0# 22-28- MA_BS1# 22-28- M_CS0# 20-28- M_CS1# 20-28- M_CLK_DDR0 20- M_CLK_DDR1 20- M_CLK_DDR1# 20- M_CKE0 20-28- M_CKE1 20-28- M_CAS# 22-28- MA_RAS# 22-28- MA_WE# 22-28- ICH_3S_SMCLK 15-19-27-32-37- ICH_3S_SMDATA 15-19-27-32-37- M_ODT0 20-28- M_ODT1 20-28- MA_DM(0) 10- MA_DM(1) 26- MA_DM(2) 52- MA_DM(3) 67- MA_DM(4) 130- MA_DM(5) 147- MA_DM(6) 170- MA_DM(7) 183- MA_DQS(0) 13- MA_DQS(1) 31- MA_DQS(2) 51- MA_DQS(3) 70- MA_DQS(4) 131- MA_DQS(5) 148- MA_DQS(6) 169- MA_DQS(7) 188- MA_DQS#(0) 11- MA_DQS#(1) 29- MA_DQS#(2) 49- MA_DQS#(3) 68- MA_DQS#(4) 129- MA_DQS#(5) 146- MA_DQS#(6) 167- MA_DQS#(7) 186- FOX_AS0A426_NARN_7F_200P

200 CN1012-1 MA_A(0) 100 A0 MA_A(1) 101 A1 MA_A(2) 100 A2 MA_A(3) 99 A3 MA_A(4) 98 A4 MA_A(5) 97 A5 MA_A(6) 94 A6 MA_A(7) 92 A7 MA_A(8) 90 A8 MA_A(9) 91 A9 MA_A(10) 105 A10 AP MA_A(11) 89 A11 MA_A(12) 89 A12 MA_A(13) 116 A13 MA_A(14) 86 A14 MA_A(15) 84 A15 MA_A(16) 85 A16 BAZ MA_BS2# 197 BA0 MA_BS0# 196 BA1 MA_BS1# 110 S0# M_CS0# 115 S0# M_CS1# 32 CK0 M_CLK_DDR0 164 CK0# M_CLK_DDR1 166 CK1# M_CLK_DDR1# 79 CKE0 80 CKE1 113 CAS# 108 RAS# 109 WE# 198 SA0 200 SA1 197 SCL 195 SDA ODT0 114 ODT1 119 DM0 10 DM1 26 DM2 52 DM3 67 DM4 130 DM5 147 DM6 170 DM7 183 DQS0 13 DQS1 31 DQS2 51 DQS3 70 DQS4 131 DQS5 148 DQS6 169 DQS7 188 DQS#0 11 DQS#1 29 DQS#2 49 DQS#3 68 DQS#4 129 DQS#5 146 DQS#6 167 DQS#7 186 MA_DATA(0) 5 D00 MA_DATA(1) 7 D01 MA_DATA(2) 17 D02 MA_DATA(3) 19 D03 MA_DATA(4) 4 D04 MA_DATA(5) 6 D05 MA_DATA(6) 14 D06 MA_DATA(7) 16 D07 MA_DATA(8) 23 D08 MA_DATA(9) 25 D09 MA_DATA(10) 35 DQ10 MA_DATA(11) 37 DQ11 MA_DATA(12) 20 DQ12 MA_DATA(13) 22 DQ13 MA_DATA(14) 36 DQ14 MA_DATA(15) 38 DQ15 MA_DATA(16) 40 DQ16 MA_DATA(17) 45 DQ17 MA_DATA(18) 55 DQ18 MA_DATA(19) 57 DQ19 MA_DATA(20) 44 DQ20 MA_DATA(21) 46 DQ21 MA_DATA(22) 58 DQ22 MA_DATA(23) 61 DQ23 MA_DATA(24) 63 DQ24 MA_DATA(25) 73 DQ25 MA_DATA(26) 75 DQ26 MA_DATA(27) 62 DQ27 MA_DATA(28) 64 DQ28 MA_DATA(29) 74 DQ29 MA_DATA(30) 76 DQ30 MA_DATA(31) 123 DQ31 MA_DATA(32) 125 DQ32 MA_DATA(33) 135 DQ33 MA_DATA(34) 137 DQ34 MA_DATA(35) 124 DQ35 MA_DATA(36) 126 DQ36 MA_DATA(37) 134 DQ37 MA_DATA(38) 136 DQ38 MA_DATA(39) 141 DQ39 MA_DATA(40) 143 DQ40 MA_DATA(41) 151 DQ41 MA_DATA(42) 153 DQ42 MA_DATA(43) 140 DQ43 MA_DATA(44) 142 DQ44 MA_DATA(45) 152 DQ45 MA_DATA(46) 154 DQ46 MA_DATA(47) 157 DQ47 MA_DATA(48) 159 DQ48 MA_DATA(49) 173 DQ49 MA_DATA(50) 175 DQ50 MA_DATA(51) 158 DQ51 MA_DATA(52) 160 DQ52 MA_DATA(53) 174 DQ53 MA_DATA(54) 176 DQ54 MA_DATA(55) 179 DQ55 MA_DATA(56) 181 DQ56 MA_DATA(57) 182 DQ57 MA_DATA(58) 184 DQ58 MA_DATA(59) 186 DQ59 MA_DATA(60) 187 DQ60 MA_DATA(61) 188 DQ61 MA_DATA(62) 189 DQ62 MA_DATA(63) 190

Layout notes: Place these Caps closed So-Dimm0

1 C86 1 C88 1 C80 1 C83 1 C85 1 C87 1 C89 1 C79 1 C82 2 0.1uF_16V 2 0.1uF_16V 2 0.1uF_16V 2 0.1uF_16V 2 2.2uF_16V 2 2.2uF_16V 2 2.2uF_16V 2 2.2uF_16V

+V3S 5-13-14-15-19-20-21-23-24-27-29-30-31-32-33-34-37-39-40-41-43-45-46-47-48-49-50-

C50 1 0.1uF_16V 2 C49 1 2.2uF_16V 2 M_VREF 12-20-27- PM_EXTTS#0 20- C66 1 0.1uF_16V 2 C67 1 2.2uF_16V 2

200 CN1012-2 VDD1 112 VS816 18 VDD2 111 VBS17 24 VDD3 117 VBS18 41 VDD4 96 VBS19 33 VDD5 95 VBS20 42 VDD6 118 VBS21 54 VDD7 81 VBS22 59 VDD8 82 VBS23 65 VDD9 87 VBS24 60 VDD10 103 VBS25 66 VDD11 88 VBS26 127 VDD12 194 VBS27 128 VDDSPD 199 VBS28 145 VBS29 165 NC1 83 VBS30 171 NC2 120 VBS31 172 NC3 50 VBS32 177 NC4 69 VBS33 187 NCTEST 183 VBS34 178 VBS35 190 VREF 1 VBS37 9 G1 VBS38 21 GND0 61 VBS39 33 GND1 62 VBS40 155 VBS41 34 VBS42 132 VBS43 144 VBS44 156 VBS45 168 VBS46 2 VBS47 12 VBS48 48 VBS49 15 VBS50 184 VBS51 27 VBS52 78 VBS53 149 VBS54 161 VBS55 121 VBS56 28 VBS57 122 VBS58 40 VBS59 193 VBS60 138 VBS61 150 VBS62 152

FOX_AS0A426_NARN_7F_200P

SO DIMM0_9.2mm

INVENTEC

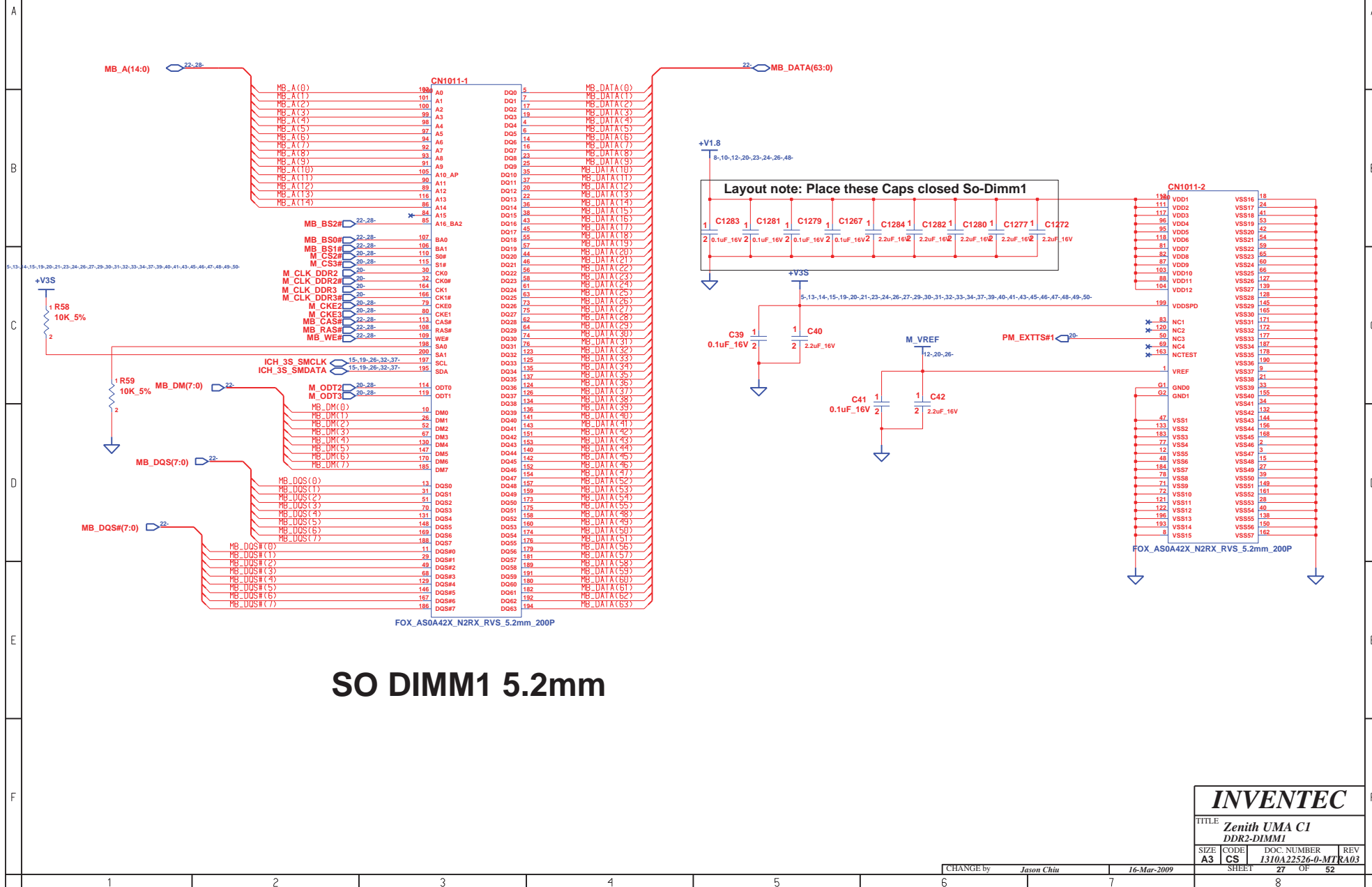
TITLE Zenith UMA C1 DDR2-DIMM-0

SIZE A3 CODE CS DOC. NUMBER 1310A22526-0-MTR A03 REV

CHANGE by Jason Chiu 16-Mar-2009

SHEET 26 OF 52

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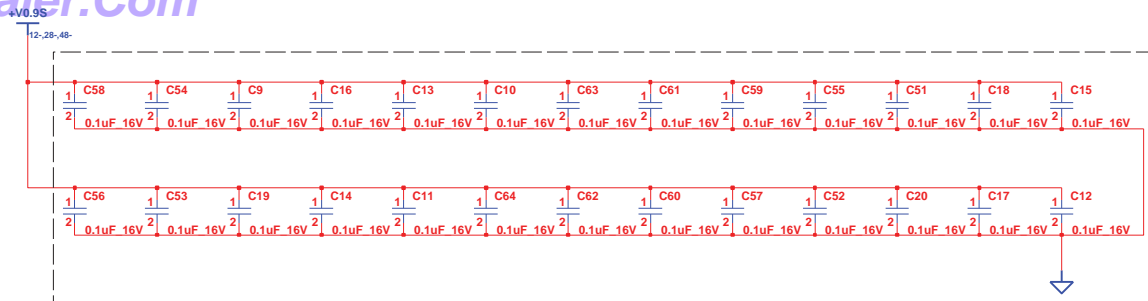
INVENTEC

TITLE Zenith UMA C1 DDR2-DIMM1			
SIZE A3	CODE CS	DOC. NUMBER I310A22526-0-MTR A03	REV
SHEET 27		OF 52	

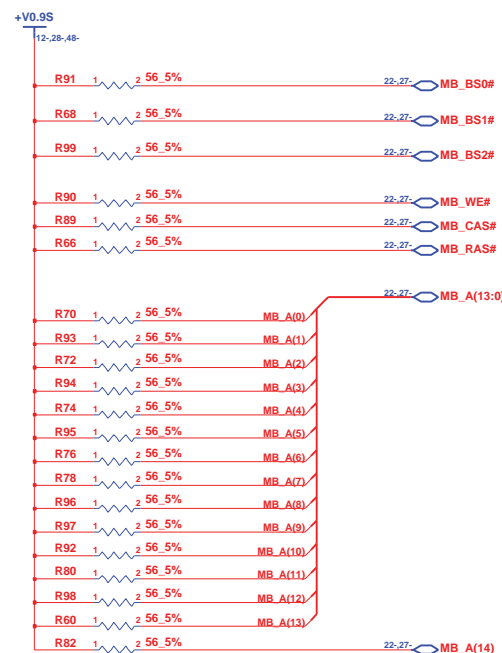
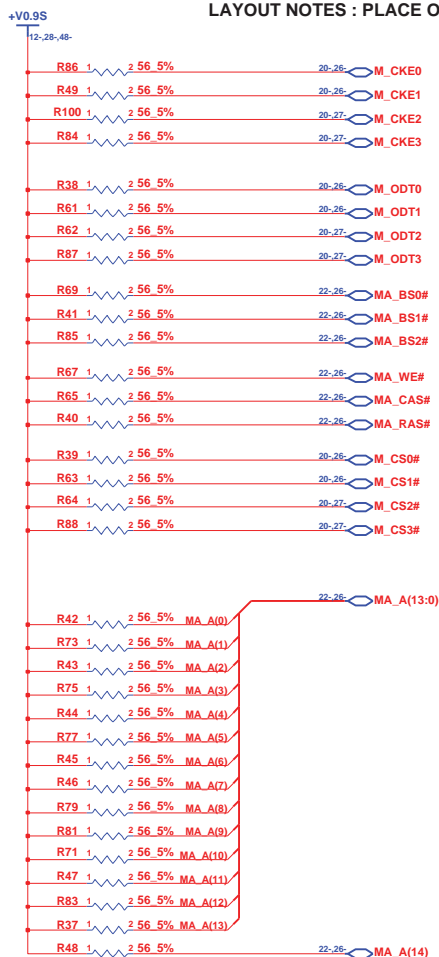
CHANGE by **Jason Chiu**

16-Mar-2009

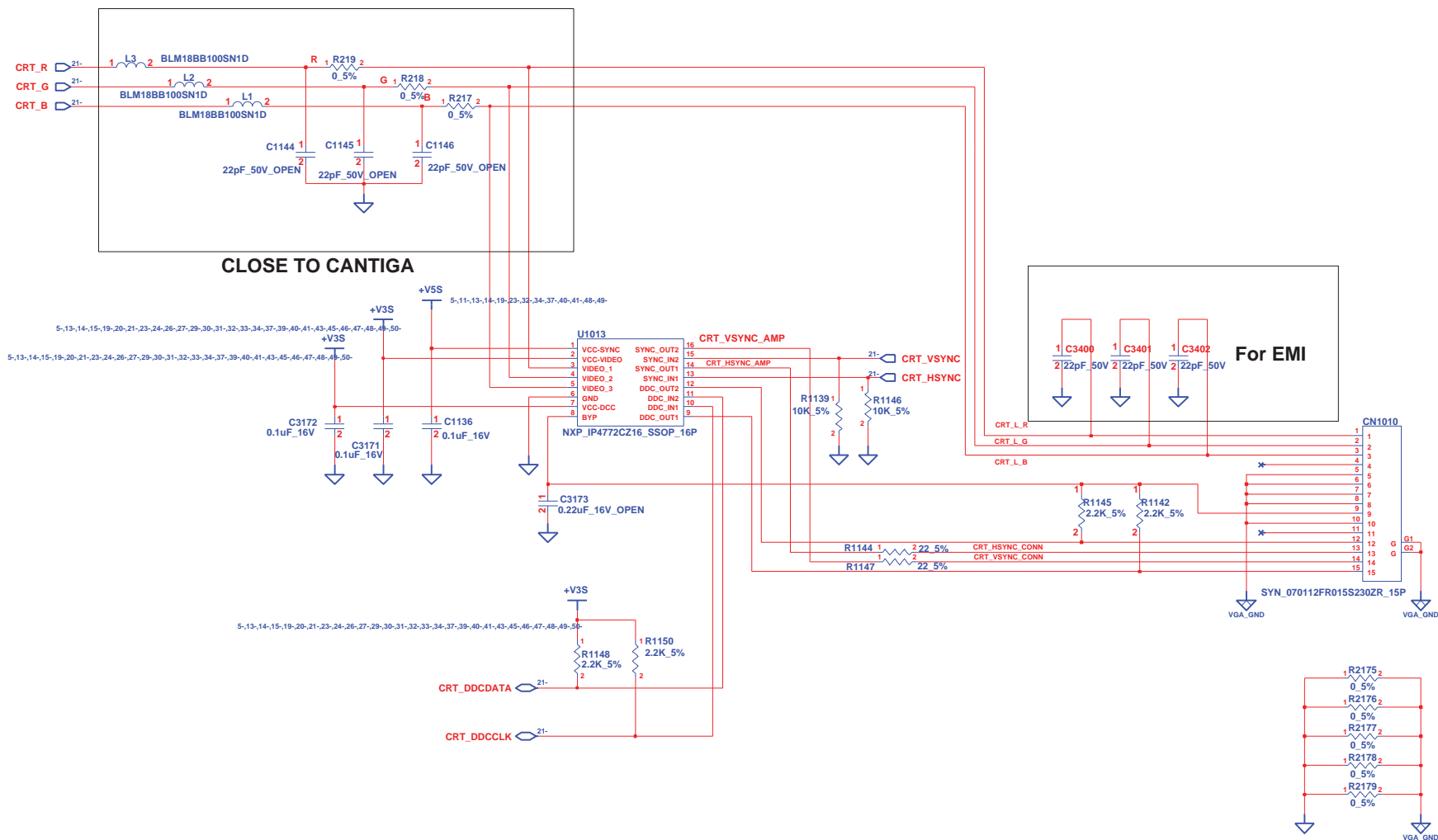
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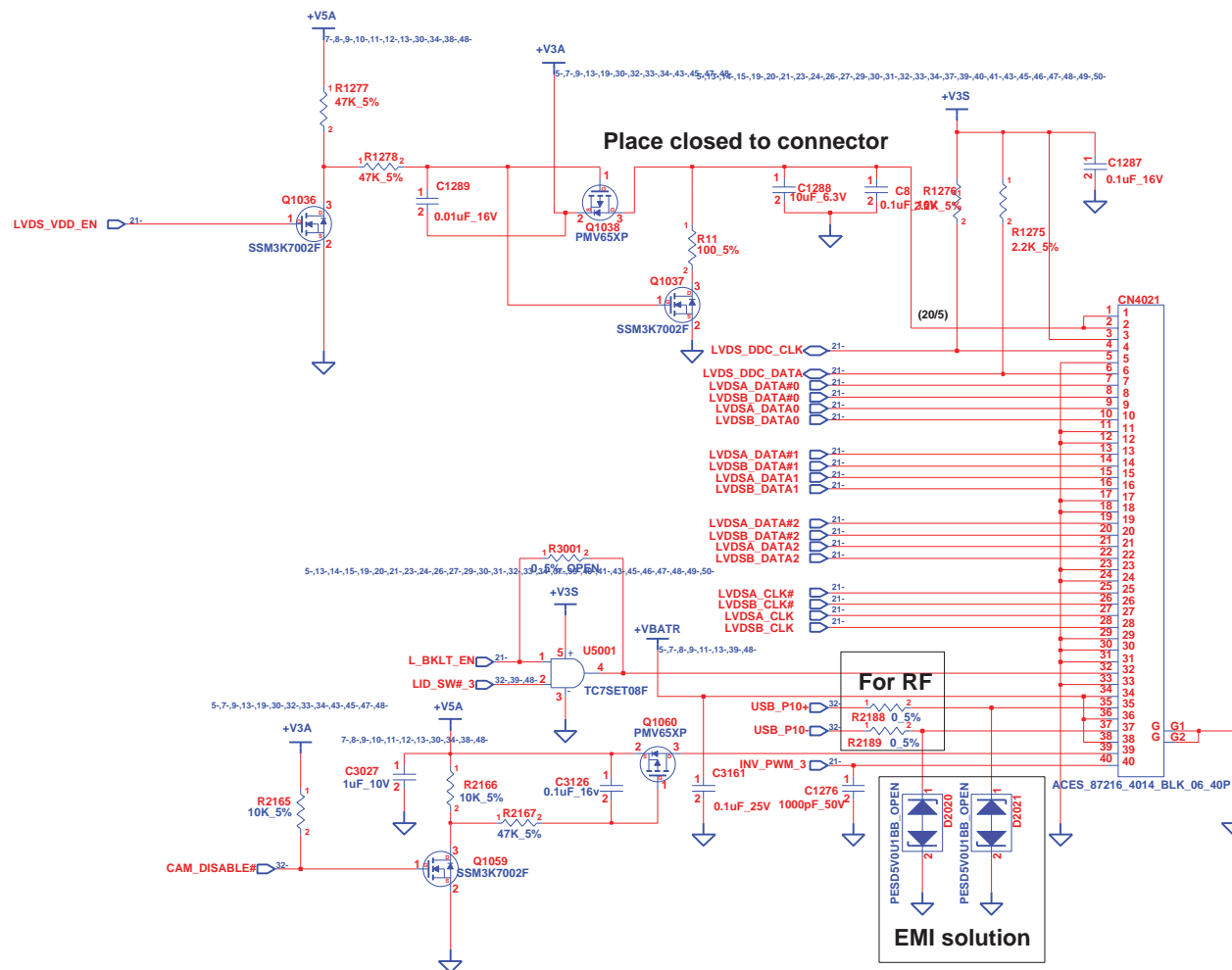


LAYOUT NOTES : PLACE ONE CAP CLOSE TO EVERY 2 PULL UP RESISTOR TERMINATED TO +V0.9S



INVENTEC			
TITLE Zenith UMA C1 DDR2-DAMPING			
SIZE A3	CODE CS	DOC. NUMBER I310A22526-0-MTR	REV A03
CHANGE by Jason Chiu		27-Sep-2003	
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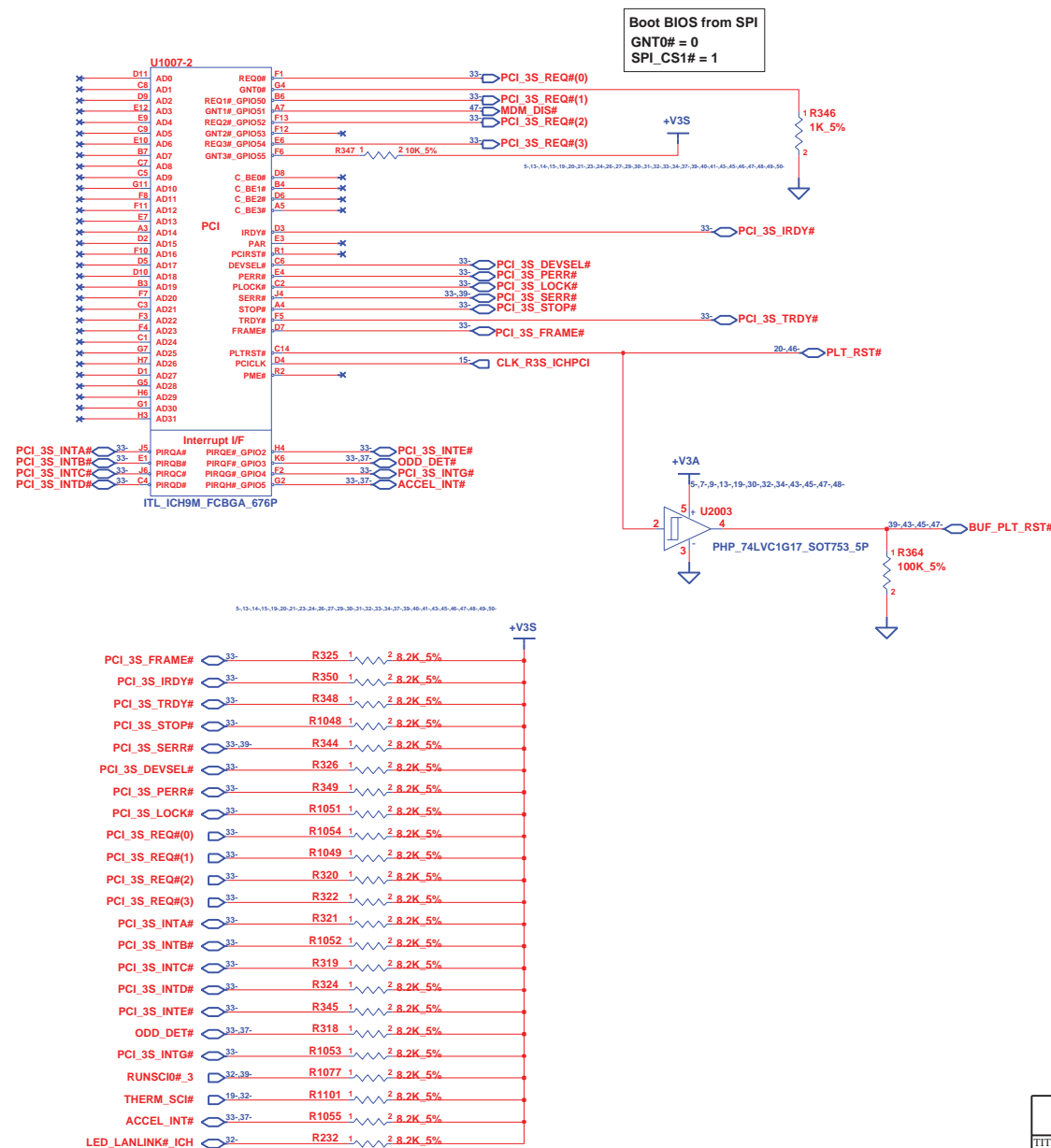
INVENTEC

TITLE
**Zenith UMA C1
LCM CONN**

SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A22526-0-MTR	A03

CHANGE by *Jason Chiu* 16-Mar-2009

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INVENTEC

TITLE
Zenith UMA C1
ICH9-3

SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A22526-0-MTR	A03

CHANGE by Jason Chiu

16-Mar-2009

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U1007-5

AA26	VSS001	VSS107	H5
AA27	VSS002	VSS108	J23
AA3	VSS003	VSS109	J26
AA6	VSS004	VSS110	J27
AB1	VSS005	VSS111	AC22
AA23	VSS006	VSS112	K28
AB26	VSS007	VSS113	K29
AB25	VSS008	VSS114	L13
AB4	VSS009	VSS115	L15
AB5	VSS010	VSS116	L2
AC11	VSS011	VSS117	L26
AC26	VSS012	VSS118	L27
AC27	VSS013	VSS119	L5
AC3	VSS014	VSS120	L7
AD1	VSS015	VSS121	M12
AD10	VSS016	VSS122	M13
AD12	VSS017	VSS123	M14
AD13	VSS018	VSS124	M15
AD14	VSS019	VSS125	M16
AD17	VSS020	VSS126	M17
AD18	VSS021	VSS127	M23
AD21	VSS022	VSS128	M28
AD26	VSS023	VSS129	M29
AD29	VSS024	VSS130	N11
AD4	VSS025	VSS131	N12
AD6	VSS026	VSS132	N13
AD6	VSS027	VSS133	N14
AD7	VSS028	VSS134	N15
AD9	VSS029	VSS135	N16
AE13	VSS030	VSS136	N17
AE13	VSS031	VSS137	N18
AE14	VSS032	VSS138	N26
AE16	VSS033	VSS139	N27
AE17	VSS034	VSS140	P12
AE3	VSS035	VSS141	P13
AE20	VSS036	VSS142	P14
AE24	VSS037	VSS143	P15
AE3	VSS038	VSS144	P16
AE4	VSS039	VSS145	P17
AE6	VSS040	VSS146	P2
AE9	VSS041	VSS147	P23
AF13	VSS042	VSS148	P28
AF16	VSS043	VSS149	P29
AF18	VSS044	VSS150	P4
AF22	VSS045	VSS151	P7
AH26	VSS046	VSS152	R11
AF28	VSS047	VSS153	R12
AF27	VSS048	VSS154	R13
AF3	VSS049	VSS155	R14
AF7	VSS050	VSS156	R15
AF9	VSS051	VSS157	R16
AG13	VSS052	VSS158	R17
AG16	VSS053	VSS159	R18
AG16	VSS054	VSS160	R28
AG20	VSS055	VSS161	T12
AG23	VSS056	VSS162	T13
AG3	VSS057	VSS163	T14
AG5	VSS058	VSS164	T15
AG9	VSS059	VSS165	T16
AH12	VSS060	VSS166	T17
AH14	VSS061	VSS167	T23
AH17	VSS062	VSS168	B26
AH19	VSS063	VSS169	U12
AH2	VSS064	VSS170	U13
AH22	VSS065	VSS171	U14
AH25	VSS066	VSS172	U15
AH28	VSS067	VSS173	U16
AH2	VSS068	VSS174	U17
AH6	VSS069	VSS175	AD23
AJ12	VSS070	VSS176	U26
AJ14	VSS071	VSS177	U27
AJ17	VSS072	VSS178	U3
AJ8	VSS073	VSS179	V1
B11	VSS074	VSS180	V13
B14	VSS075	VSS181	V15
B17	VSS076	VSS182	V23
B3	VSS077	VSS183	V28
B20	VSS078	VSS184	V29
B23	VSS079	VSS185	V4
B5	VSS080	VSS186	V5
BB	VSS081	VSS187	W26
C26	VSS082	VSS188	W27
C27	VSS083	VSS189	W3
E11	VSS084	VSS190	Y1
E14	VSS085	VSS191	Y28
E18	VSS086	VSS192	Y29
E2	VSS087	VSS193	Y4
E21	VSS088	VSS194	Y5
E21	VSS089	VSS195	AG28
E5	VSS090	VSS196	AH6
EA	VSS091	VSS197	AF2
F16	VSS092	VSS198	B25
F29	VSS093		
F29	VSS094	VSS_NCTF1	A1
G12	VSS095	VSS_NCTF2	A2
G14	VSS096	VSS_NCTF3	A28
G18	VSS097	VSS_NCTF4	A29
G21	VSS098	VSS_NCTF5	AH1
G24	VSS099	VSS_NCTF6	AH29
G25	VSS100	VSS_NCTF7	AJ1
G27	VSS101	VSS_NCTF8	AJ2
H2	VSS102	VSS_NCTF9	AJ28
H2	VSS103	VSS_NCTF10	AJ29
H23	VSS104	VSS_NCTF11	B1
H28	VSS105	VSS_NCTF12	B29
H28	VSS106		

ITL_ICH9M_FCBGA_676P

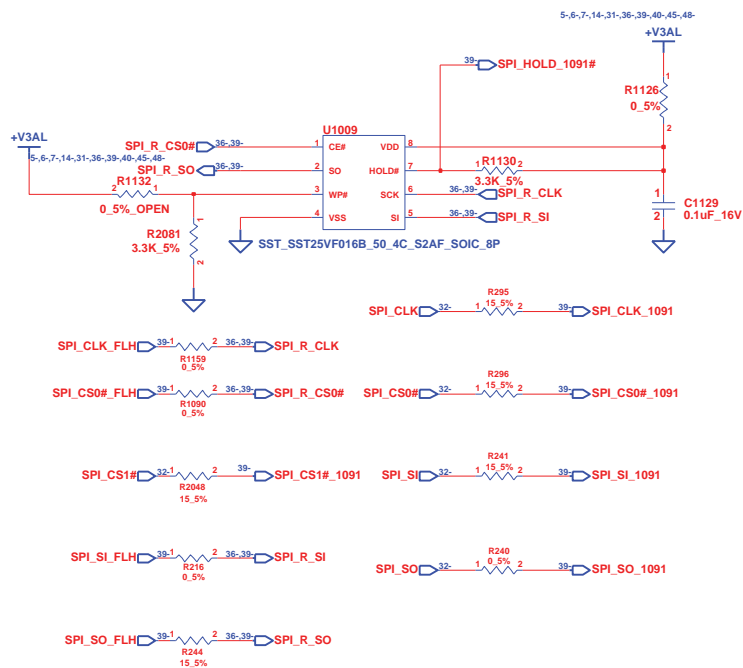
50- ICHGND1
50- ICHGND4
50- ICHGND7
50- ICHGND10

INVENTEC

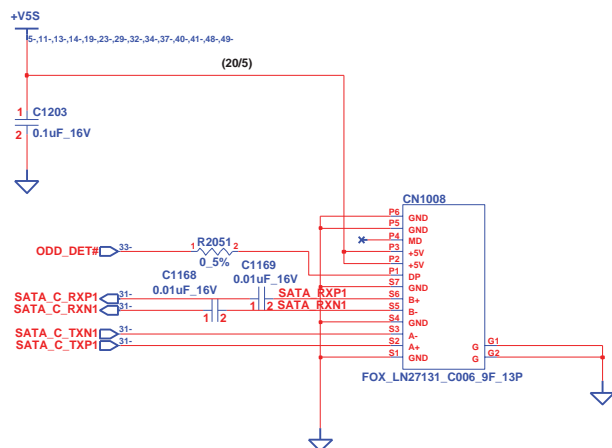
TITLE
Zenith UMA C1
ICH9-5

SIZE A3	CODE CS	DOC. NUMBER 1310A22526-0-MTR	REV A03
SHEET 35		OF 52	

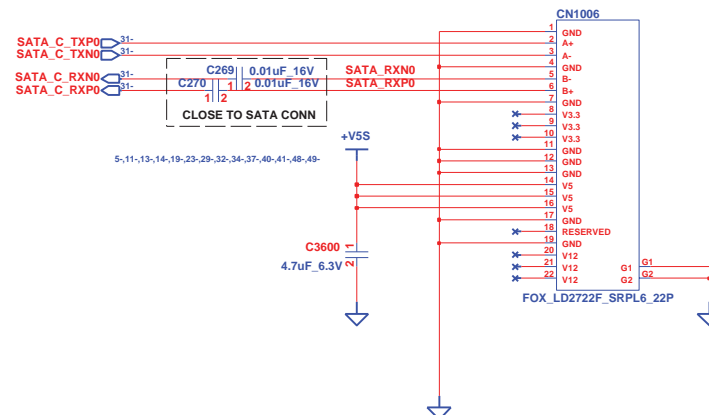
CHANGE by Jason Chiu 16-Mar-2009



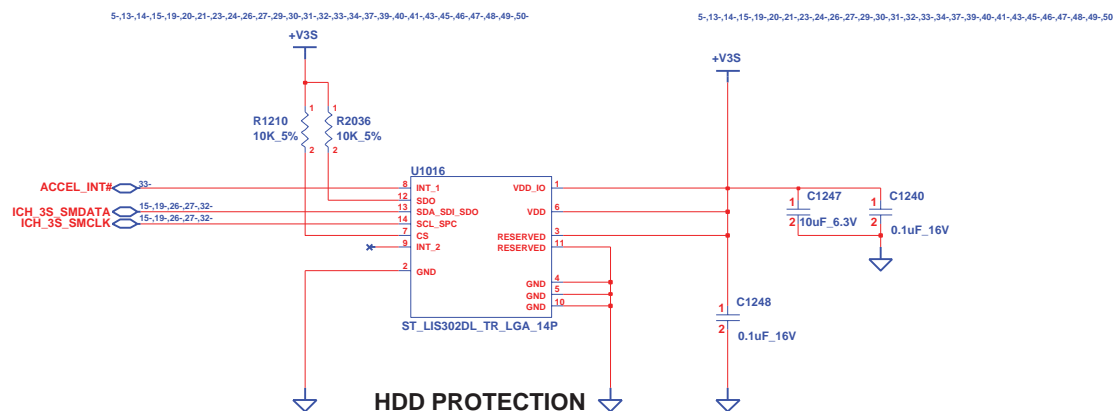
INVENTEC			
TITLE Zenith UMA C1 SYSTEM BIOS			
SIZE A3	CODE CS	DOC. NUMBER 1310A22526-0-MTR	REV A03
CHANGE by Jason Chiu		16-Mar-2009	
SHEET 36		OF 52	



SATA ODD CNTR

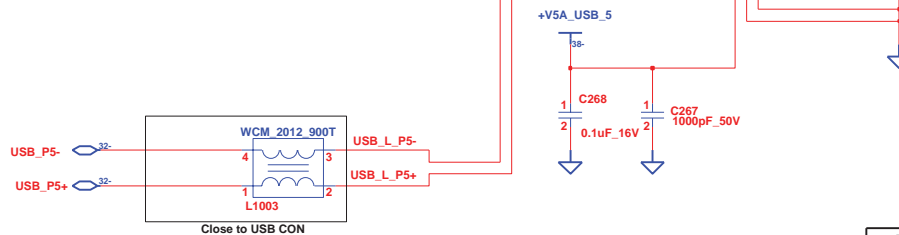
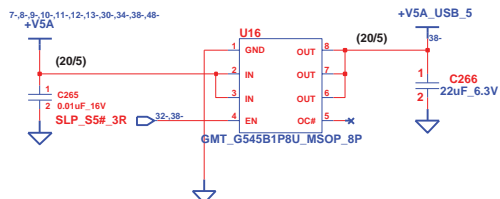
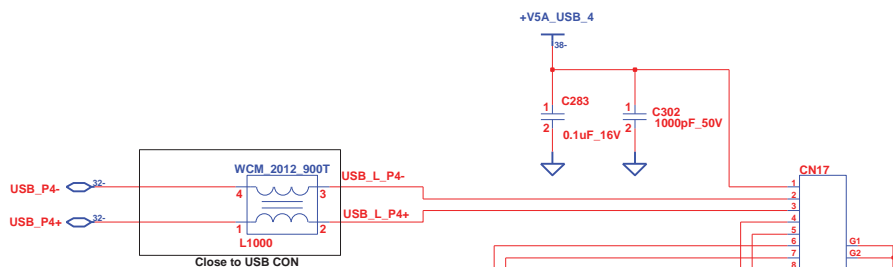
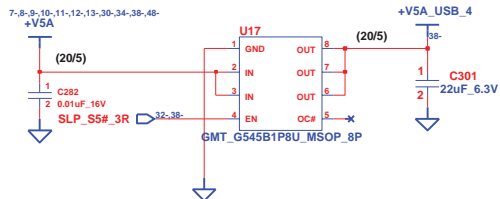
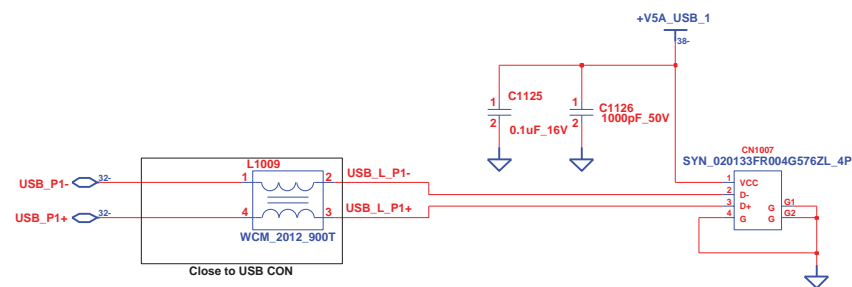
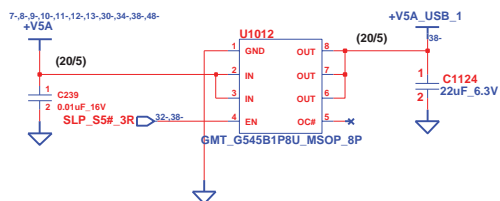
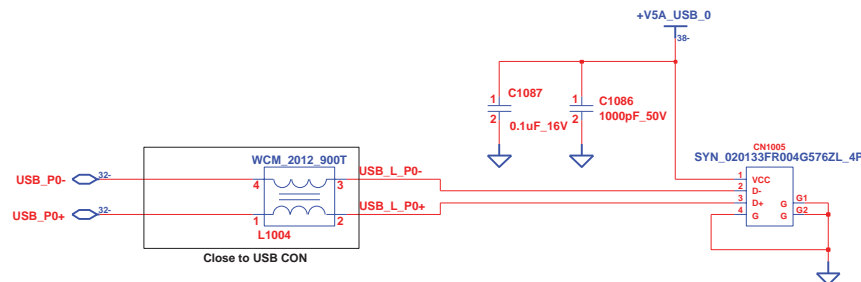
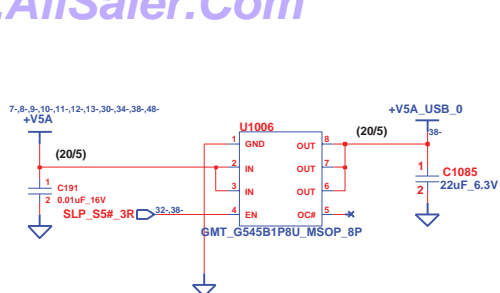


SATA HDD CNTR



HDD PROTECTION

INVENTEC			
TITLE: Zenith UMA C1			
HDD & ODD CONN & HD Protec			
SIZE: A3	CODE: CS	DOC. NUMBER: 1310A22526-0-MTR	REV: RA03
CHANGE by: Jason Chiu		16-Mar-2009	SHEET 37 OF 52



INVENTEC

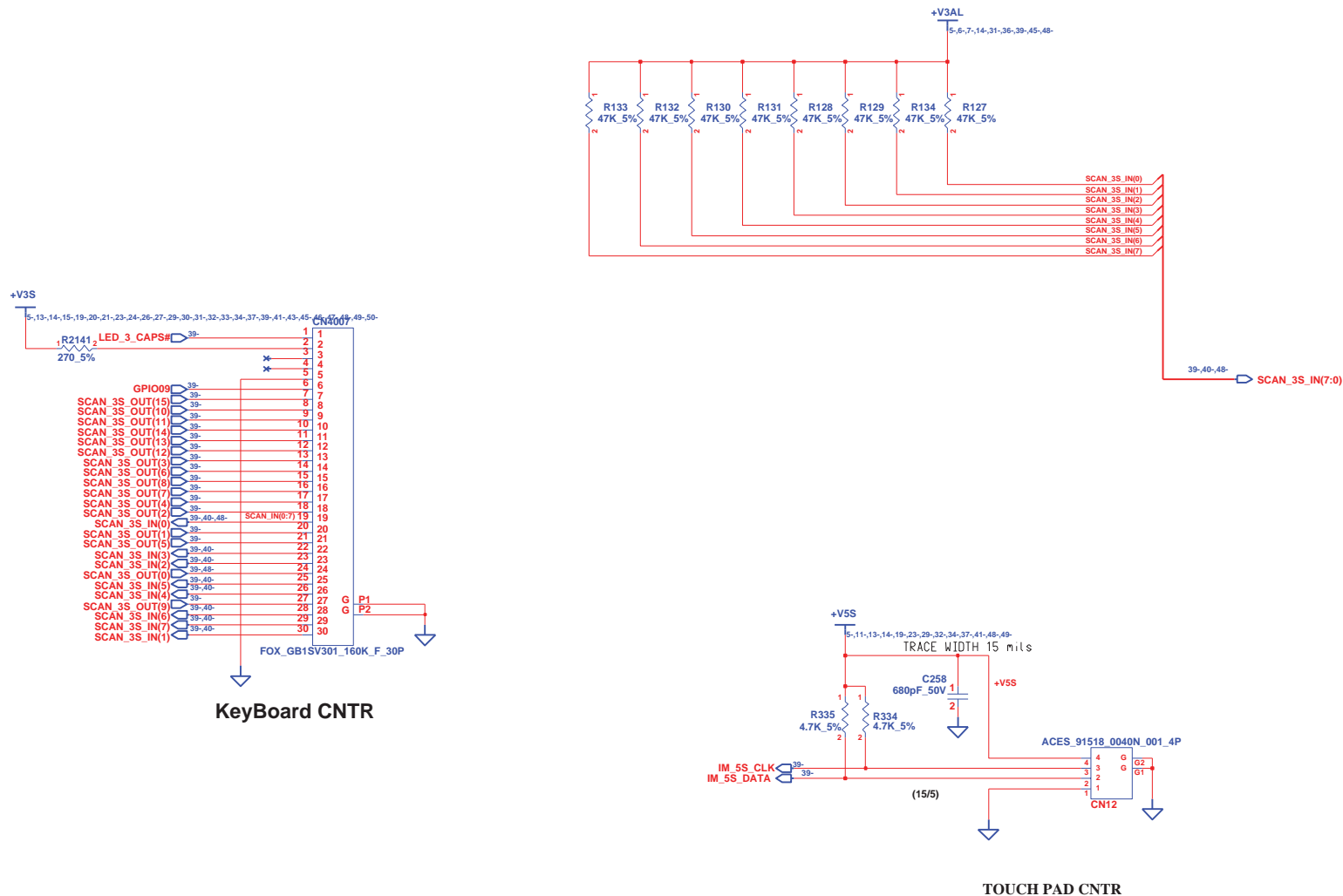
TITLE
**Zenith UMA C1
USB CONN**

SIZE CODE DOC. NUMBER REV
A3 CS 1310A22526-0-MTR A03

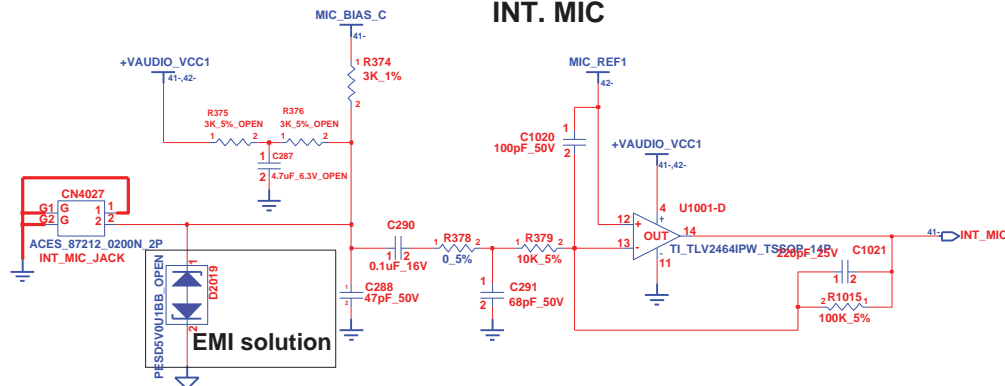
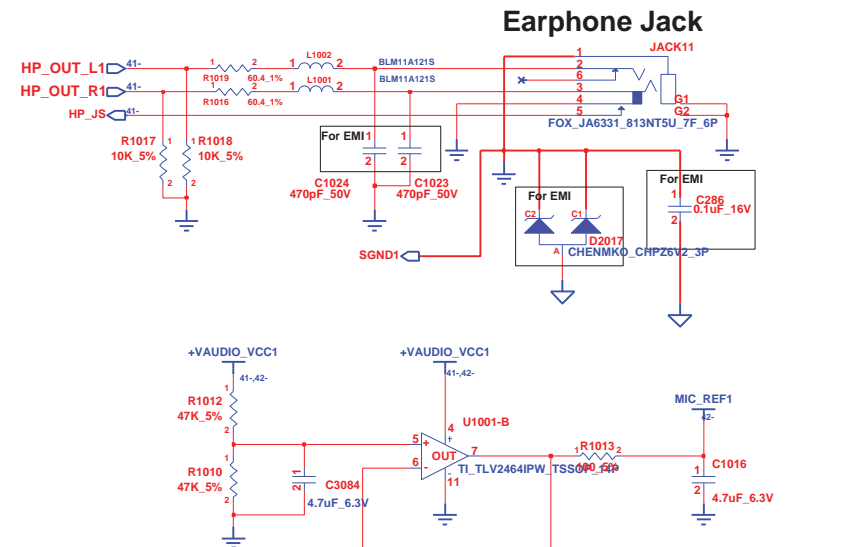
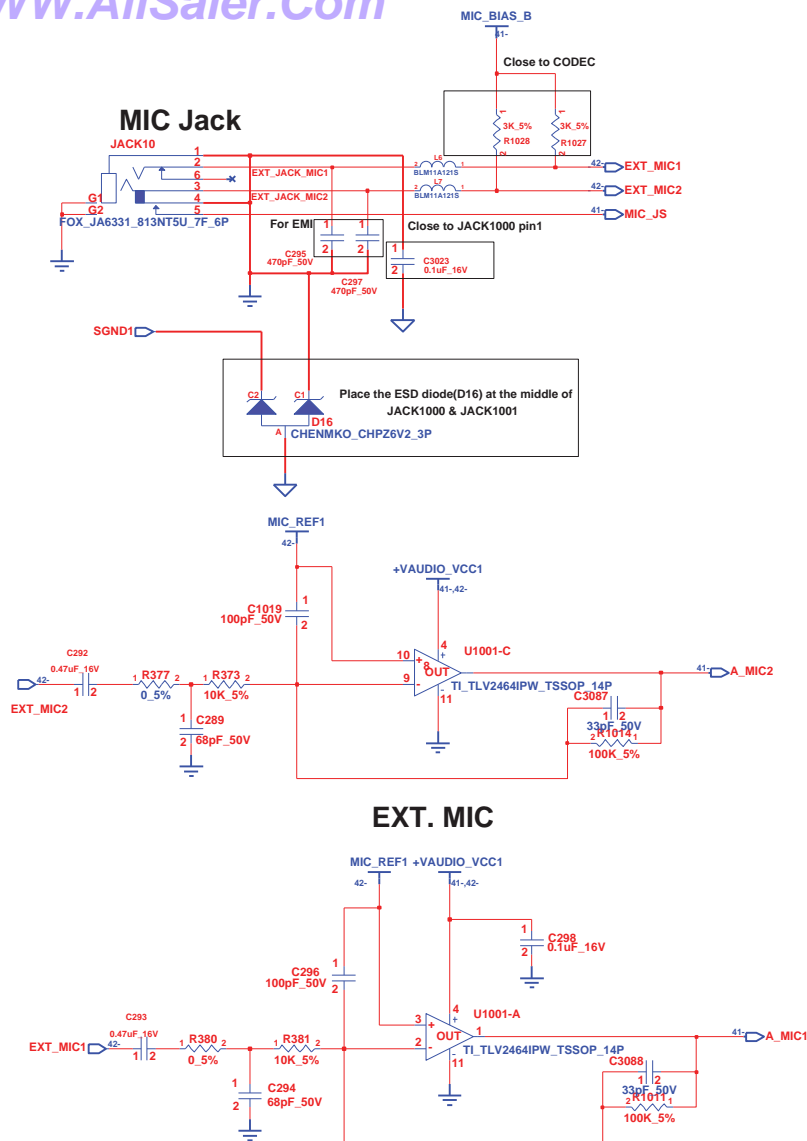
CHANGE by **Jason Chiu**

16-Mar-2009

SHEET **38** OF **52**



INVENTEC			
TITLE Zenith UMA C1 KB & TP CONN			
SIZE A3	CODE CS	DOC NUMBER 1310A22526-0-MTR	REV A03
CHANGE by Jason Chiu		16-Mar-2009	
SHEET 40		OF 52	



INVENTEC

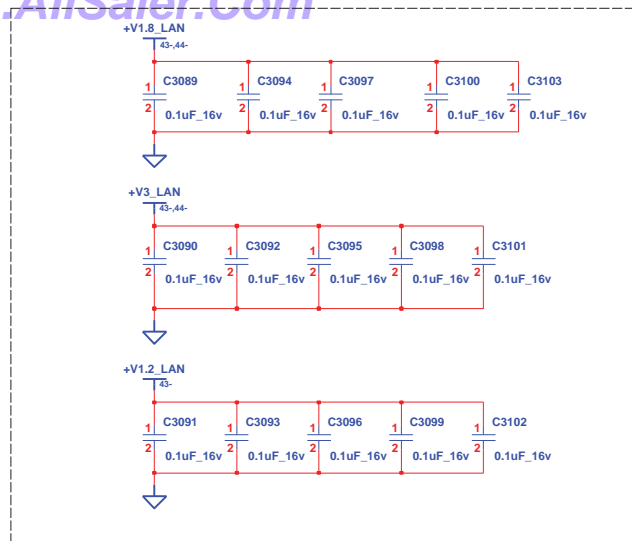
TITLE
**Zenith UMA C1
AUDIO JACK**

SIZE CODE DOC. NUMBER REV
A3 CS 1310A22526-0-MTR A03

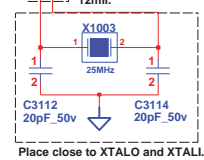
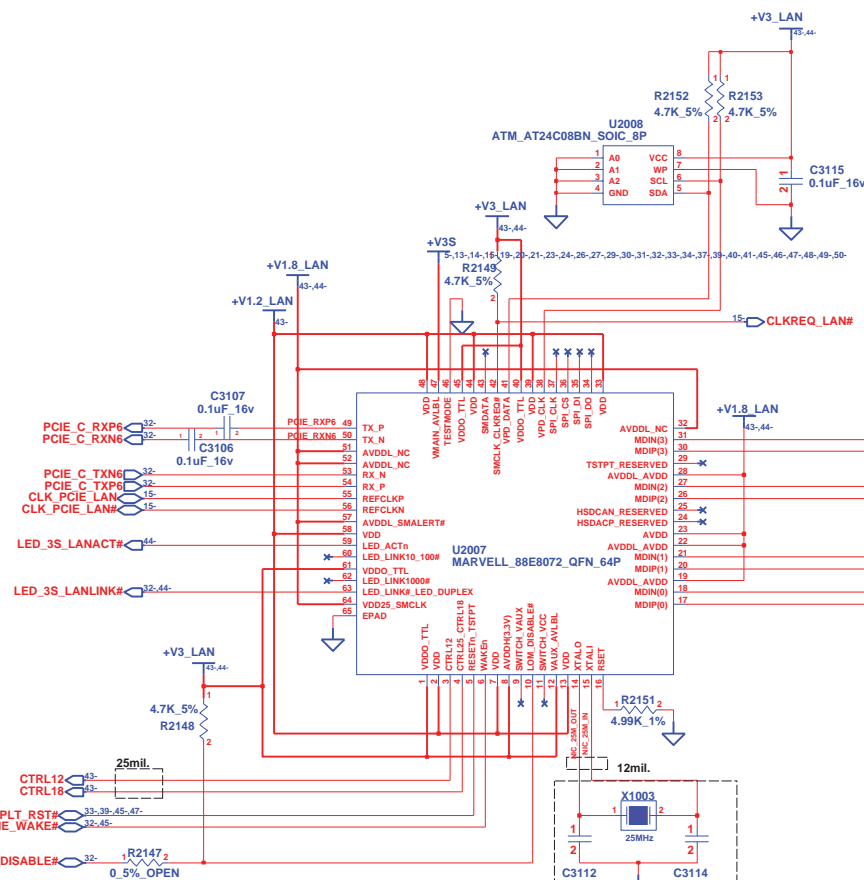
CHANGE by Jason Chiu

16-Mar-2009

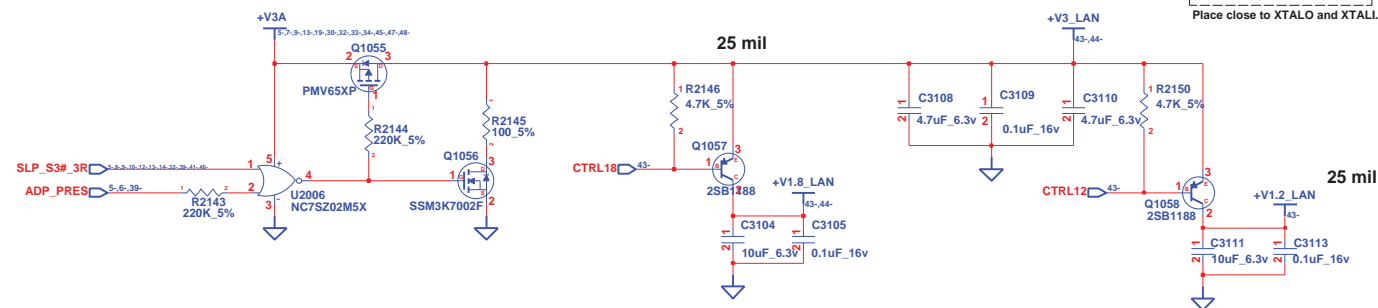
SHEET 42 OF 52



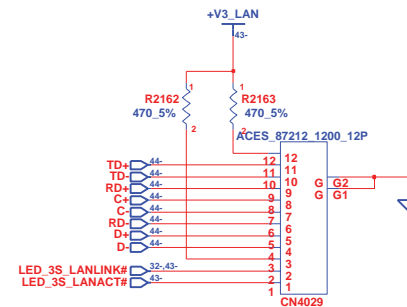
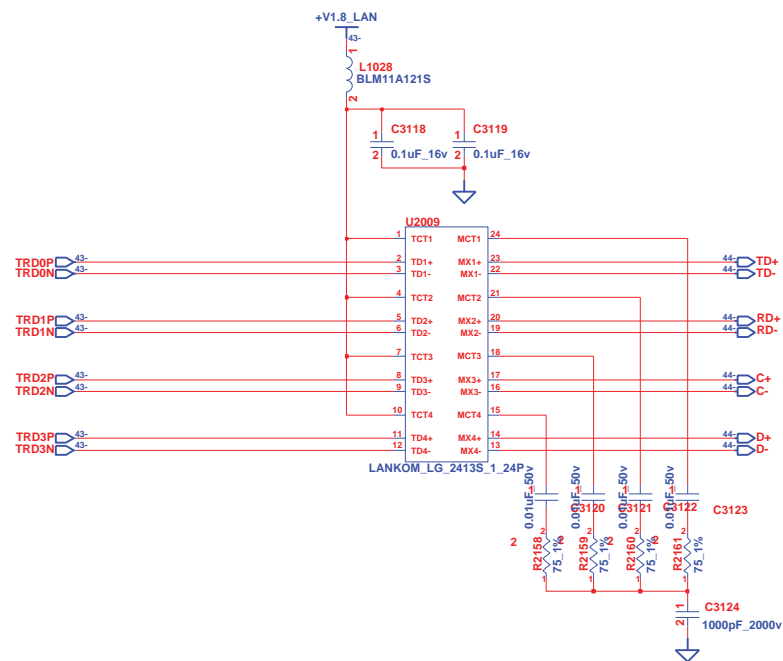
Place bypass cap as close as possible with every power pin.



Place close to XTALO and XTALI.



INVENTEC			
TITLE Zenith UMA C1 Giga LAN			
SIZE A3	CODE CS	DOC. NUMBER 1310A22526-0-MTR	REV A03
CHANGE by Jason Chiu		16-Mar-2009	
SHEET 43		OF 52	



INVENTEC

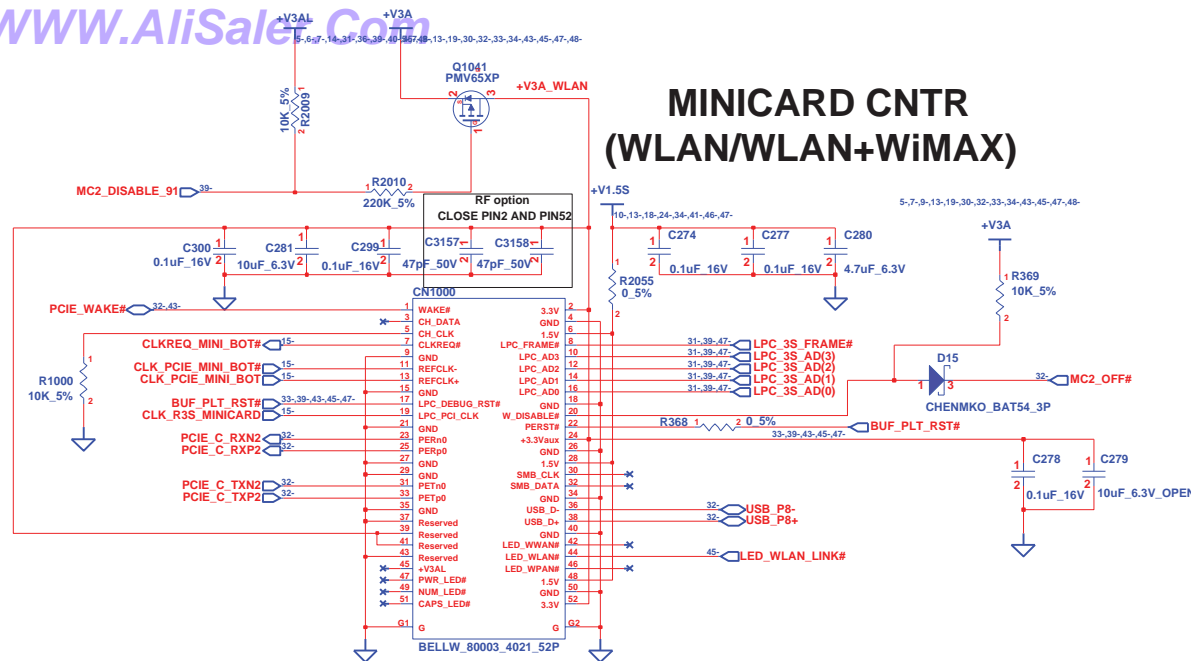
TITLE
Zenith UMA C1
RJ-45 CONN

SIZE A3	CODE CS	DOC. NUMBER 1310A22526-0-MTR	REV A03
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CHANGE by **Jason Chiu** 16-Mar-2009

SHEET **44** OF **52**

MINICARD CNTR (WLAN/WLAN+WiMAX)

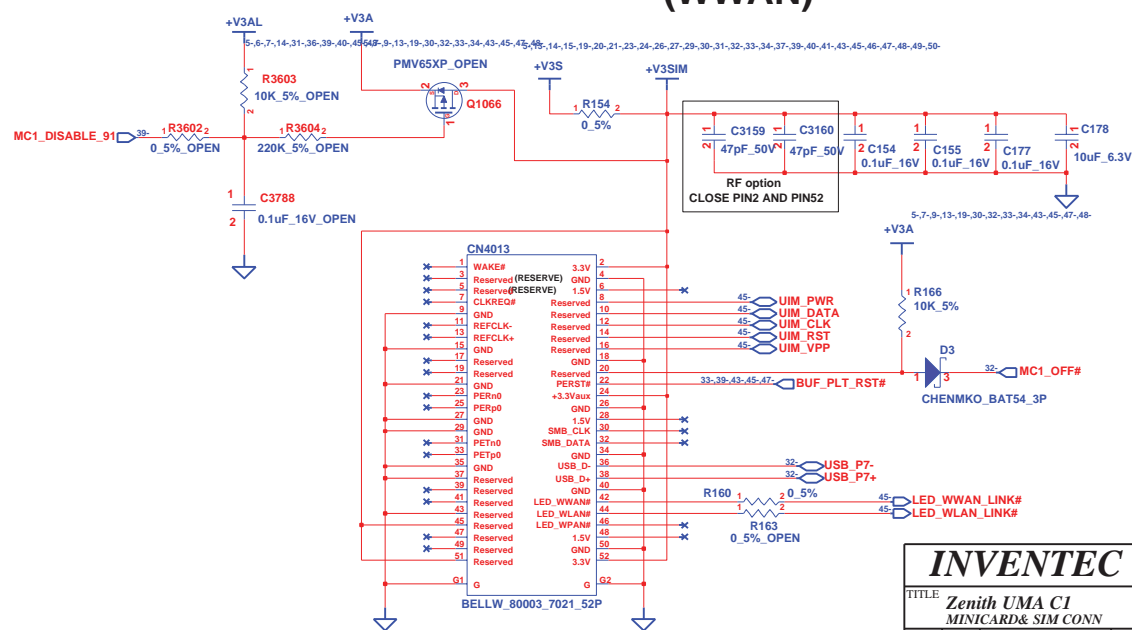


5,13,14,15,19,20,21,23,24,26,27,29,30,31,32,33,34,37,39,40,41,43,45,46,47,48,49,50-

5,13,14,15,19,20,21,23,24,26,27,29,30,31,32,33,34,37,39,40,41,43,45,46,47,48,49,50-

WLAN_Bluetooth LED

MINICARD CNTR (WWAN)



INVENTEC

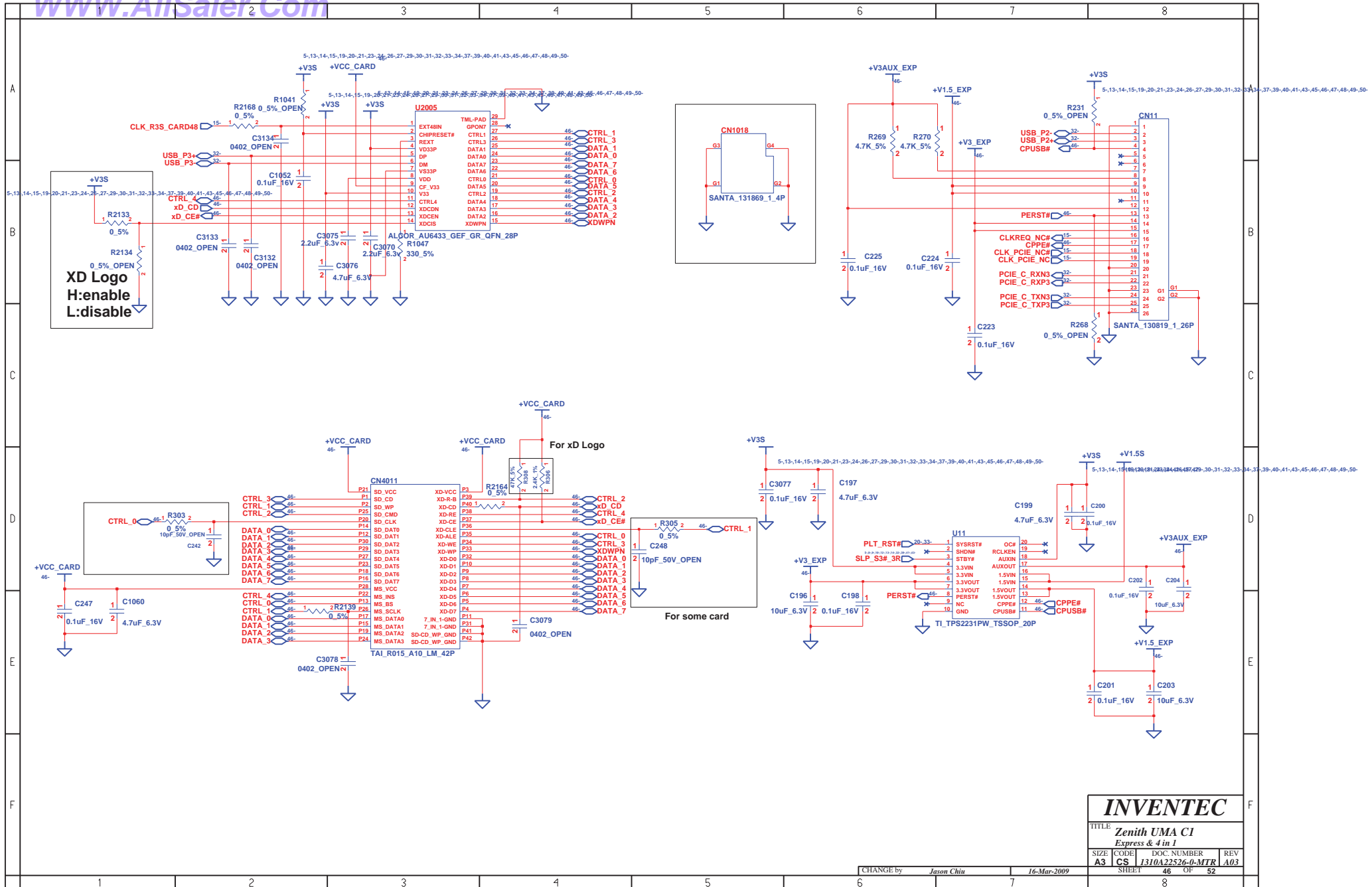
TITLE
Zenith UMA C1
MINICARD& SIM CONN

SIZE CODE DOC NUMBER REV
A3 CS 1310A22526-0-MTR A03

CHANGE by Jason Chiu

16-Mar-2009

SHEET 45 OF 52



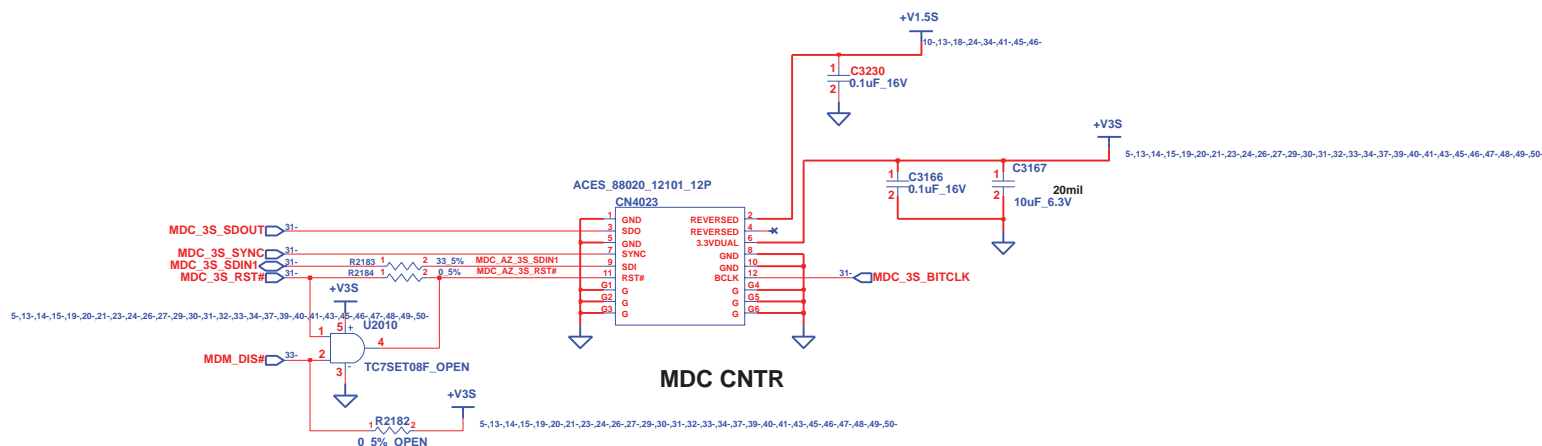
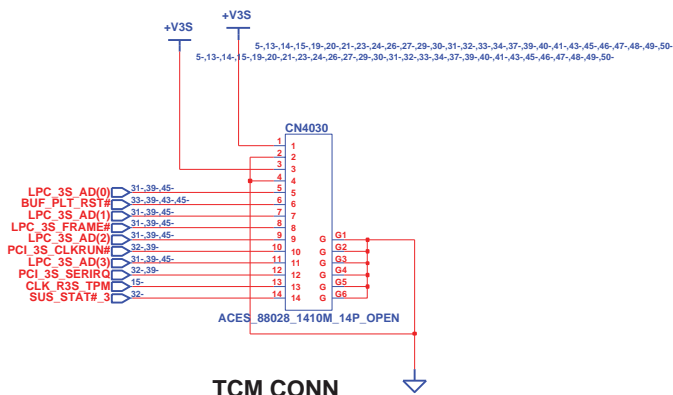
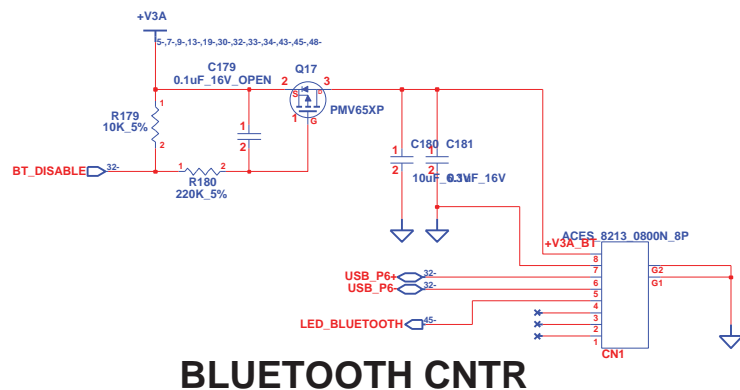
INVENTEC

Zenith UMA C1

Express & 4 in 1

SIZE: CODE: DOC. NUMBER: REV: A3 CS I310A22526-0-MTR A03

CHANGE by: Jason Chiu 16-Mar-2009 SHEET 46 OF 52



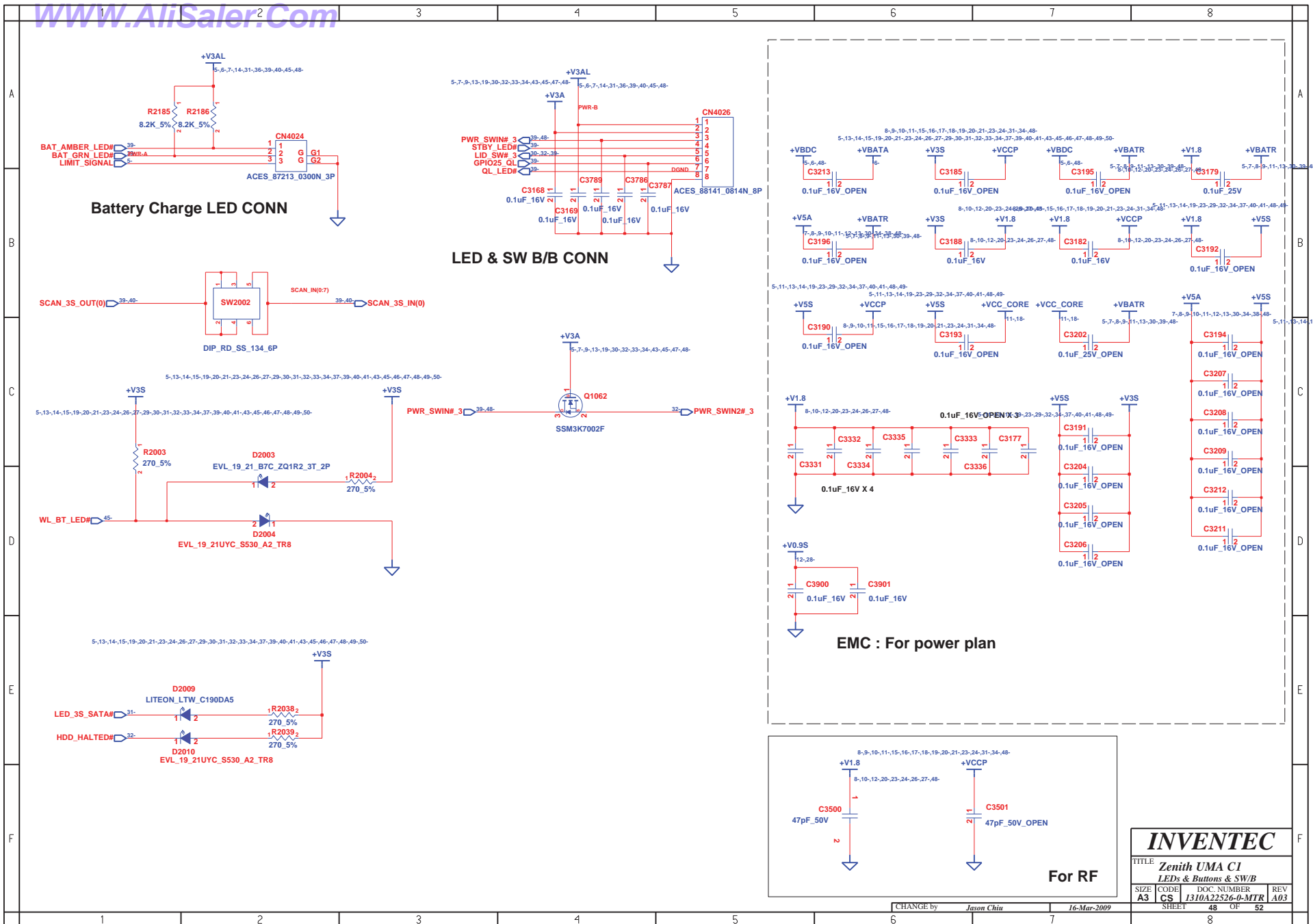
INVENTEC

TITLE
**Zenith UMA C1
BT & MDC & TCM**

SIZE CODE DOC NUMBER REV
A3 CS 1310A22526-0-MTR A03

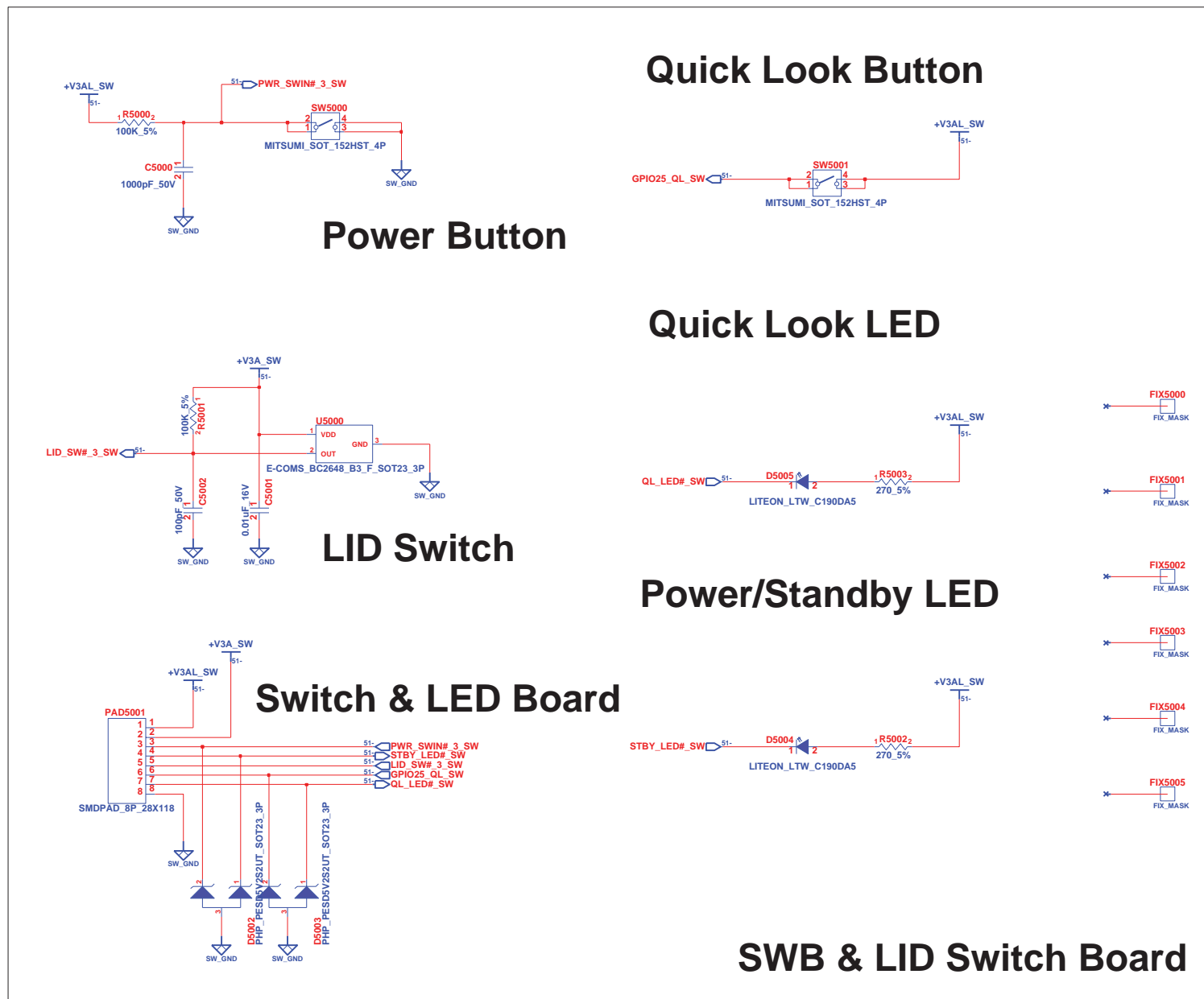
CHANGE by Jason Chiu 16-Mar-2009

SHEET 47 OF 52



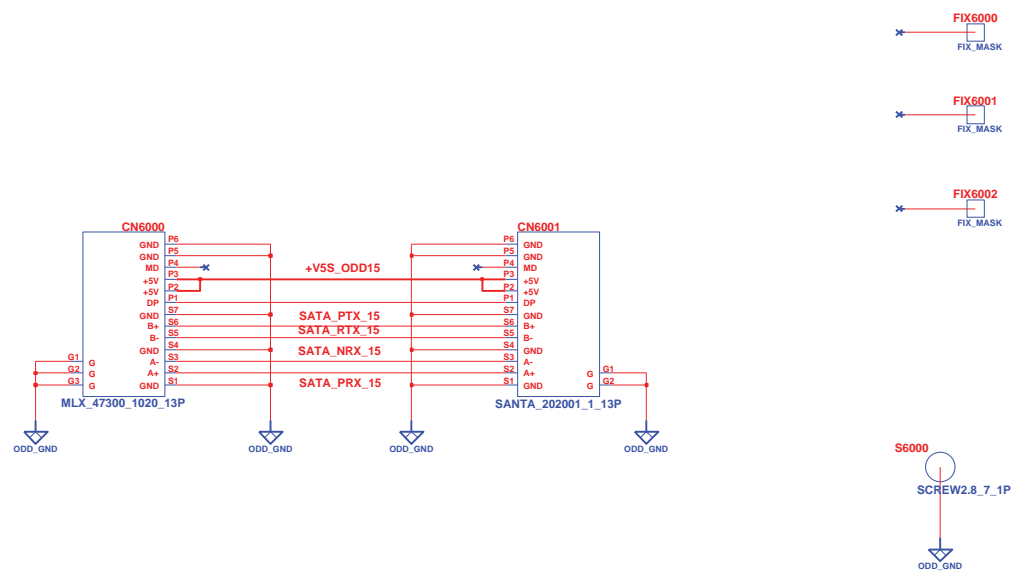
INVENTEC

TITLE Zenith UMA C1			
LEDs & Buttons & SW/B			
SIZE A3	CODE CS	DOC NUMBER I310A22526-0-MTR	REV A03
SHEET 48		OF 52	



- FIX5000
FIX_MASK
- FIX5001
FIX_MASK
- FIX5002
FIX_MASK
- FIX5003
FIX_MASK
- FIX5004
FIX_MASK
- FIX5005
FIX_MASK

INVENTEC			
TITLE Zenith UMA C1 SWB & LID Switch			
SIZE A3	CODE CS	DOC. NUMBER 1310A22526-0-MTR	REV A03
CHANGE by Jason Chiu		16-Mar-2009	
SHEET		51	OF 52



15" ODD Extend Board

INVENTEC			
TITLE Zenith UMA C1 ODD Extend Board			
SIZE A3	CODE CS	DOC. NUMBER 1310A22526-0-MTR	REV A03
CHANGE by Jason Chiu		16-Mar-2009	
SHEET		52	OF 52