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A	<div>WWW.AliSaler.Com</div>								A																																																								
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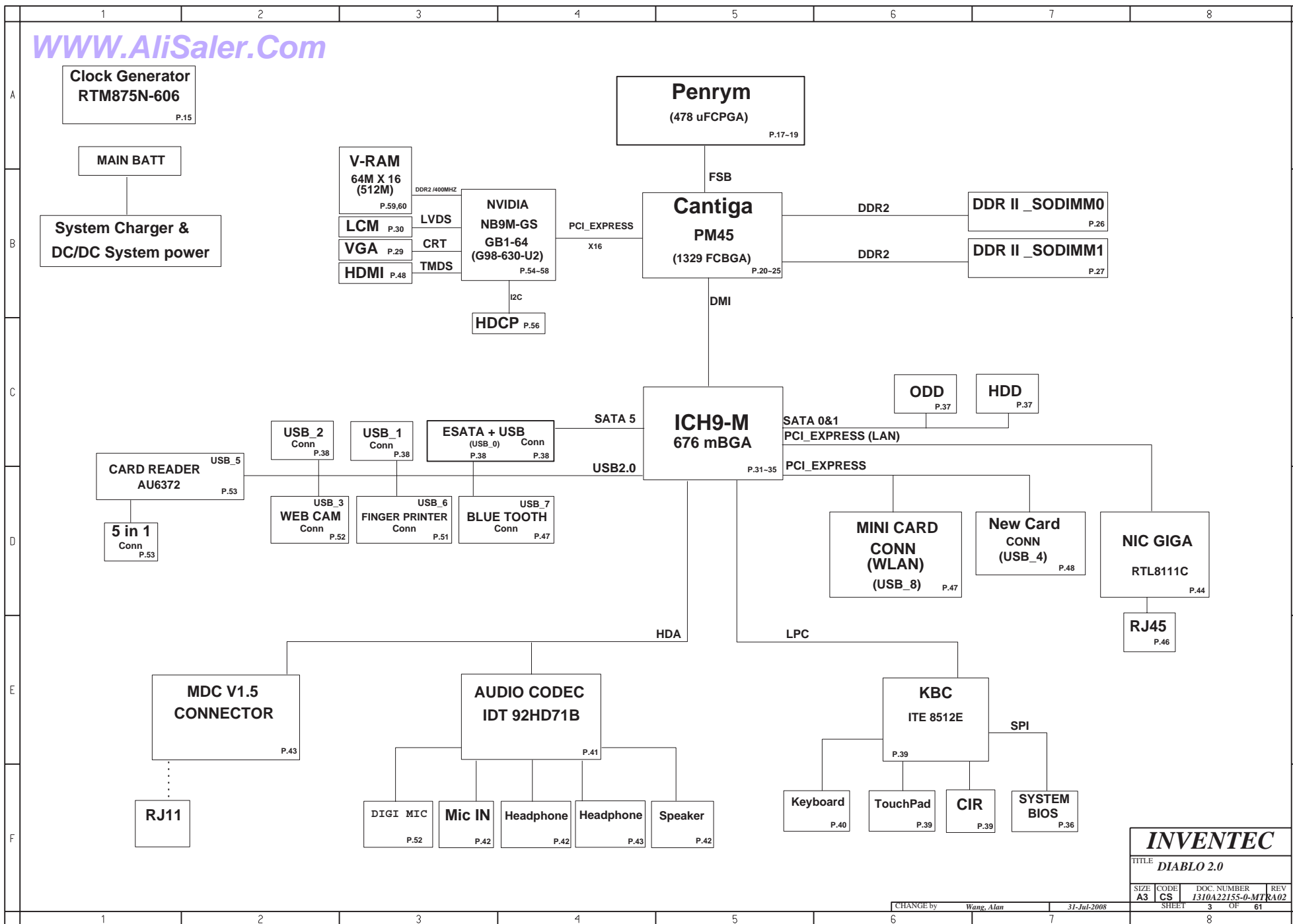
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INVENTEC

TITLE
DIABLO 2.0

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CHANGE by Wang, Alan 31-Jul-2008



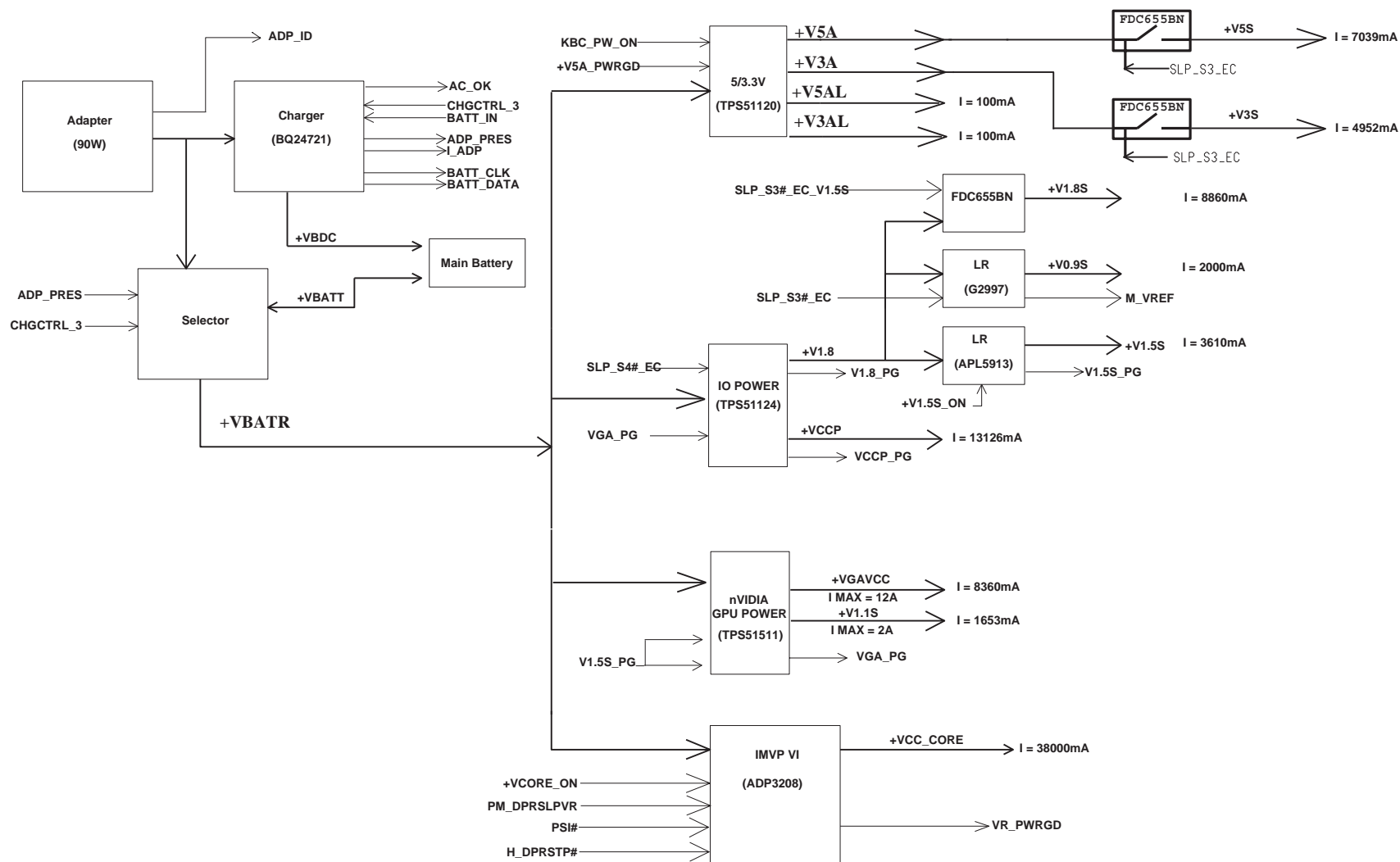
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DIABLO 2.0

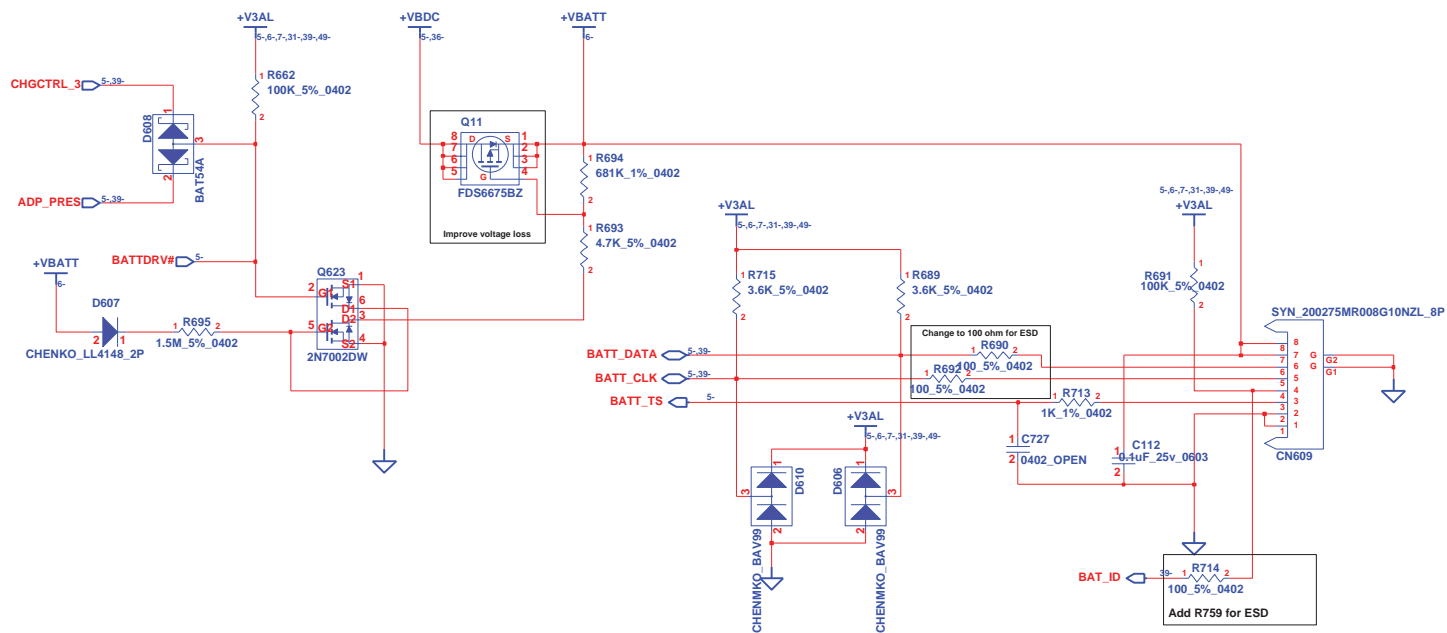
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A3	CS	1310A22155-0-MTR	A02

CHANGE by Wang, Alan 31-Jul-2008

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TITLE			
DIABLO 2.0			
DC & BATTERY CHARGER			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A22155-0-MTR	A02
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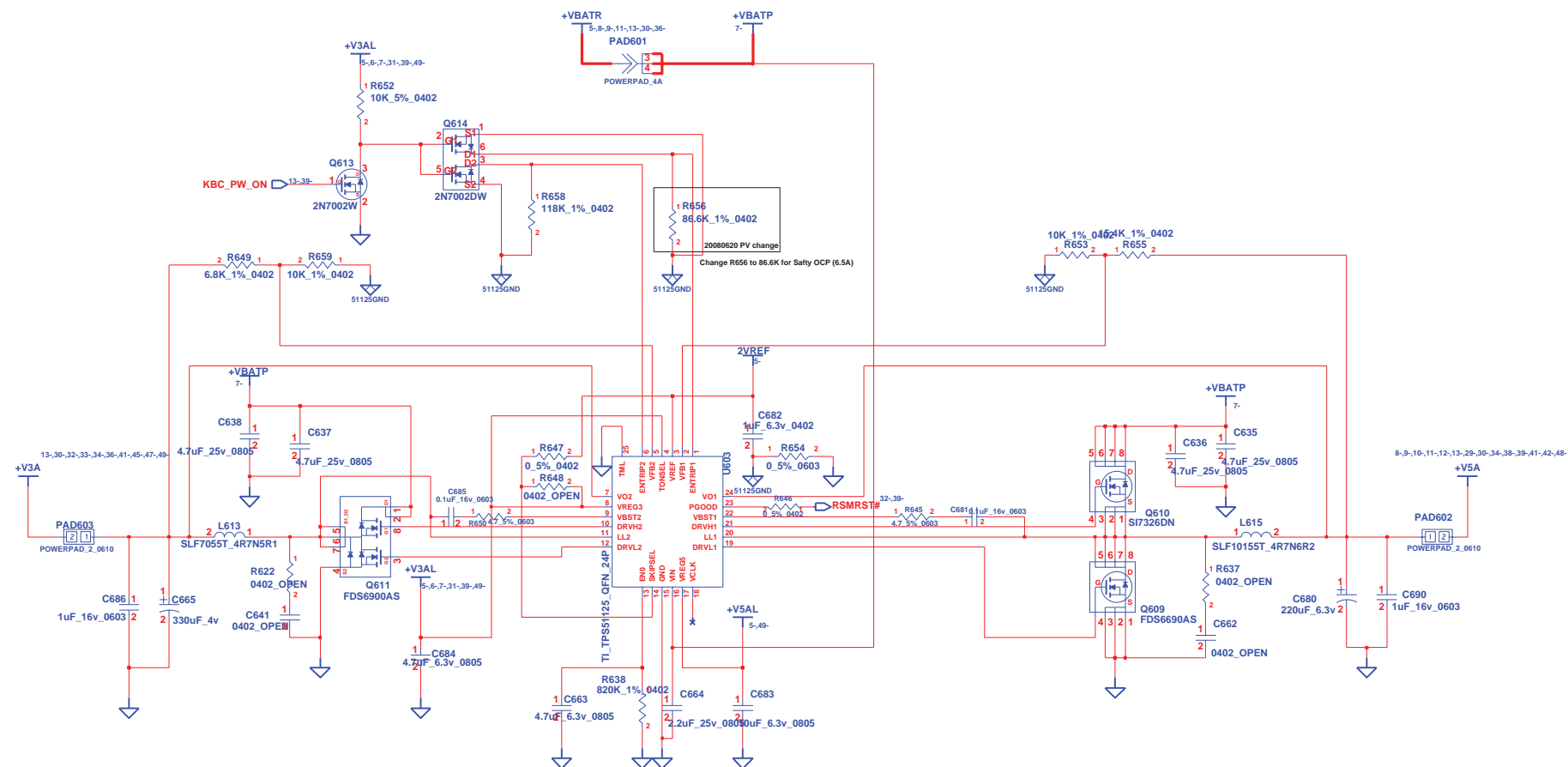
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BATTERY CONN**

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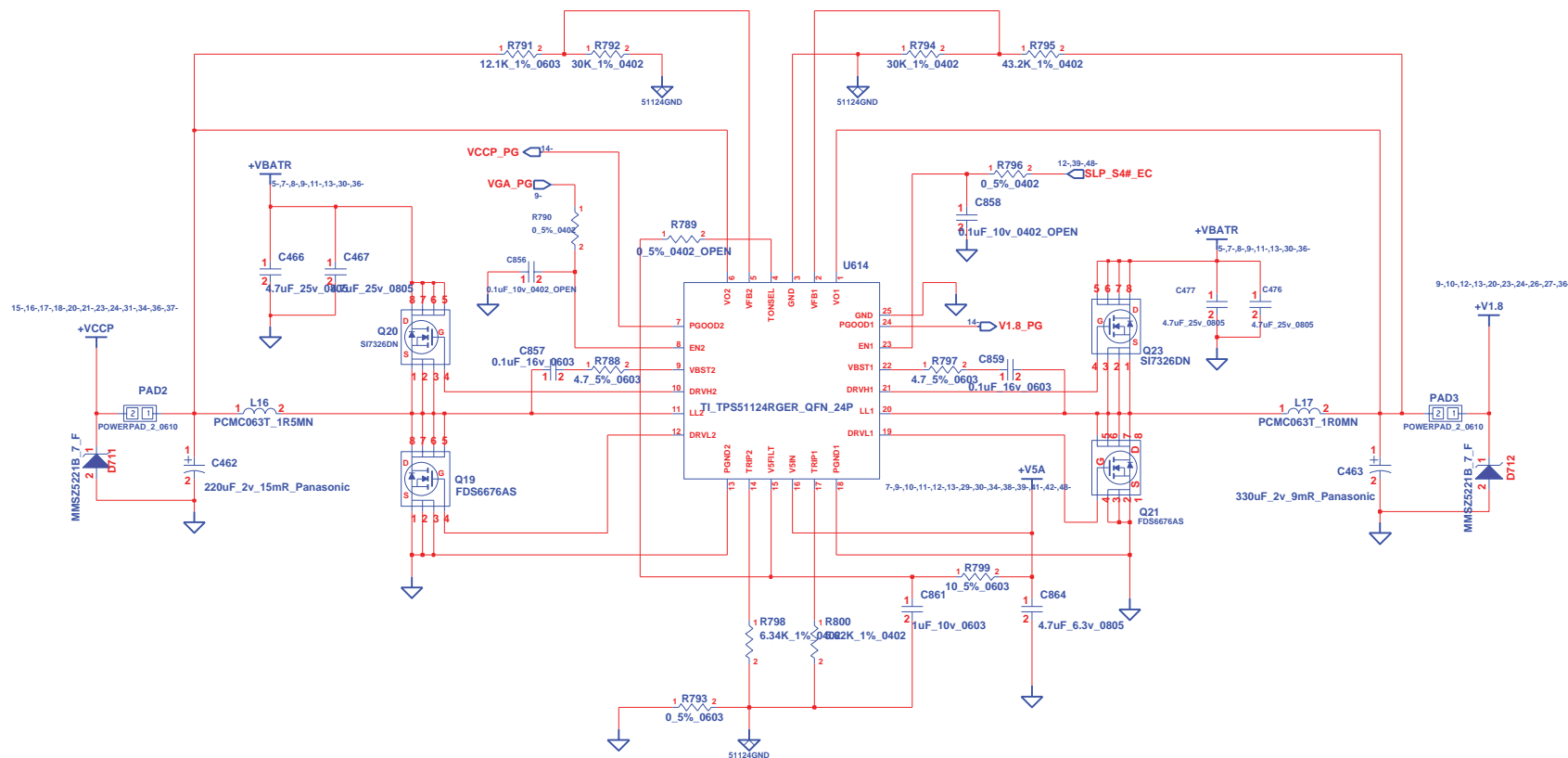
CHANGE by Wang, Alan 31-Jul-2008

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INVENTEC

TITLE DIABLO 2.0 SYSTEM POWER(3V/5V/12V)			
SIZE A3	CODE CS	DOC. NUMBER I310A22155-0-MTR	REV A02
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INVENTEC

TITLE
DIABLO 2.0
SYSTEM POWER(+V1.8/+V1.25S)

SIZE A3	CODE CS	DOC. NUMBER I310A22155-0-MTR	REV A02
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CHANGE by Wang, Alan 11-Aug-2008

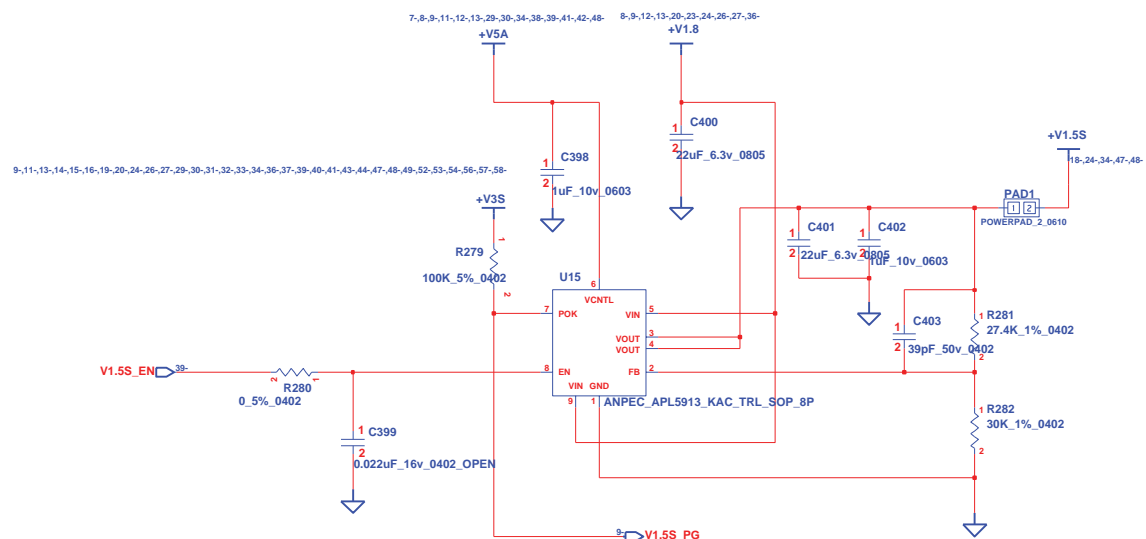
SHEET 8 OF 61

VGA_ALTV1 (GPIO6)	VGA_ALTV0 (GPIO5)	+VGA_VCC
0	0	0.9
1	0	1.09
1	1	1.17

INVENTEC

TITLE	DIABLO 2.0 GRAPHIC POWER (+VGFX_CORE)
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SIZE A3	CODE CS	DOC. NUMBER <i>1310A22155-0-MTRA02</i>	REV
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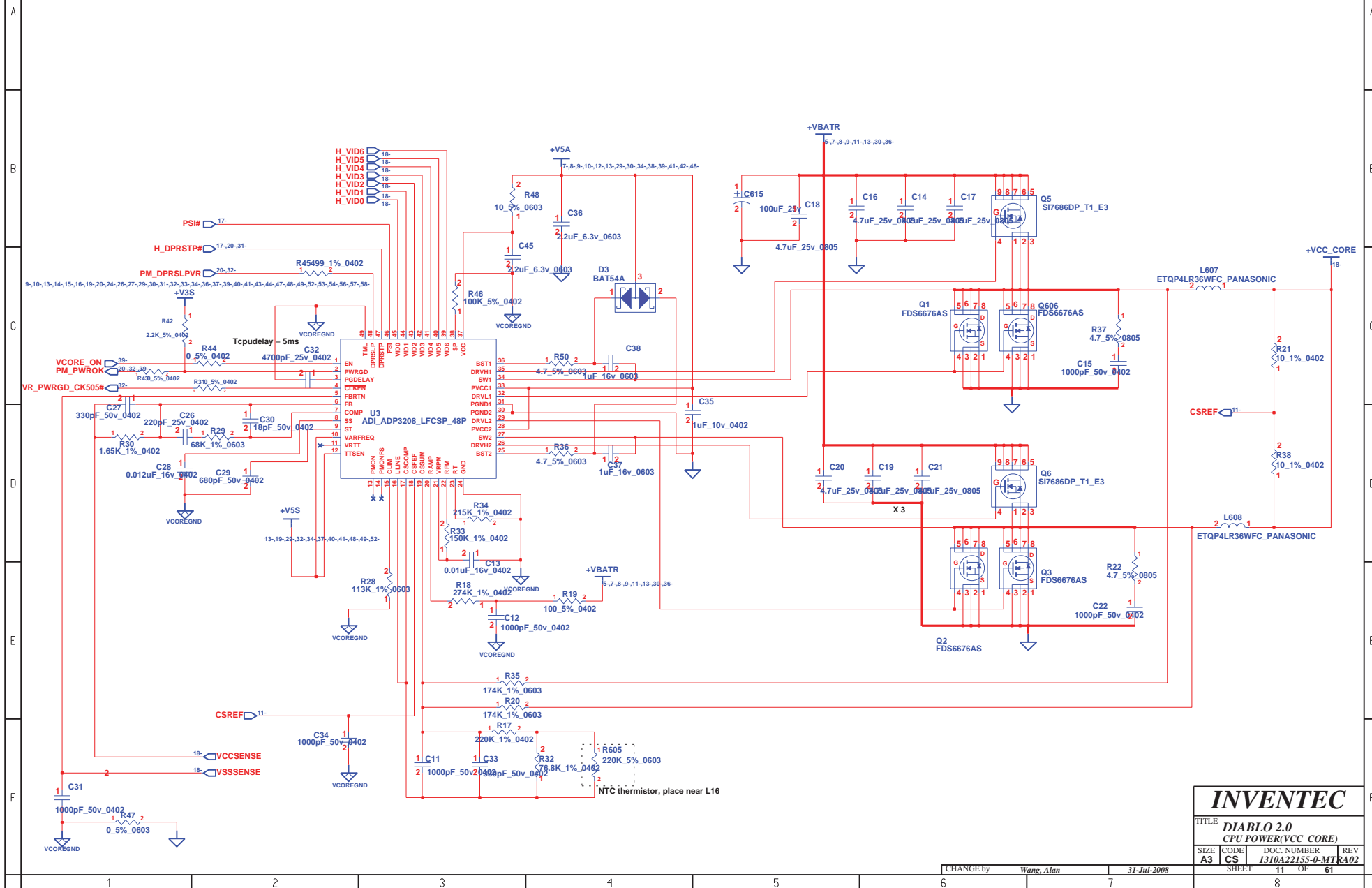
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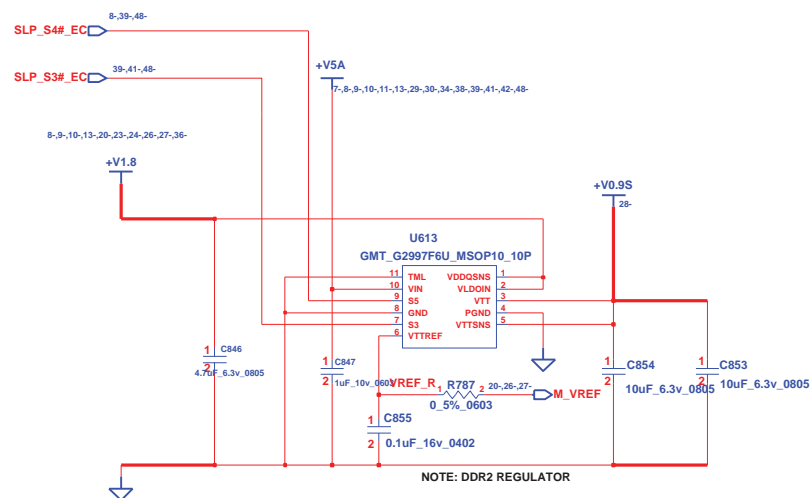
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DIABLO 2.0
SYSTEM POWER(+VCCP/+V1.5S)

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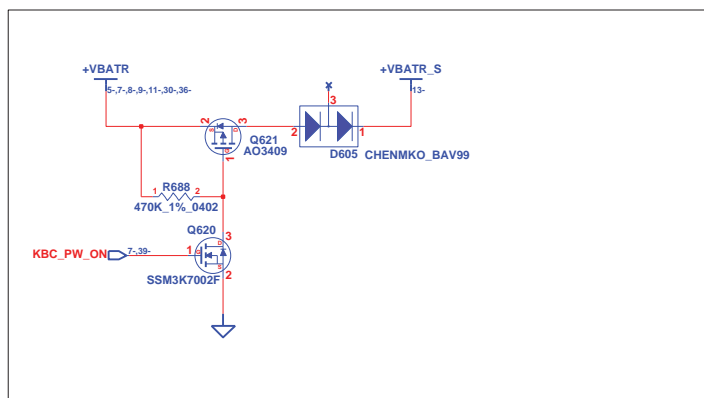
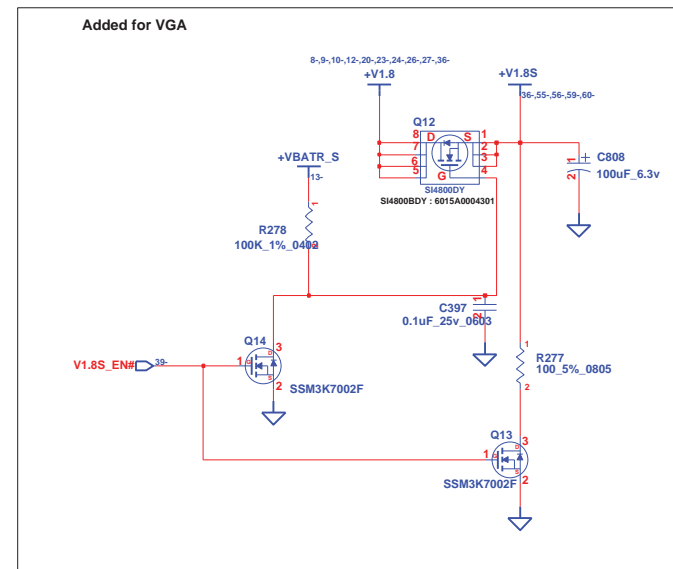
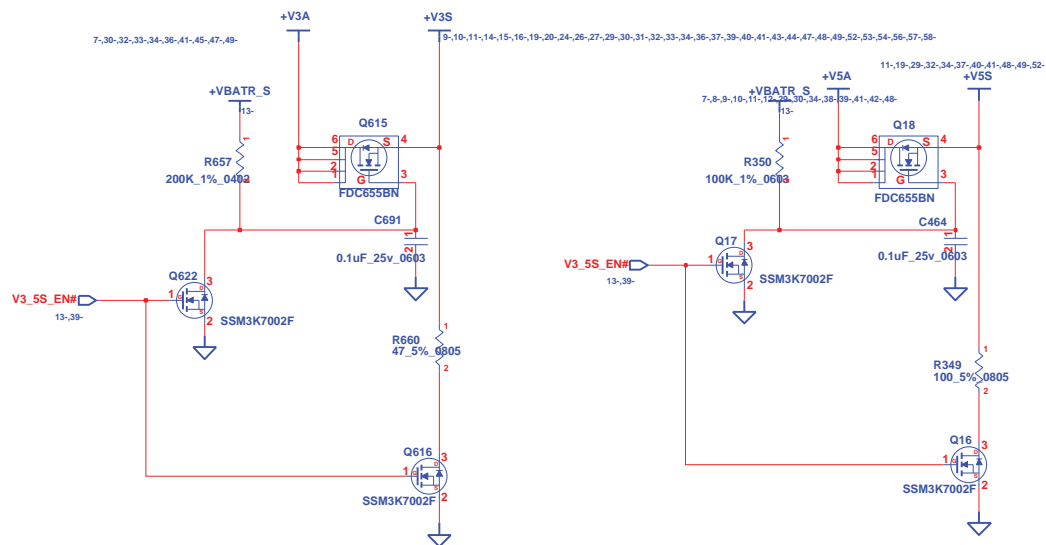
CHANGE by Wang, Alan 31-Jul-2008

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INVENTEC			
TITLE: DIABLO 2.0 DDR TERMINATION VOLTAGE			
SIZE A3	CODE CS	DOC. NUMBER 1310A22155-0-MTR	REV A02
CHANGE by: Wang, Alan		31-Jul-2008	
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INVENTEC

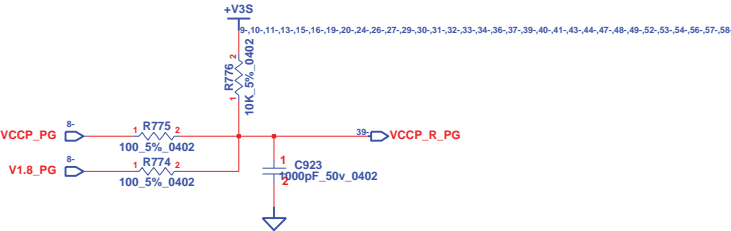
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**DIABLO 2.0
POWER(SLEEP)**

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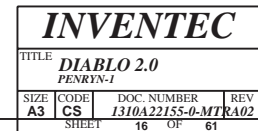
CHANGE by **Wang, Alan** 31-Jul-2008

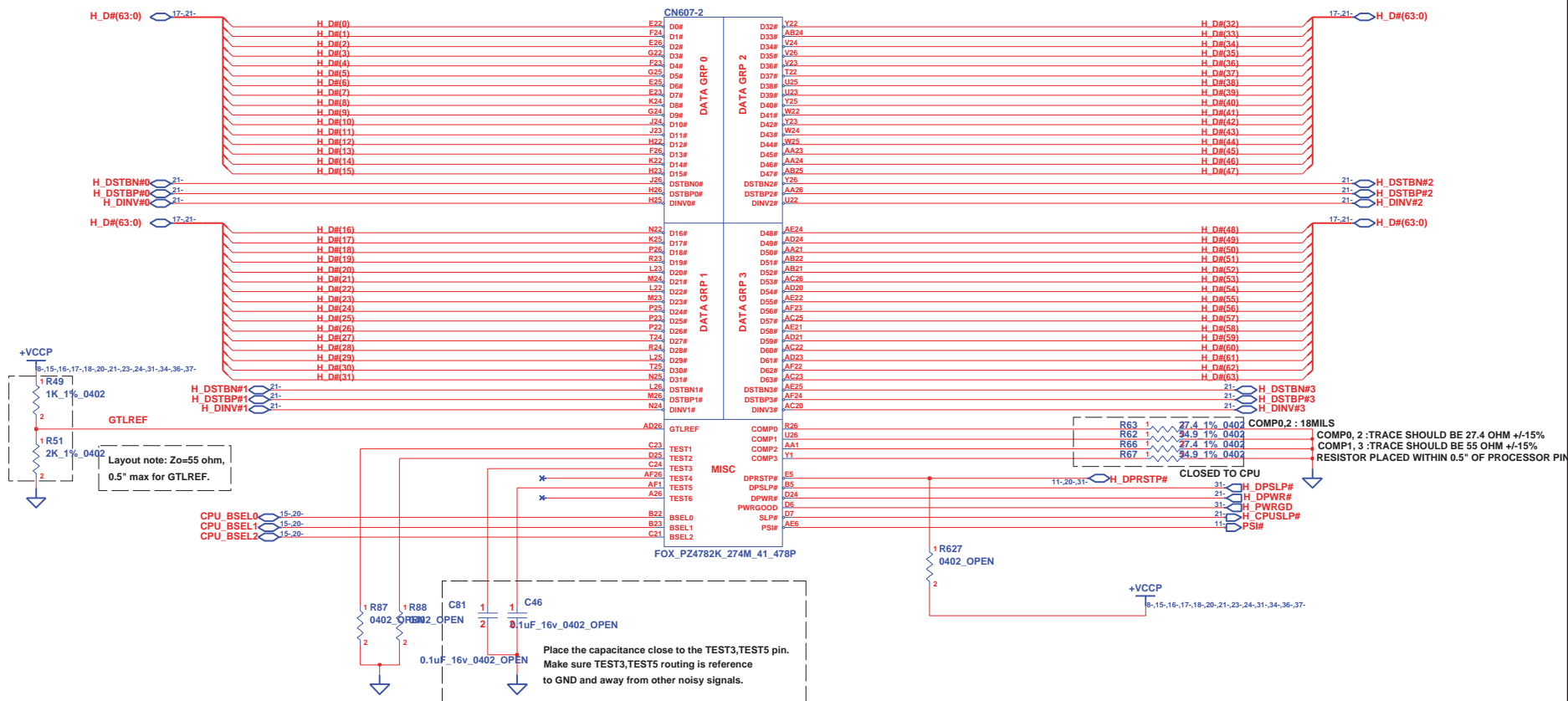
SHEET 13 OF 61

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A	WWW.AliSaler.Com								A
B									B
C									C
D									D
E									E
F									F
	1	2	3	4	5	6	7	8	



INVENTEC			
TITLE DIABLO 2.0 POWER(SEQUENCE)			
SIZE A3	CODE CS	DOC. NUMBER 1310A22155-0-MTR	REV A02
CHANGE by Wang, Alan		31-Jul-2008	
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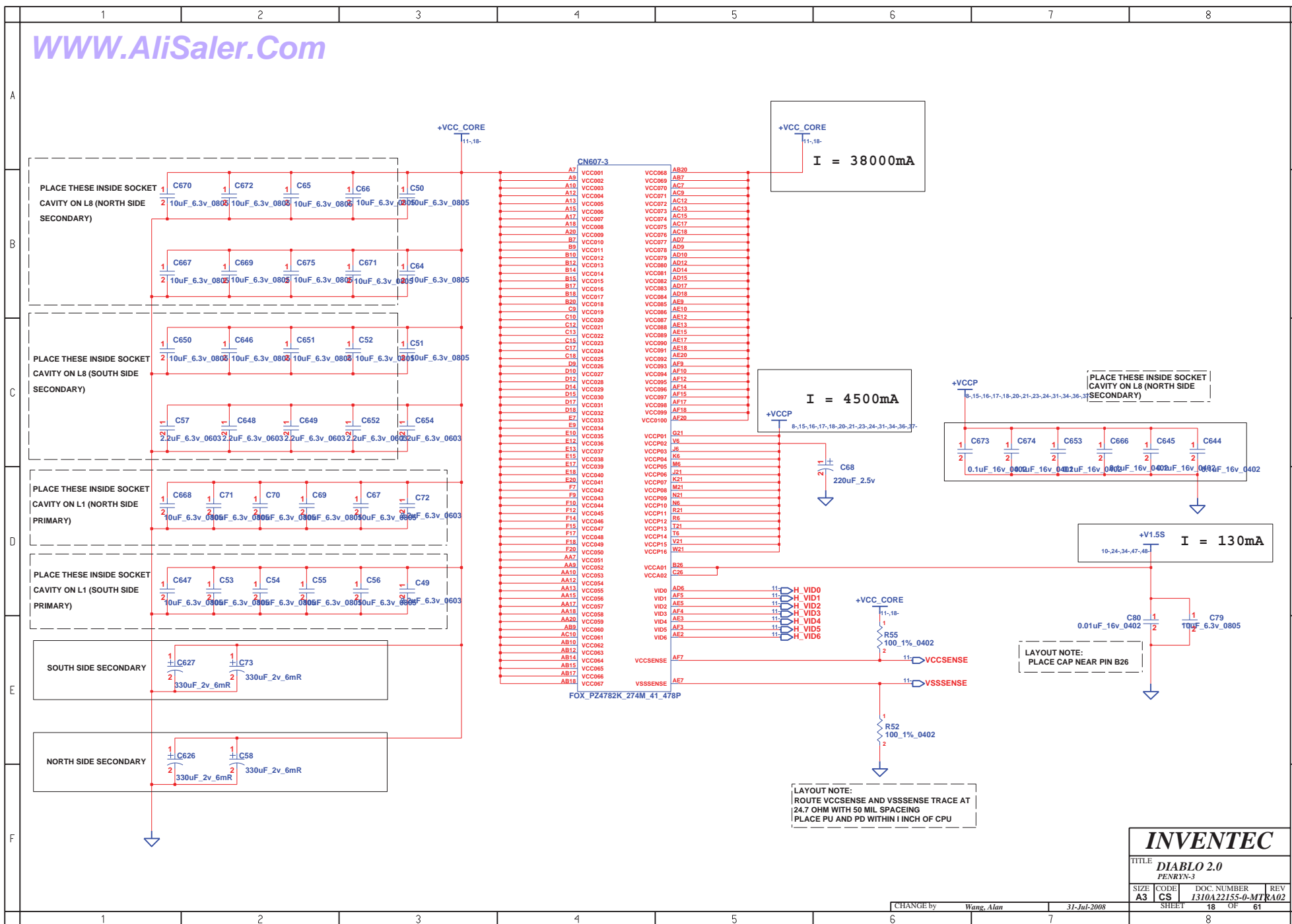
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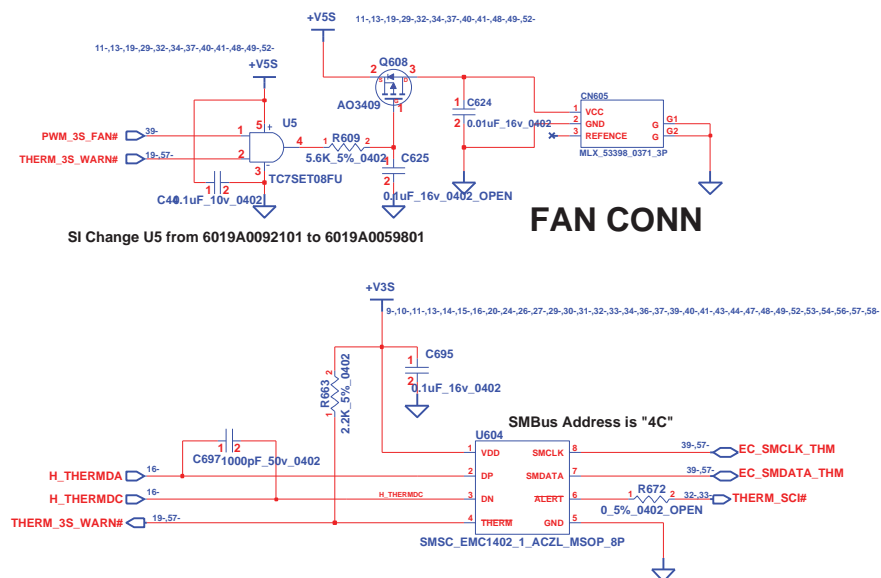
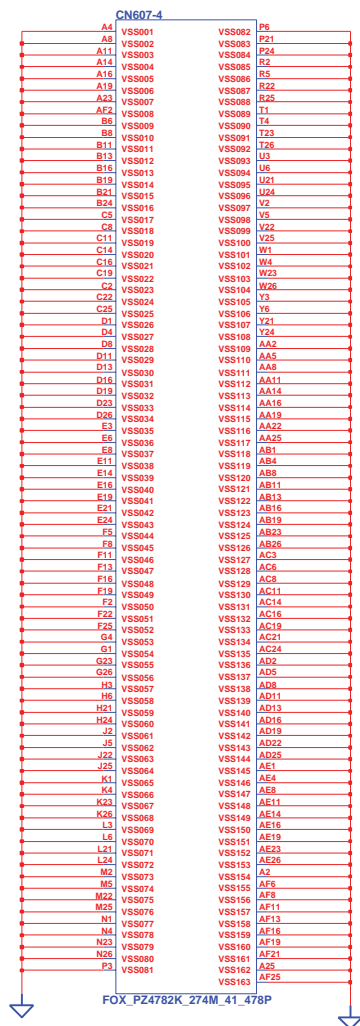
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DIABLO 2.0
PENYIN-2

SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A22155-0-MT	A02

CHANGE by Wang, Alan 31-Jul-2008

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INVENTEC

TITLE
DIABLO 2.0
THERMAL&FAN

SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A22155-0-MTR	A02

CHANGE by Wang, Alan 31-Jul-2008

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MA_DATA(63:0) 26-

MB_DATA(63:0) 27-



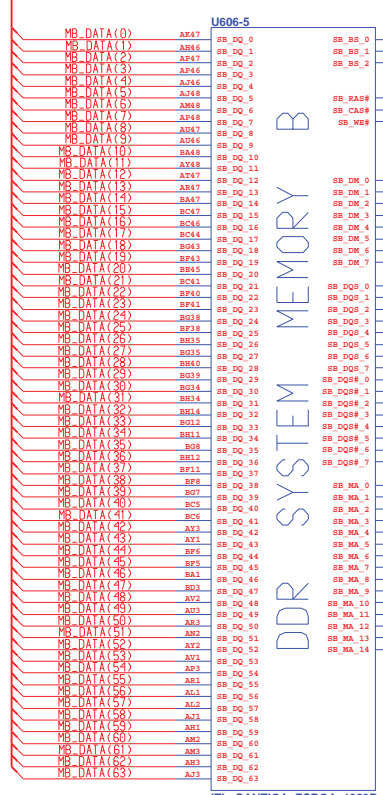
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MA_DM(0) 26- MA_DM(7:0)

MA_DQS(0) 26- MA_DQS(7:0)

MA_DQS#(0) 26- MA_DQS#(7:0)

MA_A(0) 26-28 MA_A(14:0)



DDR SYSTEM MEMORY B

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MB_DQS(0) 27- MB_DQS(7:0)

MB_DQS#(0) 27- MB_DQS#(7:0)

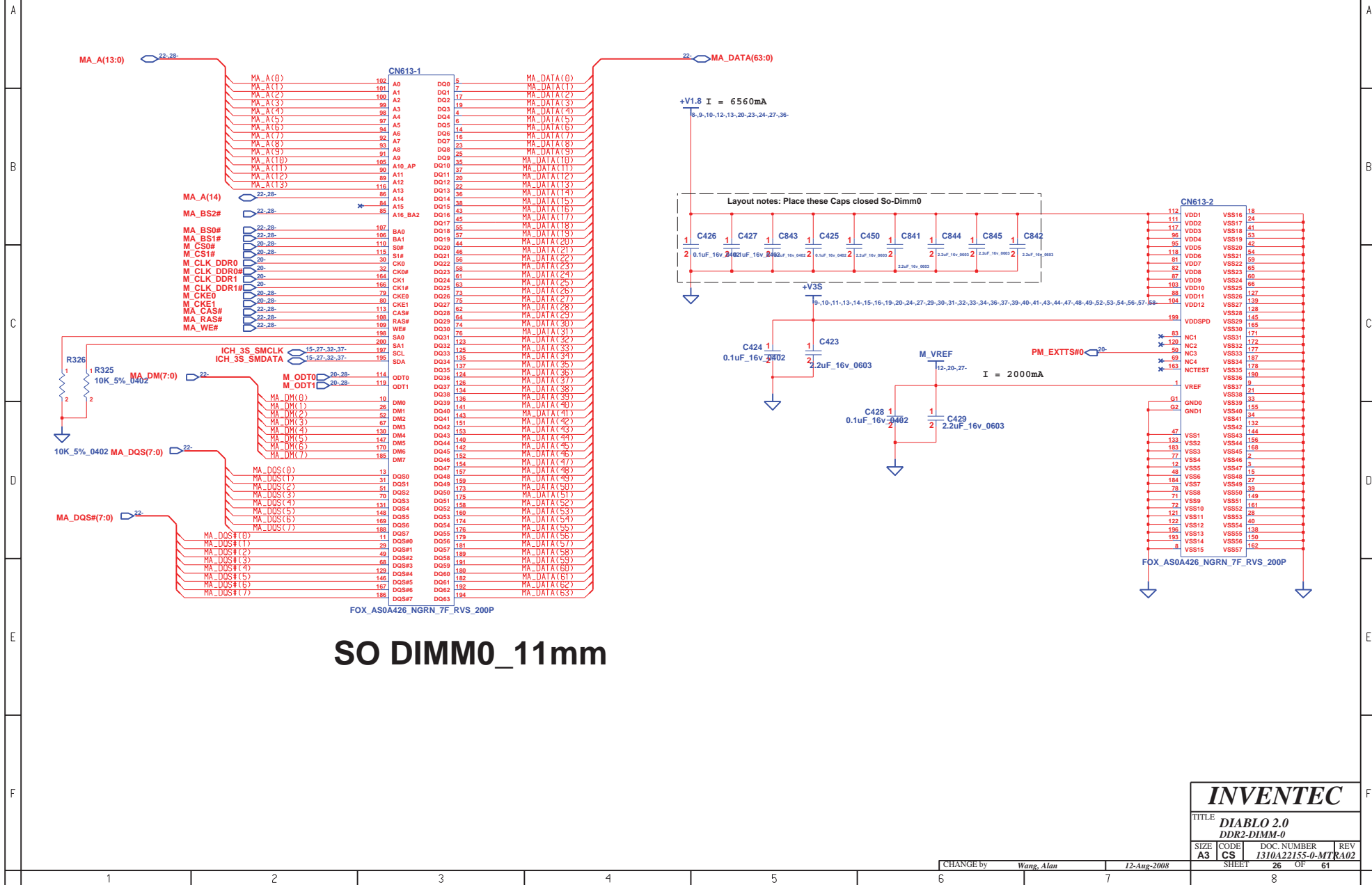
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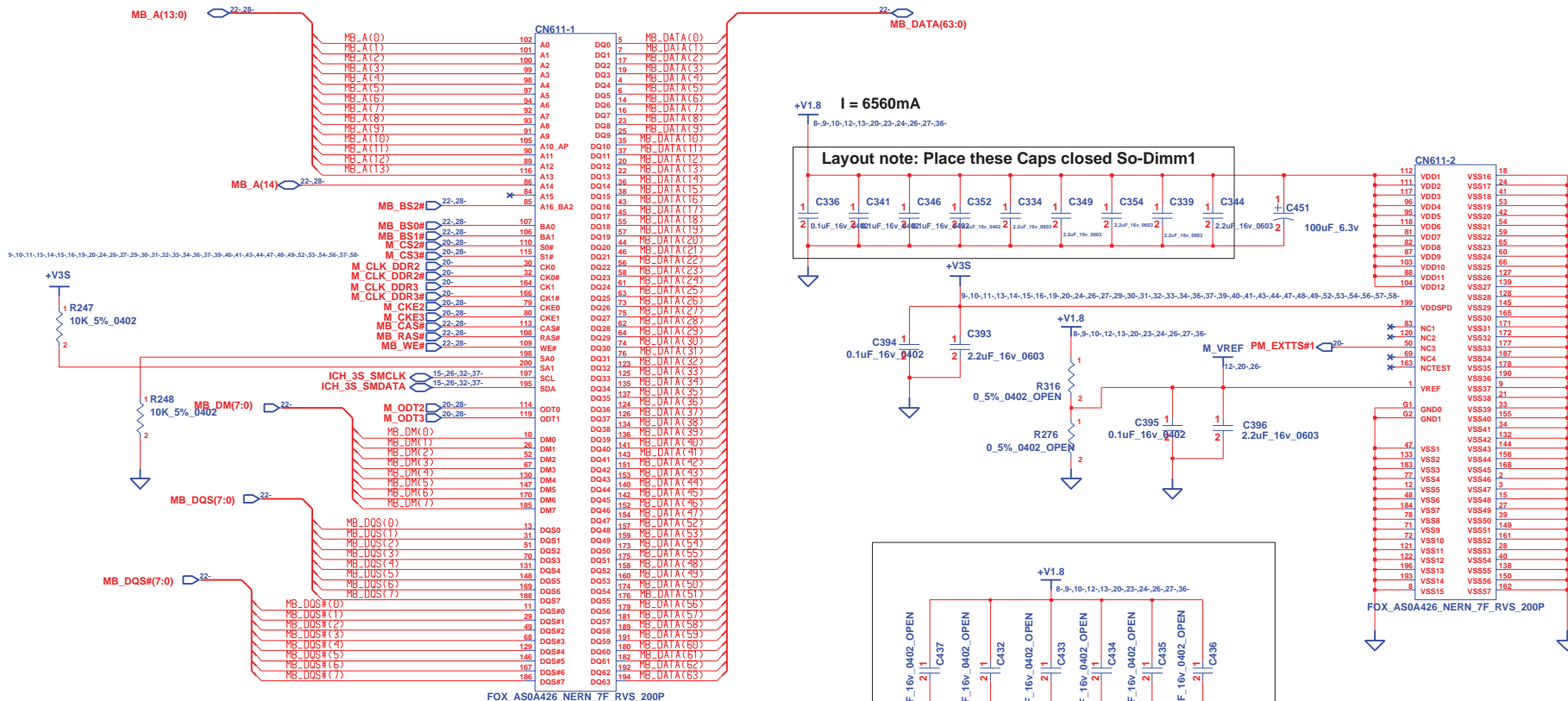
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TITLE DIABLO 2.0 N/B Cantiga - 3			
SIZE A3	CODE CS	DOC. NUMBER 1310A22155-0-MTR	REV A02
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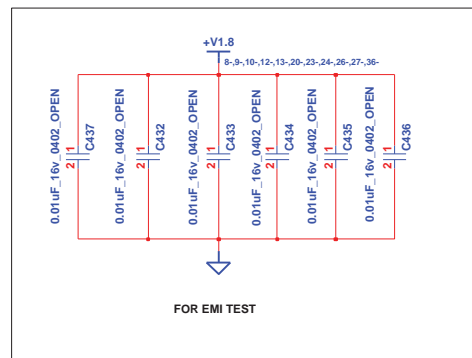
CHANGE by Wang, Alan 31-Jul-2008



SO DIMM0_11mm



SO DIMM1 6.5mm

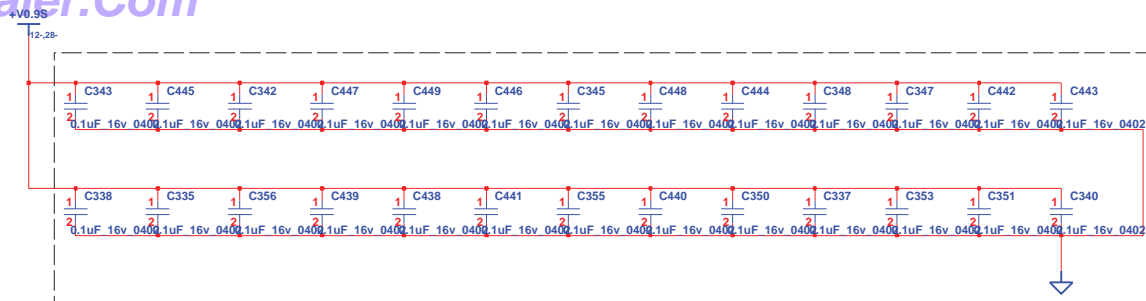


FOR EMI TEST

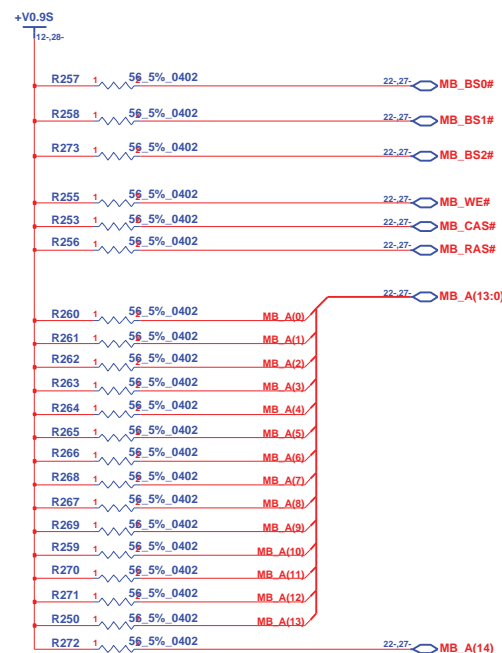
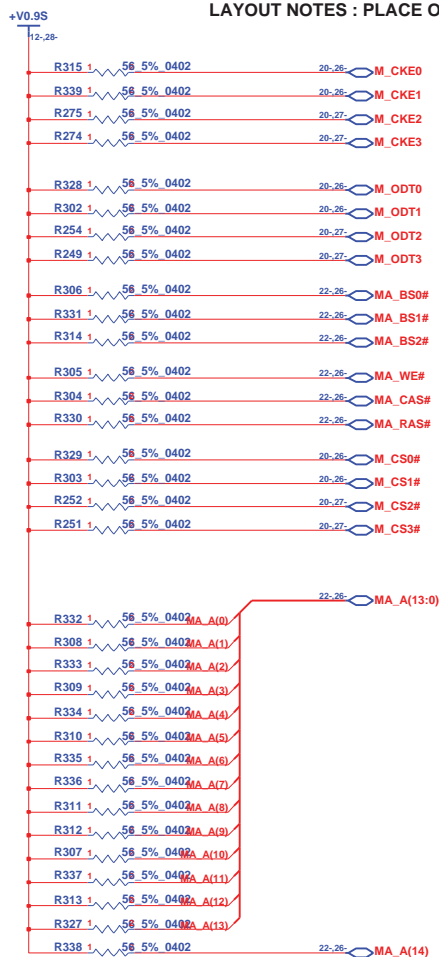
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TITLE DIABLO 2.0 DDR2-DIMM1			
SIZE A3	CODE CS	DOC. NUMBER 1310A22155-0-MT	REV A02
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CHANGE by Wang, Alan 12-Aug-2008



LAYOUT NOTES : PLACE ONE CAP CLOSE TO EVERY 2 PULL UP RESISTOR TERMINATED TO +V0.9S

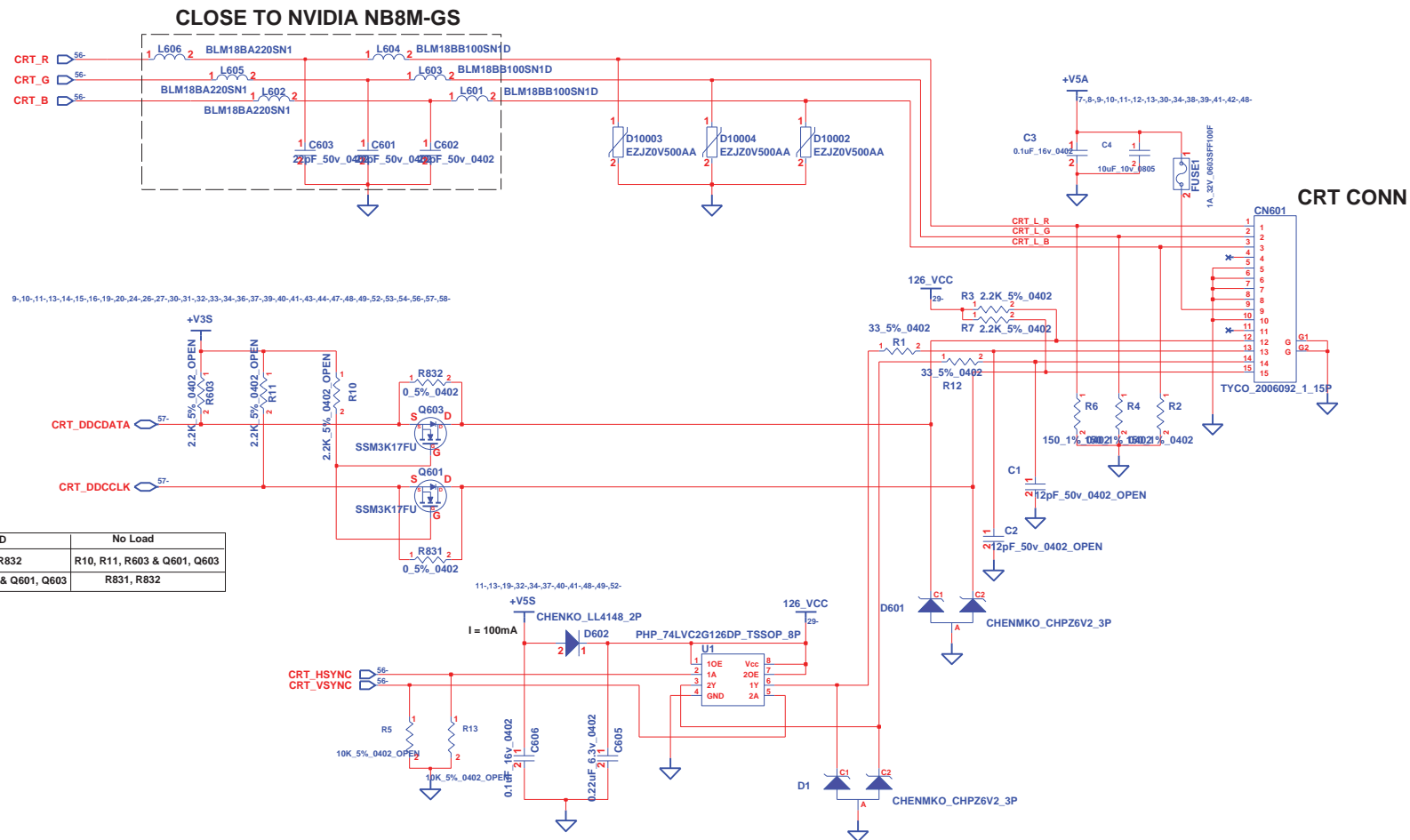


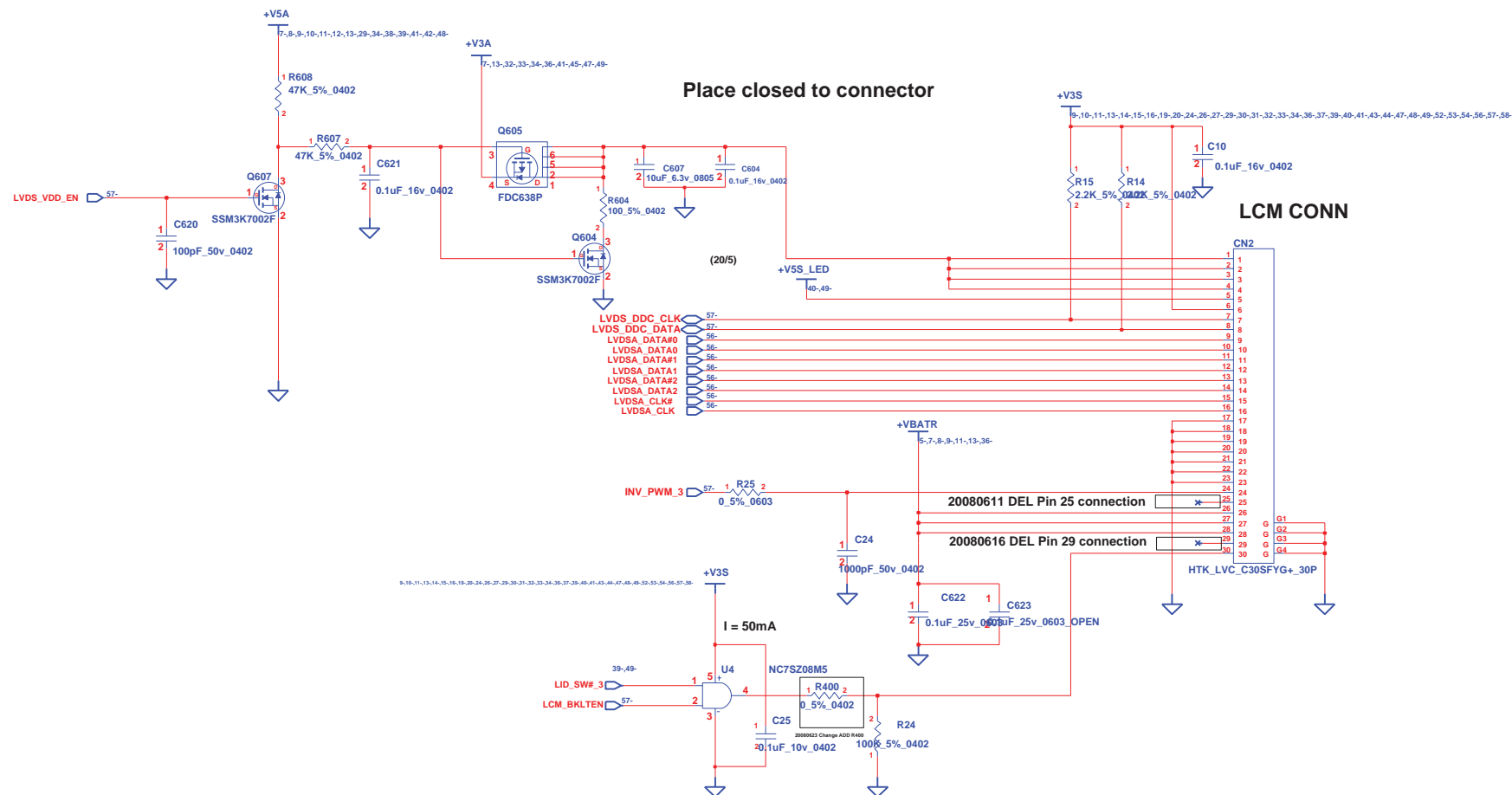
INVENTEC

TITLE
DIABLO 2.0
DDR2-DAMPING

SIZE A3	CODE CS	DOC. NUMBER 1310A22155-0-MTR	REV A02
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CHANGE by **Wang, Alan** **27-Sep-2003**





INVENTEC

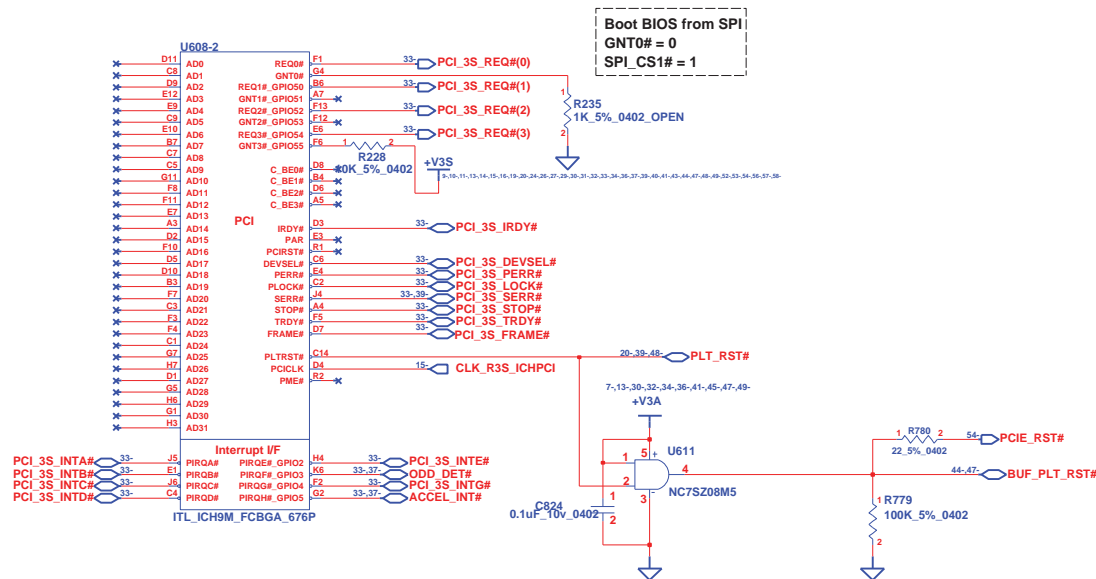
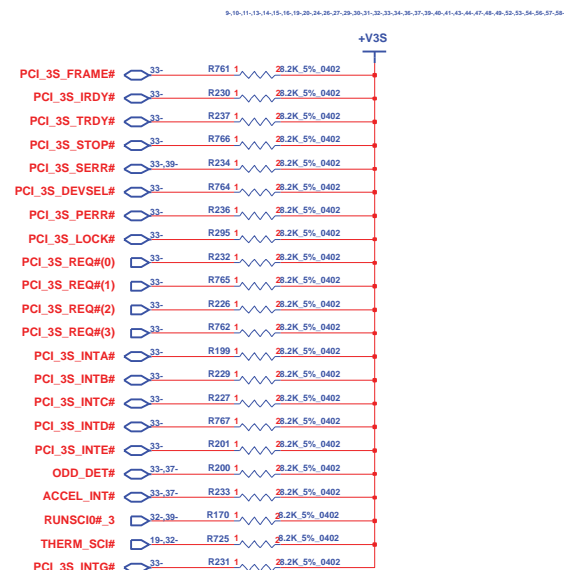
TITLE
DIABLO 2.0
LCM CONN

SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A22155-0-MTR	A02

CHANGE by Wang, Alan 31-Jul-2008

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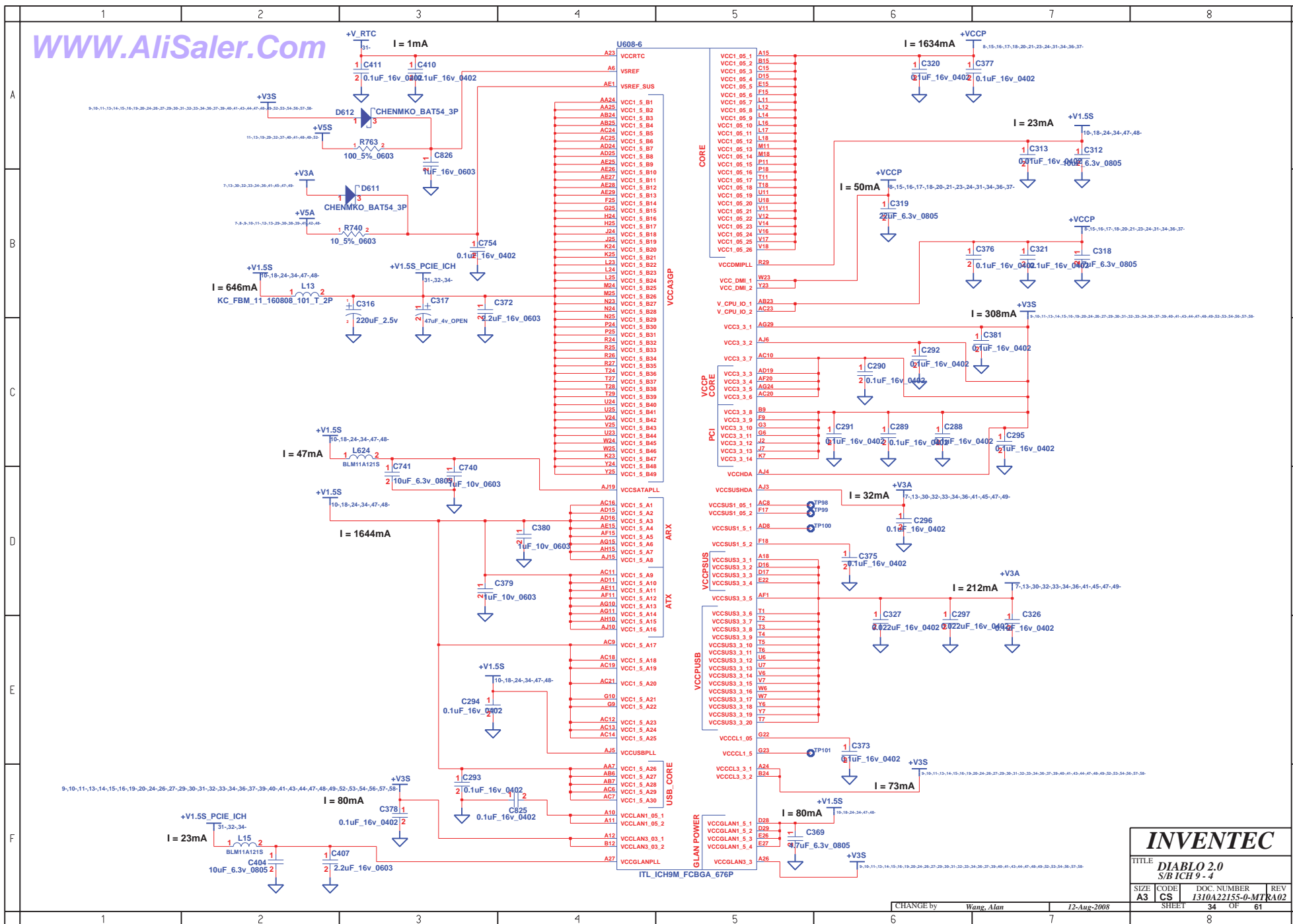
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TITLE
DIABLO 2.0
S/B ICH9-3

SIZE	CODE	DOC. NUMBER	REV
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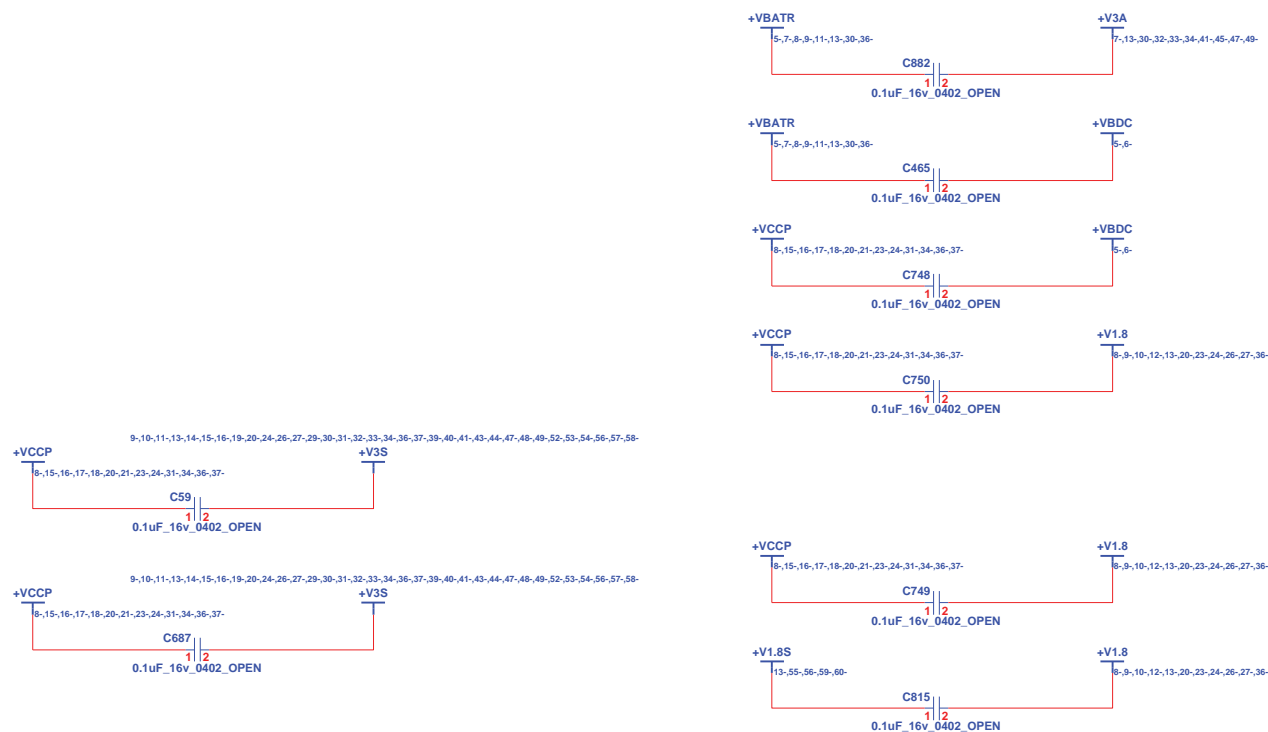
CHANGE by Wang, Alan 31-Jul-2008

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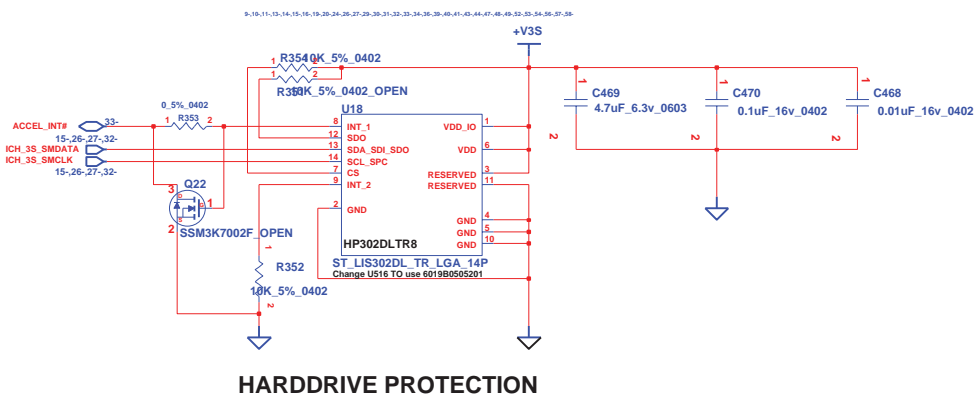
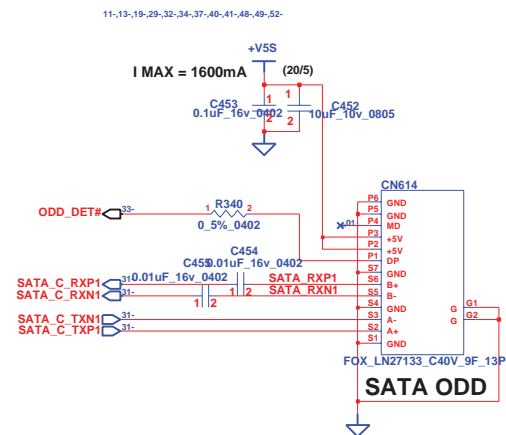
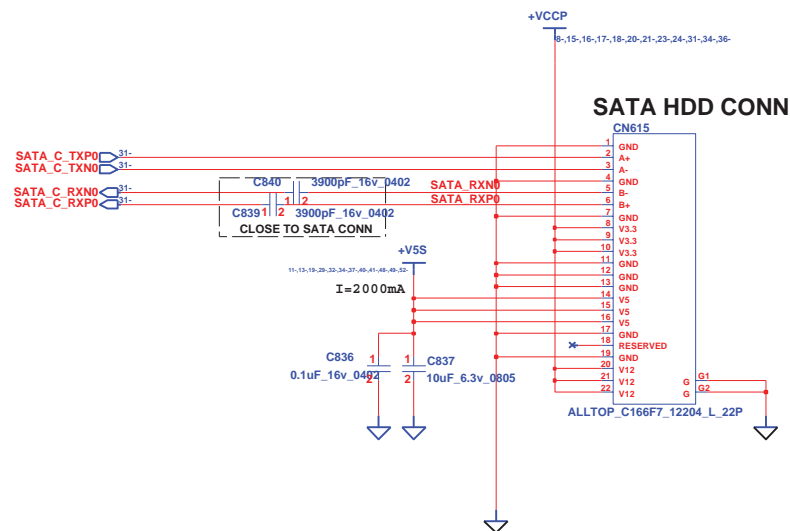


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SIZE A3	CODE CS	DOC. NUMBER 1310A22155-0-MTR	REV A02
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C									
D									
E									
F									
					CHANGE by Wang, Alan		31-Jul-2008		



INVENTEC			
TITLE DIABLO 2.0			
SYSTEM BIOS&ODD EXT/B			
SIZE A3	CODE CS	DOC. NUMBER 1310A22155-0-MTR	REV A02
CHANGE by Wang, Alan		31-Jul-2008	
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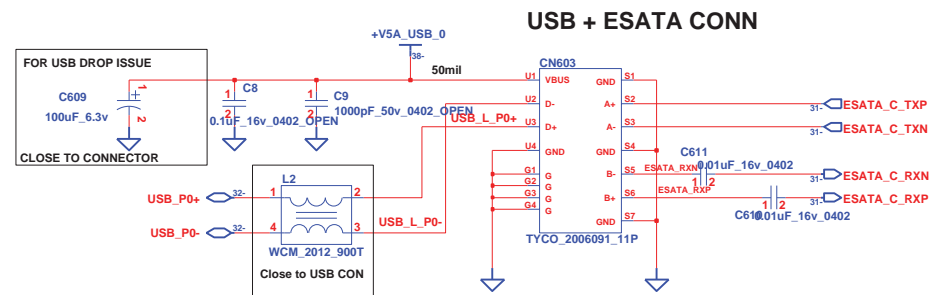
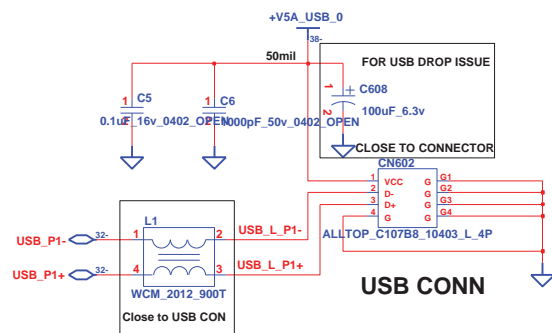
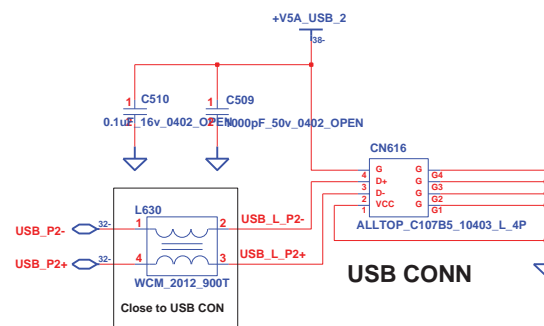
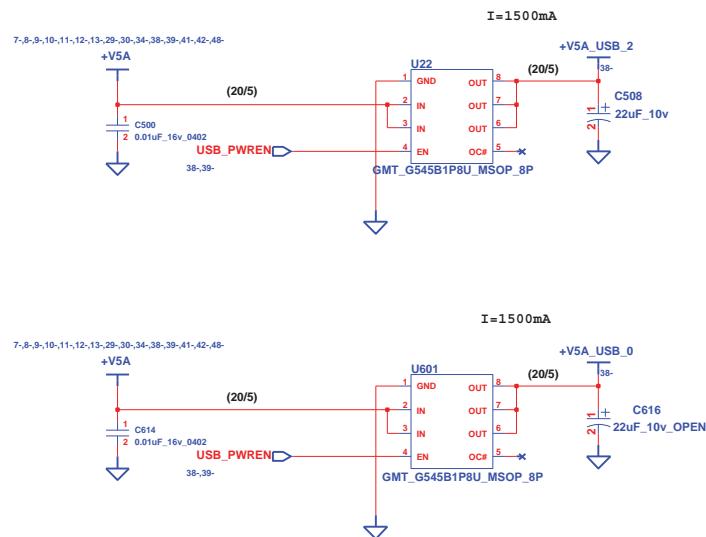


INVENTEC

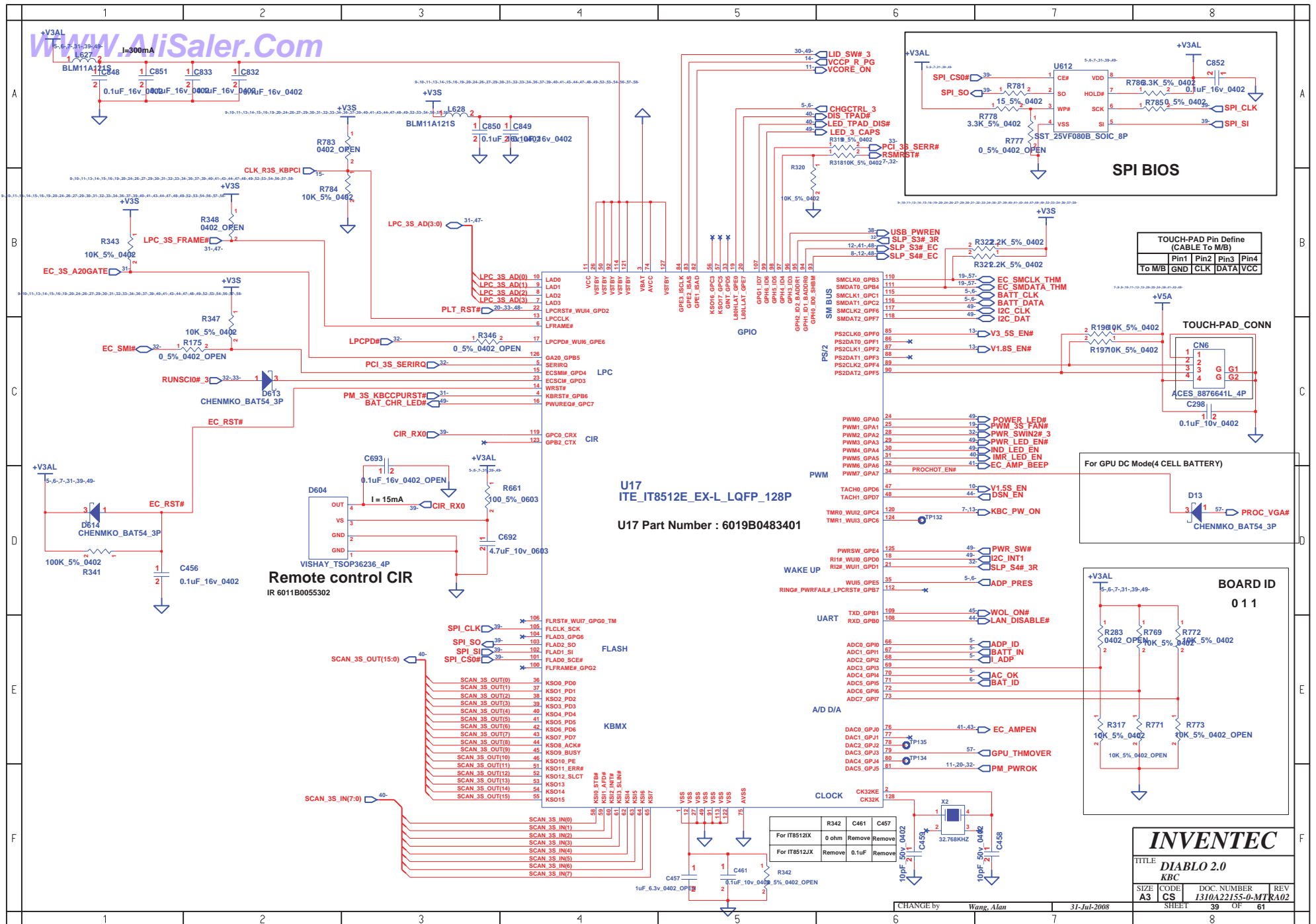
TITLE			
DIABLO 2.0			
HDD & ODD CONN & Accelerometer			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A22155-0-MTR	A02

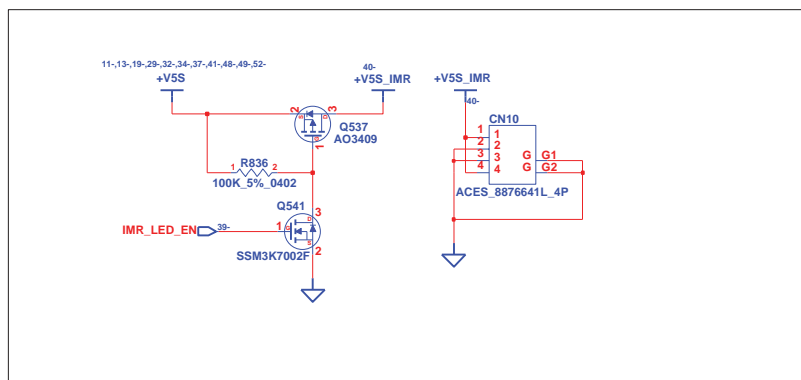
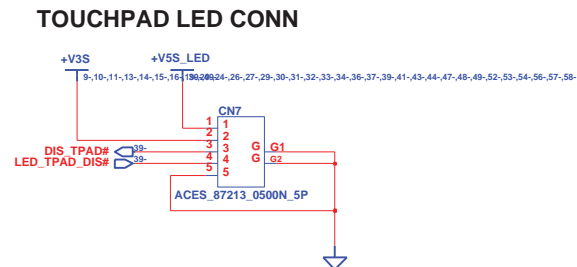
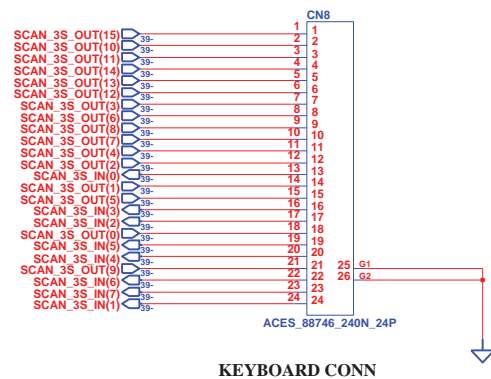
CHANGE by Wang, Alan 31-Jul-2008

SHEET 37 OF 61

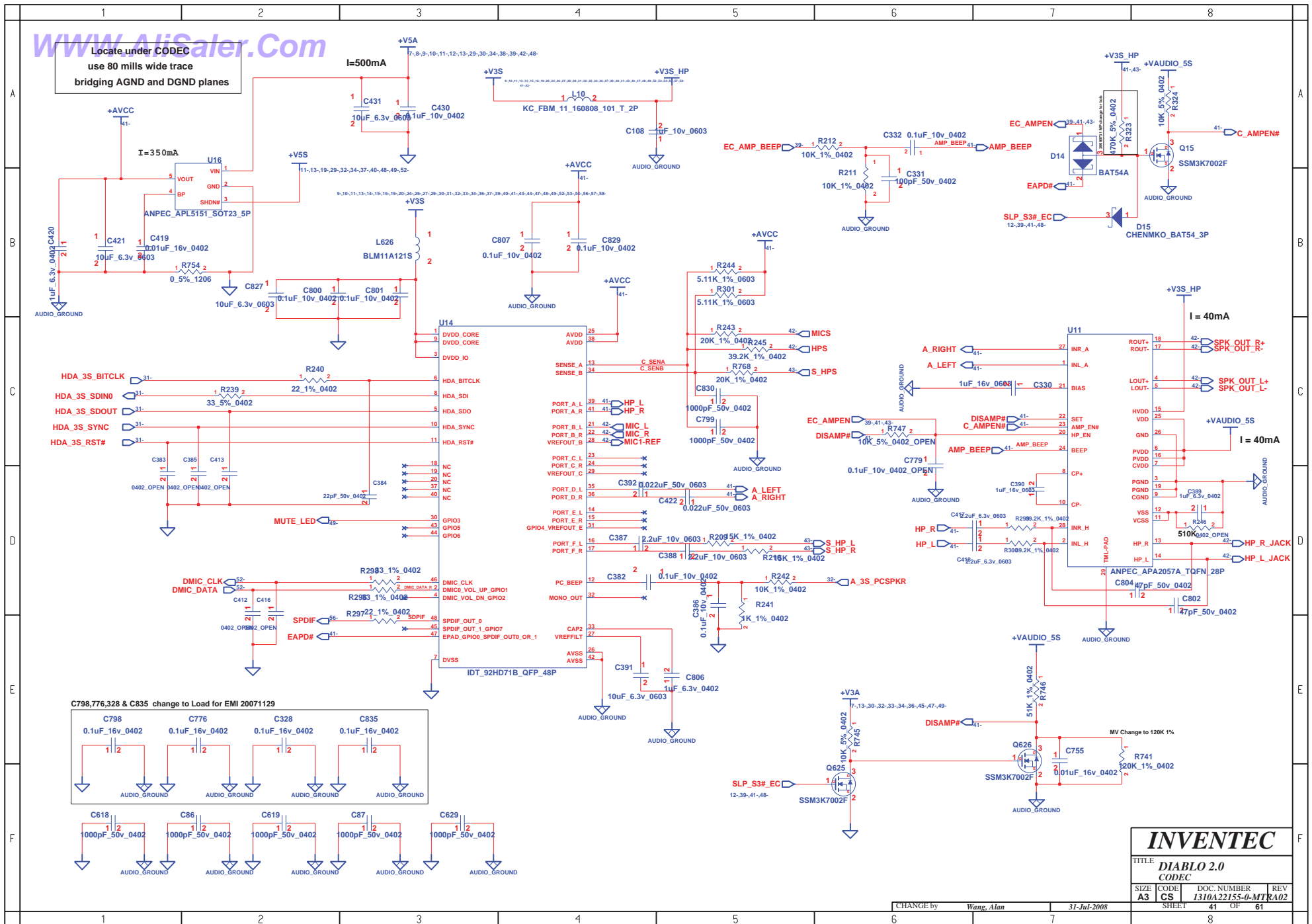


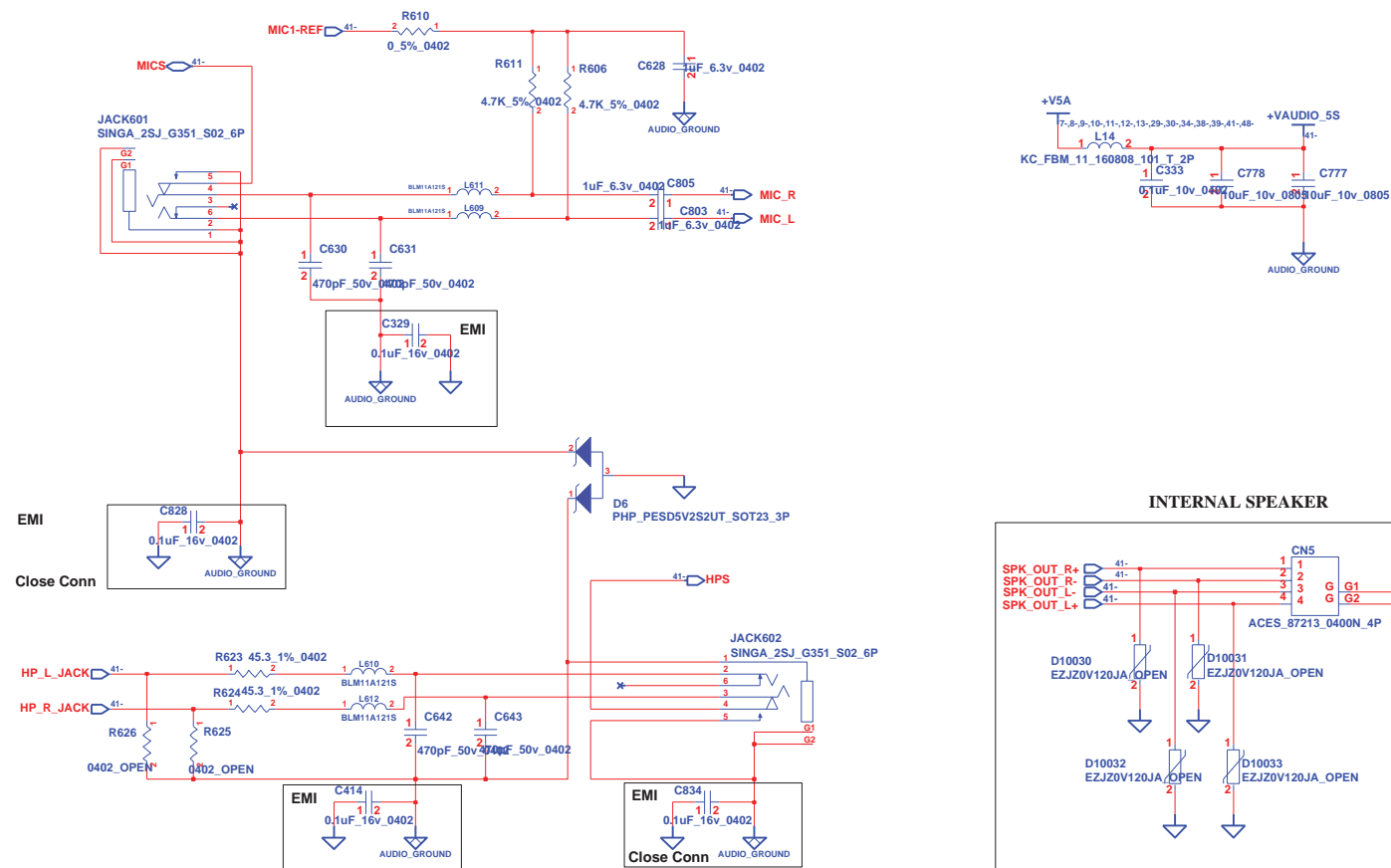
INVENTEC			
TITLE			
DIABLO 2.0			
USB CONN			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A22155-0-MT	A02
CHANGE by		Wang, Alan	31-Jul-2008
SHEET		38	OF 61



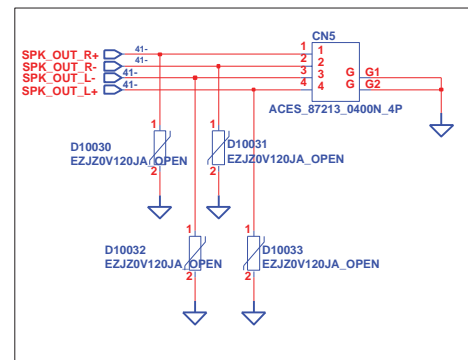


INVENTEC			
TITLE DIABLO 2.0 KB & TP CONN			
SIZE A3	CODE CS	DOC. NUMBER I310A22155-0-MT	REV A02
CHANGE by Wang, Alan		31-Jul-2008	
SHEET		40	OF 61





INTERNAL SPEAKER

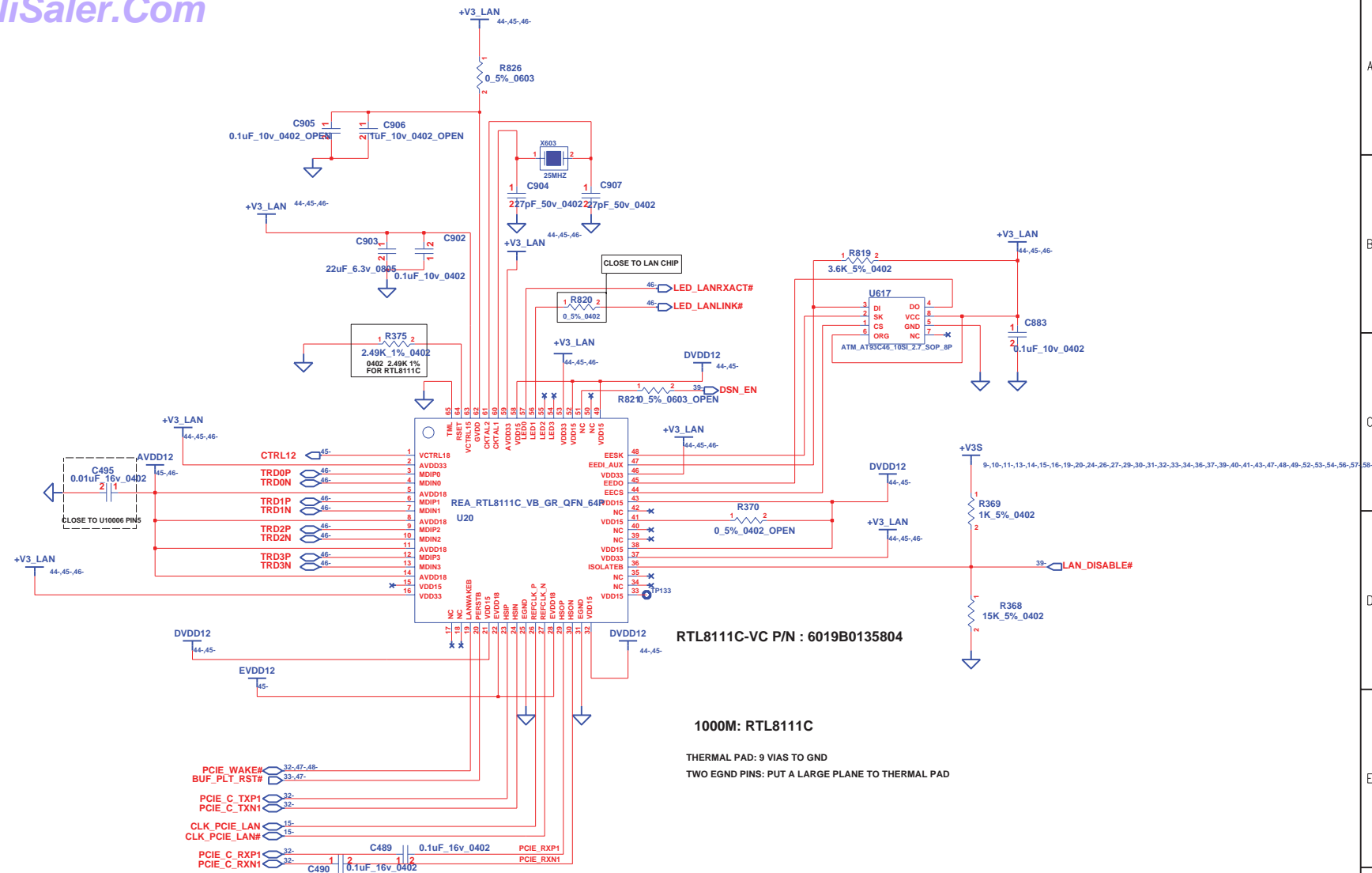


INVENTEC

TITLE
**DIABLO 2.0
AMP & MIC & HP**

SIZE A3	CODE CS	DOC. NUMBER I310A22155-0-MTR	REV A02
SHEET 42		OF 61	

CHANGE by Wang, Alan 31-Jul-2008



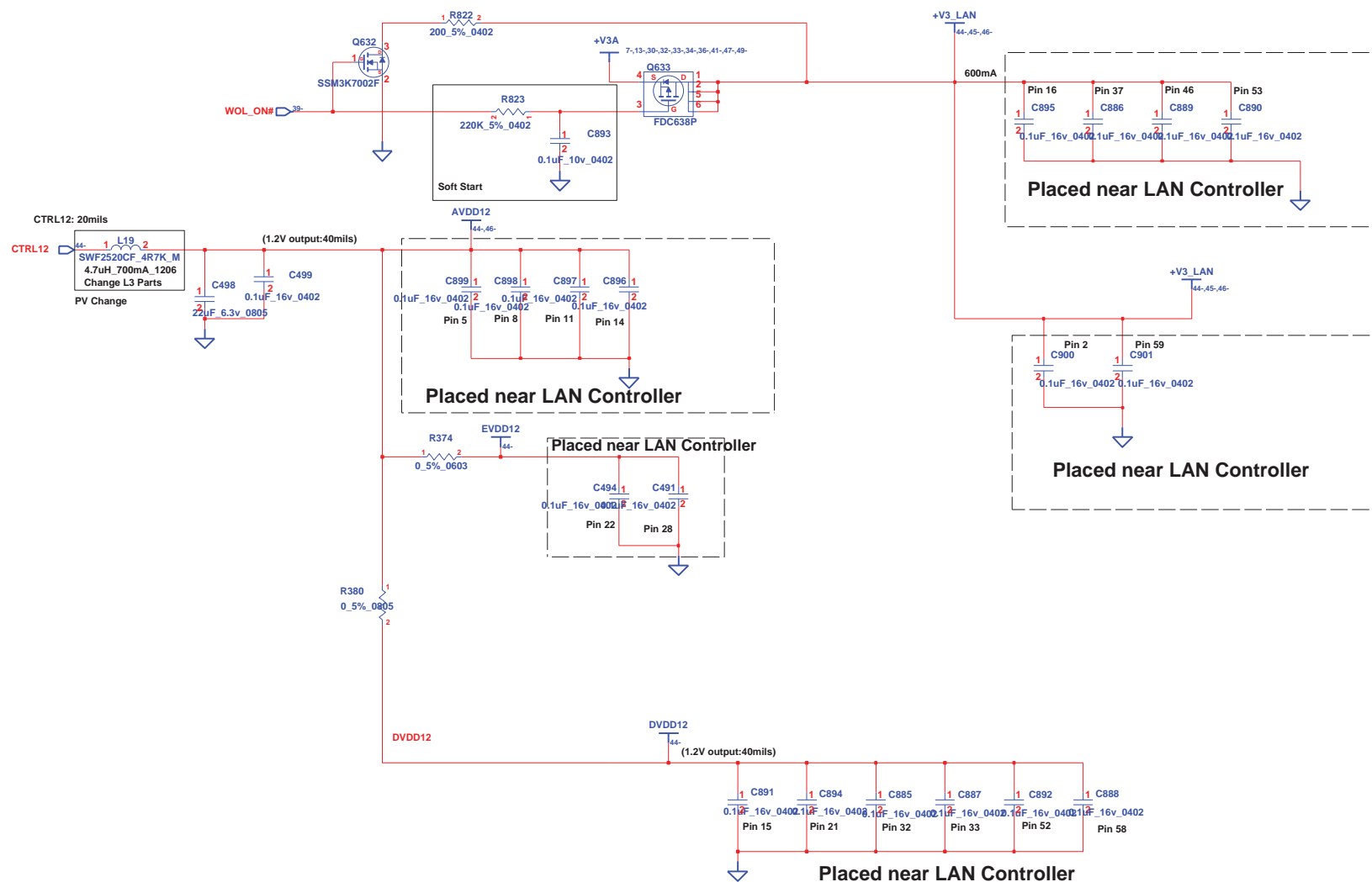
INVENTEC

TITLE
DIABLO 2.0
LAN INTERFACE

SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A22155-0-MTR	A02

CHANGE by Wang, Alan 31-Jul-2008

SHEET 44 OF 61



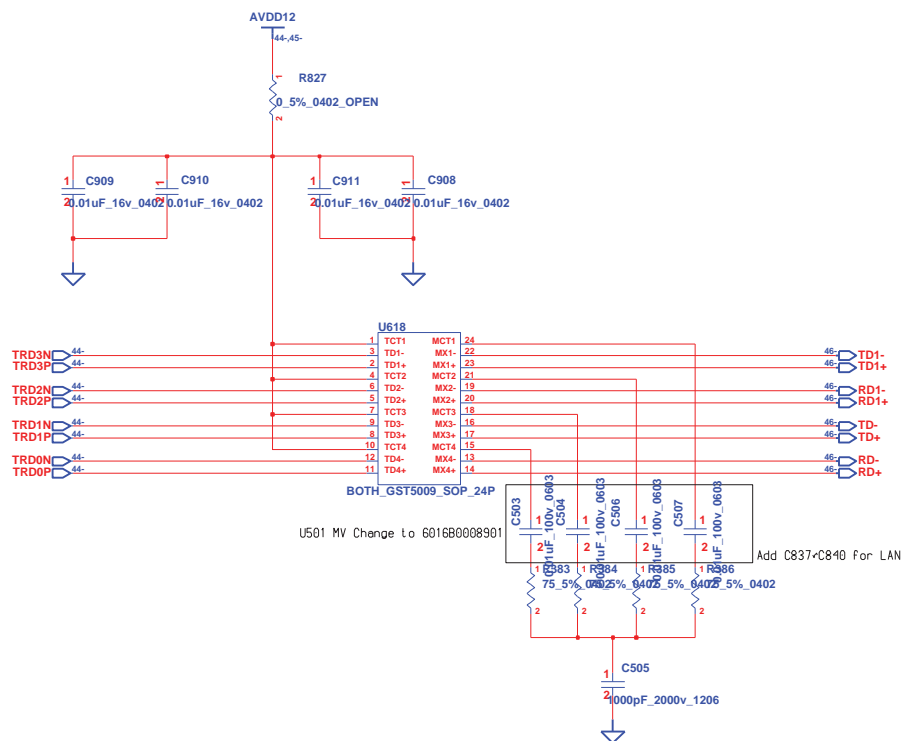
INVENTEC

TITLE
DIABLO 2.0
LAN POWER

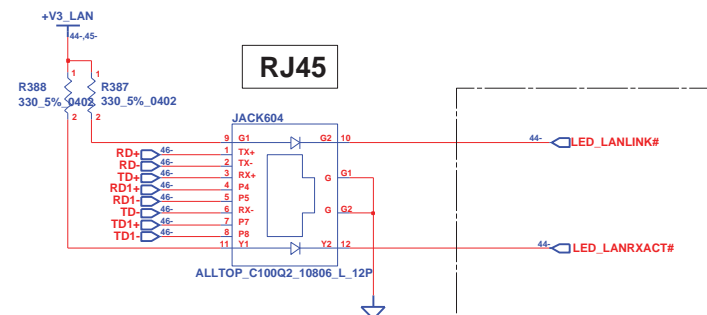
SIZE A3	CODE CS	DOC. NUMBER I310A22155-0-MTR	REV A02
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CHANGE by Wang, Alan 31-Jul-2008

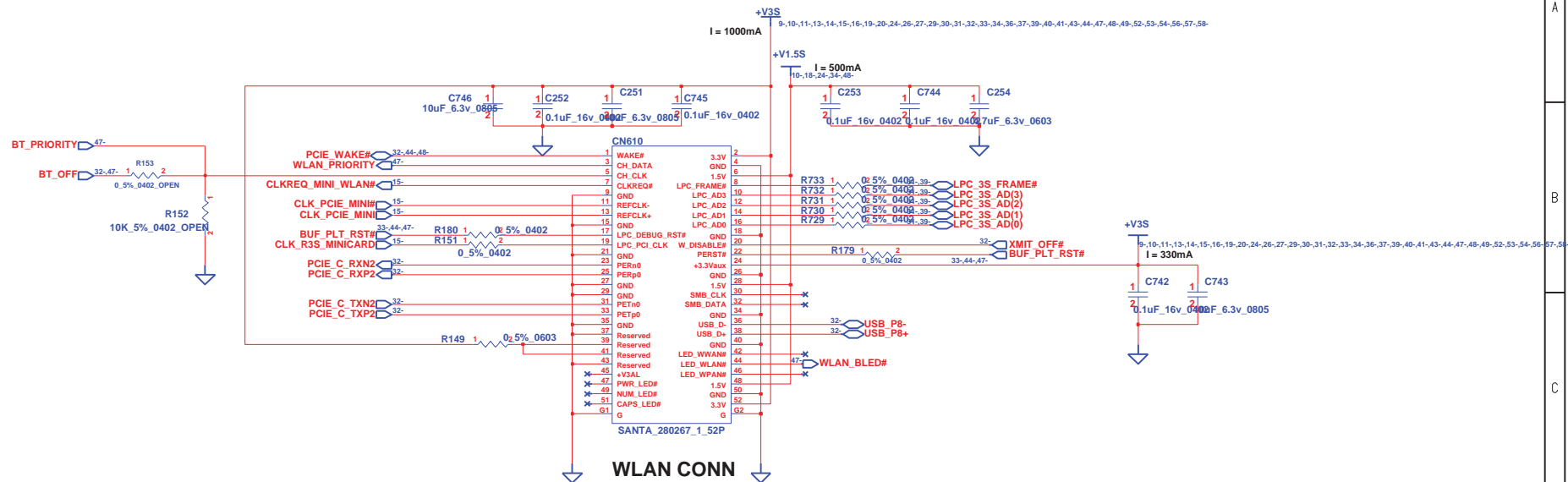
SHEET 45 OF 61



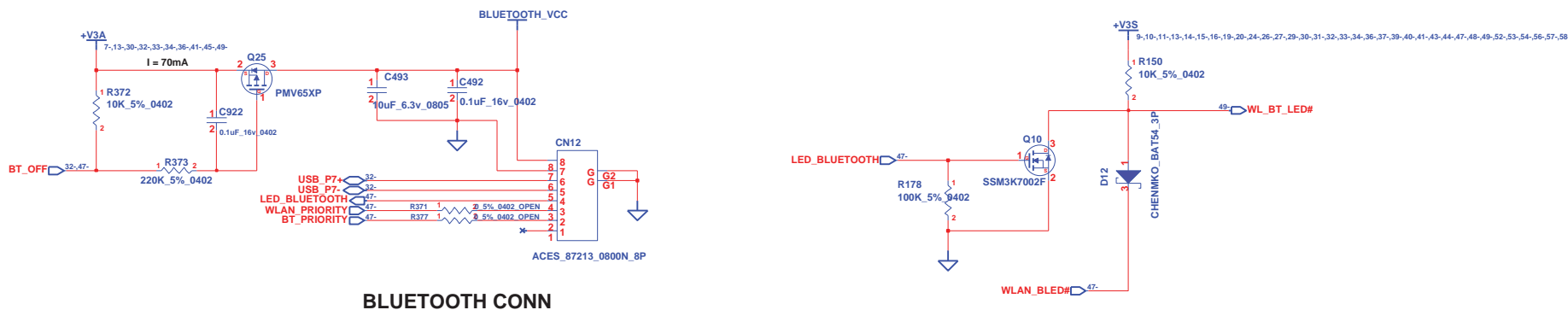
LAYOUT NOTE : Place termination resistors and caps as close to LAN controller as possible



INVENTEC			
TITLE DIABLO 2.0 LAN RJ45 CONN			
SIZE A3	CODE CS	DOC. NUMBER 1310A22155-0-MTR	REV A02
CHANGE by Wang, Alan		31-Jul-2008	
SHEET 46		OF 61	



R729,R730,R731,R732,R733 place as close to LPC signal as possible to minimize stub length for LPC bus (will be NI for FCS)



BLUETOOTH CONN

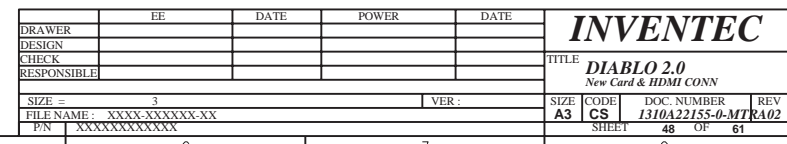
INVENTEC

TITLE
**DIABLO 2.0
MINICARD CONN**

SIZE A3	CODE CS	DOC. NUMBER 1310A22155-0-MTR	REV A02
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CHANGE by Wang, Alan 31-Jul-2008

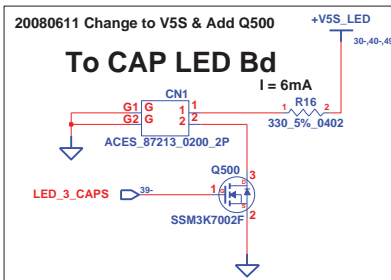
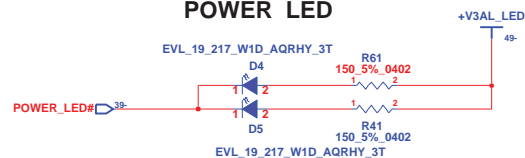
SHEET 47 OF 61



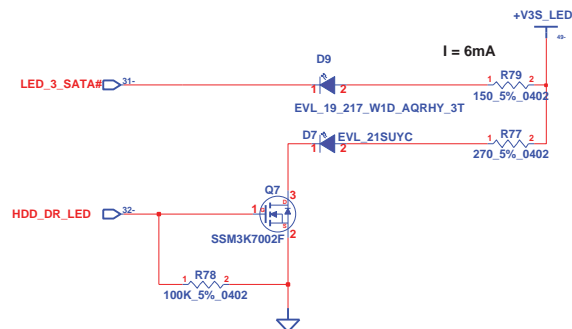
BATTERY-CHARGE LED



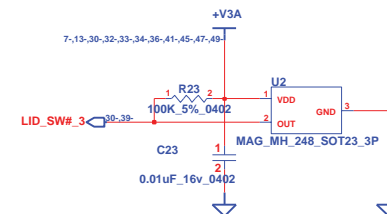
POWER LED



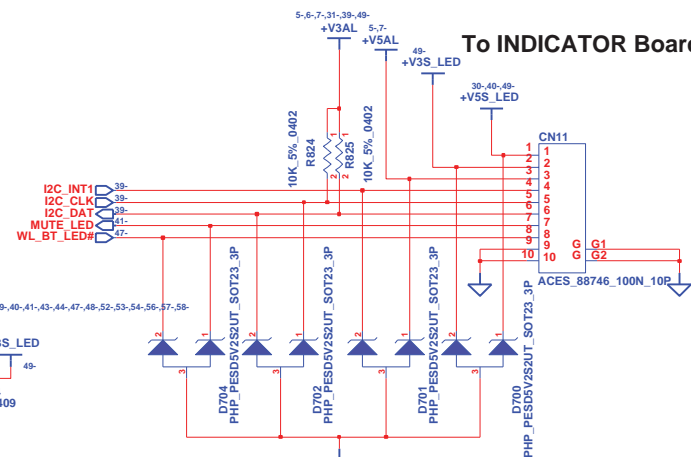
SATA LED & HDD-HALTED LED



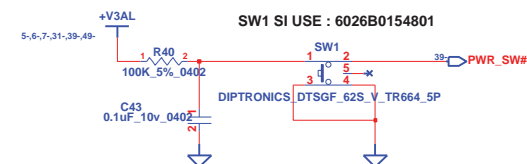
LID SWITCH



To INDICATOR Board

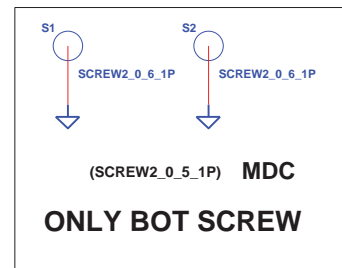
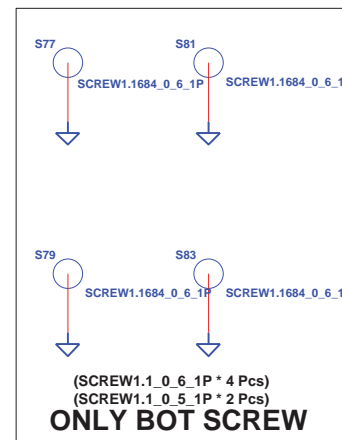
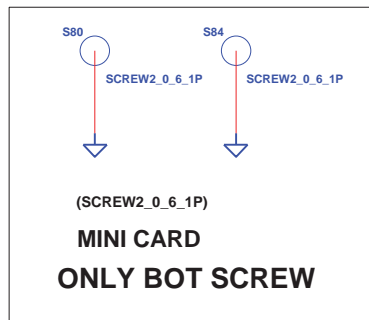
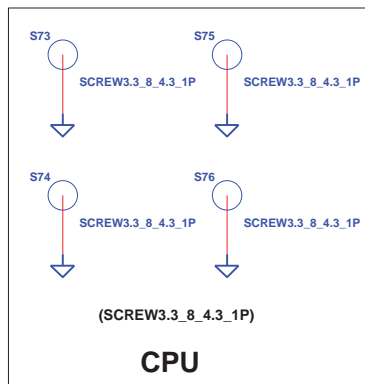
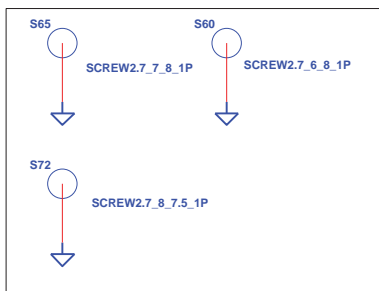
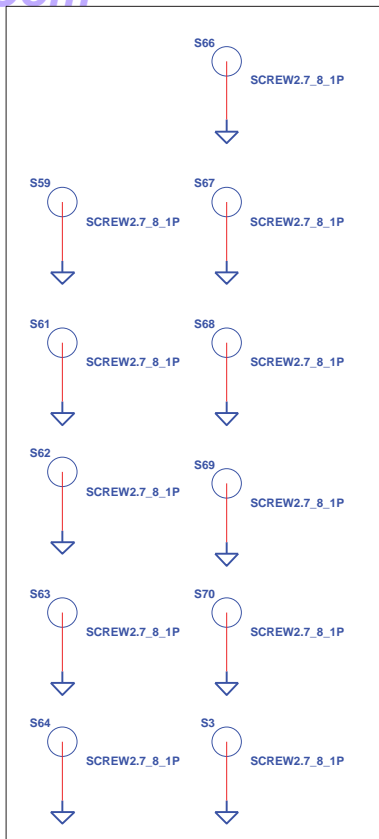
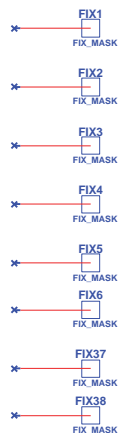


20080731 change to ESD Diode



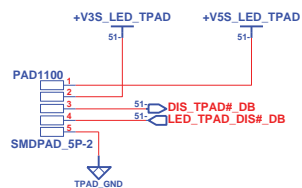
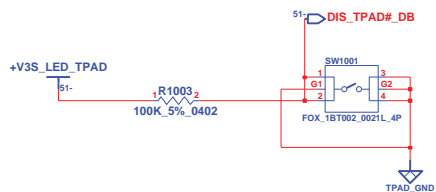
POWER BUTTON

INVENTEC			
TITLE DIABLO 2.0 BUTTON & LED			
SIZE A3	CODE CS	DOC NUMBER 1310A22155-0-MTR	REV A02
CHANGE by Wang, Alan		31-Jul-2008	
SHEET		49 OF 61	

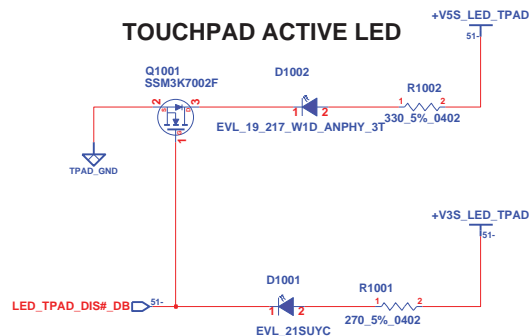


INVENTEC			
TITLE			
DIABLO 2.0			
SCREW			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	I310A22155-0-MTR	A02
CHANGE by		Wang, Alan	31-Jul-2008
SHEET		50	OF 61

TOUCHPAD-DISABLE BUTTOM_DB



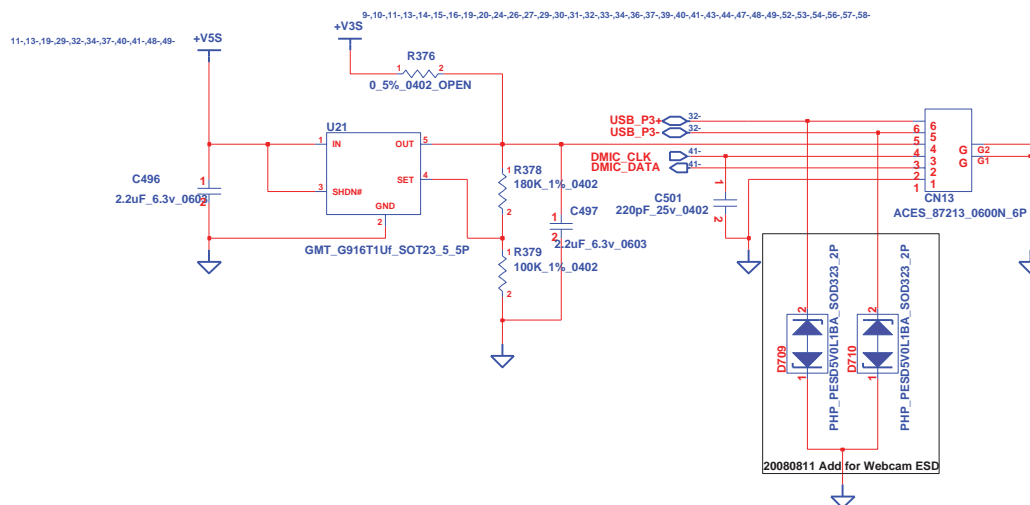
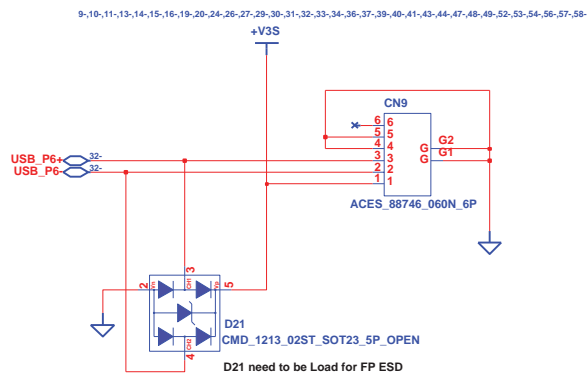
TOUCHPAD ACTIVE LED



TouchPad Disable DB

INVENTEC			
TITLE DIABLO 2.0 FINGER PRINTER CONN.			
SIZE A3	CODE CS	DOC. NUMBER I310A22192-0-MTR	REV A02
CHANGE by Wang, Alan		31-Jul-2008	
SHEET 51		OF 61	

To Finger Print Reader Board



Webcam CONN & DIGITAL MIC

INVENTEC

TITLE			
DIABLO 2.0			
WEB CAM & FP CONN			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A22155-0-MTR	A02
SHEET		52	OF 61

CHANGE by Wang, Alan 11-Aug-2008

A

B

C

D

E

F

A

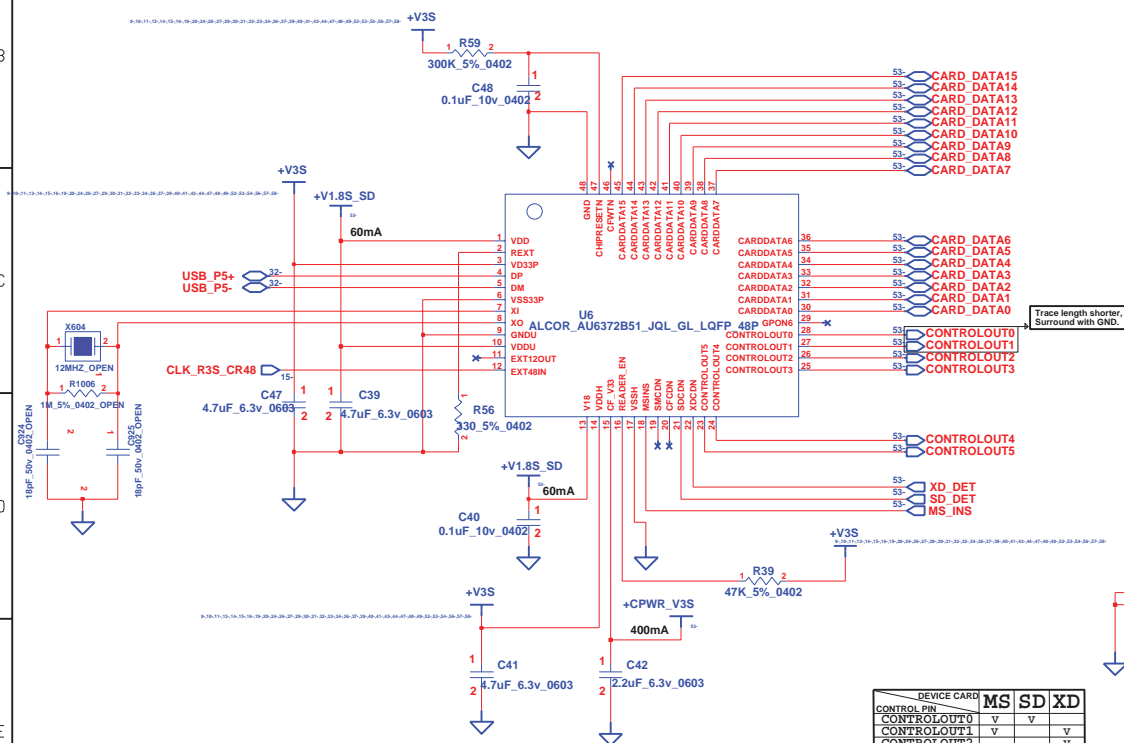
B

C

D

E

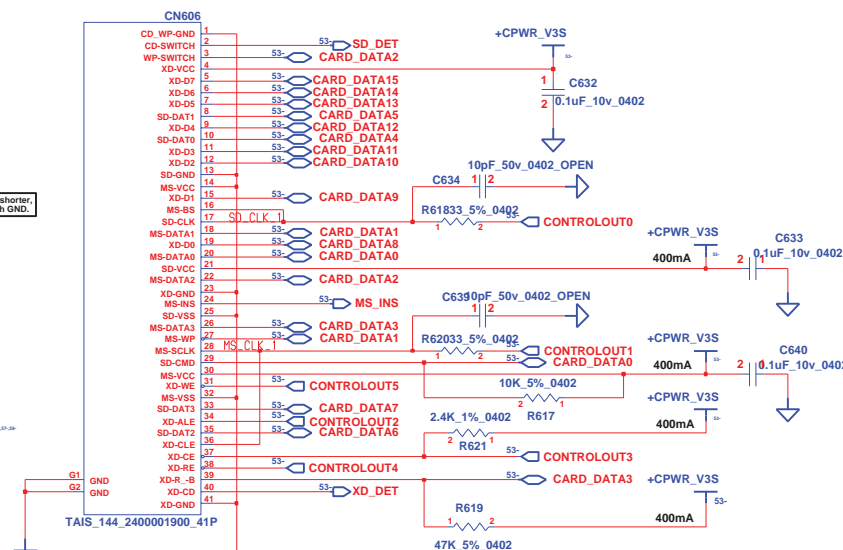
F



DEVICE CARD	MS	SD	XD
CONTROL PIN			
CONTROLOUT0	V	V	
CONTROLOUT1	V	V	
CONTROLOUT2			V
CONTROLOUT3			V
CONTROLOUT4			V
CONTROLOUT5			V

DEVICE CARD	MS	SD	XD
DATA PIN			
CARDDATA0	V	V	V
CARDDATA1	V	V	V
CARDDATA2	V	V	V
CARDDATA3	V	V	V
CARDDATA4	V	V	V
CARDDATA5	V	V	V
CARDDATA6	V	V	V
CARDDATA7	V	V	V
CARDDATA8	V	V	V
CARDDATA9	V	V	V
CARDDATA10	V	V	V
CARDDATA11	V	V	V
CARDDATA12	V	V	V
CARDDATA13	V	V	V
CARDDATA14	V	V	V
CARDDATA15	V	V	V

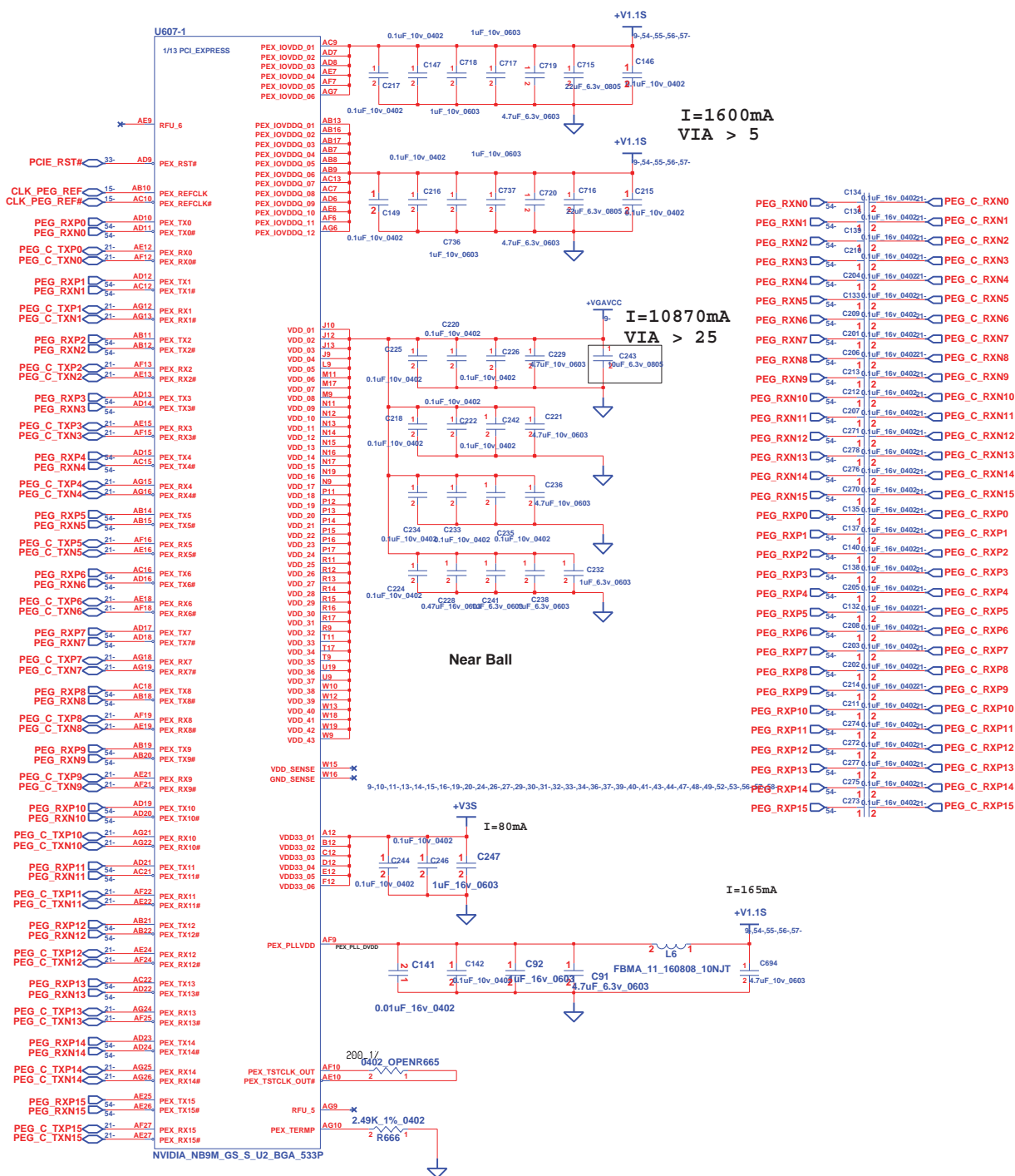
Cardreader Conn

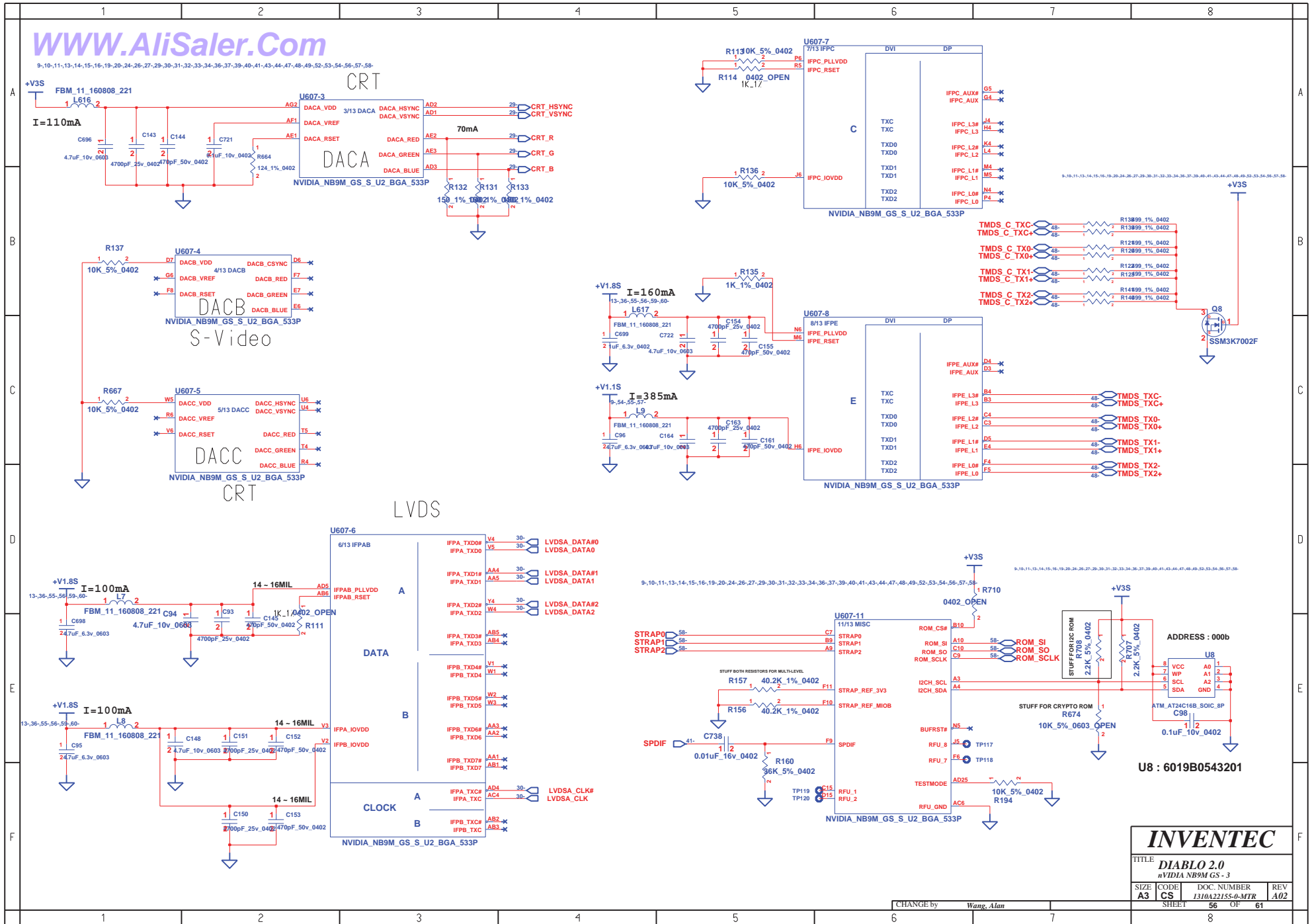


INVENTEC

TITLE			
DIABLO 2.0			
5 in 1 Card Reader Conn			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A22155-0-MTR	A02
SHEET		53	OF 61

CHANGE by Wang, Alan 31-Jul-2008







GB1 Family GPU Strap Options

Physical Strapping Pin	Power Rail	Logical Strapping Bit 3	Logical Strapping Pin 2	Logical Strapping Pin 1	Logical Strapping Pin 0
ROM_SO	+V3S	XCLK_277	TVMODE [2]	TVMODE [1]	TVMODE [0]
ROM_SCLK	+V3S	PCI_DEVID [4]	SUB_VENDOR	SLOT_CLK_CFG	PEX_PLL_EN_TERM100
ROM_SI	+V3S	RAMCFG [3]	RAMCFG [2]	RAMCFG [1]	RAMCFG [0]
STRAP2	+V3S	PCI_DEVID [3]	PCI_DEVID [2]	PCI_DEVID [1]	PCI_DEVID [0]
STRAP1	+V3S	3GIO_PADCFG [3]	3GIO_PADCFG [2]	3GIO_PADCFG [1]	3GIO_PADCFG [0]
STRAP0	+V3S	USER [3]	USER [2]	USER [1]	USER [0]

Logical Strap Bit Mapping

Resistor Values	Pull - up to VDD	Pull - down to GND
5K ohm	1000	0000
10K ohm	1001	0001
15K ohm	1010	0010
20K ohm	1011	0011
25K ohm	1100	0100
30K ohm	1101	0101
35K ohm	1110	0110
45K ohm	1111	0111

NB9M-GS RAMCFG[3:0]

32M X 16 GDDR2	RAMCFG [3]	RAMCFG [2]	RAMCFG [1]	RAMCFG [0]
Samsung	0	1	0	1
Qimonda	0	1	1	0
Hynix	0	1	1	1

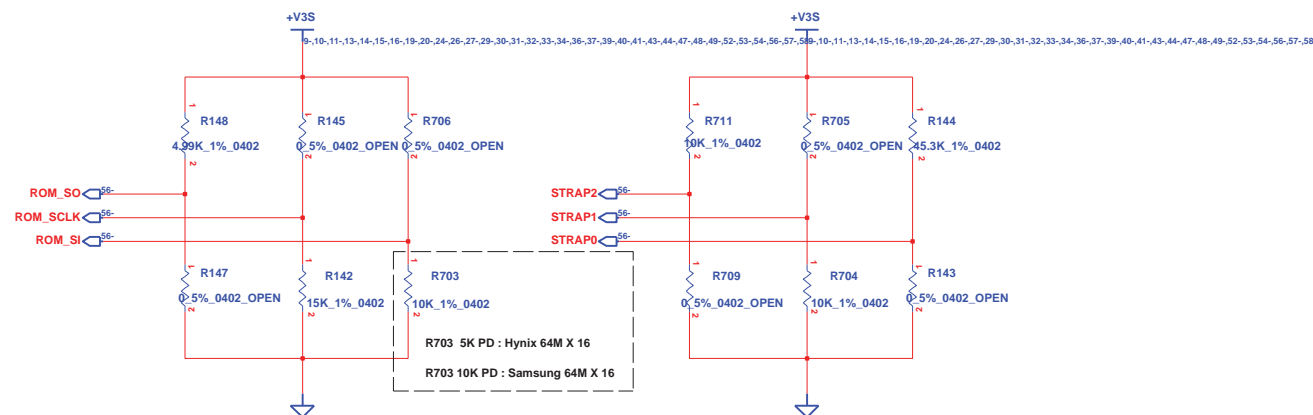
64M X 16 GDDR2	RAMCFG [3]	RAMCFG [2]	RAMCFG [1]	RAMCFG [0]
Samsung	0	0	0	1
Qimonda	0	0	1	0
Hynix	0	0	0	0

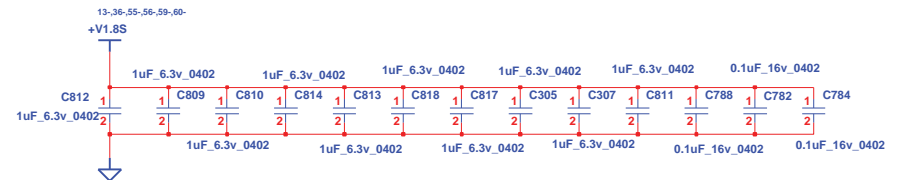
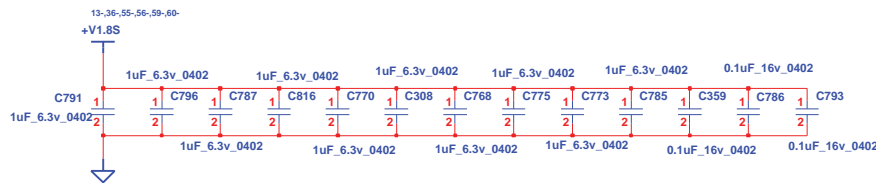
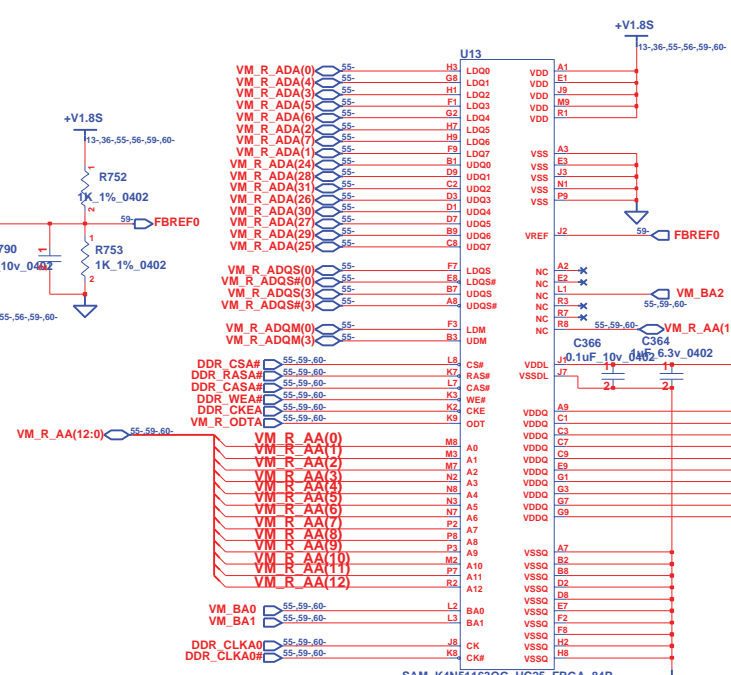
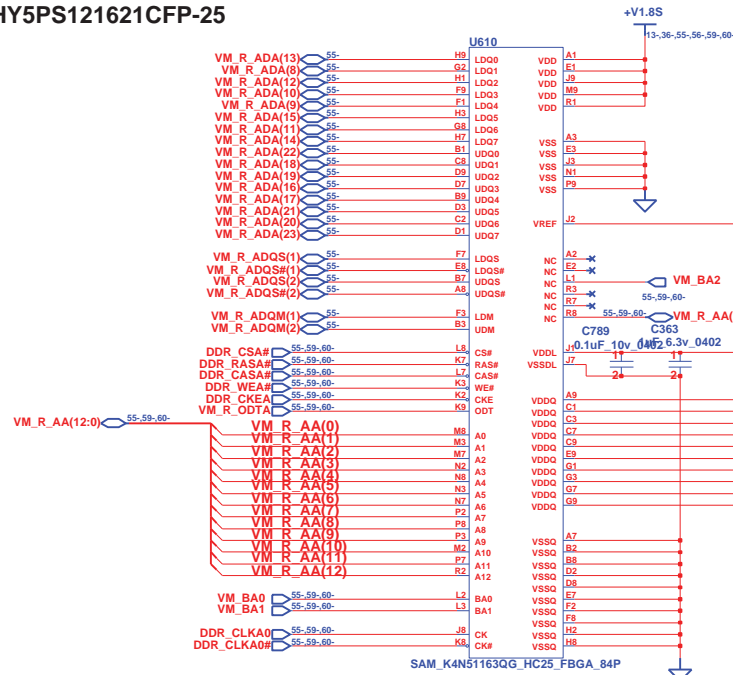
GB1 Family GPU Strap Options (DIABLO 2.0) with Samsung GDDR2 64M X 16

Physical Strapping Pin	Power Rail	Logical Strapping Bit 3	Logical Strapping Pin 2	Logical Strapping Pin 1	Logical Strapping Pin 0
ROM_SO	+V3S	1	1	1	0
ROM_SCLK	+V3S	0	0	1	0
ROM_SI	+V3S	0	0	0	1
STRAP2	+V3S	1	0	0	1
STRAP1	+V3S	0	0	0	1
STRAP0	+V3S	1	1	1	1

GB1 Family GPU Strap Options (DIABLO 2.0) with Hynix GDDR2 64M X 16

Physical Strapping Pin	Power Rail	Logical Strapping Bit 3	Logical Strapping Pin 2	Logical Strapping Pin 1	Logical Strapping Pin 0
ROM_SO	+V3S	1	1	1	0
ROM_SCLK	+V3S	0	0	1	0
ROM_SI	+V3S	0	0	0	0
STRAP2	+V3S	1	0	0	1
STRAP1	+V3S	0	0	0	1
STRAP0	+V3S	1	1	1	1





INVENTEC

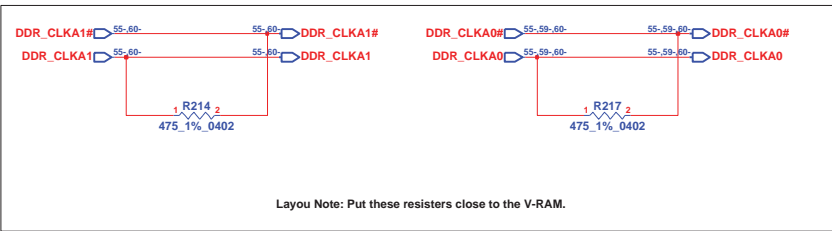
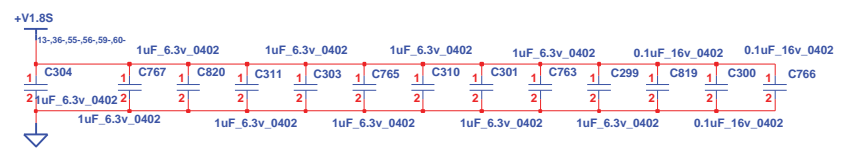
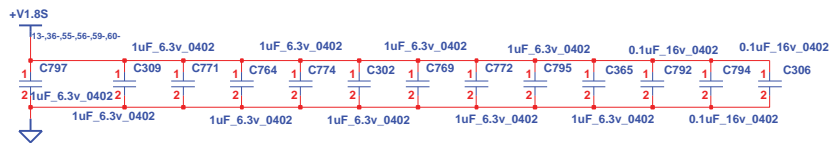
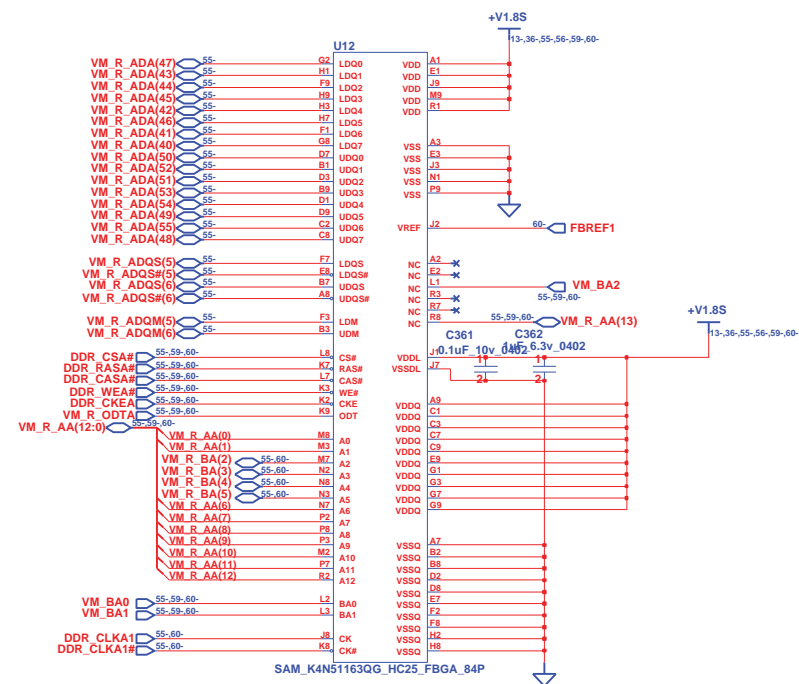
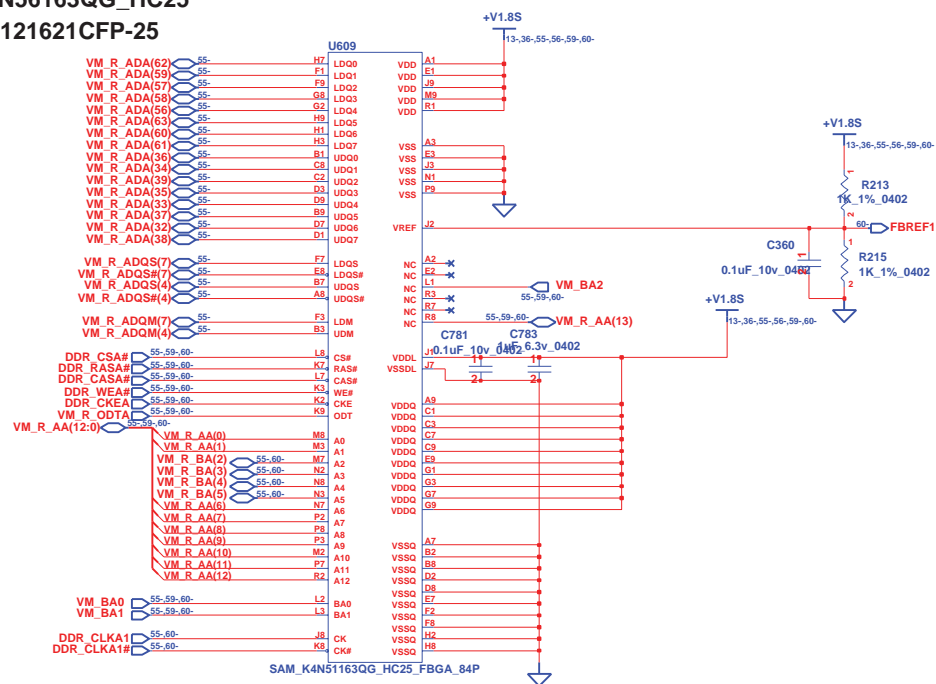
TITLE
DIABLO 2.0
VRAM (1/2)

SIZE CODE DOC. NUMBER REV
A3 CS 1310A221S5-0-MTR A02

CHANGE by Wang, Alan

SHEET 59 OF 61

DIABLO 2.0 USE MEMORY RVL
SANSUNG K4N56163QG_HC25
HYNIX HY5PS121621CFP-25



Layout Note: Put these resistors close to the V-RAM

INVENTEC

TITLE	DIABLO 2.0 VRAM (2/2)
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SIZE A3	CODE CS	DOC. NUMBER <i>1310A22155-0-MTR</i>	REV A02
SHEET		60	OF 61

CHANGE by	Wang, Alan
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60 OF 61