

QT6 BLOCK DIAGRAM

LAYER 1 : TOP
LAYER 2 : SGND
LAYER 3 : IN1
LAYER 4 : IN2
LAYER 5 : VCC
LAYER 6 : BOT

Cable Docking

VGA
RJ-45
CIR/Pwr btn
SPDIF Out
Stereo MIC
Headphone Jack
USB Port
VOL Cntr

PAGE 37

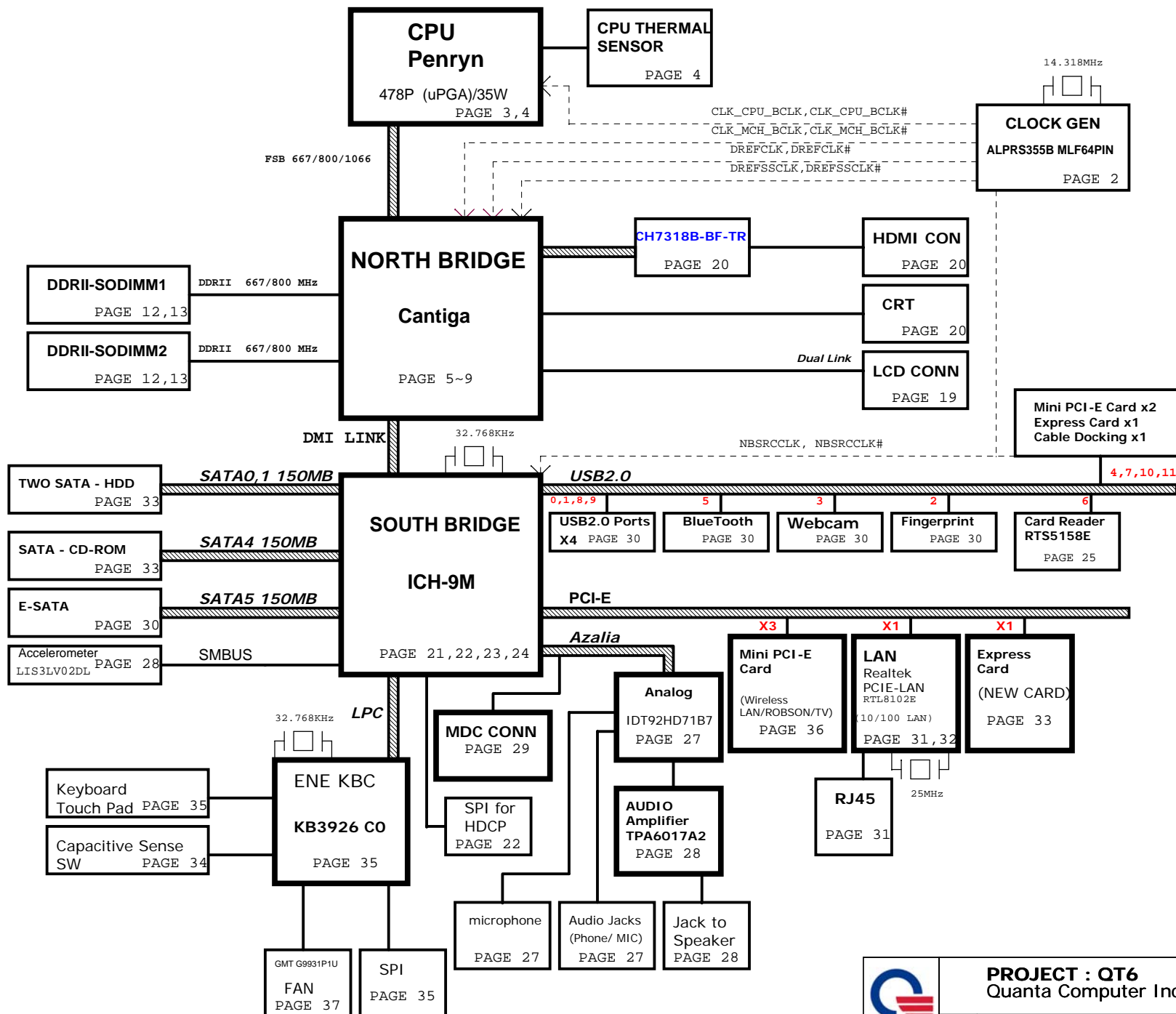
SYSTEM CHARGER(ISL6251AHAZ-T)
PAGE 38

SYSTEM POWER ISL6237IRZ-T
PAGE 39

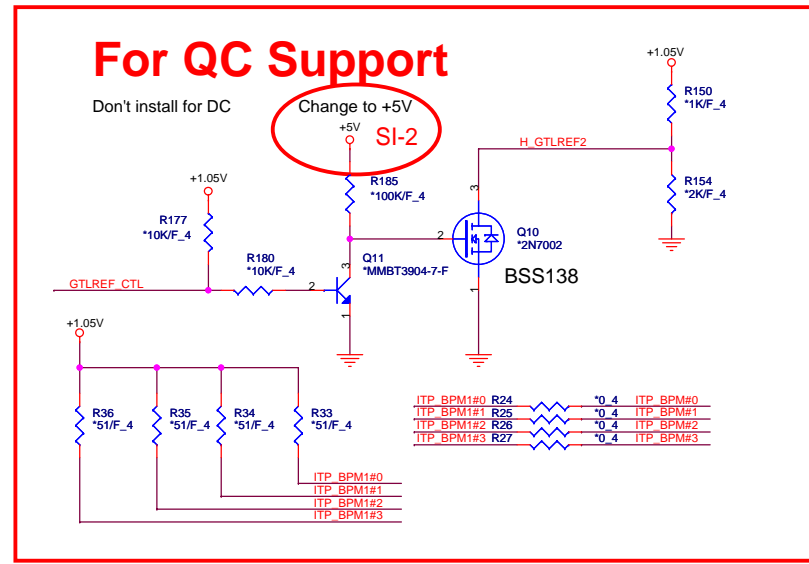
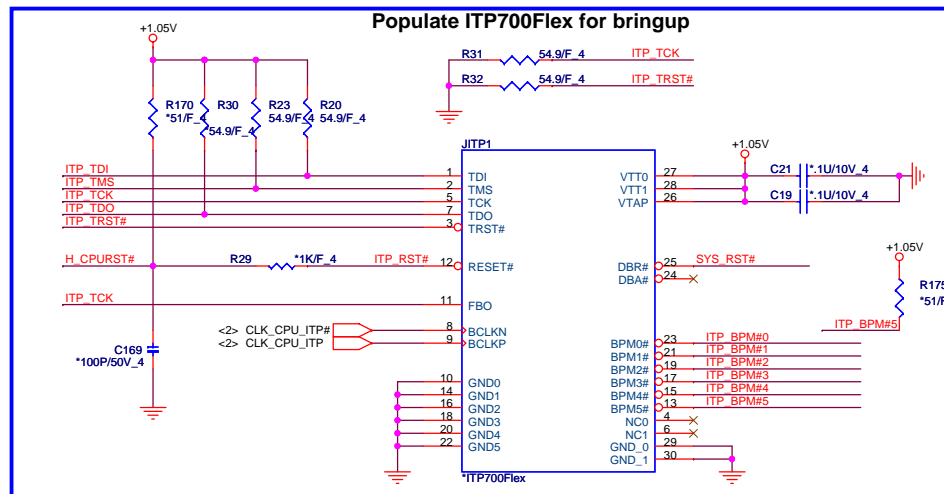
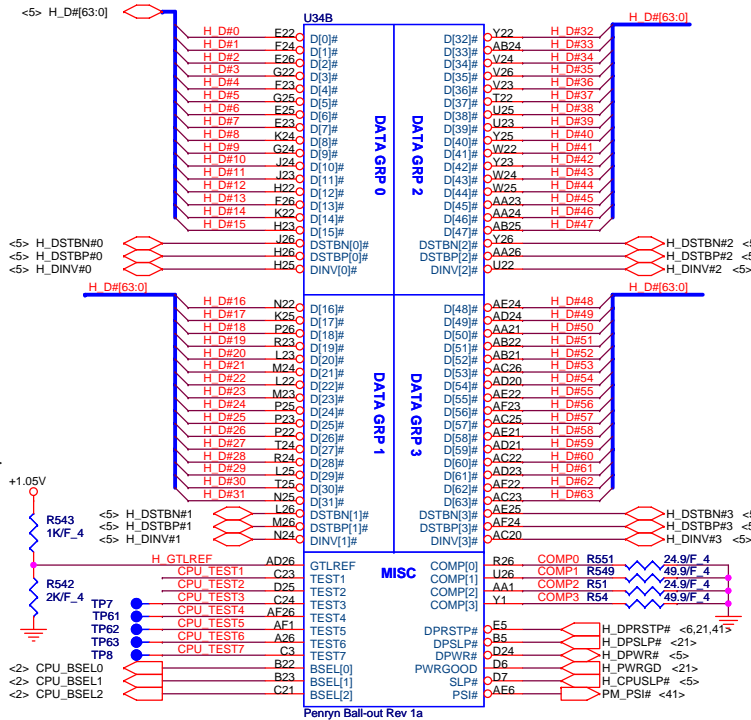
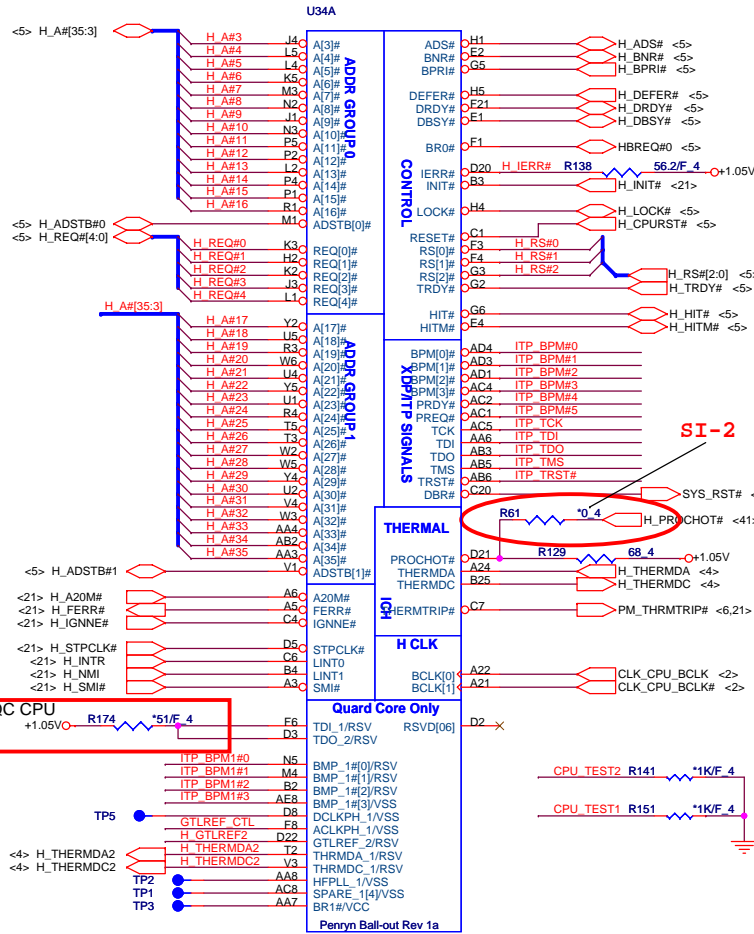
DDR II SMDRR_VTERM
1.8V/1.8VSUS(TPSS51116REGR)
PAGE 43

VCCP +1.5V AND GMCH
1.05V(RT8204)
PAGE 40

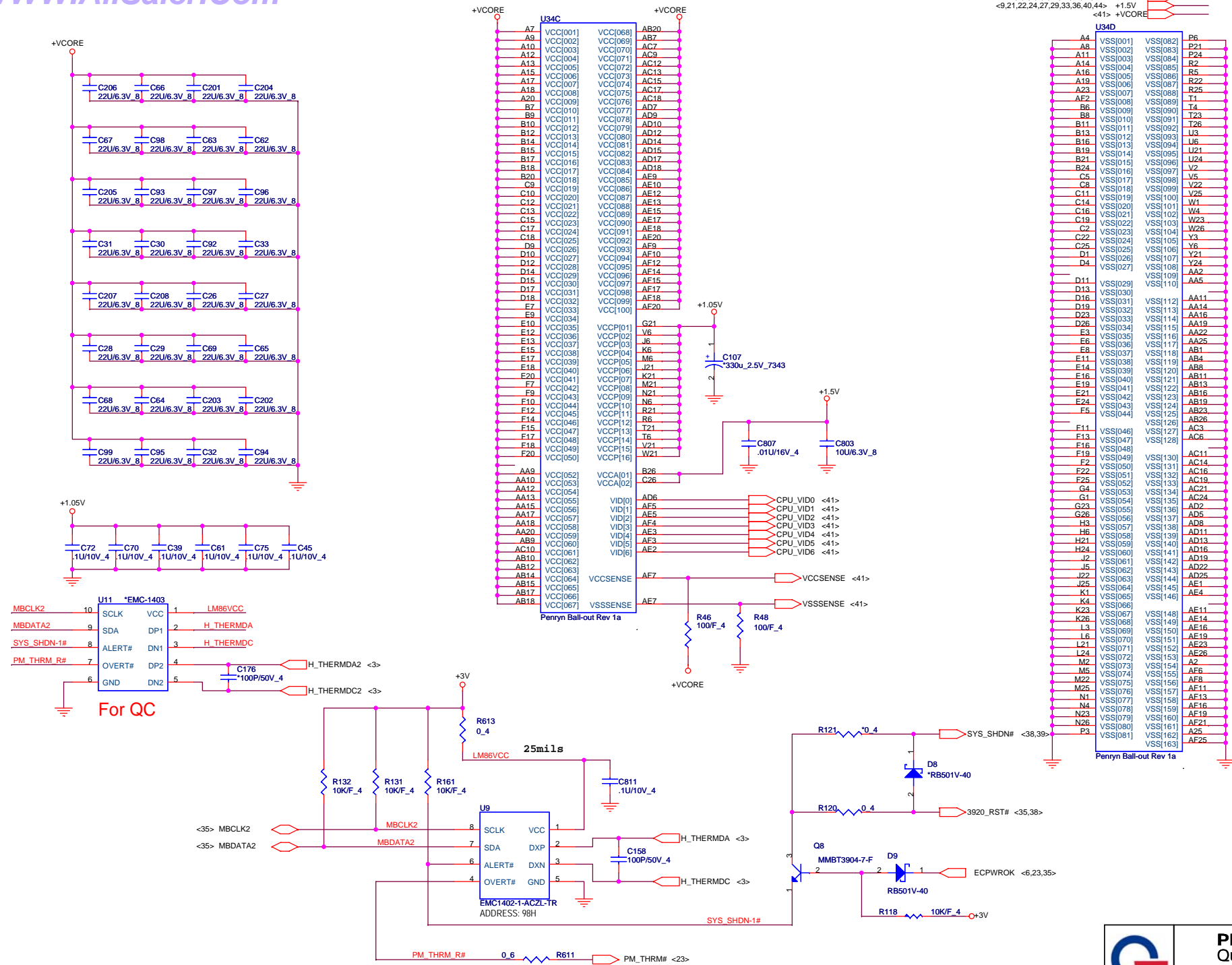
CPU CORE ISL6266A
PAGE 41

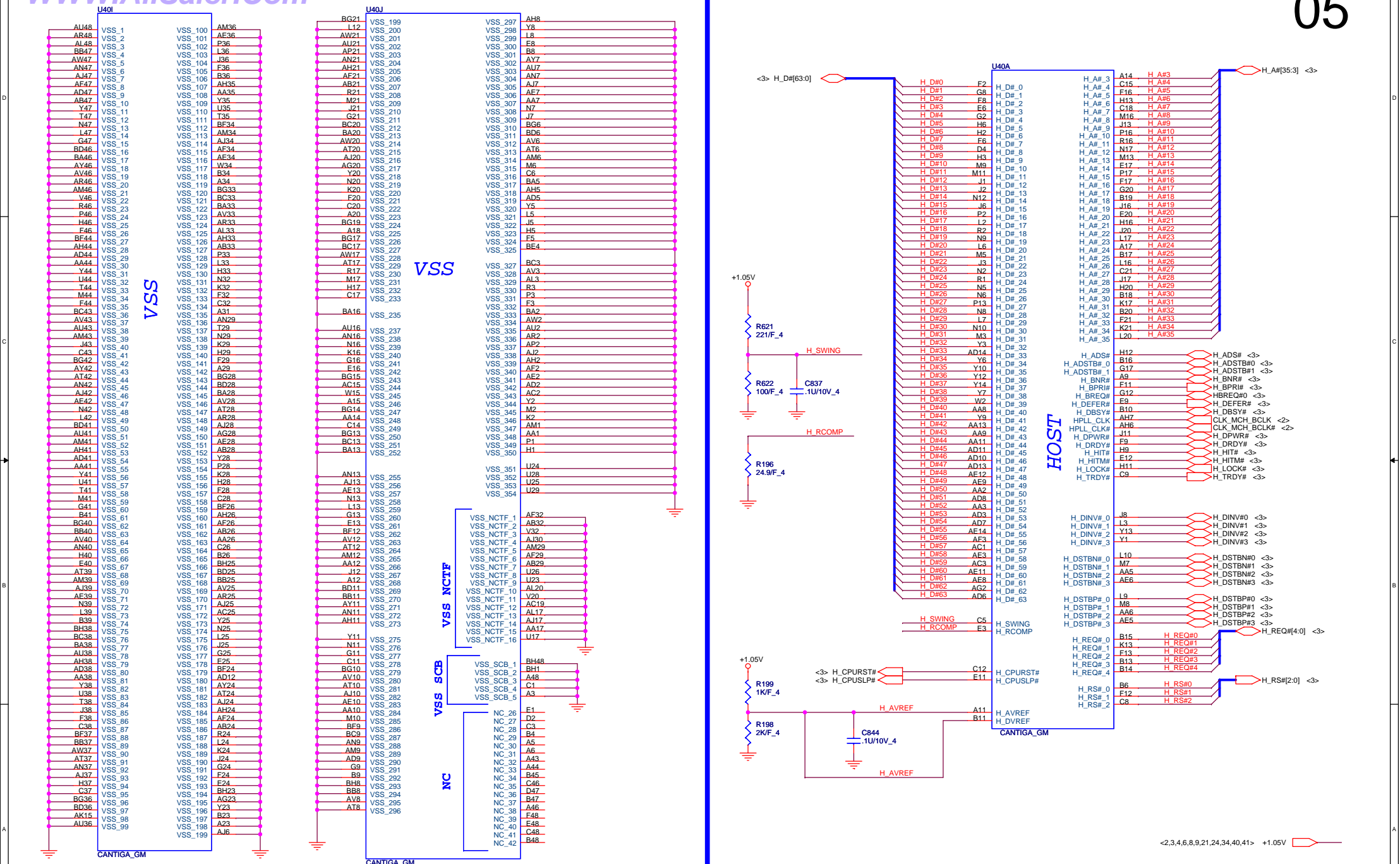


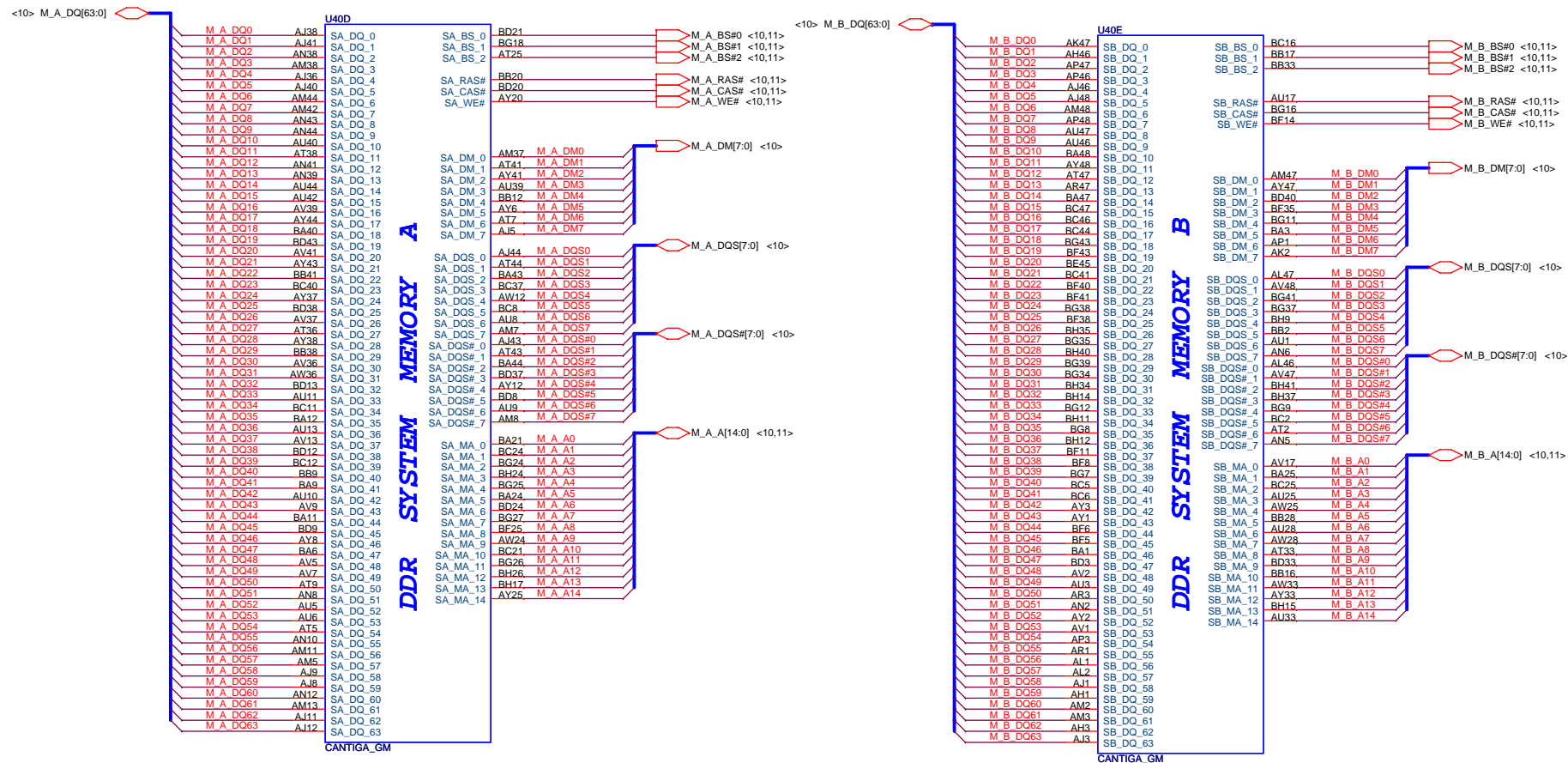




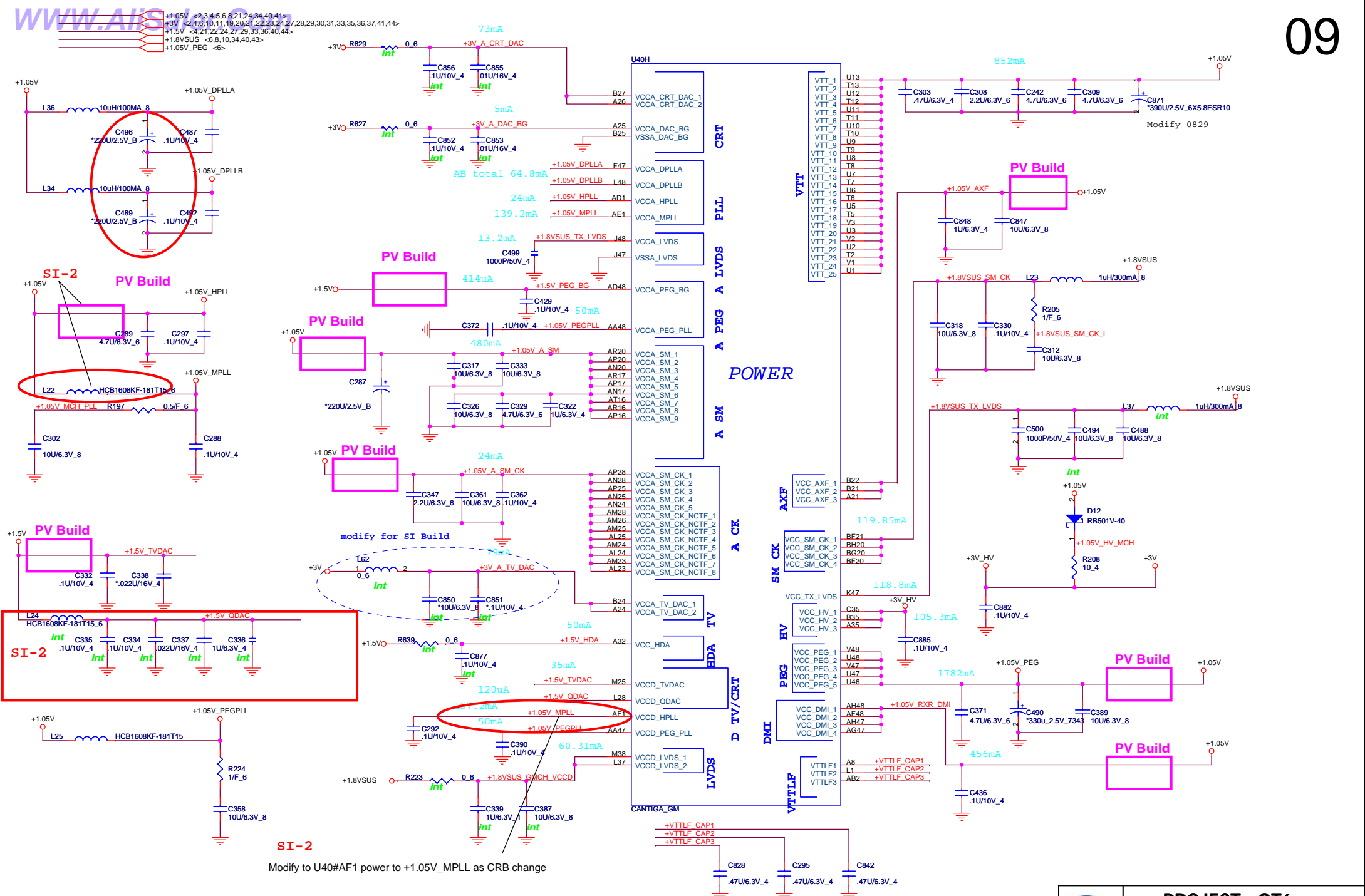
PROJECT : QT6
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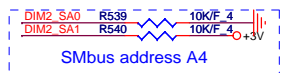
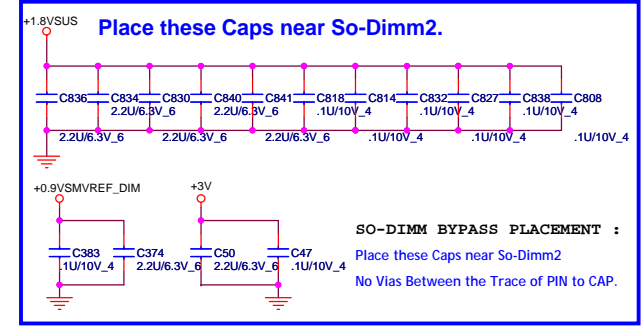
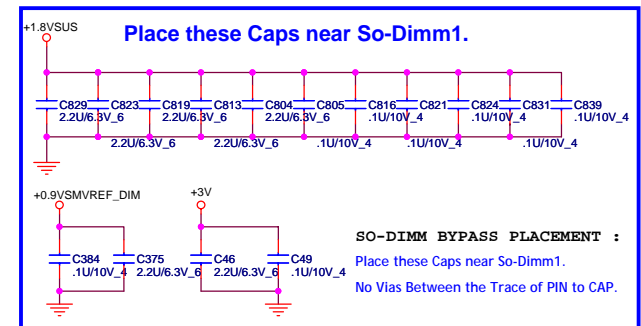
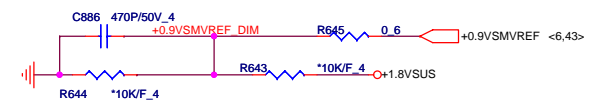
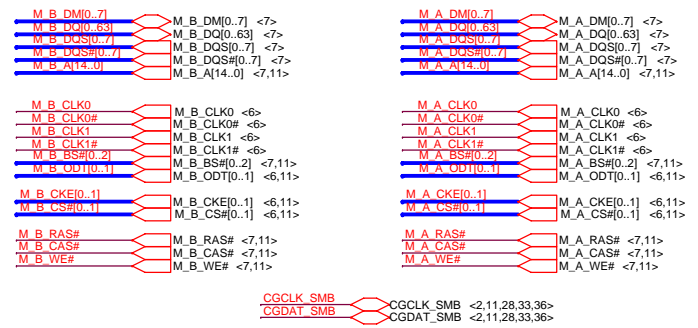
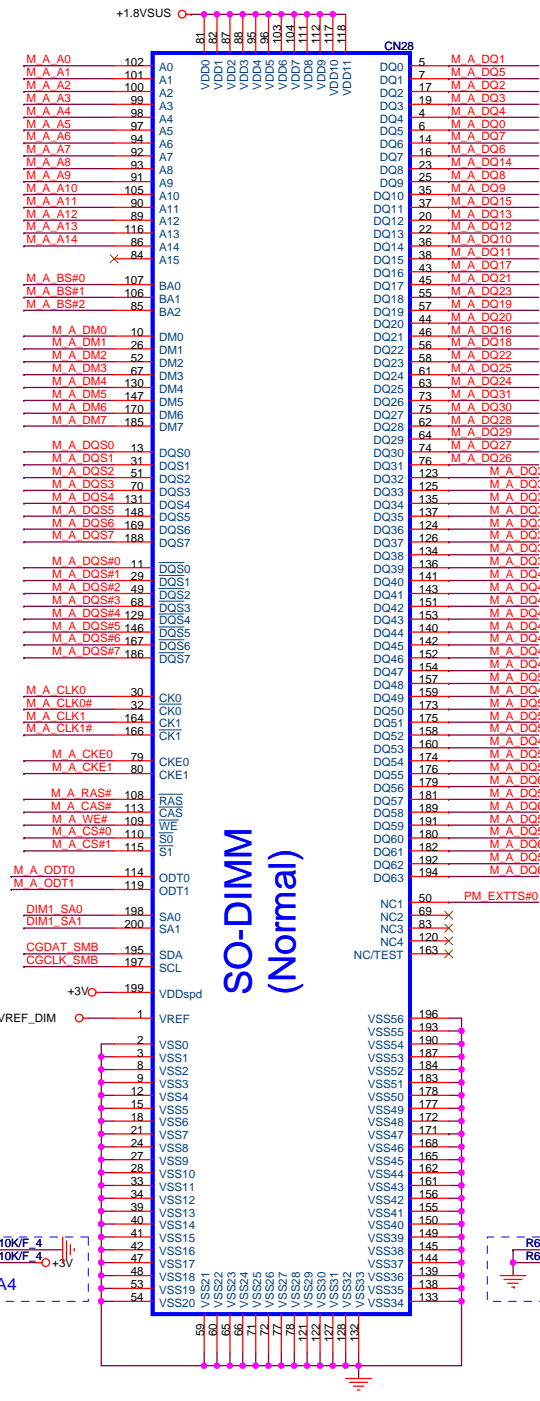
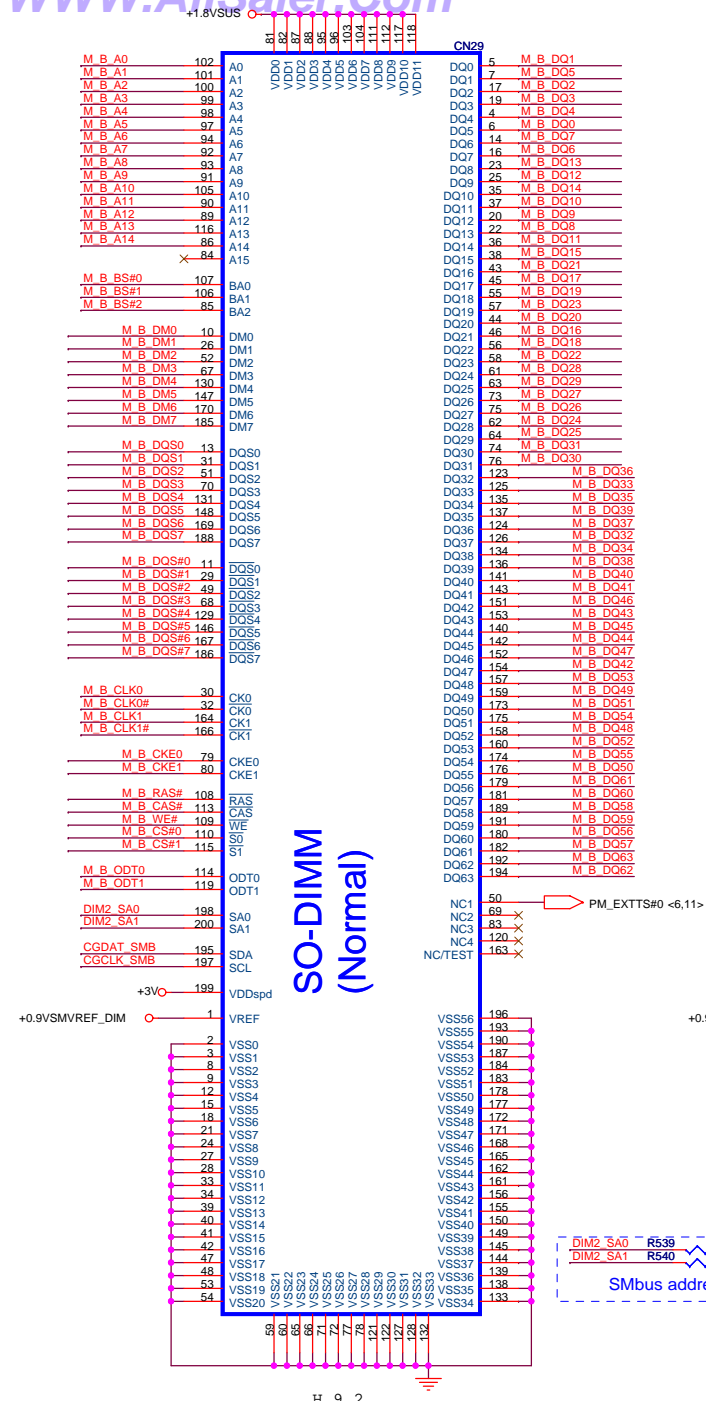






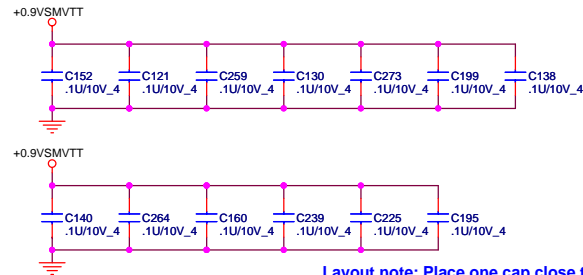


SI-2
Modify to U40#AF1 power to +1.05V_MPLL as CRB change



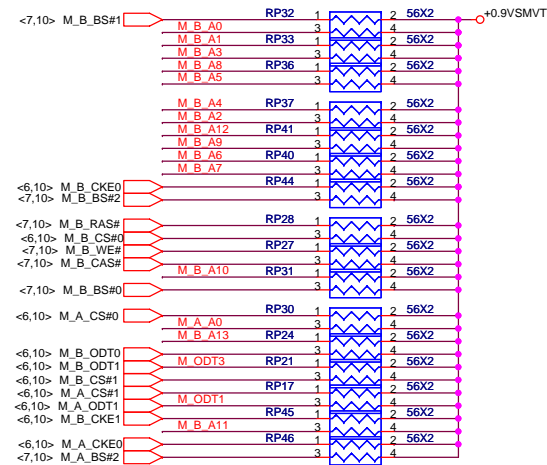
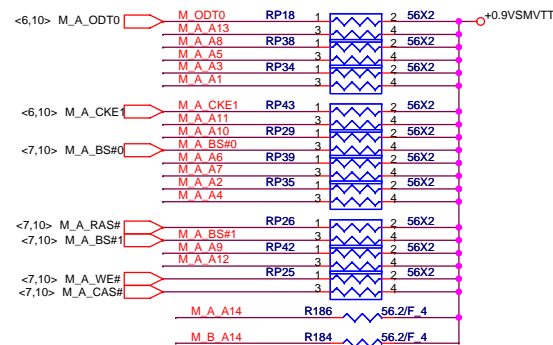
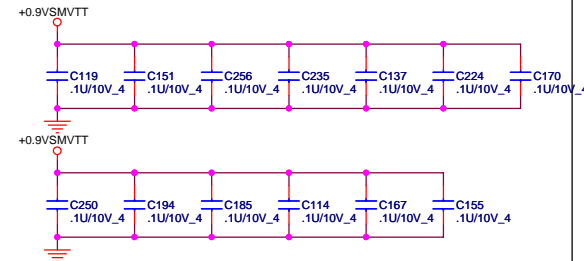
DDRII DUAL CHANNEL A,B.

DDRII A CHANNEL

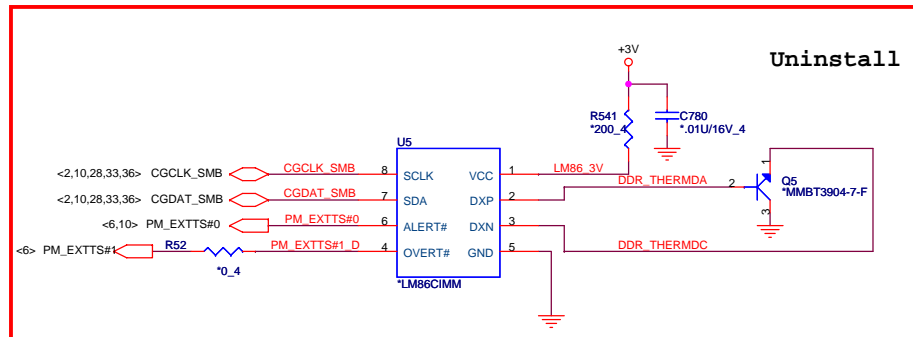


Layout note: Place one cap close to every 2 pullup resistors terminated to SMDR_VTERM


DDRII B CHANNEL





M_B_A[14..0] M_B_A[14..0] <7,10>
M_A_A[14..0] M_A_A[14..0] <7,10>



+0.9VSMVTT <43>
+3V <2,4,6,9,10,19,20,21,22,23,24,27,28,29,30,31,33,35,36,37,41,44>

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NB5


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Date: Wednesday, February 27, 2008 Sheet 14 of 44		




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Size Custom	Document Number Blank	Rev 2A
Date: Wednesday, February 27, 2008 Sheet 16 of 44		



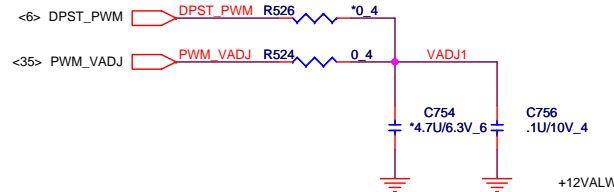
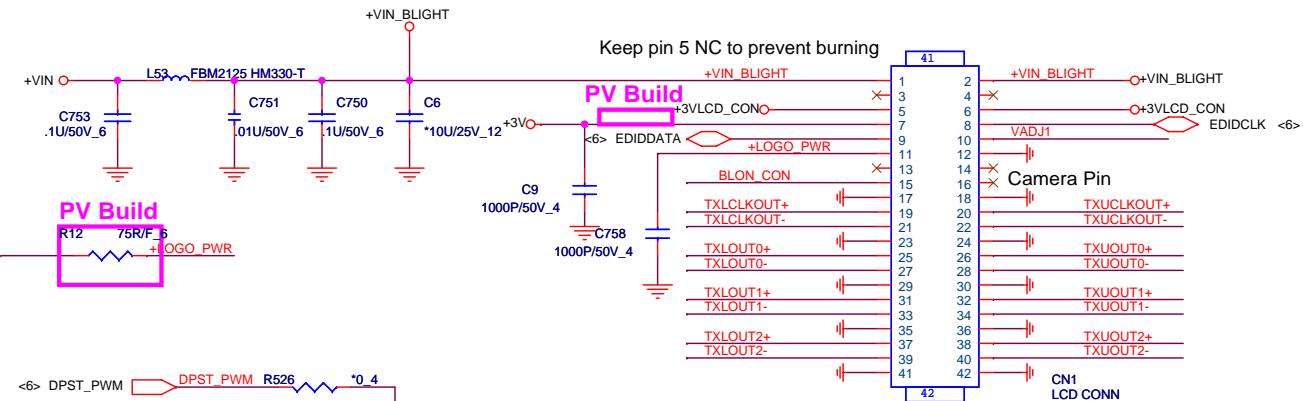
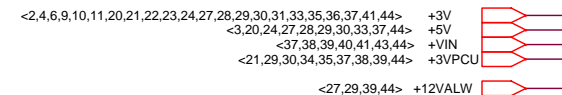
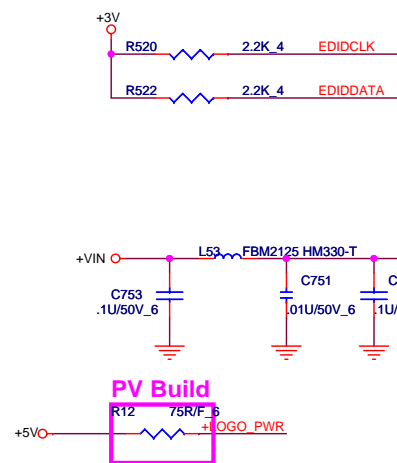
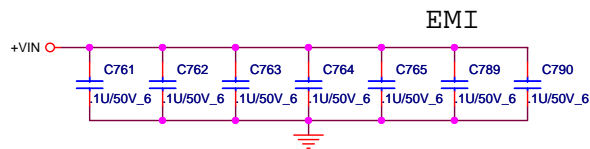
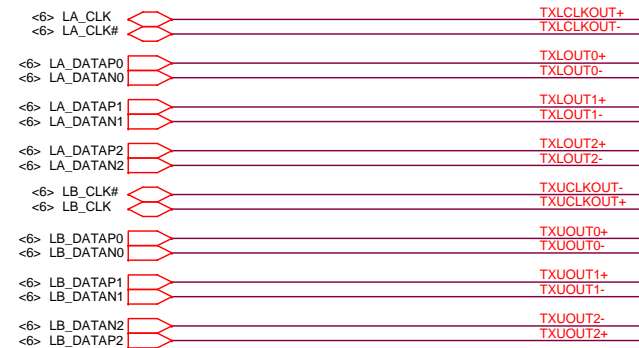
PROJECT : QT6
Quanta Computer Inc.

Size Custom	Document Number Blank	Rev 2A
Date: Wednesday, February 27, 2008 Sheet 17 of 44		

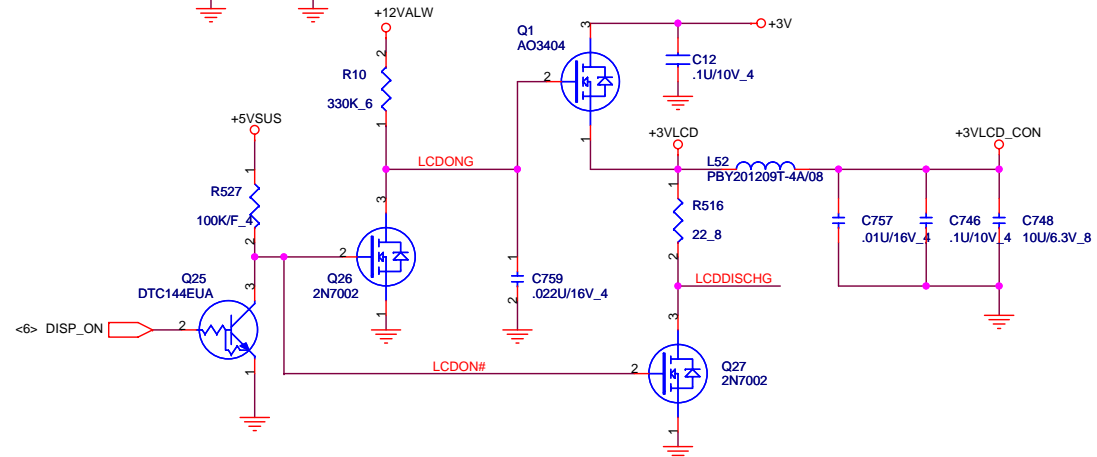
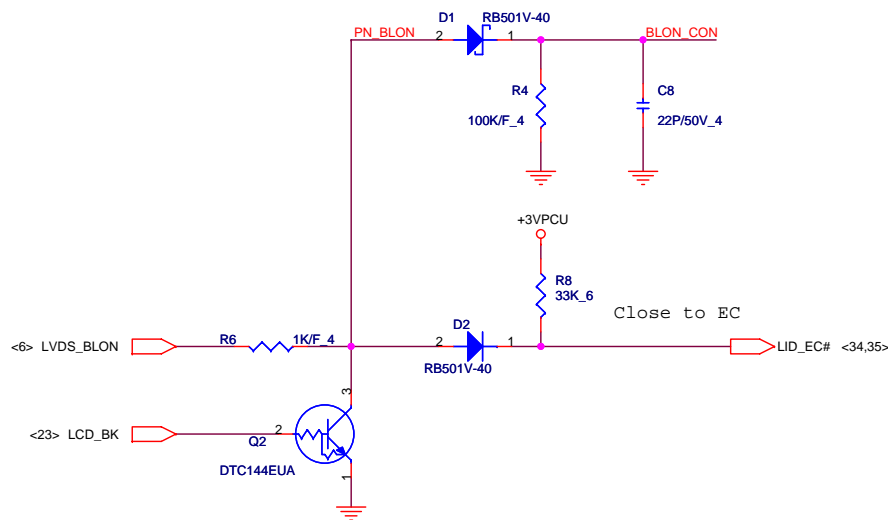



PROJECT : QT6
Quanta Computer Inc.

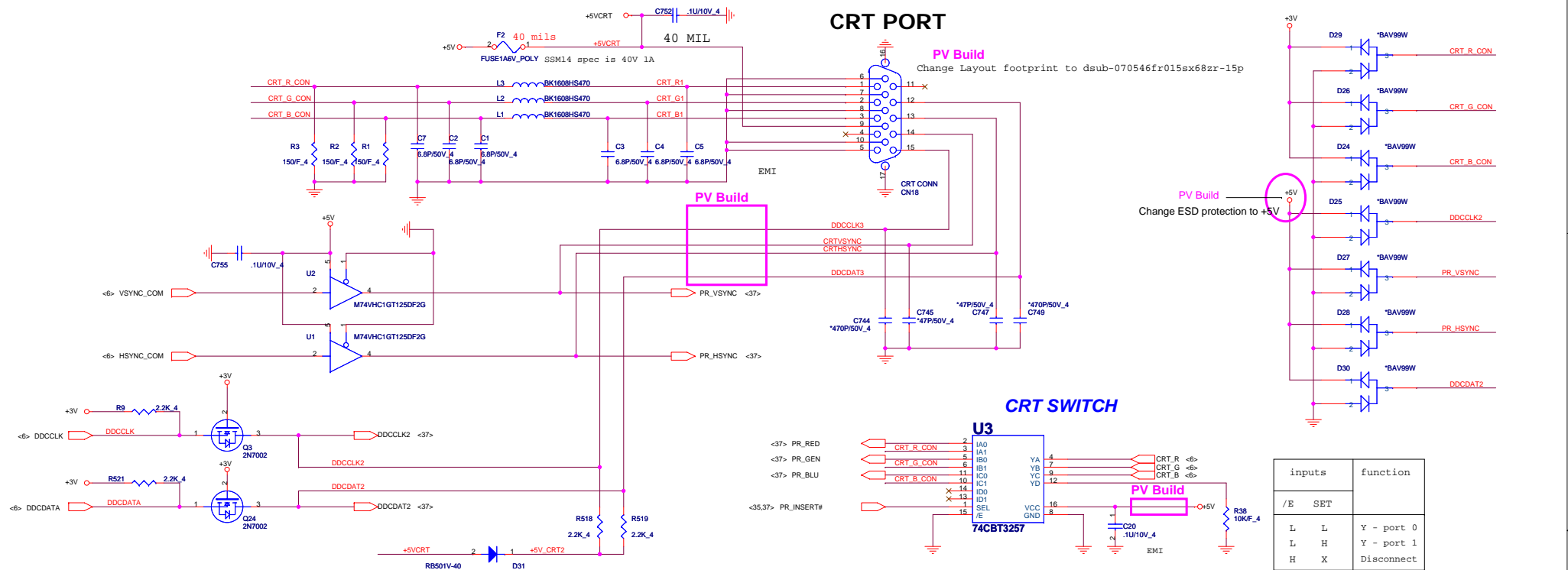
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Date: Wednesday, February 27, 2008 Sheet 18 of 44		

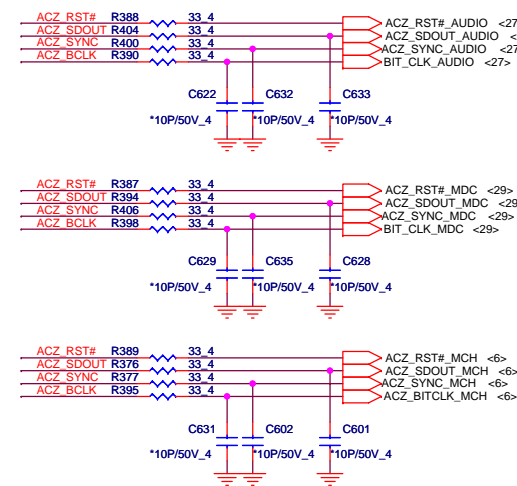


PV Build
Delete CN20,R63,C761(Remove Logo light2)



		PROJECT : QT6 Quanta Computer Inc.	
		Size B Document Number LCD CONN/Lid function	Rev 2A
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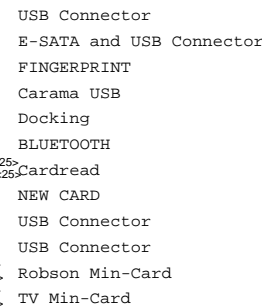


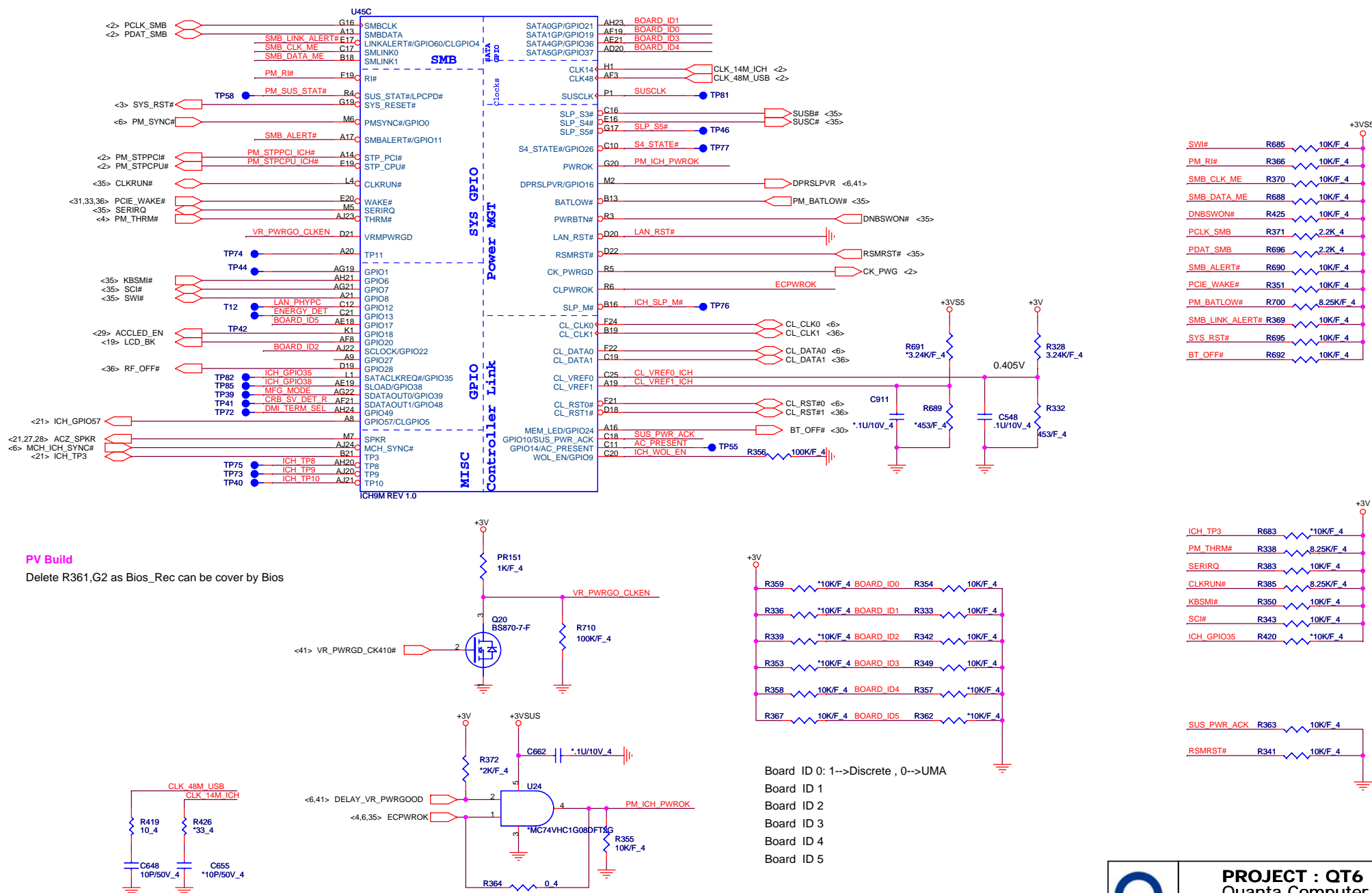


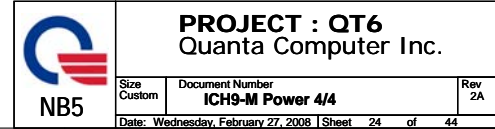
Z_SPKR	Low: Default Hi: No reboot
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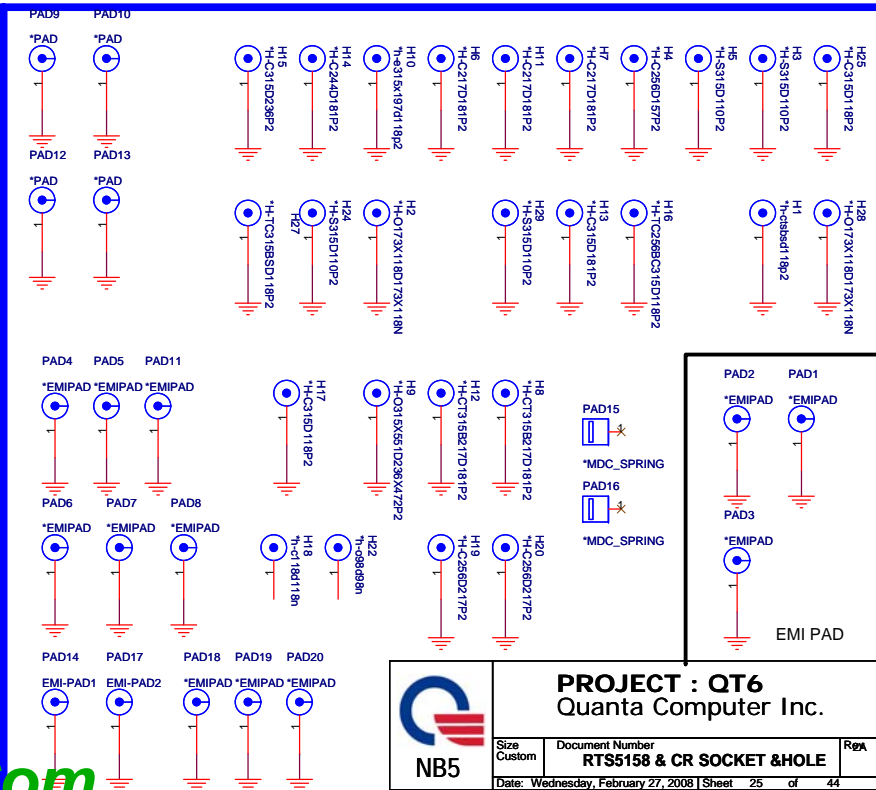
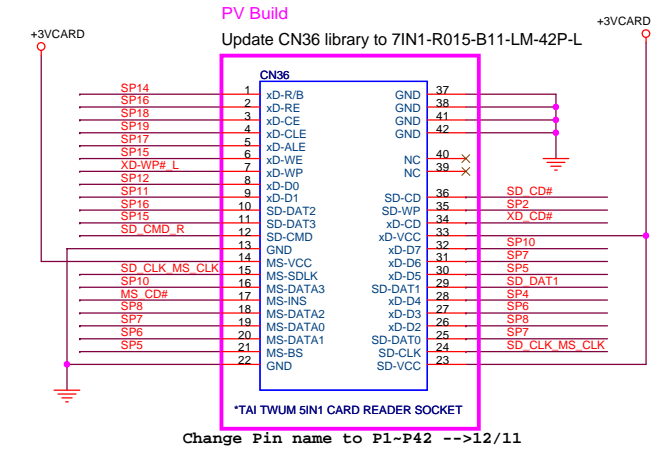
TPM physical presence

ICH_GPIO57	Low: Defau
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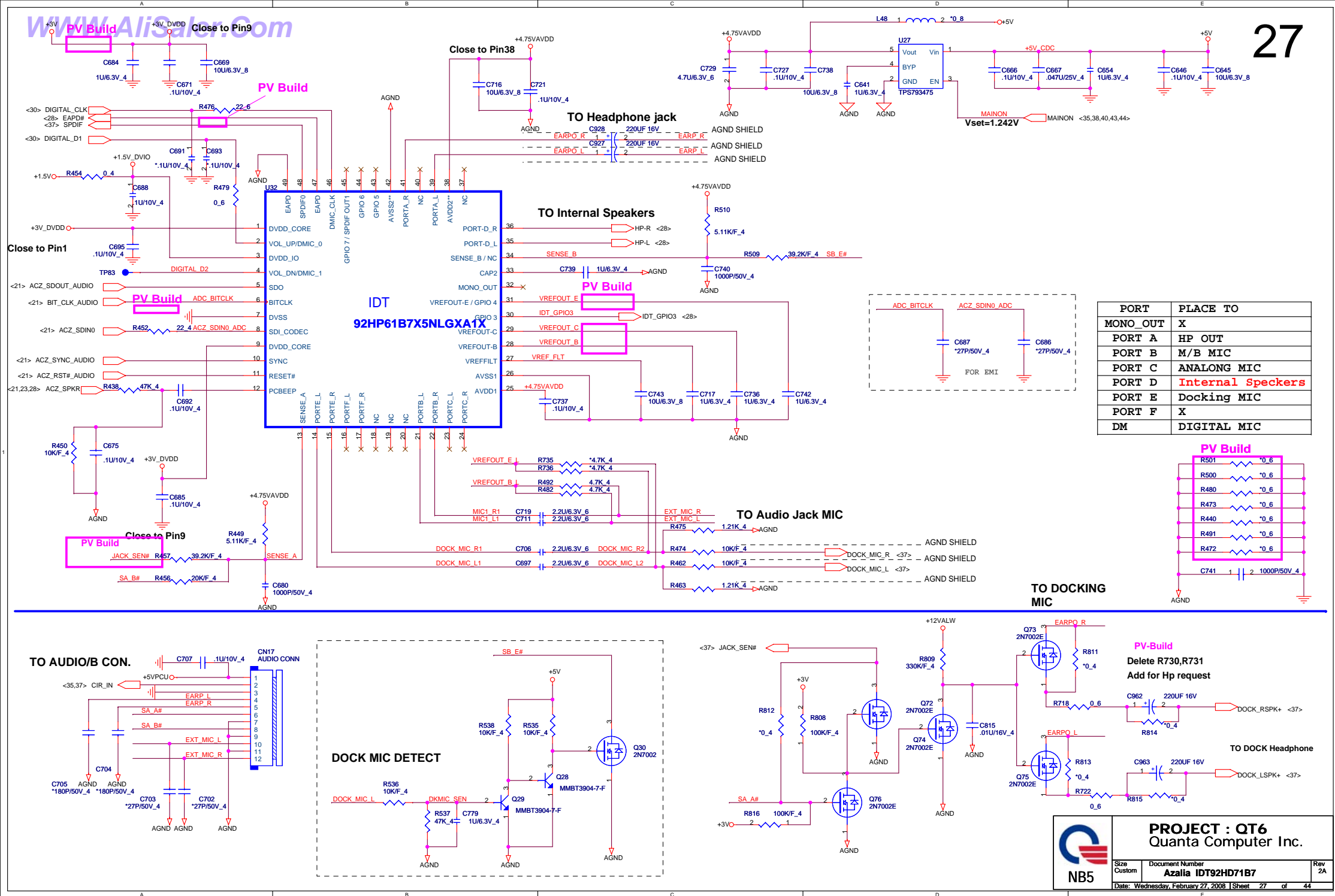


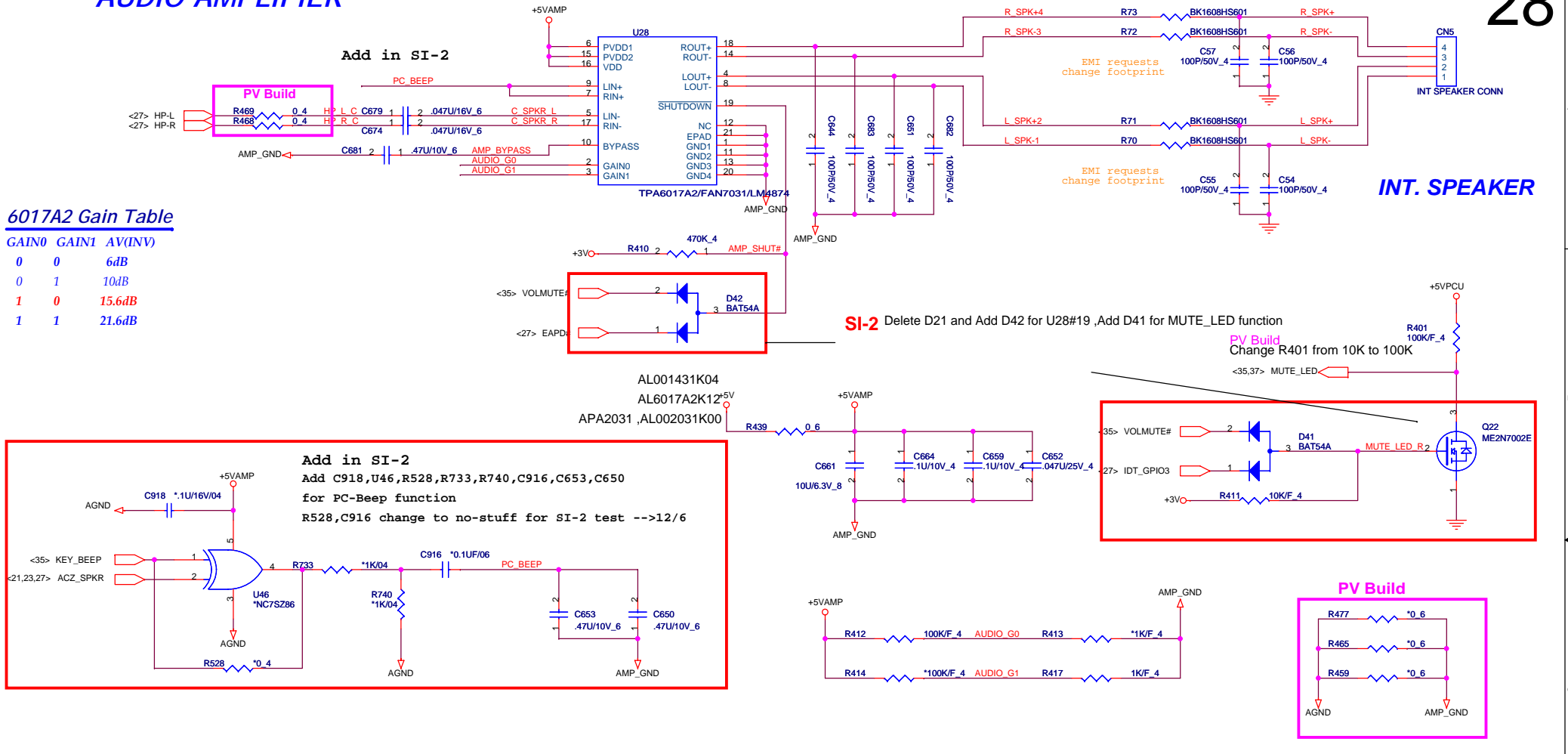


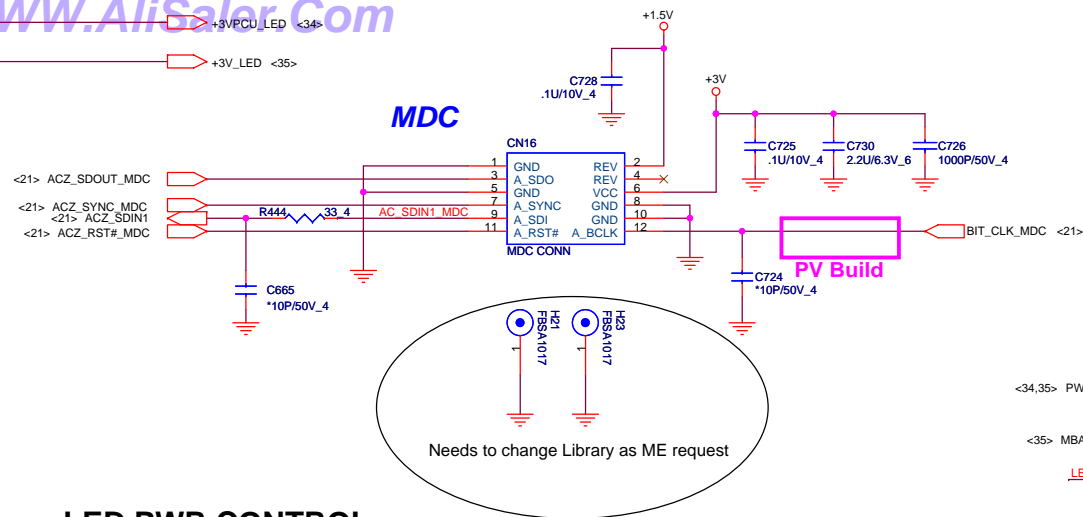


PROJECT : QT6
Quanta Computer Inc.

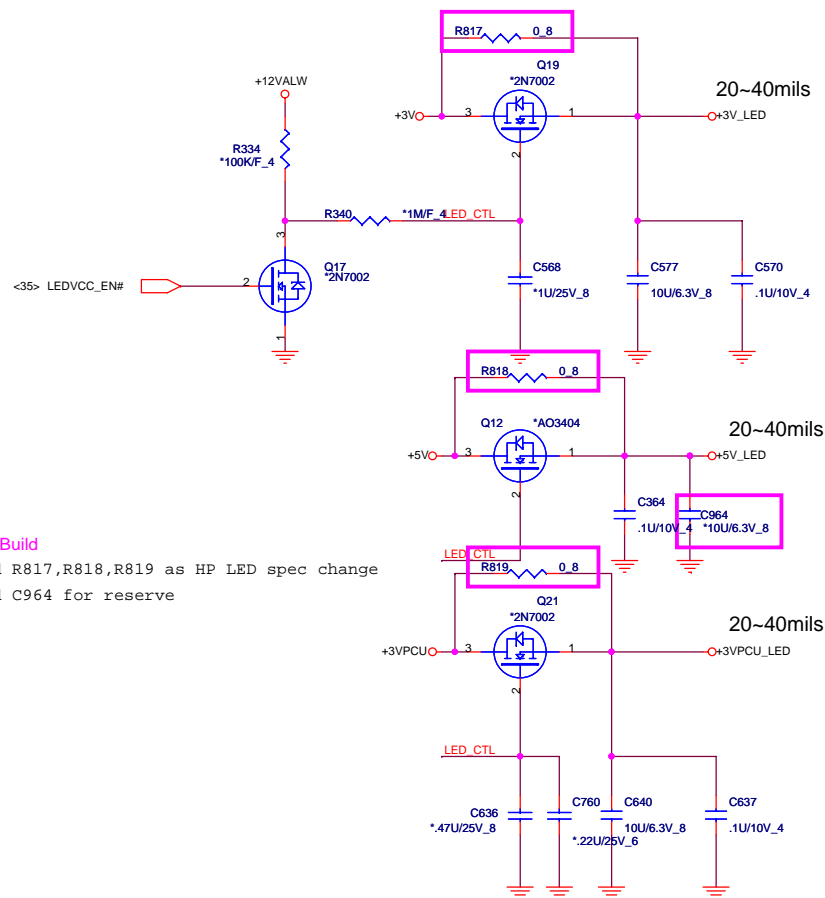
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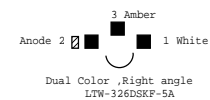
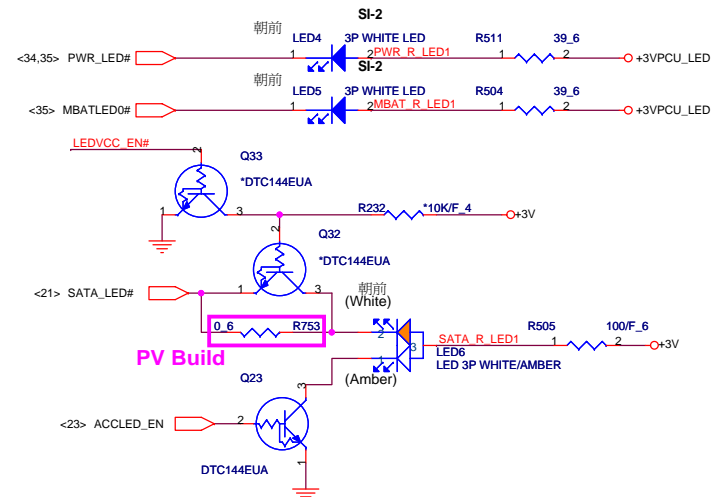


LED PWR CONTROL

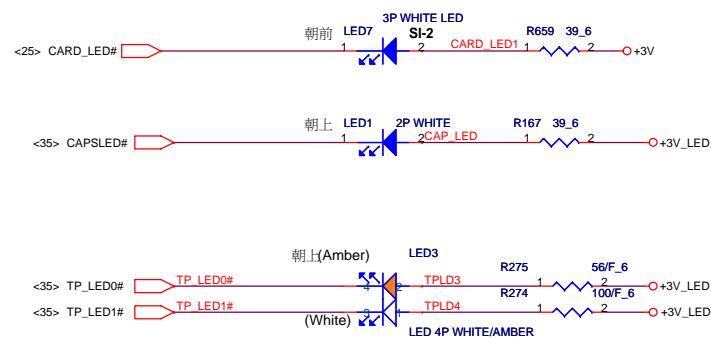


PV Build

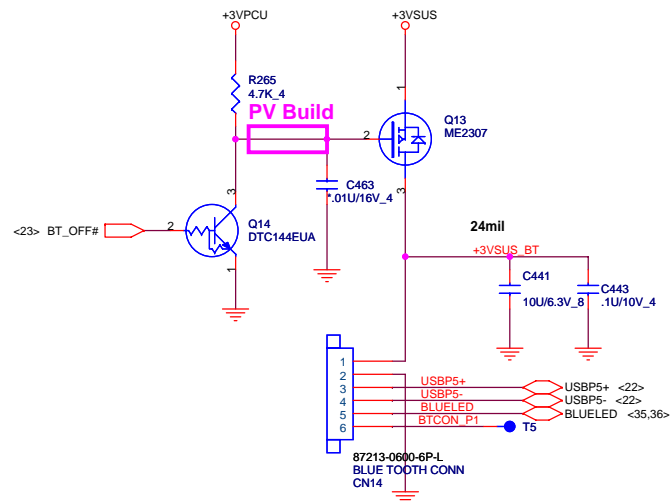
Add R817,R818,R819 as HP LED spec change
Add C964 for reserve



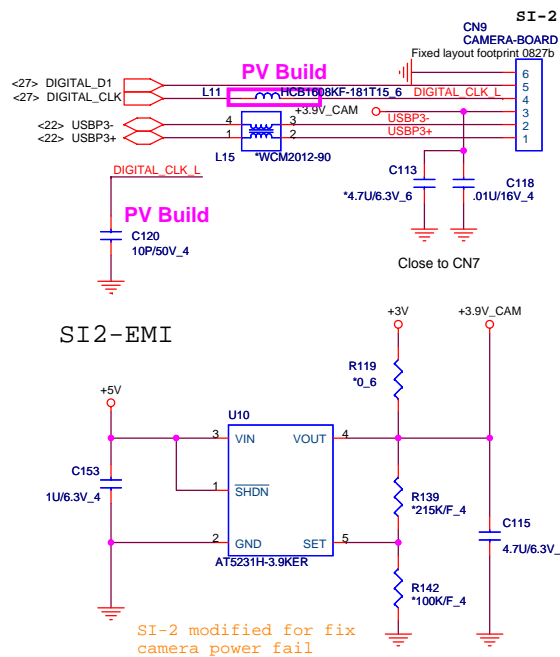
FOR CAP SW BOARD CONN



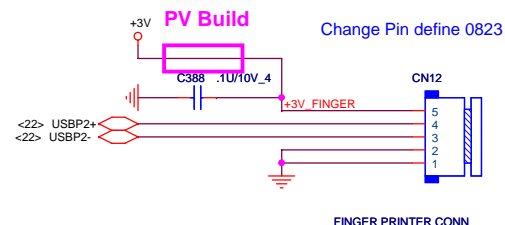
BLUETOOTH



USB CAMERA /DIGITAL MIC CONNECT



USB fingerprint CON

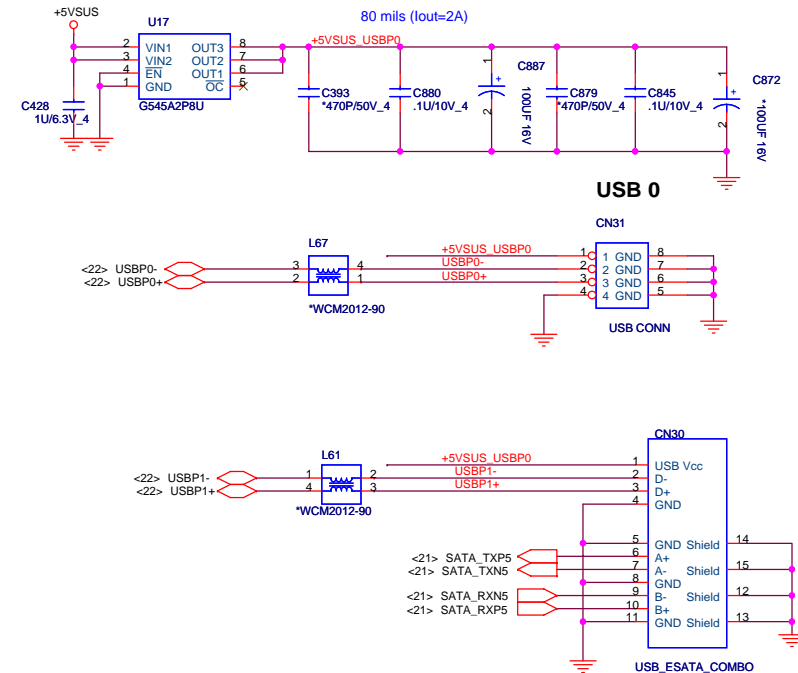


PV Build
Change CN12 to BL123-05R-5P-L-QT6-A

1. ESD GND
2. SYSTEM GND
3. USB-
4. USB+
5. USB PWR(+3V)

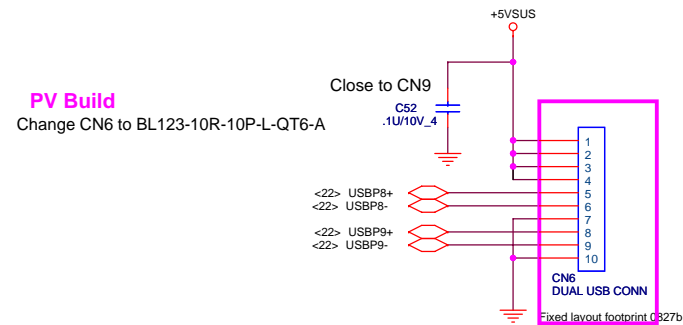
LEFT SIDE USBX1 and E-SATA/USB COMBO

30



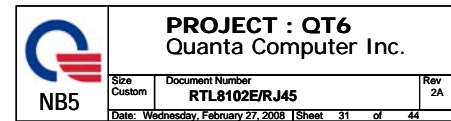
USB & ESATA

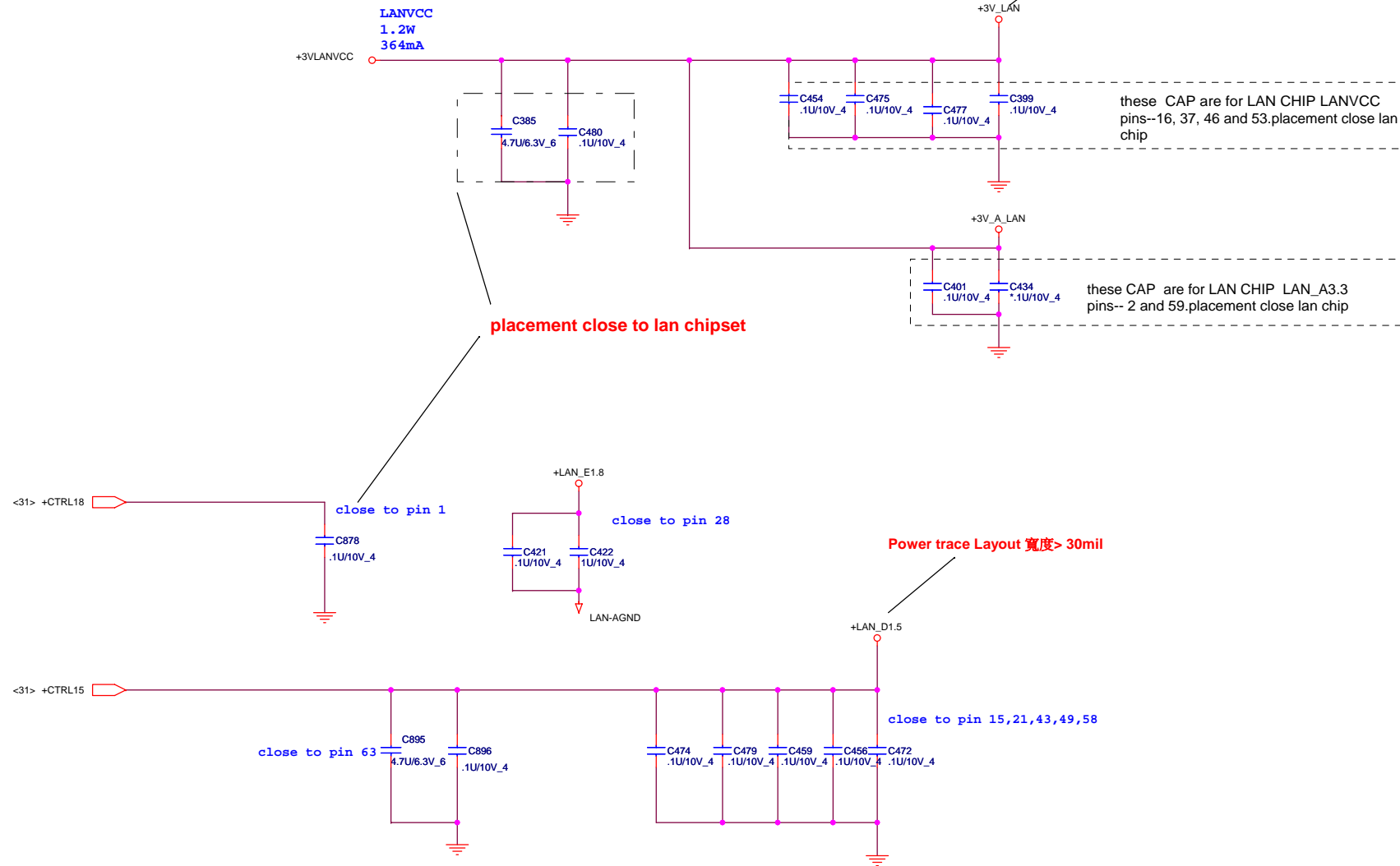
RIGHT SIDE USBX2



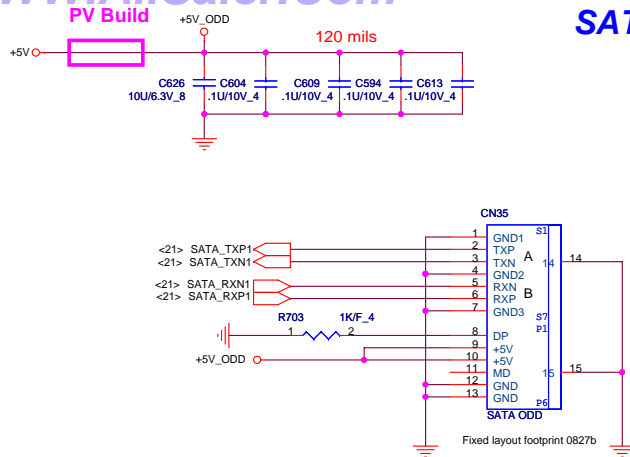
PROJECT : QT6
Quanta Computer Inc.

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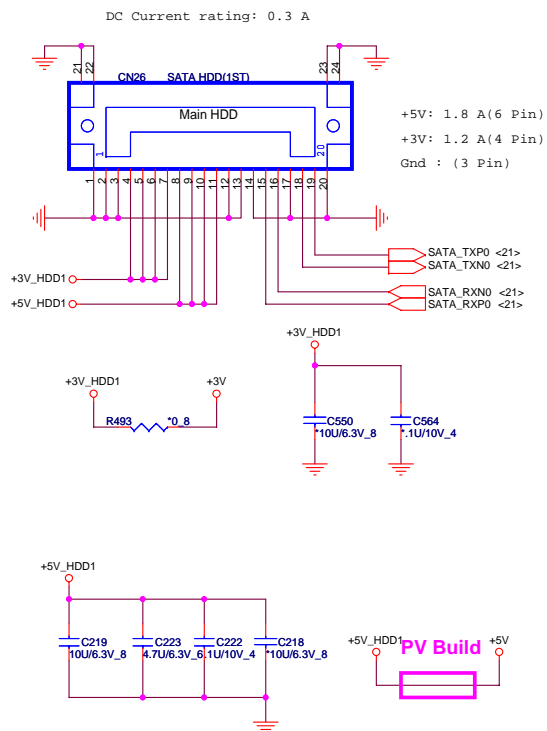




SATA CD-ROM



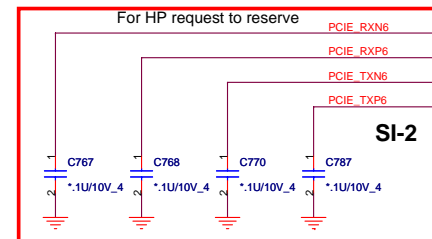
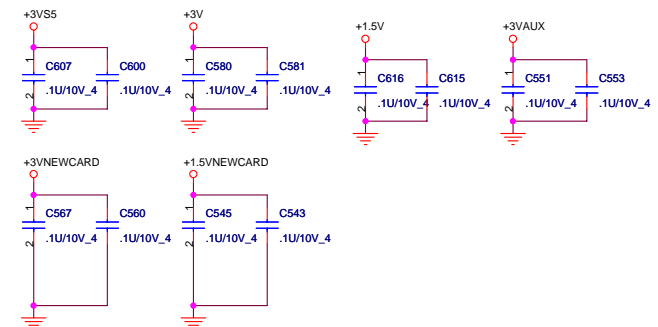
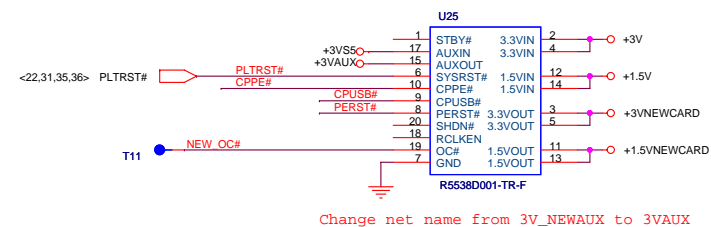
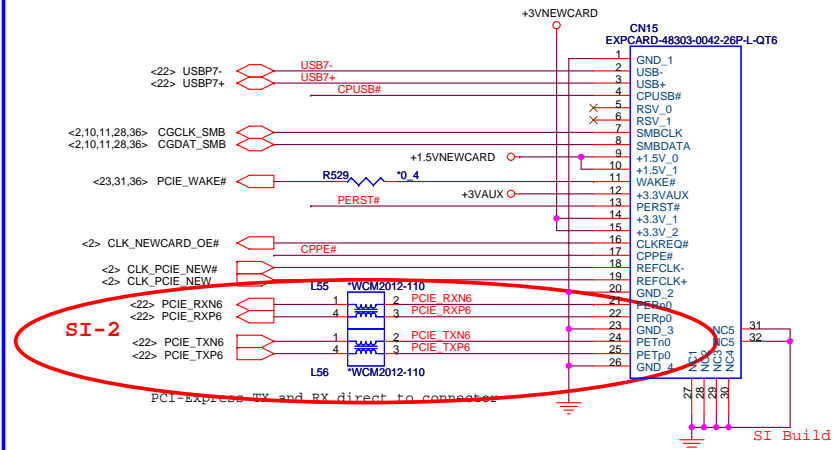
SATA_1 CONNECTOR



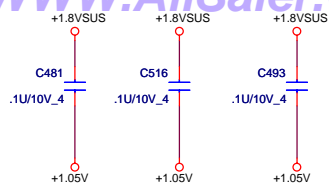
NEWCARD

NEWCARD (PCIEXPRESS*1 + USB*1)

33



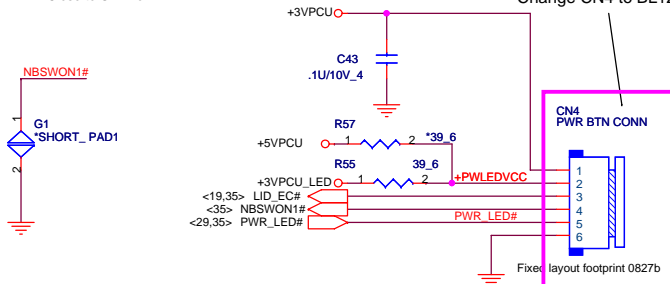
NB5	PROJECT : QT6 Quanta Computer Inc.		
	Size Custom	Document Number ODD/HDD/NEW CARD	Rev 2A
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Close to U21 For EMI

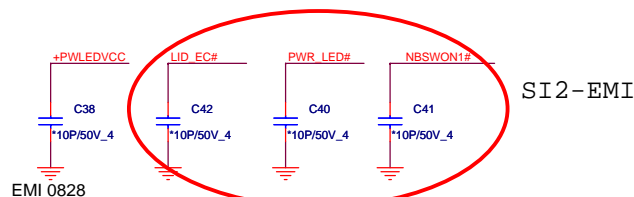
PV Build

Change CN4 to BL123-06R-6P-L-QT6-A

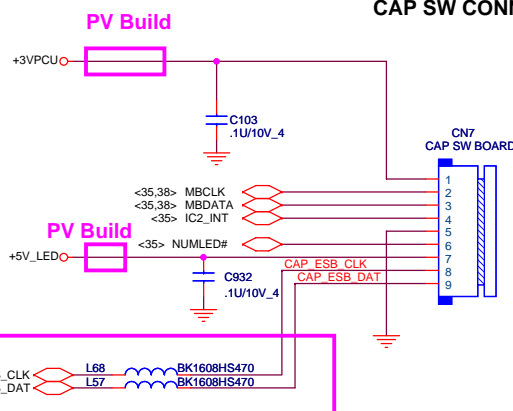


1. +3VPCU(LIDSWITCH PWR)
2. LEDVCC(+3VPCU)
3. LIDSWITCH
4. POWERON#
5. PWRLED#
6. GND

POWER BUTTON CONNECT



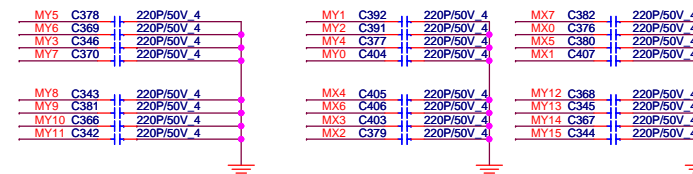
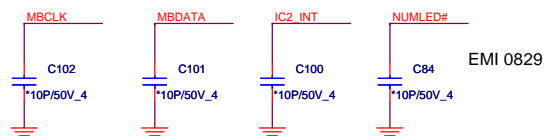
CAP SW CONNECT



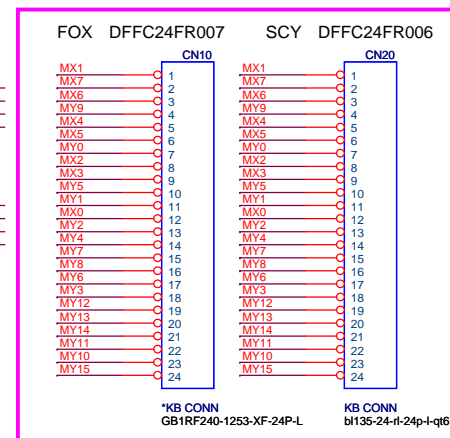
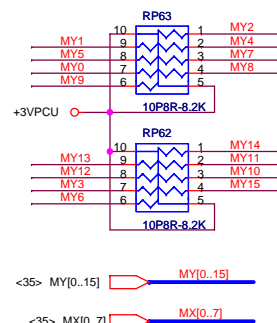
PV Build

Change CN7 to BL123-09R-9P-L-QT6-A

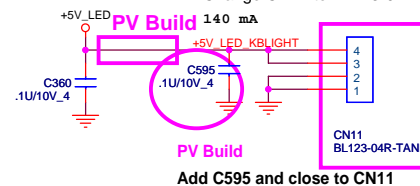
1. +3VPCU
2. MBCLK
3. MBDATA
4. CAP_INT
5. GND
6. NUM LOCK LED
7. +5V_LED
8. ESB_CLK
9. ESB_DAT



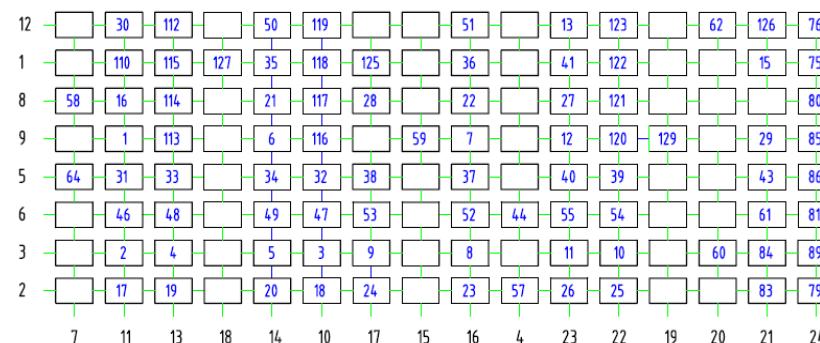
KEYBOARD PULL-UP



Change CN11 to BL123-04R-4P-L-QT6-A

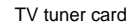
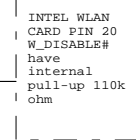


Add C595 and close to CN11



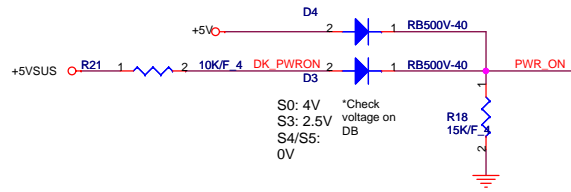
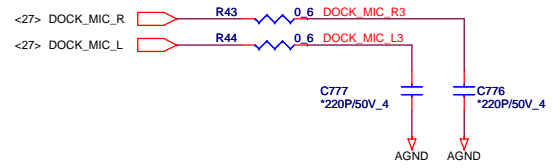
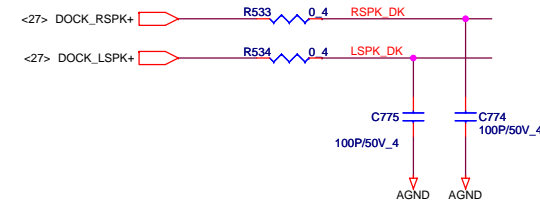
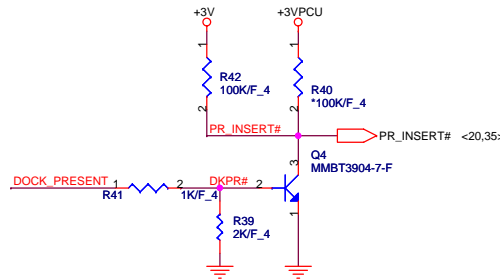
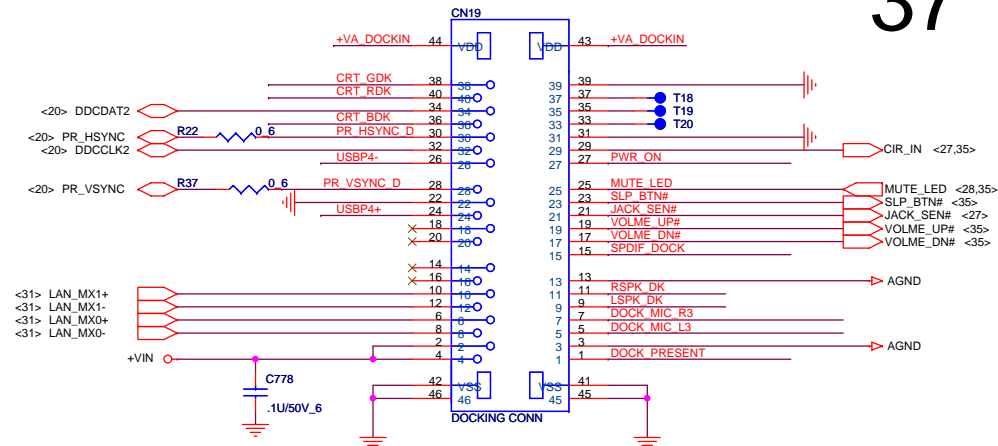
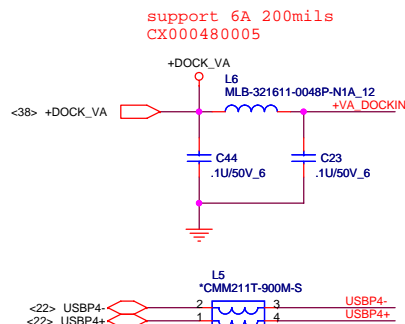
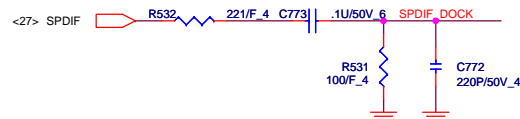


PV Build

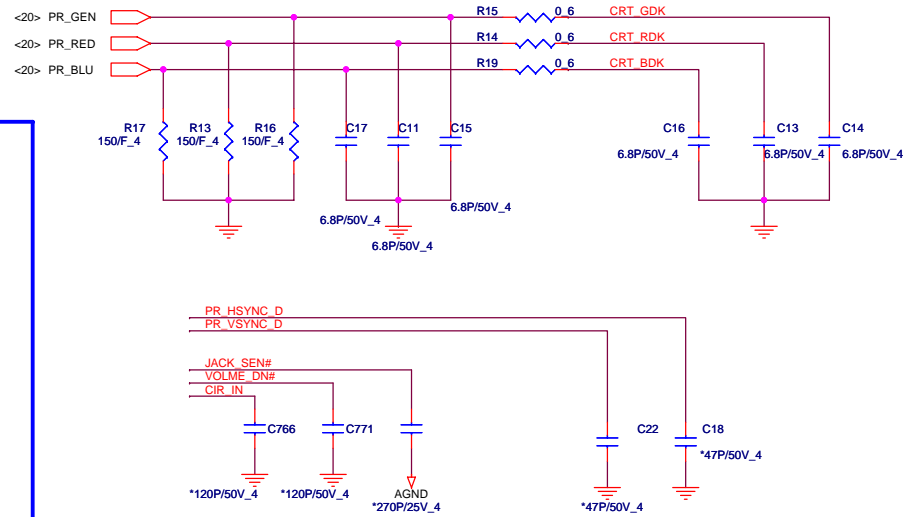
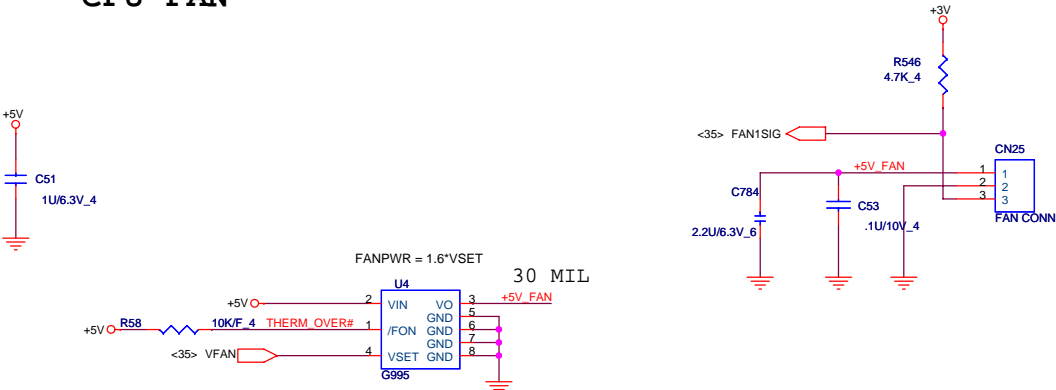


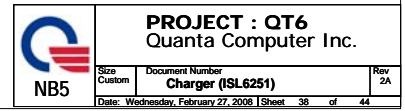
	Peak	Normal
+3.3Vaux:	2750mA	1100mA
+1.5V:	500mA	375mA





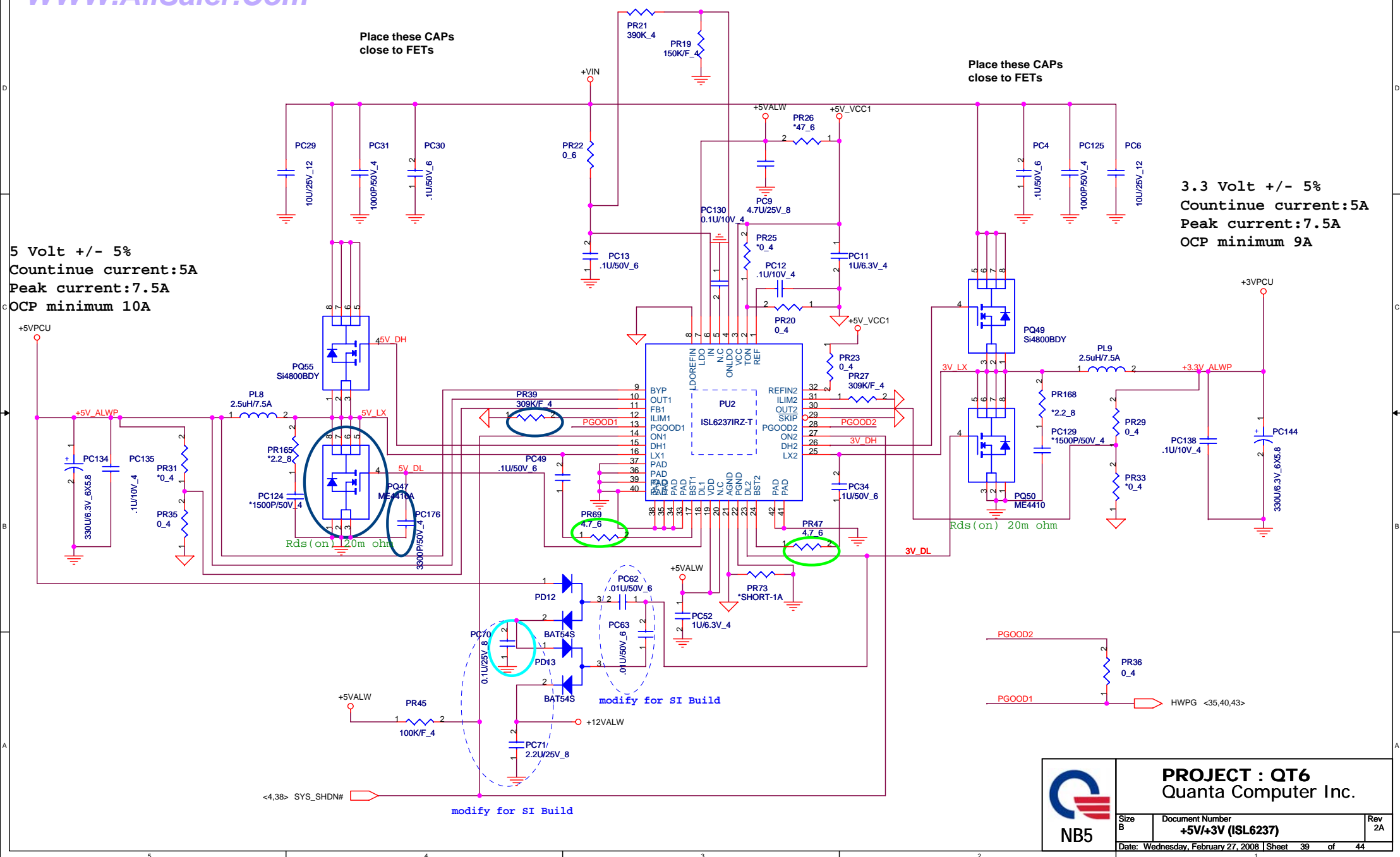
CPU FAN






5 Volt +/- 5%
Countinue current:5A
Peak current:7.5A
OCP minimum 10A

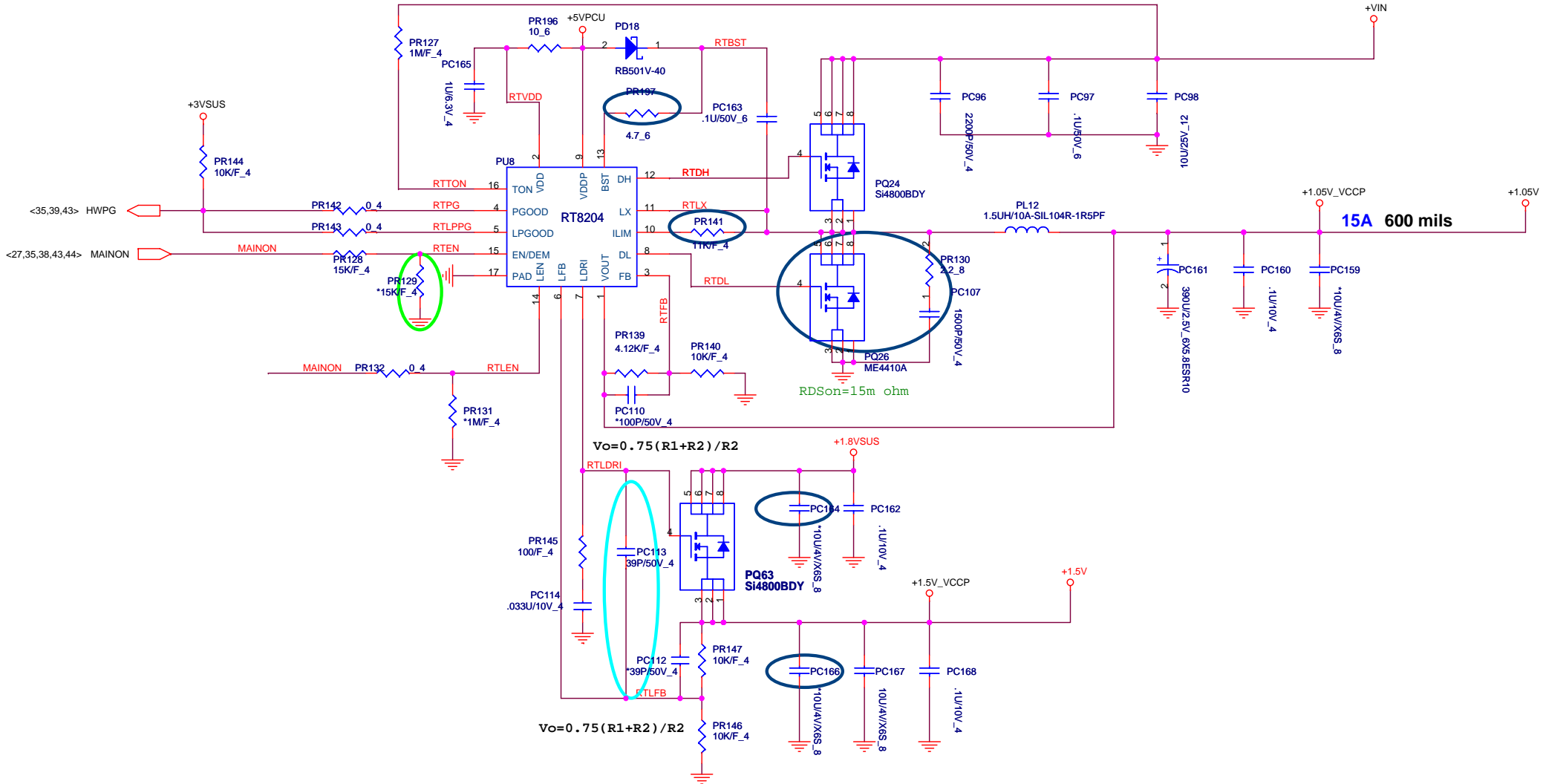
3.3 Volt +/- 5%
Countinue current:5A
Peak current:7.5A
OCP minimum 9A



 PROJECT : QT6 Quanta Computer Inc.		
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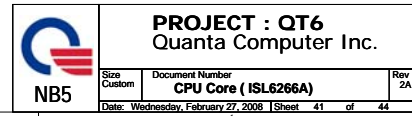
WWW.AliSaler.Com
VCCP1.05V & +1.5V


+1.05Volt +/- 5%
Countinue current:7.5A
Peak current:10A
OCP minimum 12A

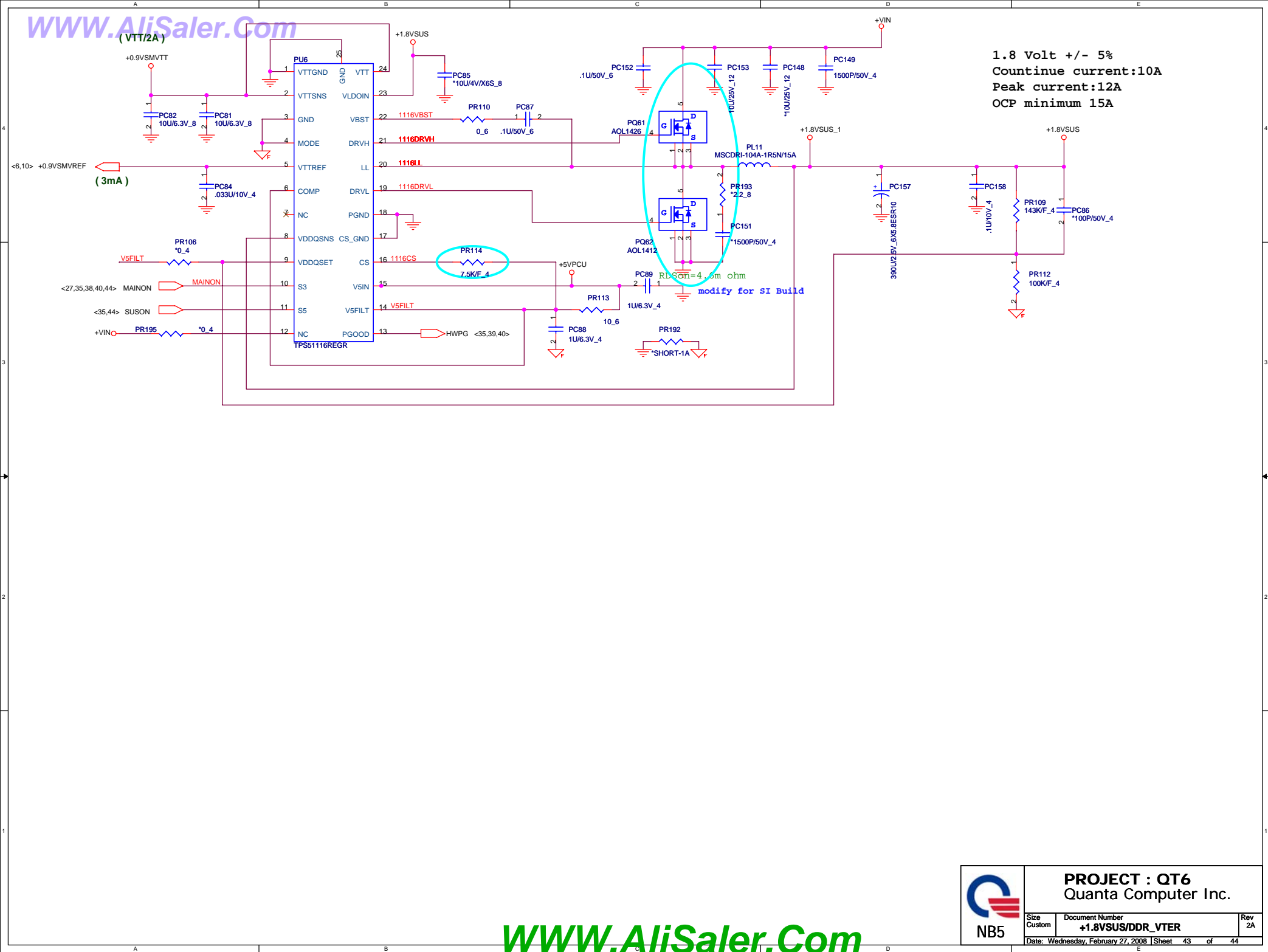


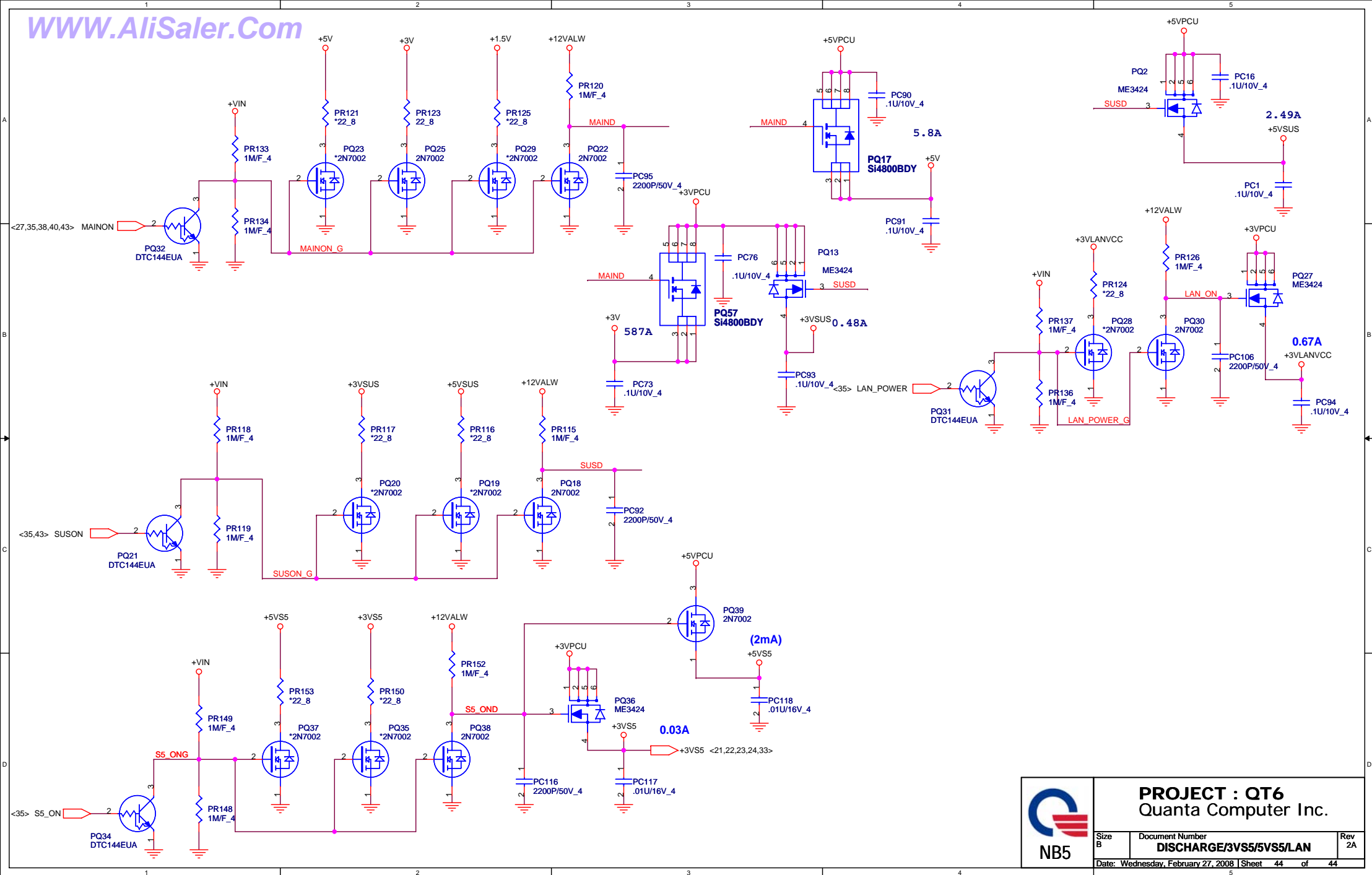
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