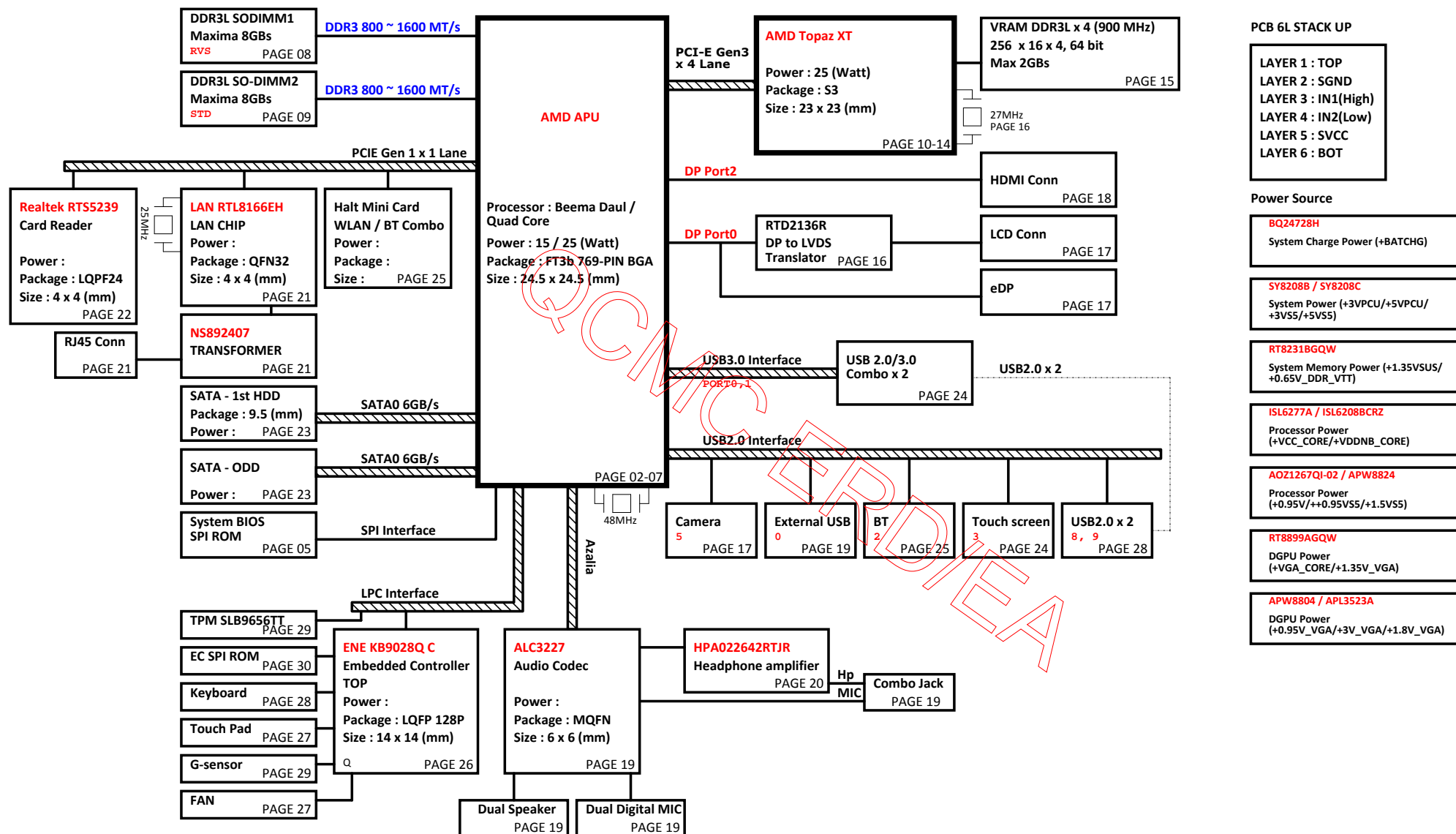
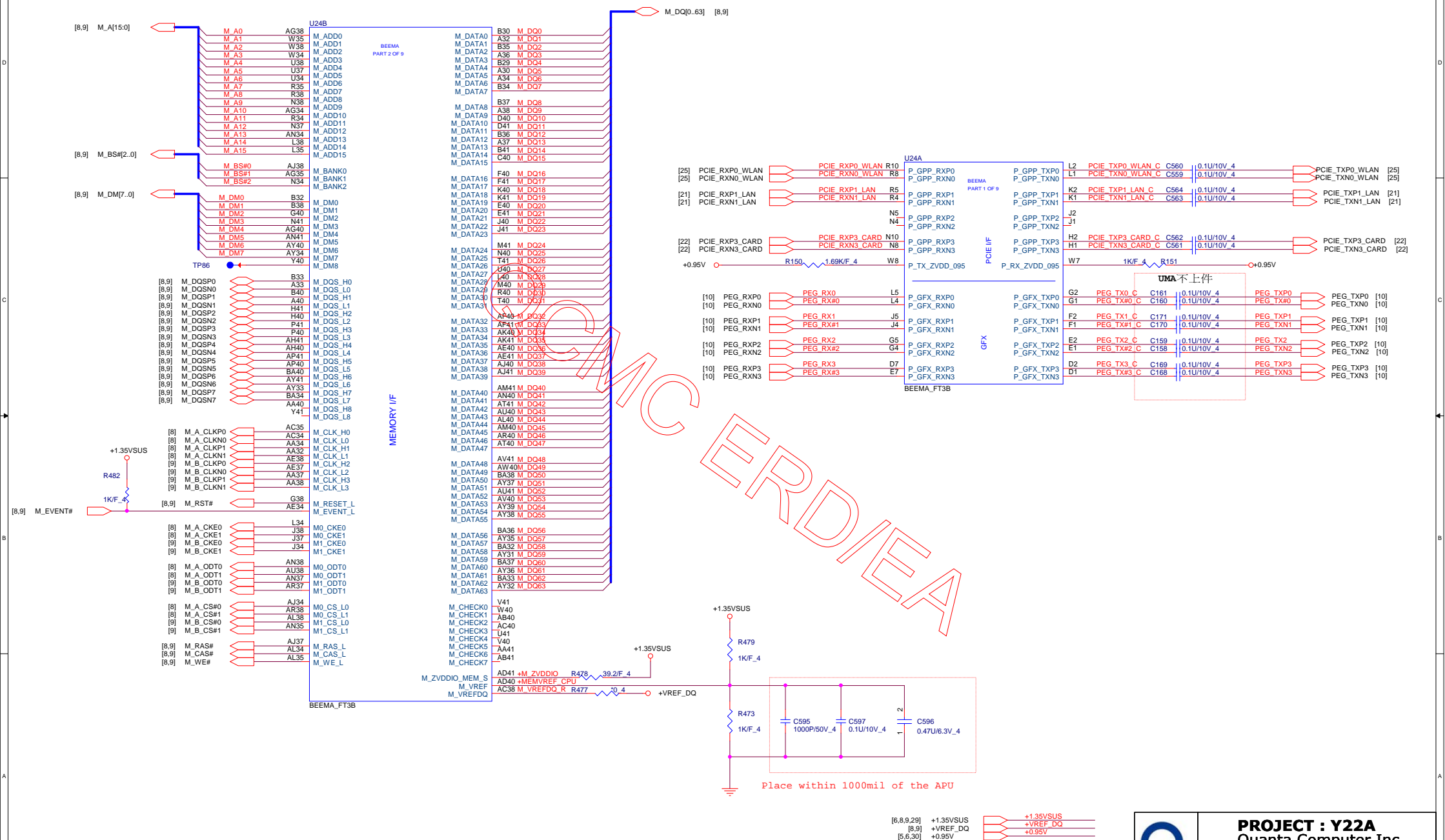
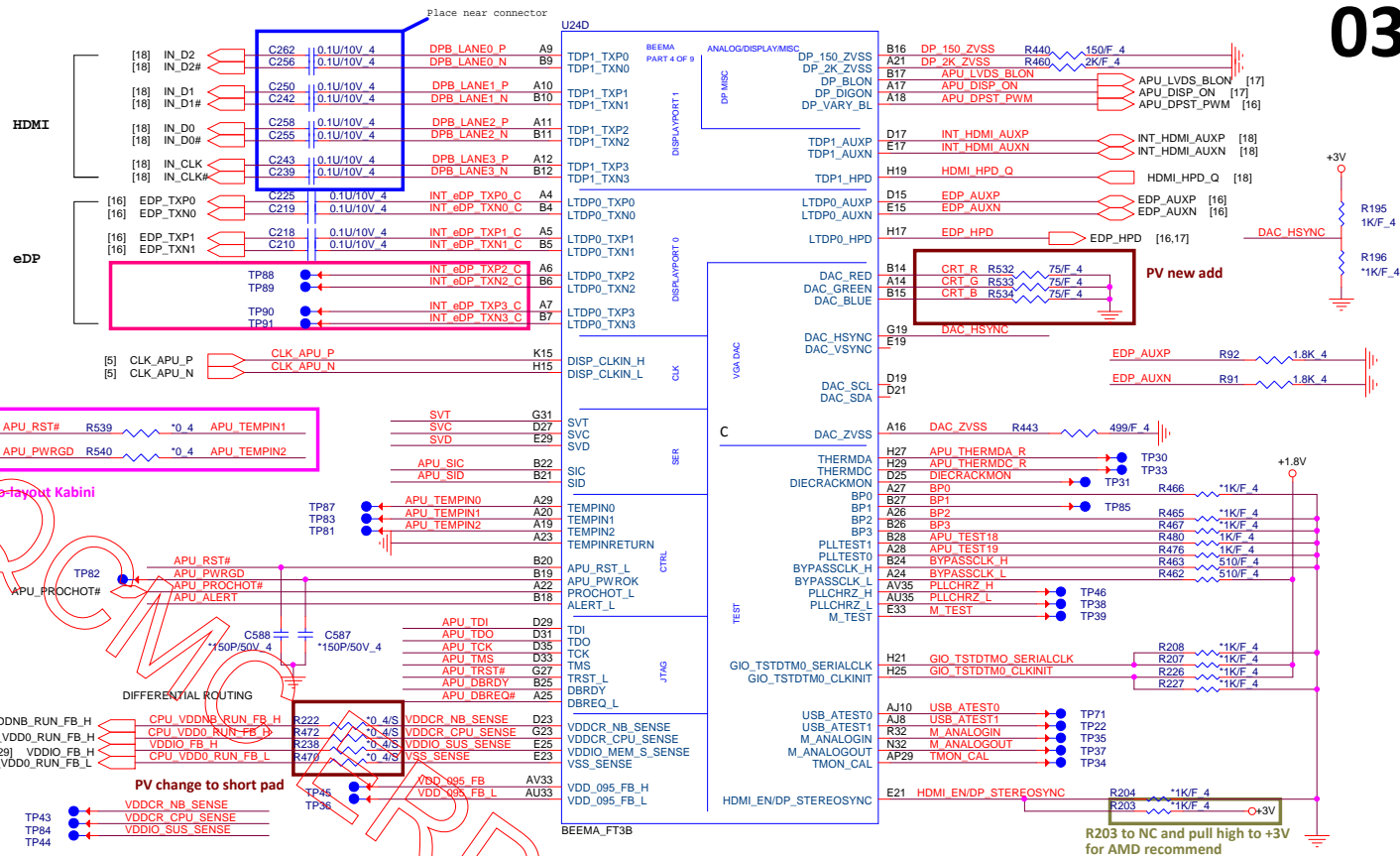
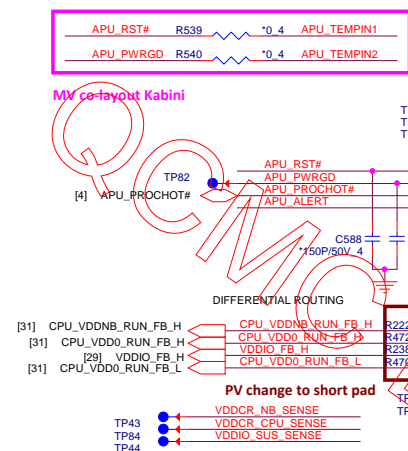
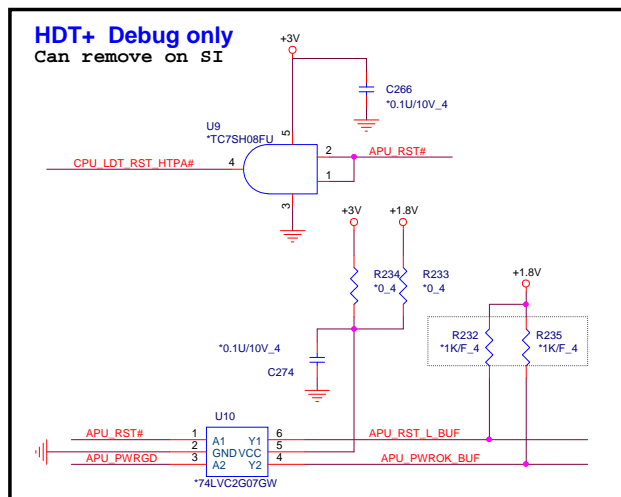
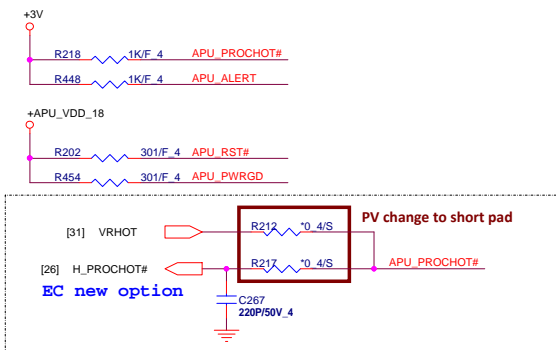


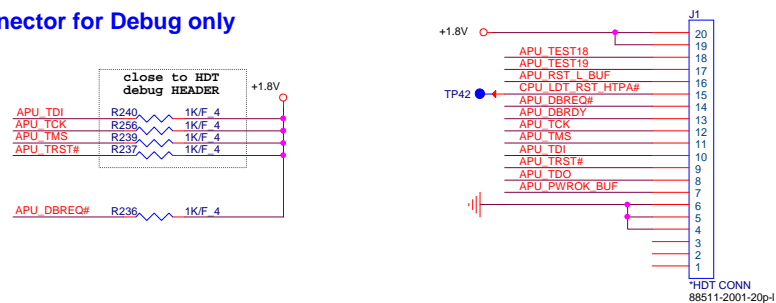
VINE_AMD Beema DIS/UMA (14.0"/15.6"/17.3")



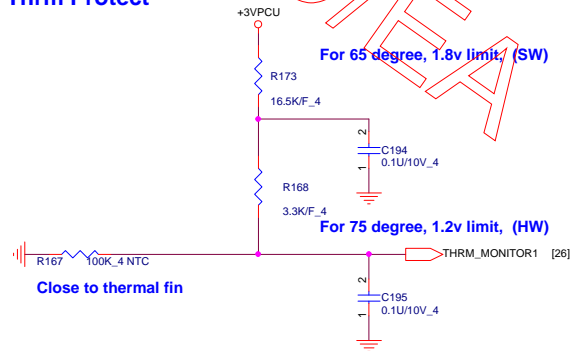




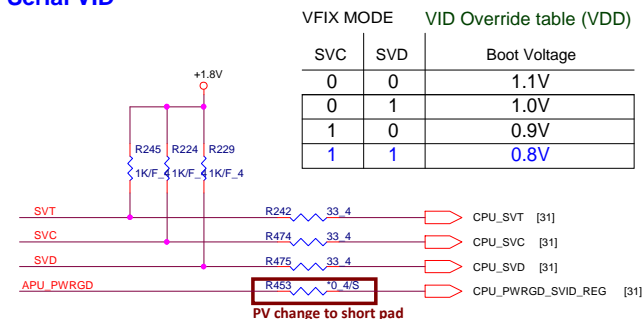
HDT+ Connector for Debug only



IO Thrm Protect

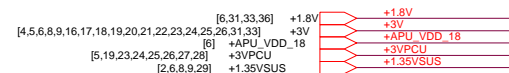
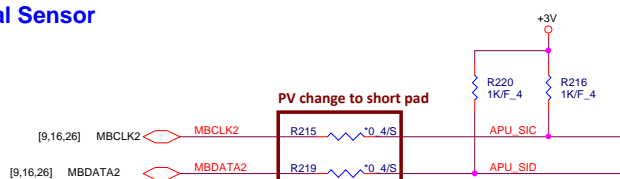


Serial VID



VFIX MODE		VID Override table (VDD)	
SVC	SVD	Boot Voltage	
0	0	1.1V	
0	1	1.0V	
1	0	0.9V	
1	1	0.8V	

Thermal Sensor



PROJECT : Y22A
Quanta Computer Inc.

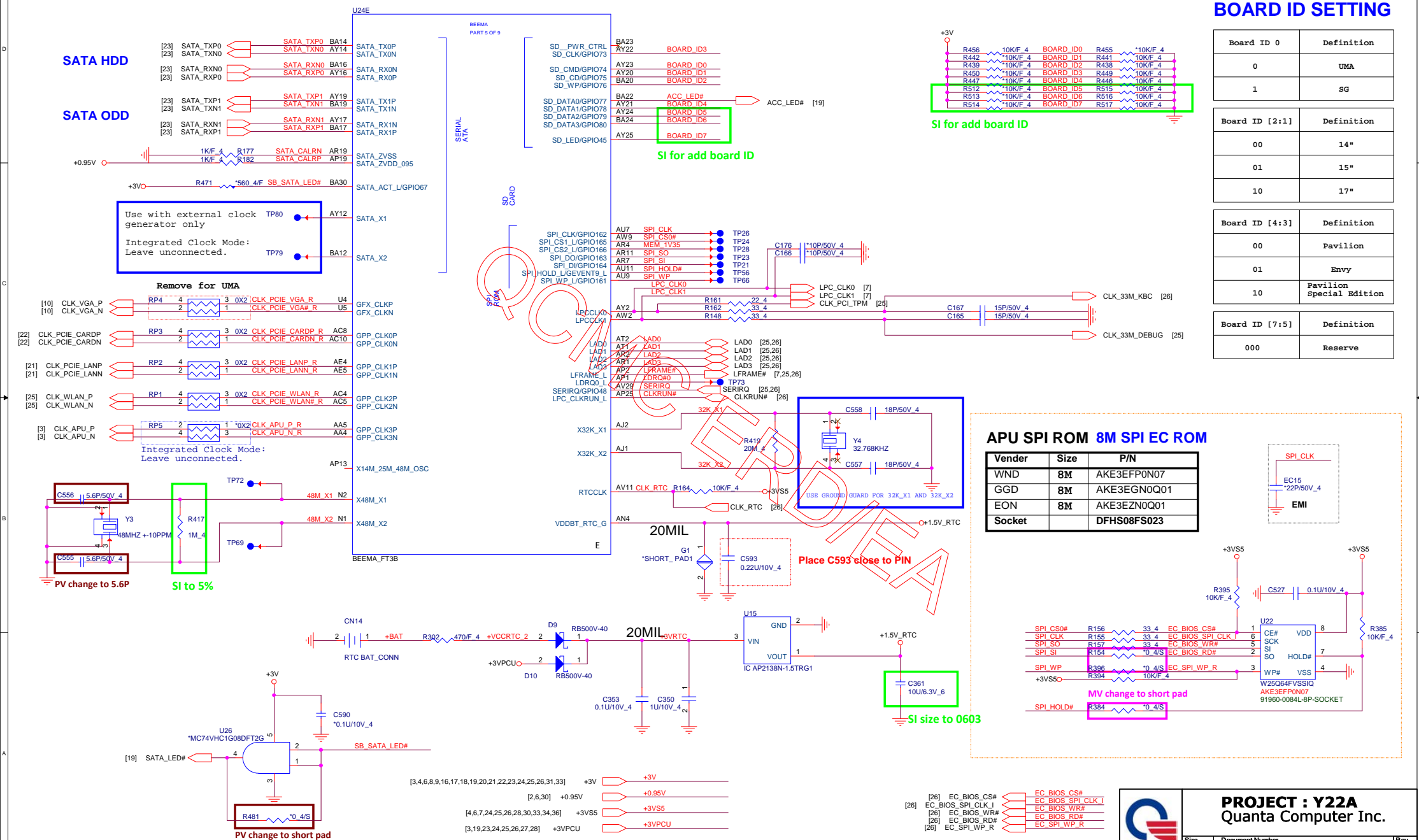
Size: **DIS/MI (2/6)**

Date: Thursday, March 27, 2014

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Rev 1A



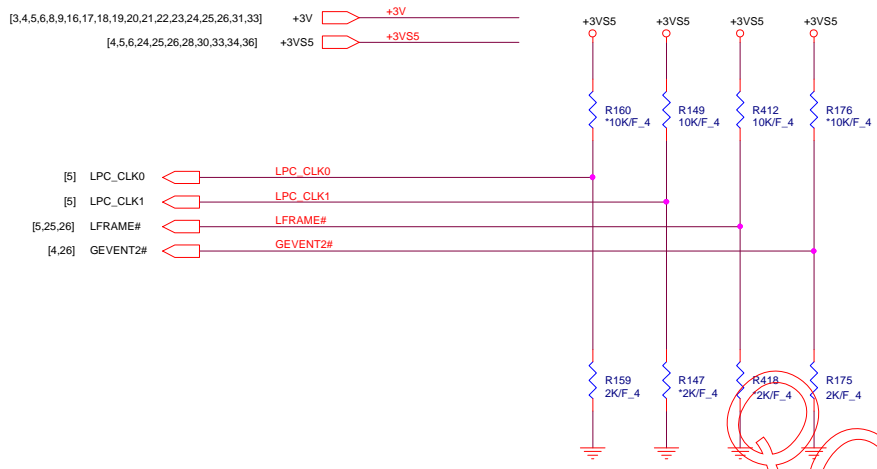






STRAPS PINS

OVERLAP COMMON PADS WHERE POSSIBLE FOR DUAL-OP RESISTORS.

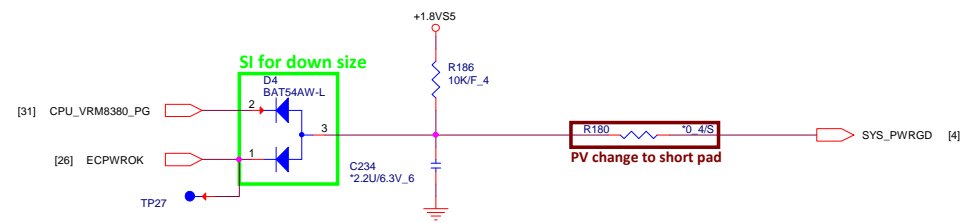


REQUIRED STRAPS

					LPC_CLK0	LPC_CLK1	LFRAME#	GEVENT2#
PULL HIGH					BOOT FAIL TIMER ENABLED	CLKGEN ENABLED DEFAULT	SPI ROM DEFAULT	1.8V SPI ROM
PULL LOW					BOOT FAIL TIMER DISABLED DEFAULT	CLKGEN DISABLED	LPC ROM	3.3V SPI ROM DEFAULT

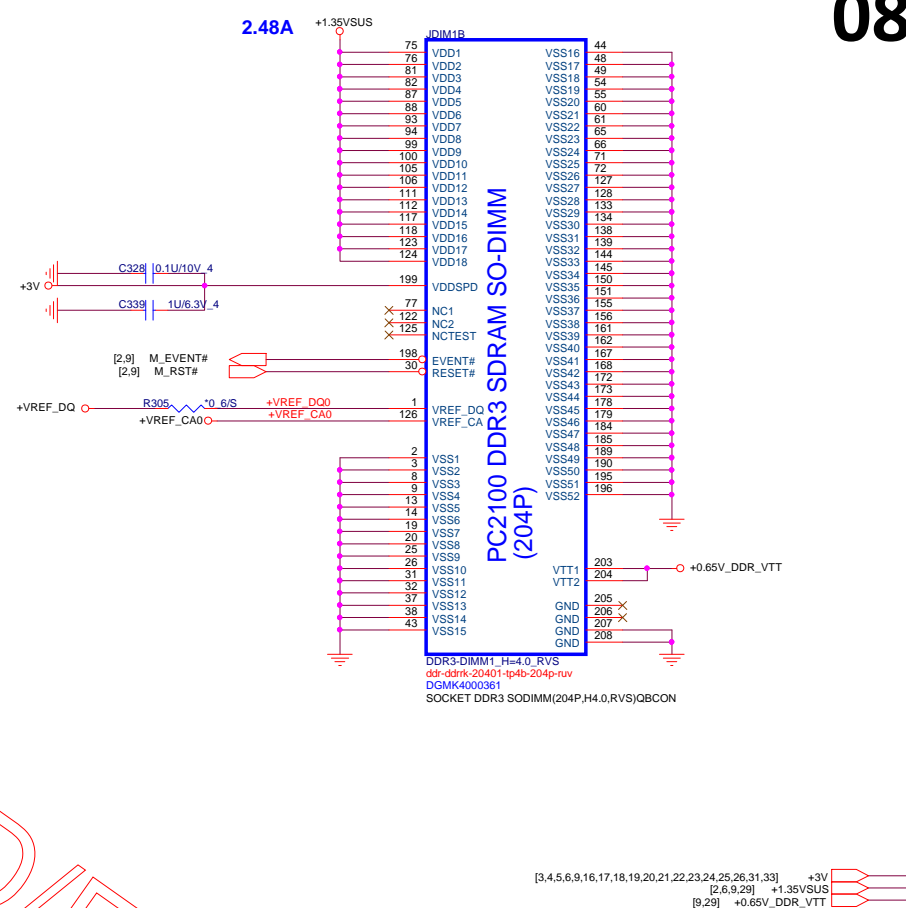
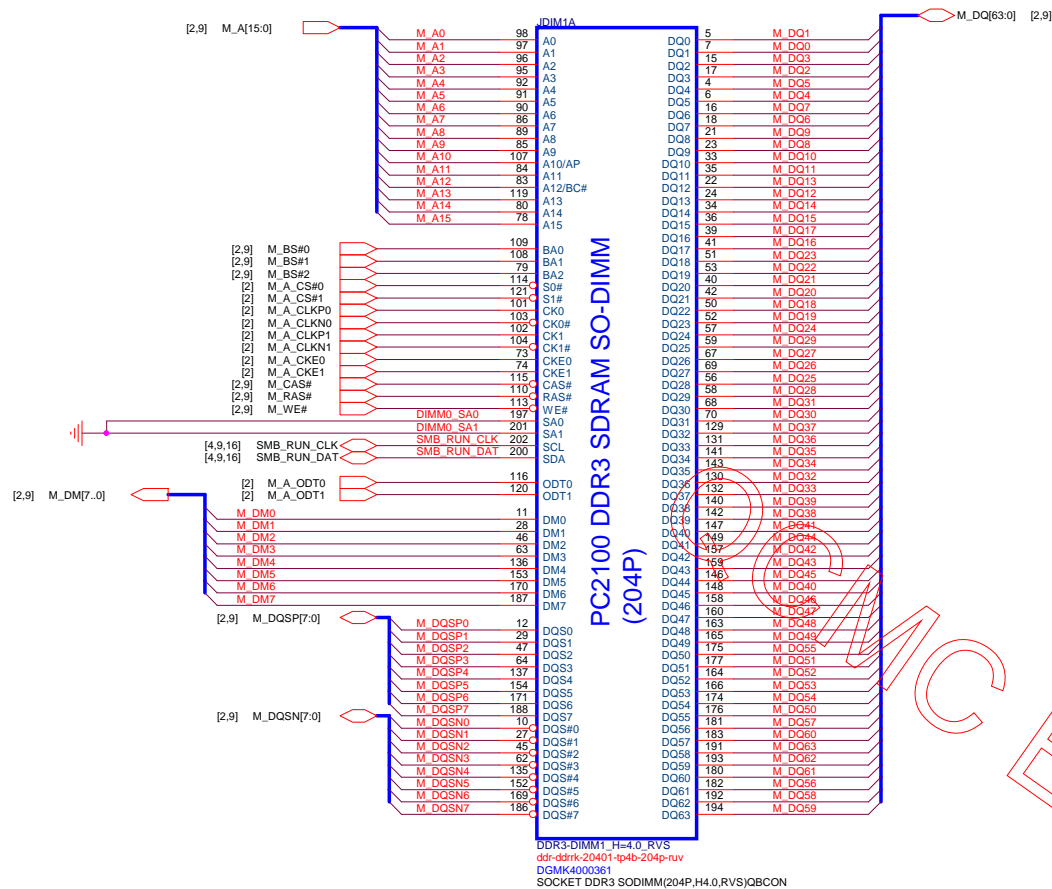
DEBUG STRAPS

SYS_PWRGD

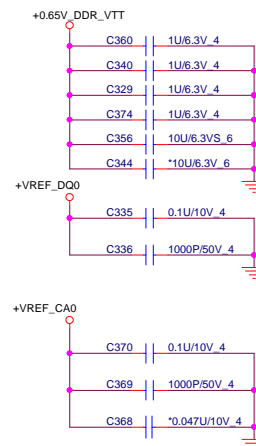
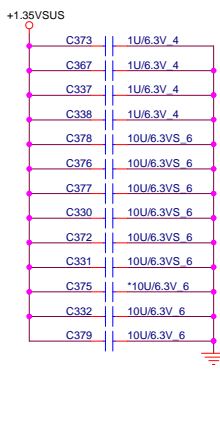


PROJECT : Y22A
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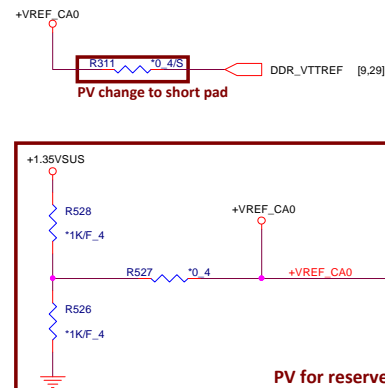
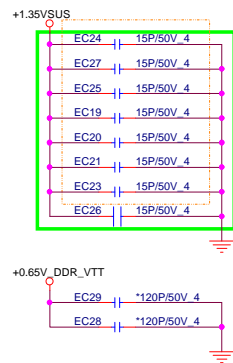
Size	Document Number	Rev
	STRAP (6/6)	1A
Date: Wednesday, March 19, 2014	Sheet 7	of 36



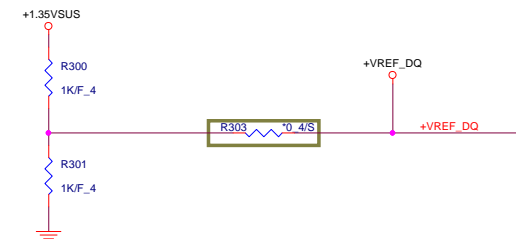
Place these Caps near So-Dimm0.

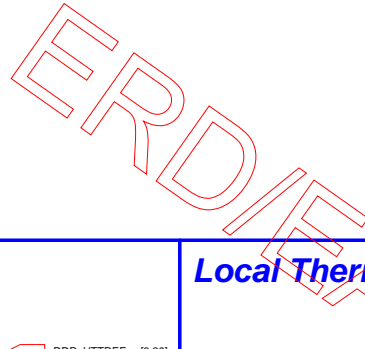


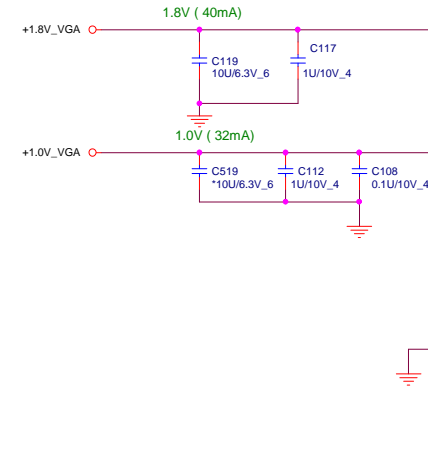
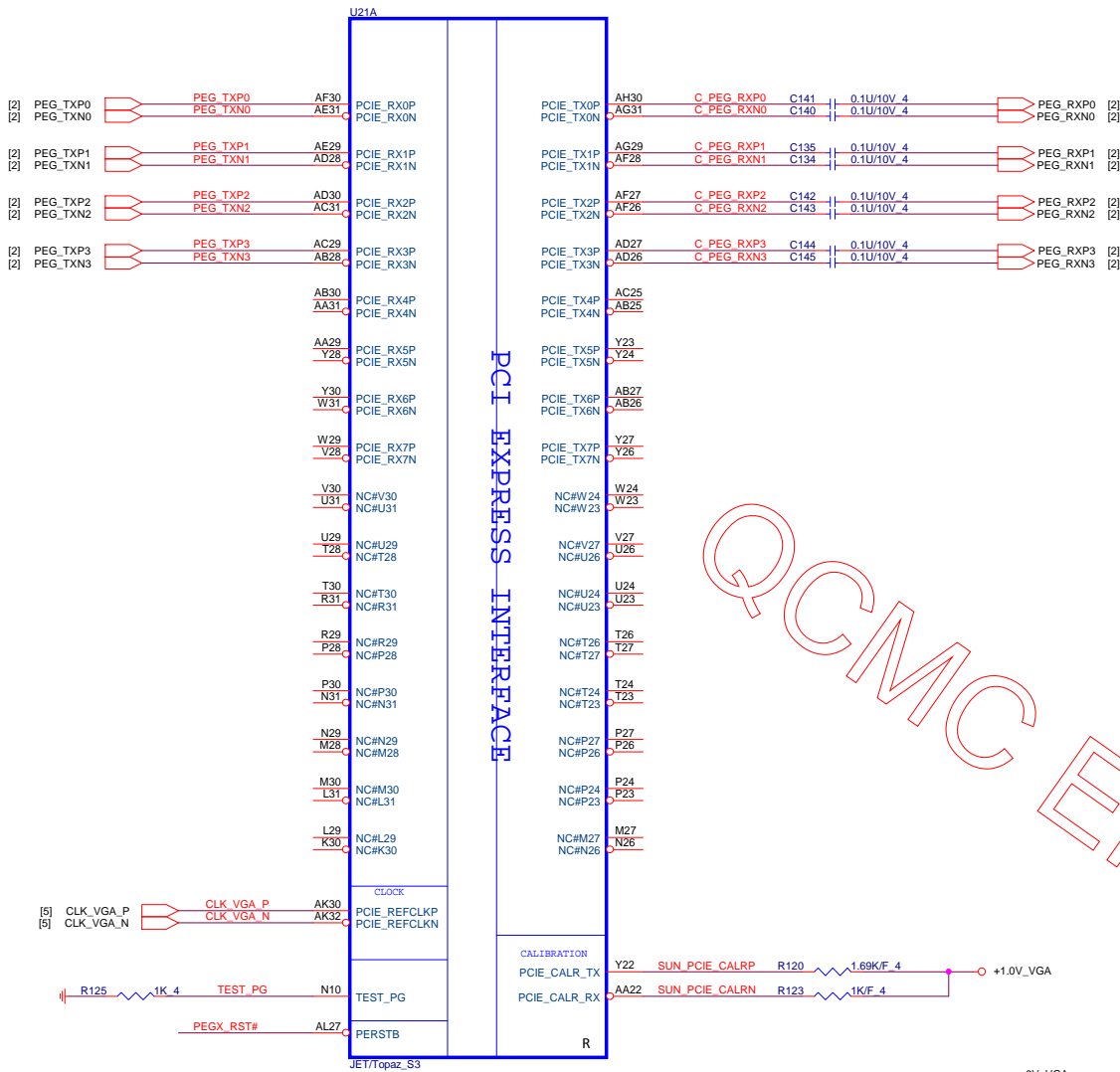
For EMI RESERVE



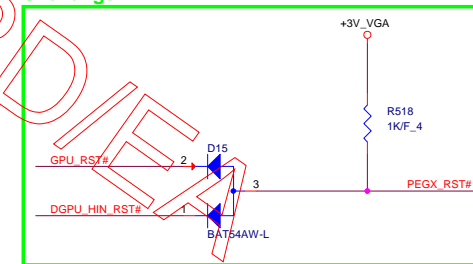
Reserved for AMD suggest



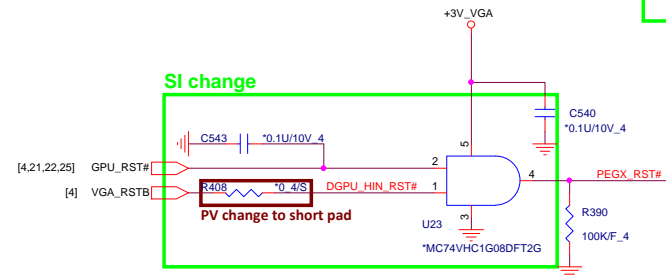




SI change



SI change



[11,13,36] +3V_VGA

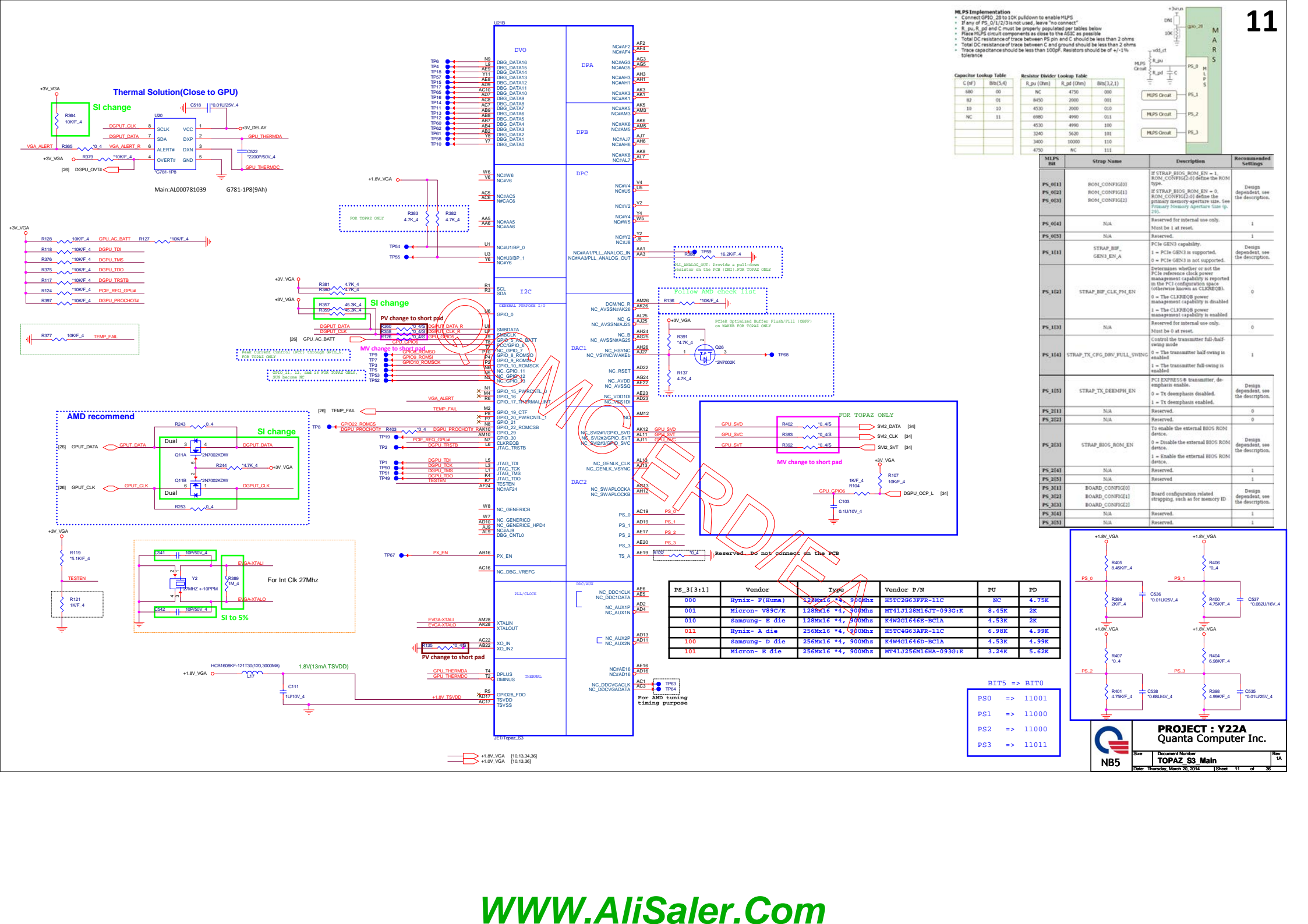
[11,13,34,36] +1.8V_VGA

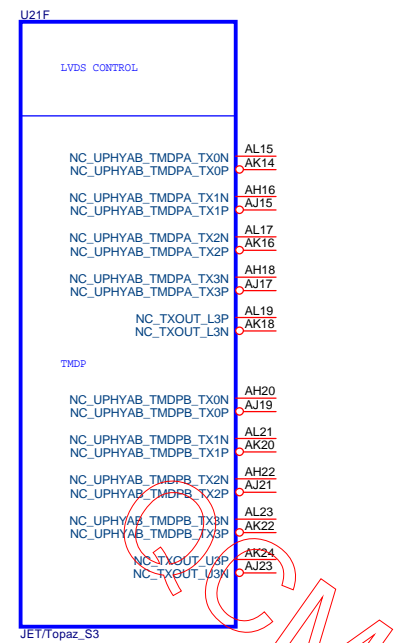
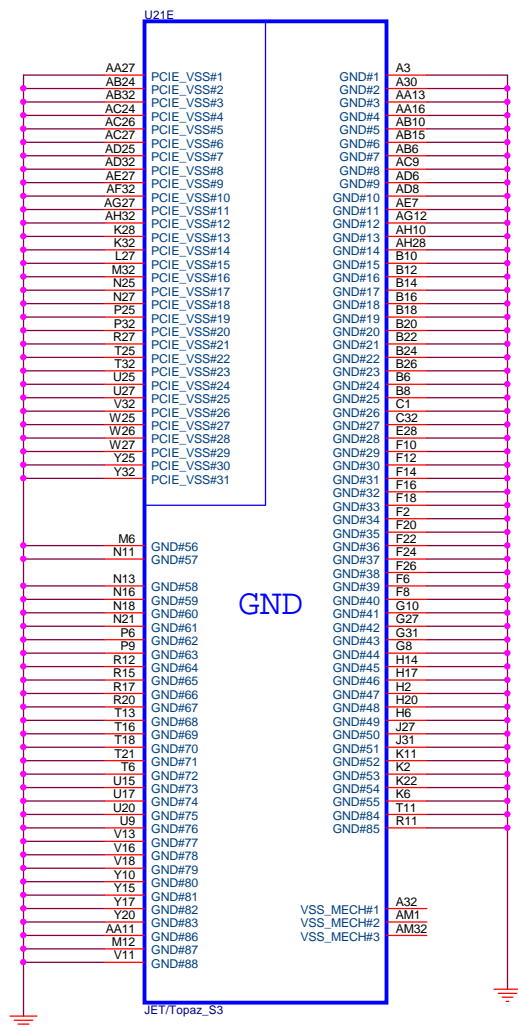
[13,36] +1.0V_VGA



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Quanta Computer Inc.

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	TOPAZ S3 PCIE/DP power	1A
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CONFIGURATION STRAPS-- SEE EACH DATABOOK FOR STRAP DETAILS

ALLOW FOR PULLUP PADS FOR THESE STRAPS AND IF THESE GPIOs ARE USED, THEY MUST NOT CONFLICT DURING RESET

RECOMMENDED SETTINGS
0= DO NOT INSTALL RESISTOR
1= INSTALL 3K RESISTOR
X= DESIGN DEPENDANT
NA= NOT APPLICABLE

STRAPS	PIN	DESCRIPTION OF DEFAULT SETTINGS	
TX_PWRS_ENB	GPIO0	PCIE FULL TX OUTPUT SWING	0
TX_DEEMPH_EN	GPIO1	PCIE TRANSMITTER DE-EMPHASIS ENABLED	X
RSVD	GPIO2	RESERVED	0
RSVD	GPIO8	RESERVED	0
BIF_VGA_DIS	GPIO9	VGA ENABLED	0
RSVD	GPIO21	RESERVED	0
BIOS_ROM_EN	GPIO_22_ROMCSB	ENABLE EXTERNAL BIOS ROM	0
ROMIDCFG(2:0)	GPIO[13:11]	SERIAL ROM TYPE OR MEMORY APERTURE SIZE SELECT	0 0 1
VIP_DEVICE_STRAP_ENA	V2SYNC	IGNORE VIP DEVICE STRAPS (Removed on Seymour/W/histler)	0
RSVD	H2SYNC	RESERVED	0
AUD[1] AUD[0]	HSYNC VSYNC	SEE DATABOOK FOR DETAIL SEE DATABOOK FOR DETAIL	0 0
RSVD	GENERICC	RESERVED	0

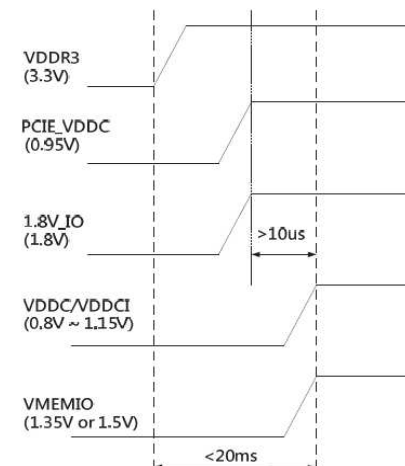
NOTE1: AMD RESERVED CONFIGURATION STRAPS

ALLOW FOR PULLUP PADS FOR THESE STRAPS BUT DO NOT INSTALL RESISTOR. IF THESE GPIOs ARE USED, THEY MUST KEEP "LOW" AND NOT CONFLICT DURING RESET.

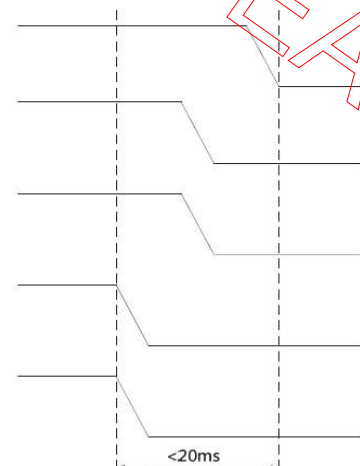
GPIO21 H2SYNC GENERICC GPIO8 GPIO2

POWER UP / POWER DOWN SEQUENCE

POWER UP

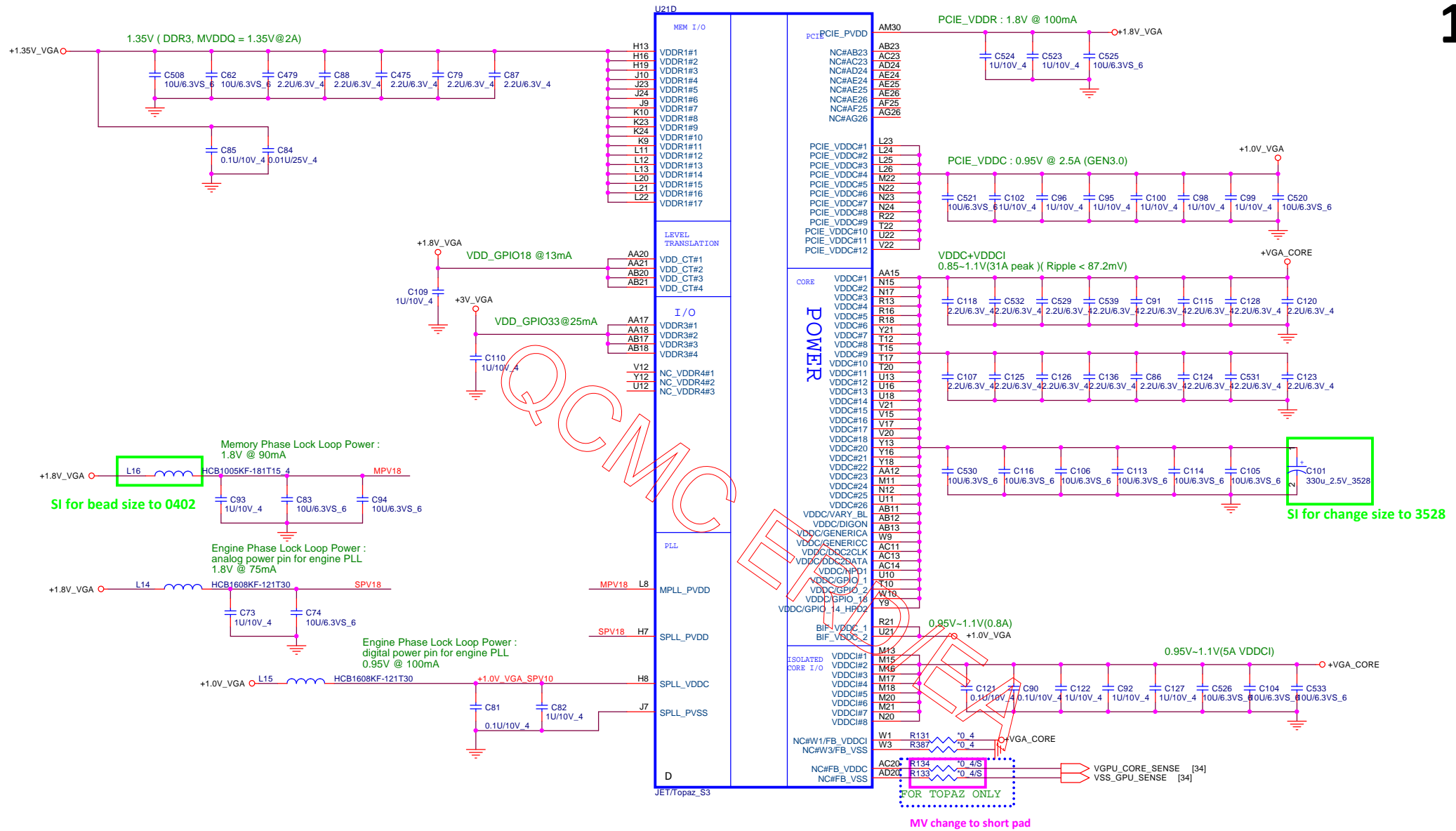


POWER DOWN



PROJECT : Y22A
Quanta Computer Inc.

Size	Document Number	Rev
	TOPAZ_S3_GND/LVDS/Strap	1A
Date: Wednesday, March 19, 2014	Sheet 12 of 36	

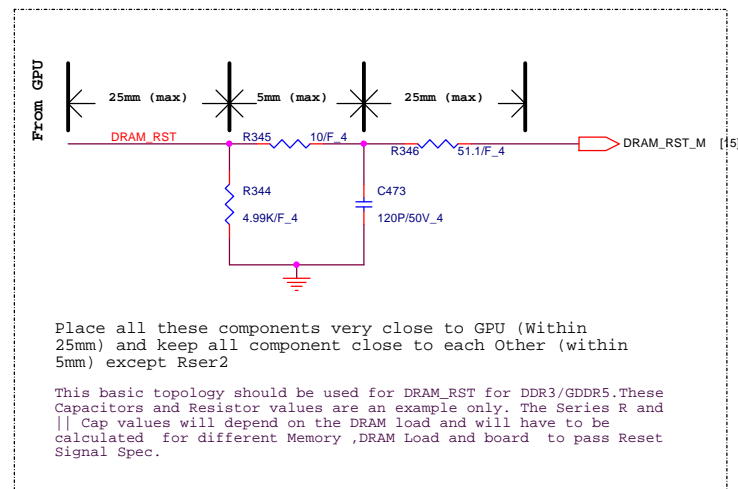


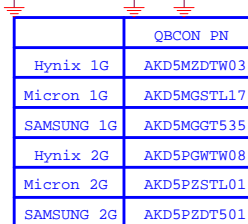
+1.35V_VGA [14,15,34]
+1.8V_VGA [10,11,34,36]
+1.0V_VGA [10,36]
+VGA_CORE [34,35]



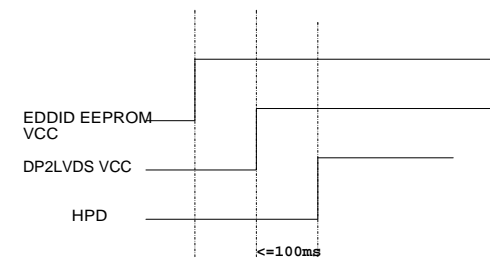
PROJECT : Y22A
Quanta Computer Inc.

Size	Document Number	Rev
	TOPAZ S3 Power	1A
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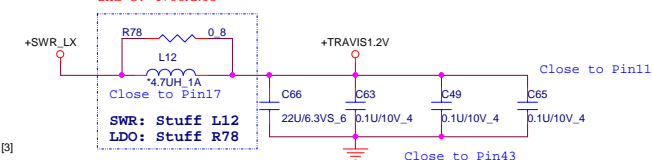


RTD2136S Power Up Sequence



[3,17] EDP_HPDP EDP_HPDP R87 1K Ω 4 EDP_HPDP_2136

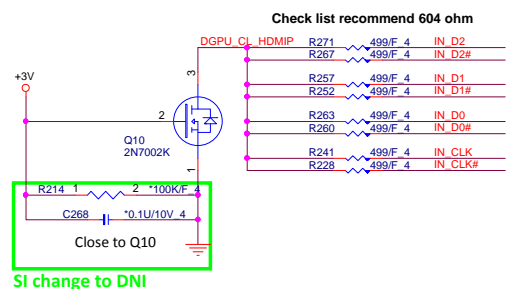
L12: need use CV-4709MN00 for Vendor suggestion
2nd CV-4708MN03



SI for Level shift is needless

[3,4,5,6,8,9,17,18,19,20,21,22,23,24,25,26,31,33] +3V
[19,25,33] +1.5V

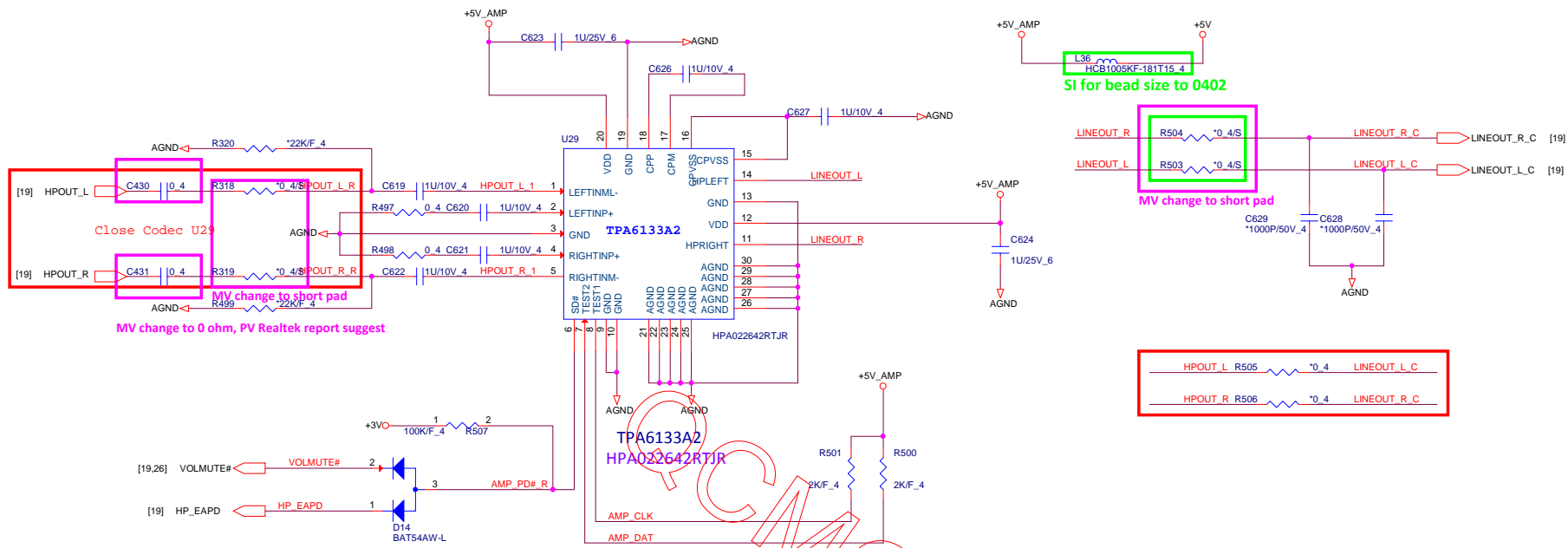
	PROJECT : Y22A		Rev 1A
	Quanta Computer Inc.		
	Size Custom	Document Number	
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HDMI HPD SENSE

2KV ESD protection

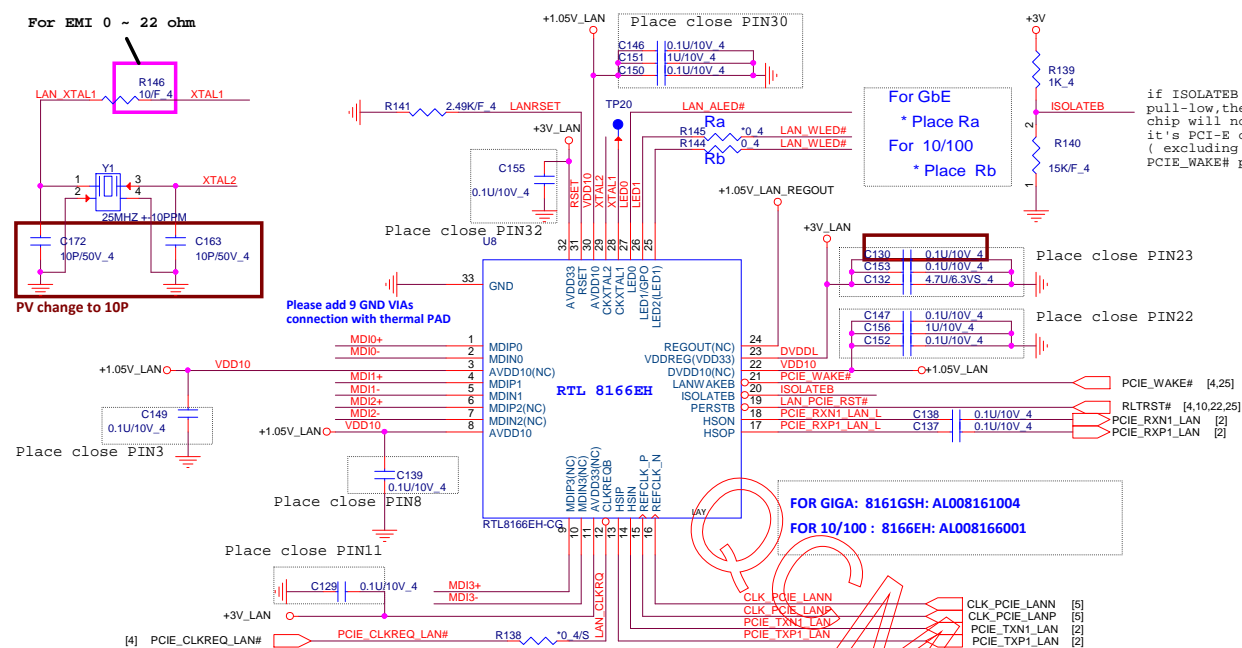




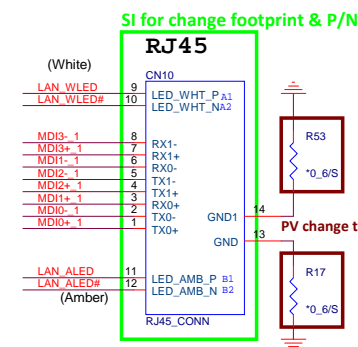
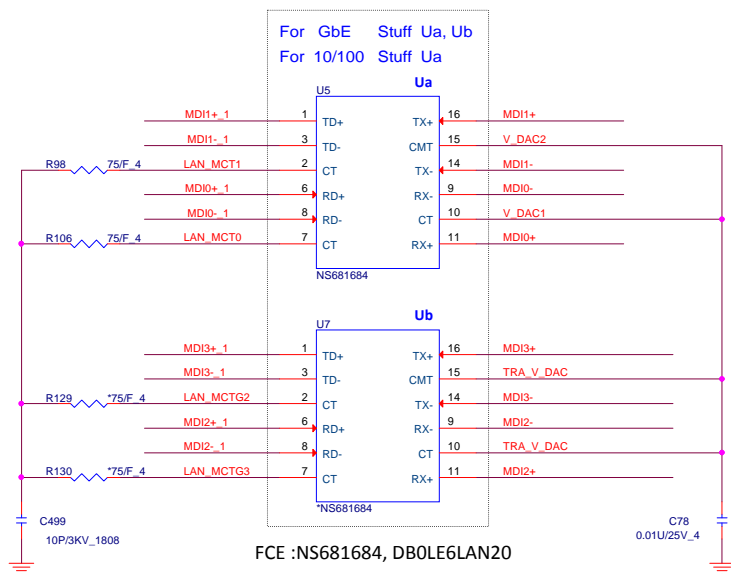
[19,24,28,29,30,31,32,33,34,36] +5VS5
 [3,4,5,6,8,9,16,17,18,19,21,22,23,24,25,26,31,33] +3V
 [18,19,23,24,33] +5V
 [3,5,19,23,24,25,26,27,28] +3VPCU

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	Quanta Computer Inc.		
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For EMI 0 ~ 22 ohm



LAN conn



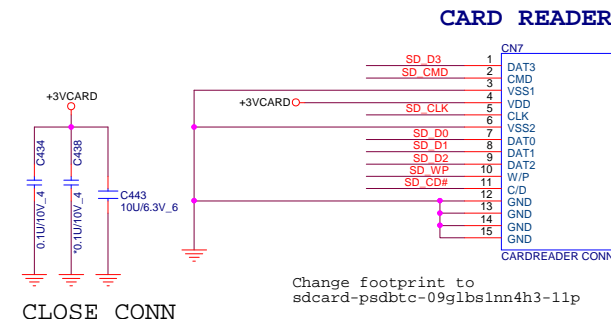
PROJECT : Y22A
Quanta Computer Inc.

Size	Document Number	Rev
Custom	RTL 8166EH/8161EH/RJ45	1A
Date: Monday, March 24, 2014	Sheet 21 of 36	

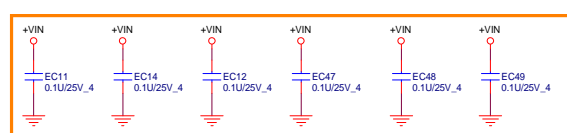
[3,4,5,6,8,9,16,17,18,19,20,22,23,24,25,26,31,33]

+3V

+3VLANVCC



FOR EMI



[17,27,28,29,30,32,34,35] +VIN

QCMC ERD

ve for AMD

15" & 17" SATA ODD

OD1517

18

17

20

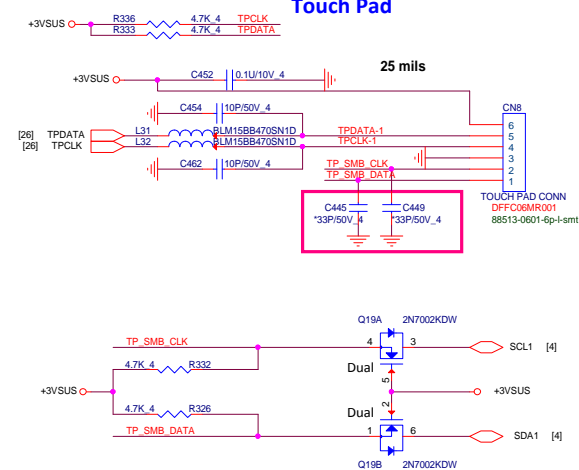
19

16

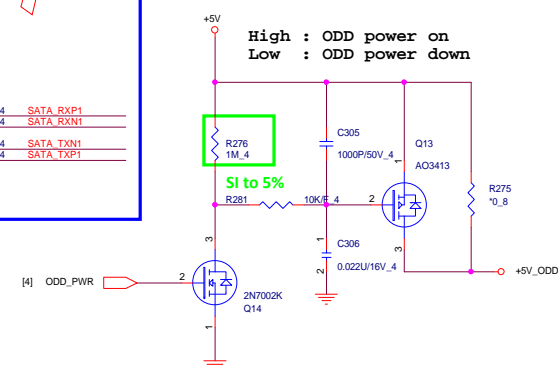
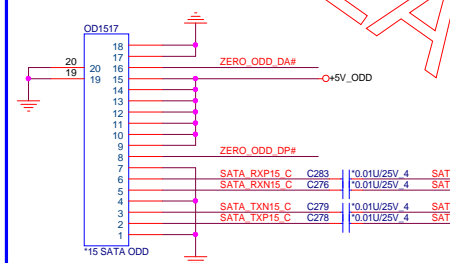
ZERO_ODD_DA#

TPDATA
TPCLK

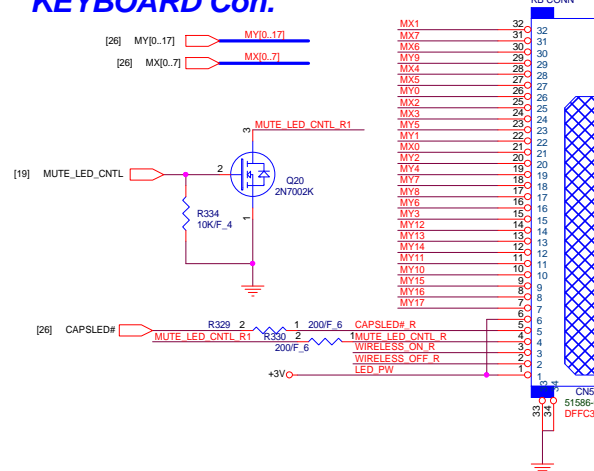
+3VSUS



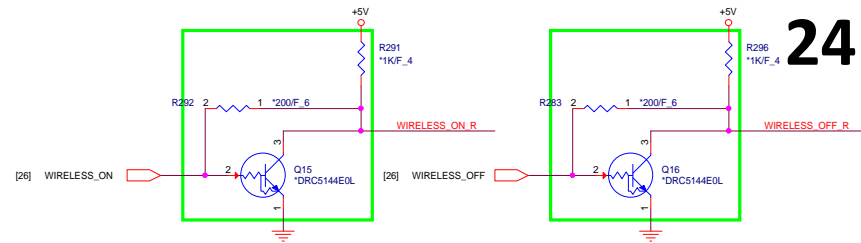
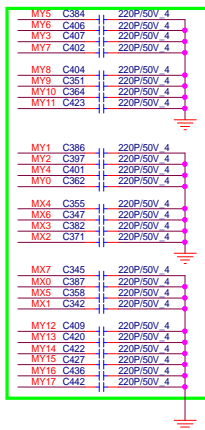
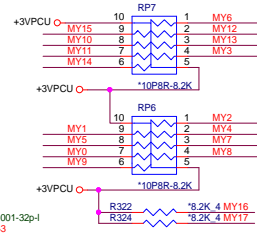
15"&17" SATA ODD



KEYBOARD Con.

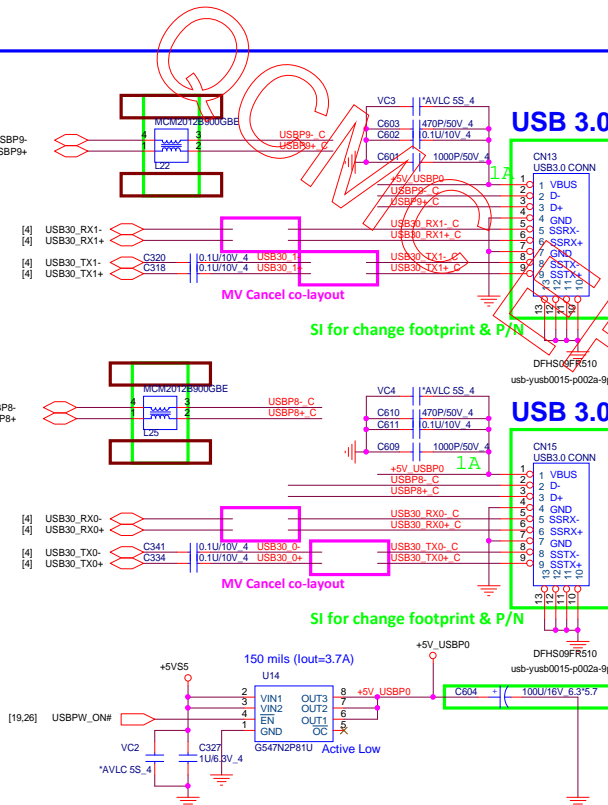
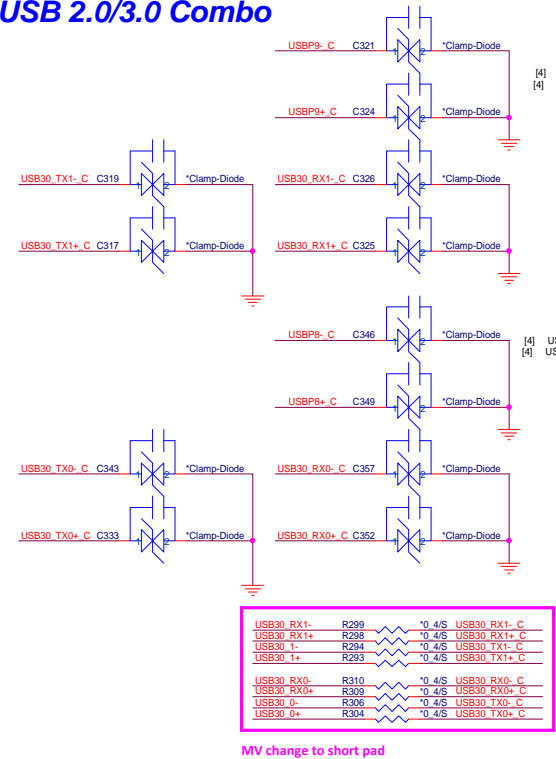


KEYBOARD PULL-UP

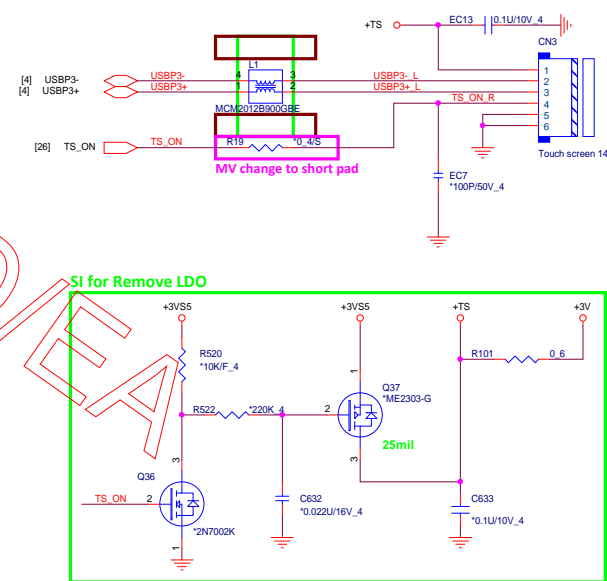


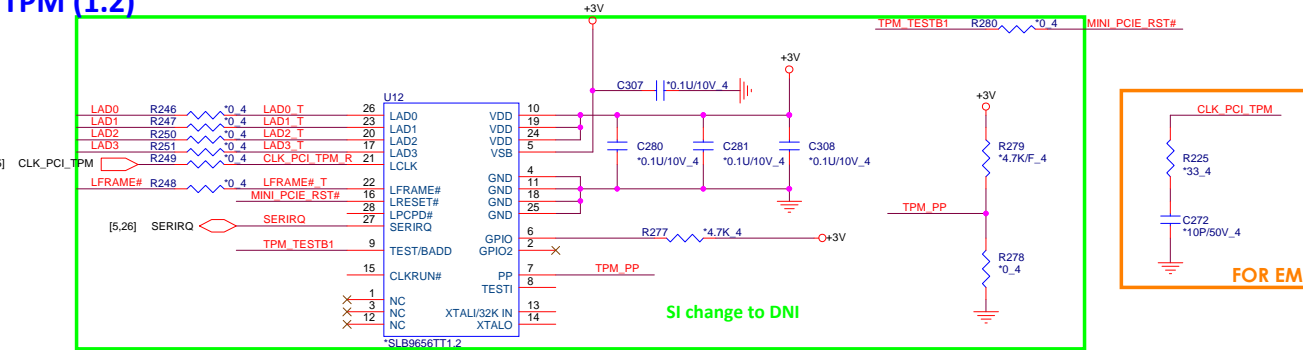
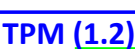
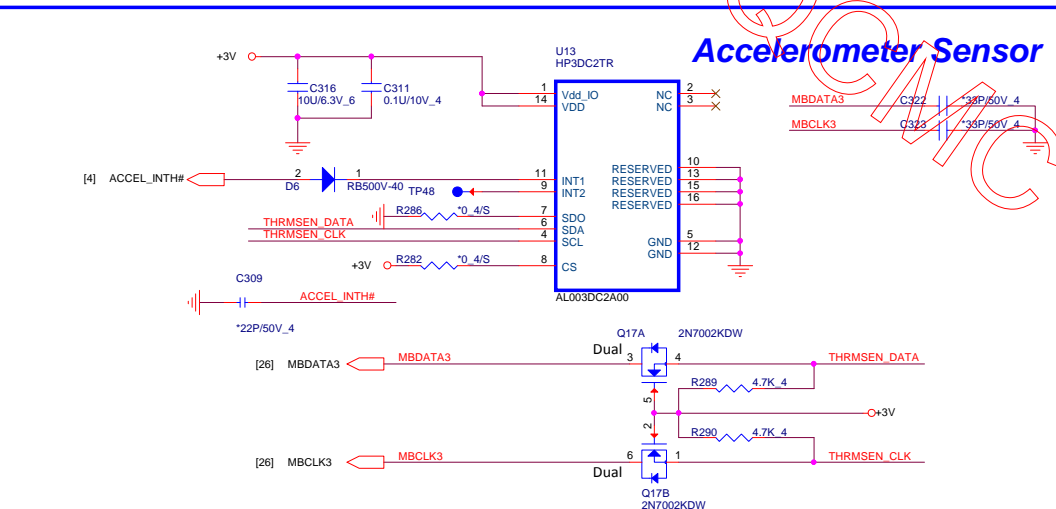
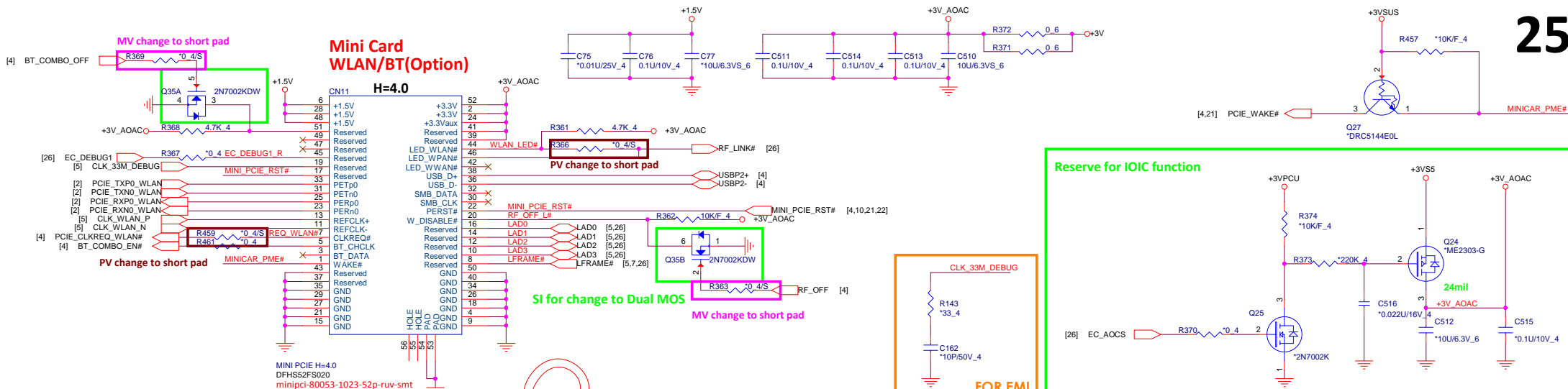
24


USB 2.0/3.0 Combo

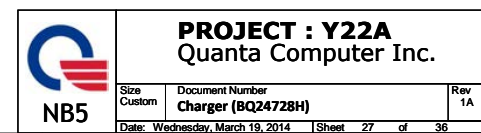


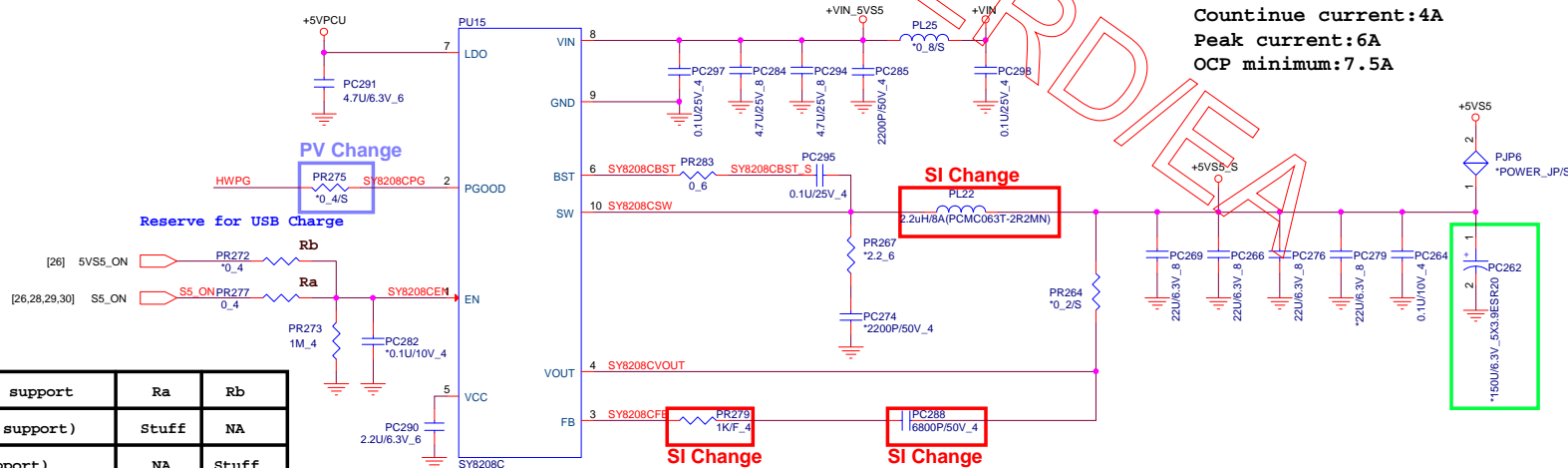
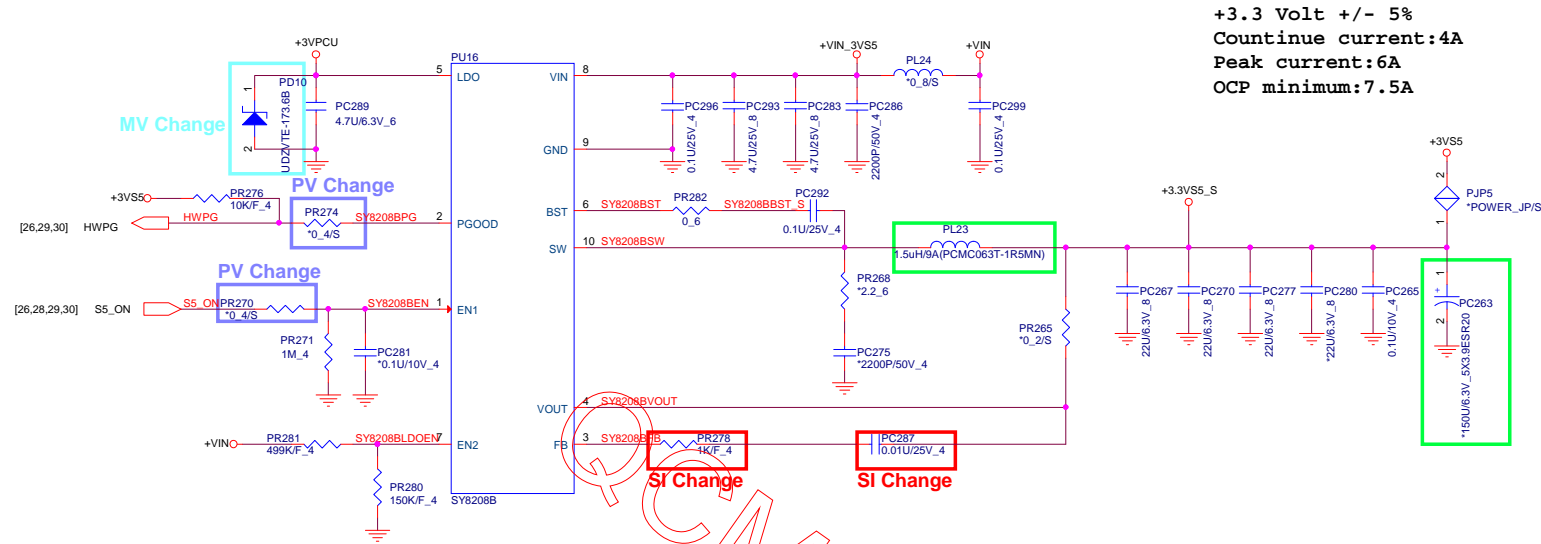
Touch screen



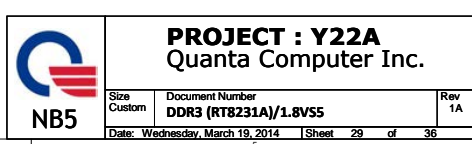


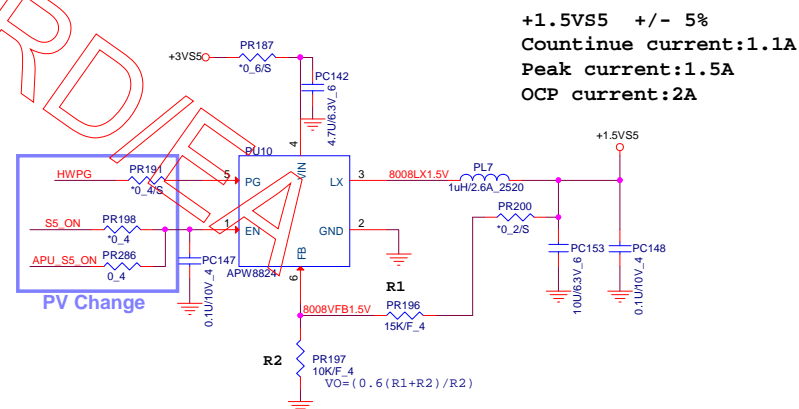
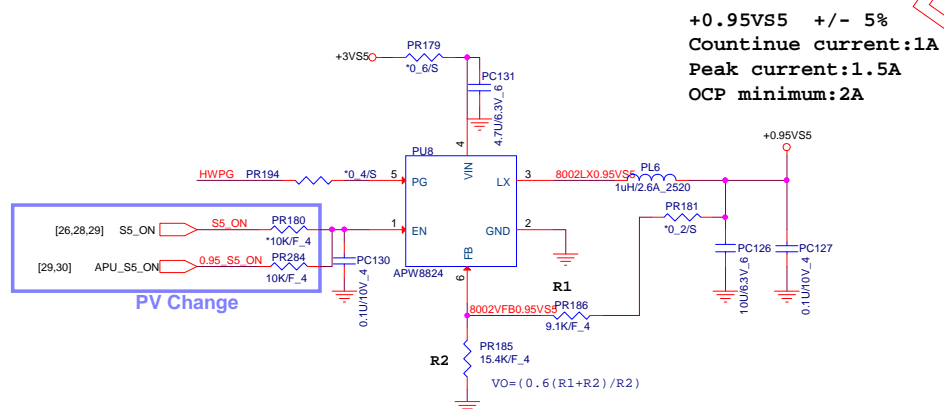
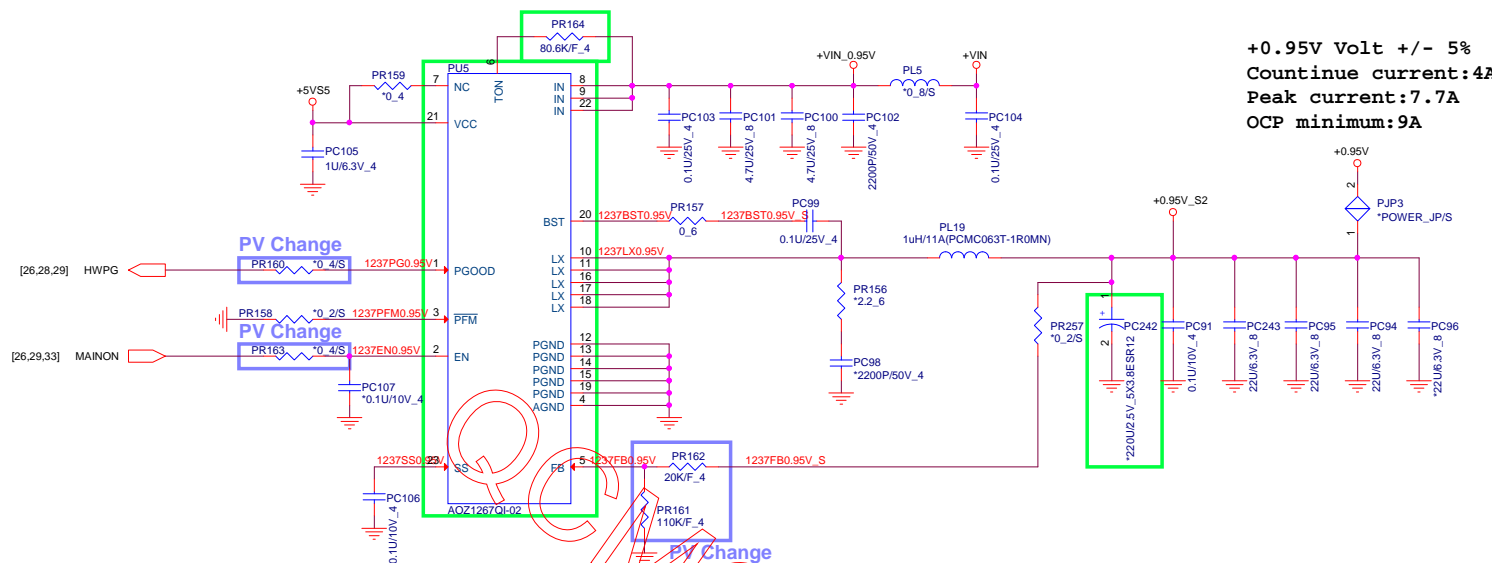
 NB5	PROJECT : Y22A Quanta Computer Inc.		
	Size Custom	Document Number WLAN/G-Sensor/GCLK/TPM	Rev 1A
	Date: Thursday, March 20, 2014 Sheet 25 of 36		

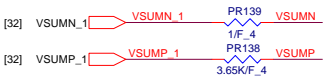


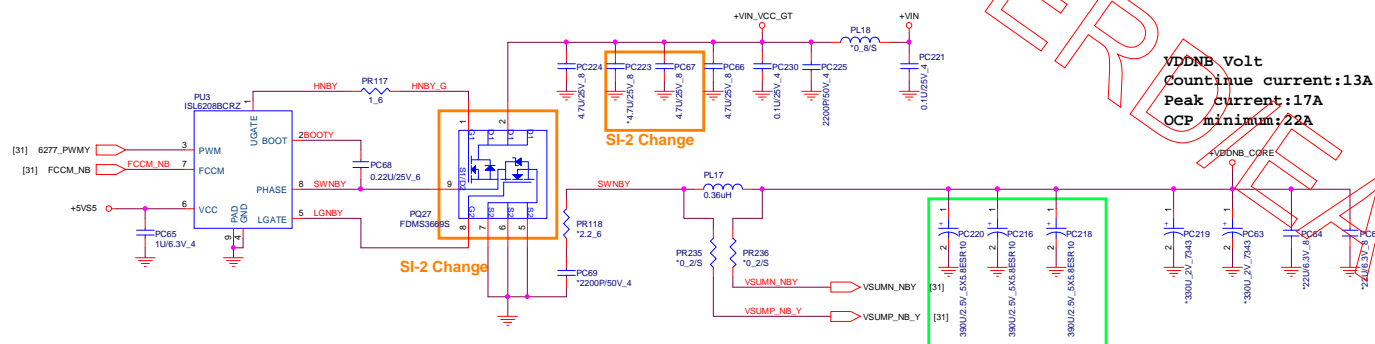
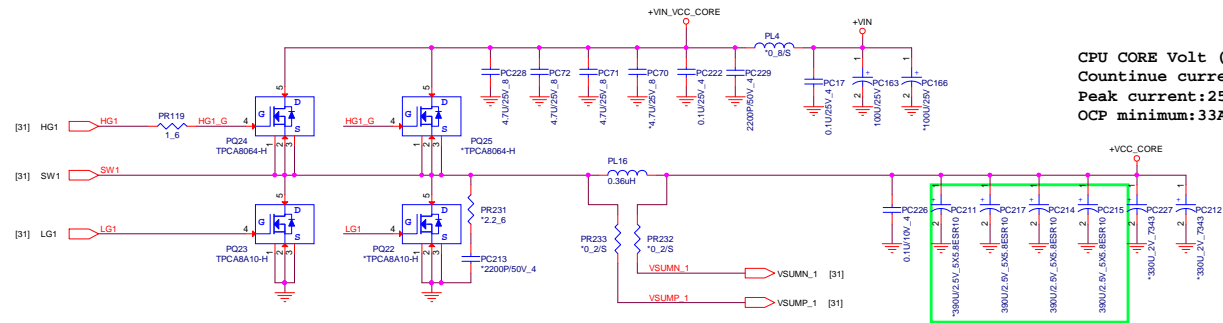


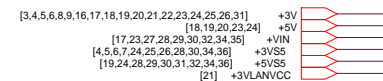
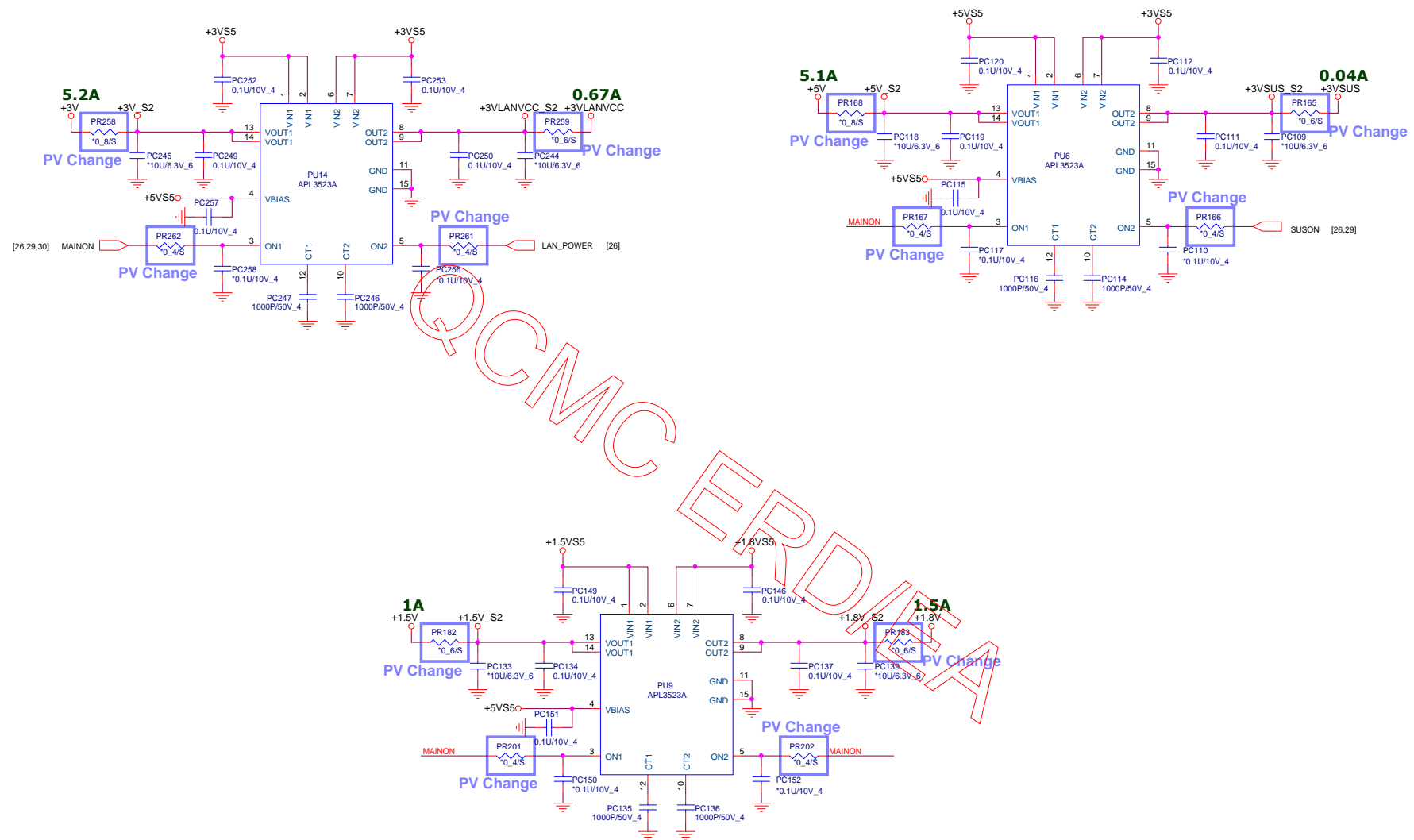
USB Charge support	Ra	Rb
Vine (No support)	Stuff	NA
Envy (Support)	NA	Stuff



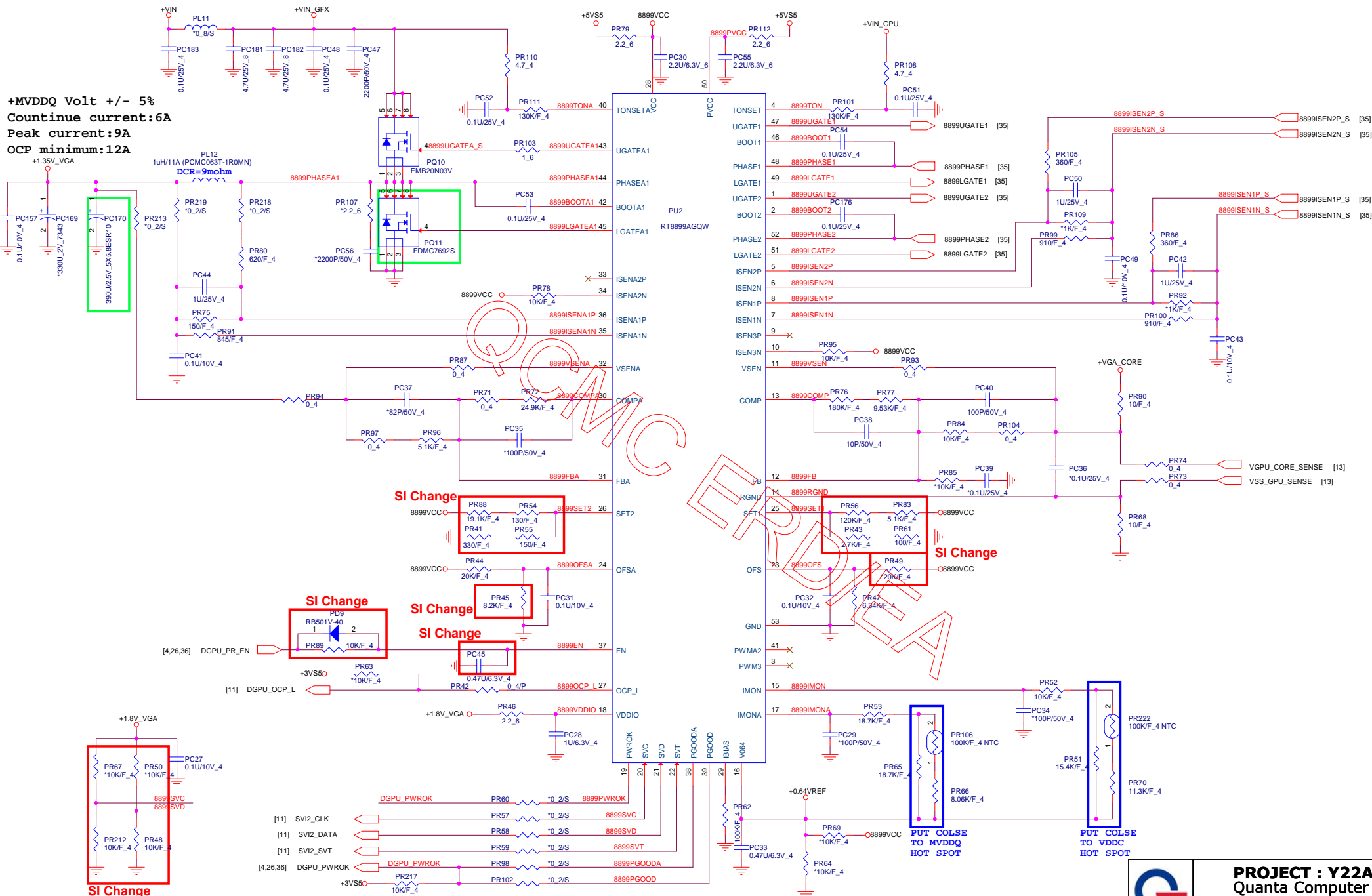








+MVDDQ Volt +/- 5%
 Countinue current:6A
 Peak current:9A
 OCP minimum:12A



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 Quanta Computer Inc.

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Custom	Vcore(RT8899A)	1A
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