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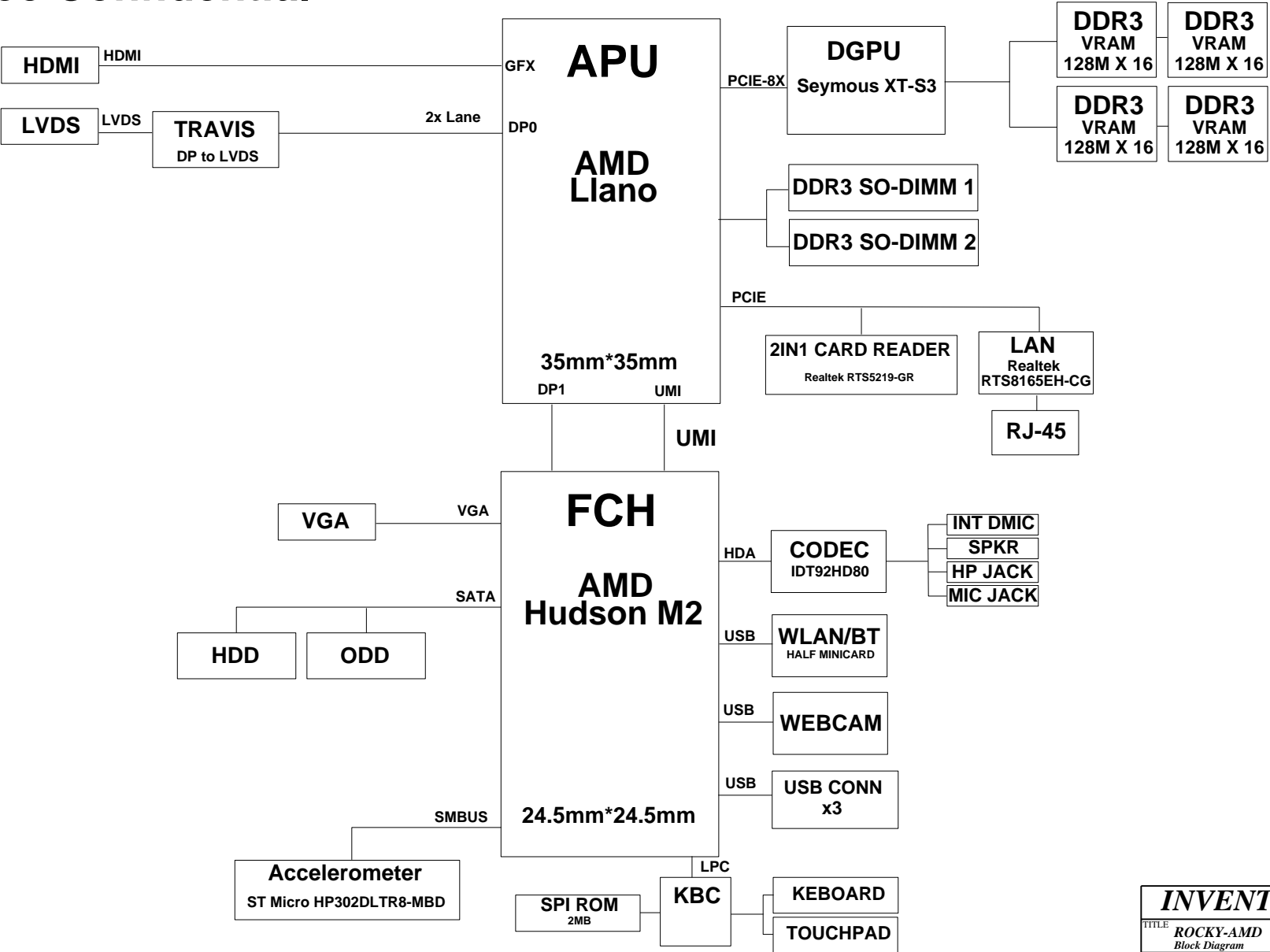
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INVENTEC			
TITLE ROCKY-AMD Index			
SIZE A3	CODE CS	DOC. NUMBER 1234	REV X01
CHANGE by Ken, Chiang		29-Jul-2010	
SHEET		2	OF 58

Inventec Confidential

# Sabine - DIS



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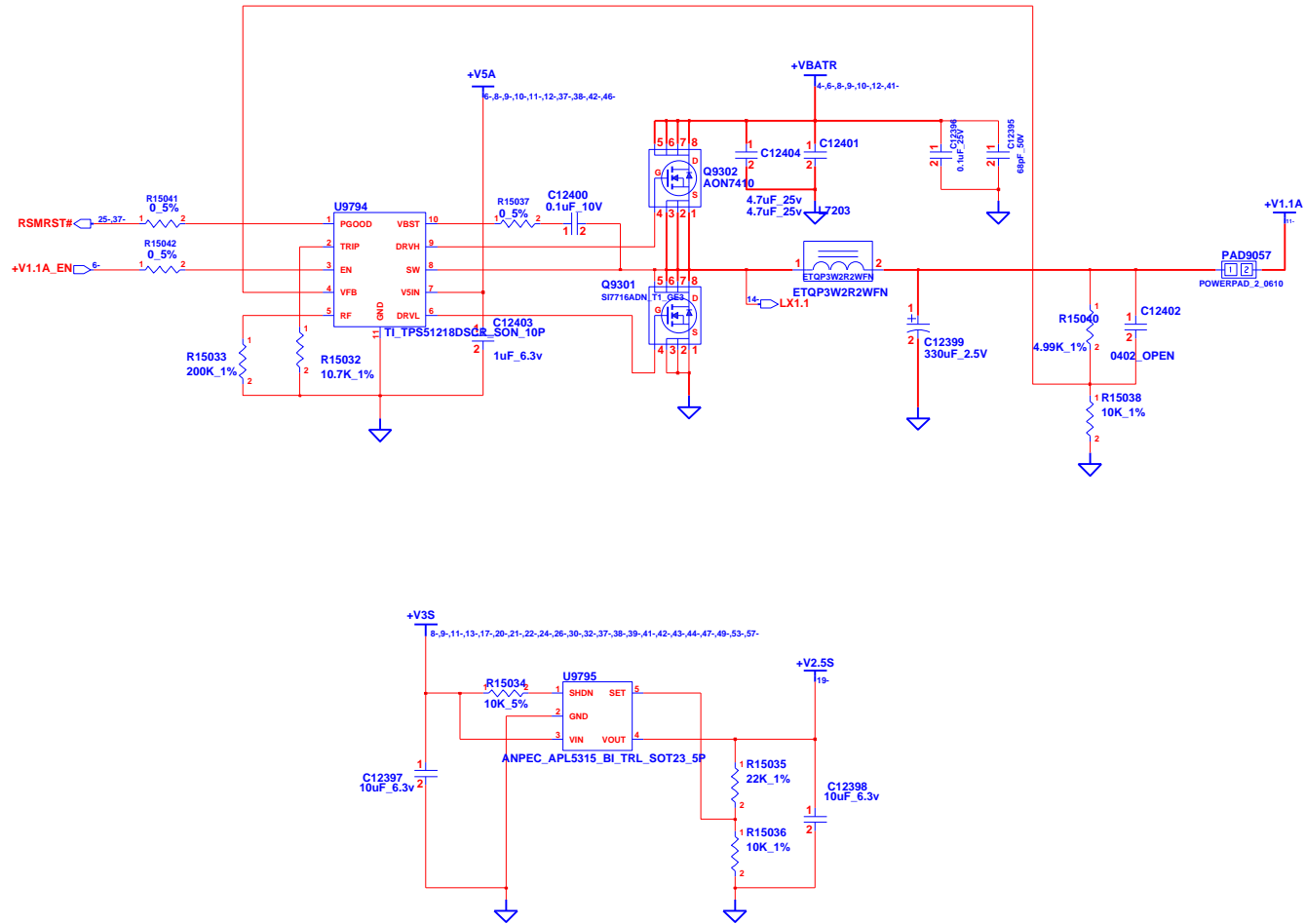
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SIZE A3	CODE CS	DOC. NUMBER 1234	REV X01
SHEET		3	OF 58

CHANGE by Ken, Chiang 29-Jul-2010

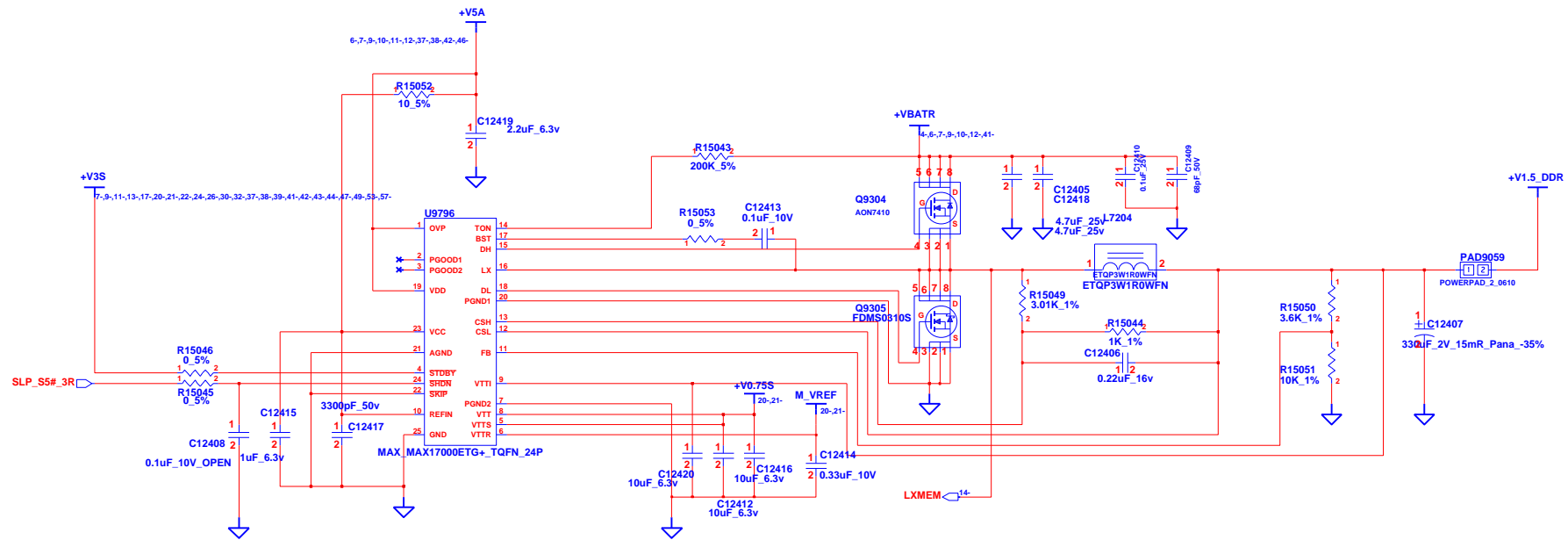








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TITLE: ROCKY-AMD DC & BATTERY CHARGER			
SIZE: A3	CODE: CS	DOC. NUMBER: 1234	REV: X01
CHANGE by: Ken, Chiang		9-Jun-2010	
SHEET: 7		OF: 58	



NOTE: DDR3 REGULATOR

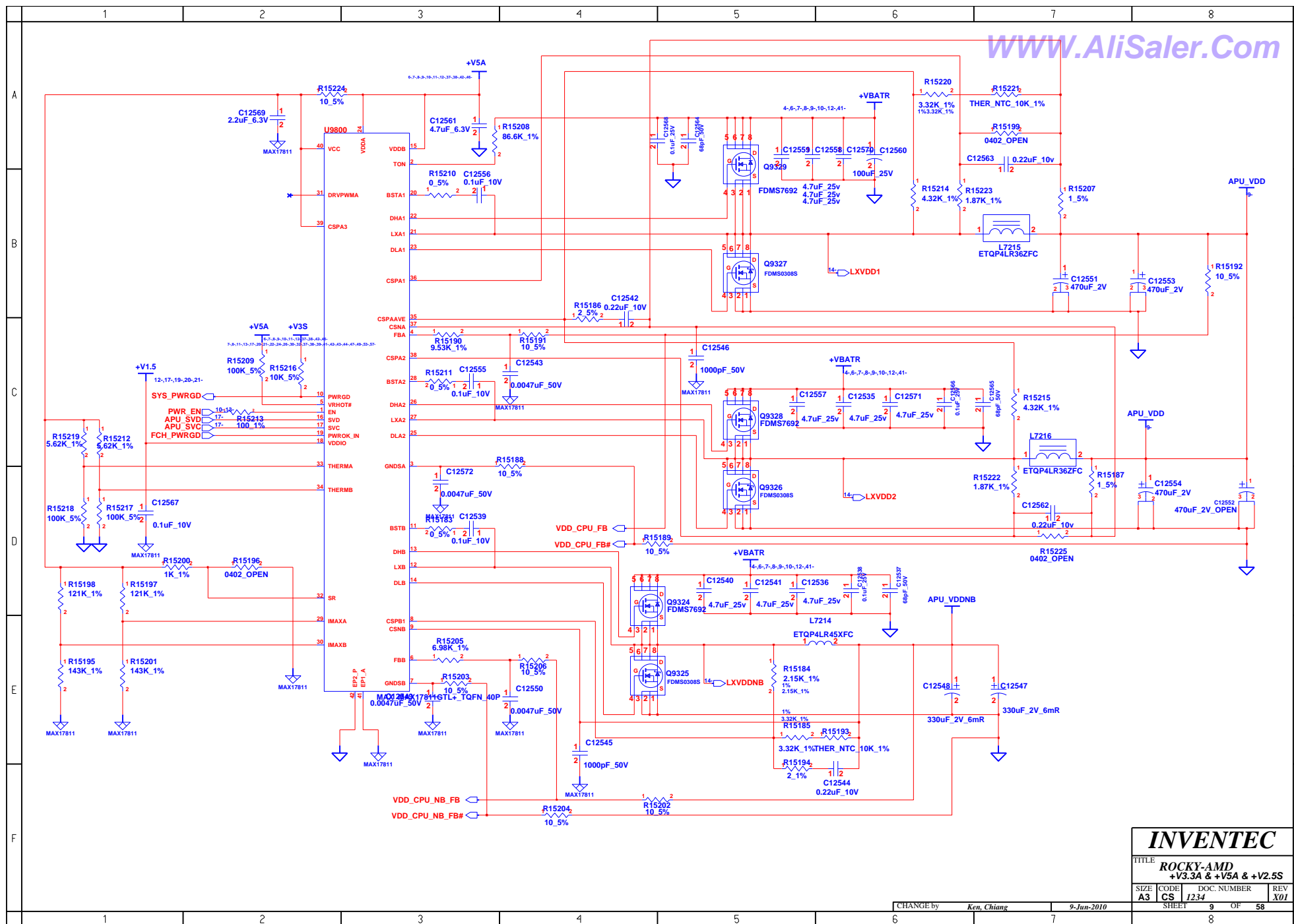
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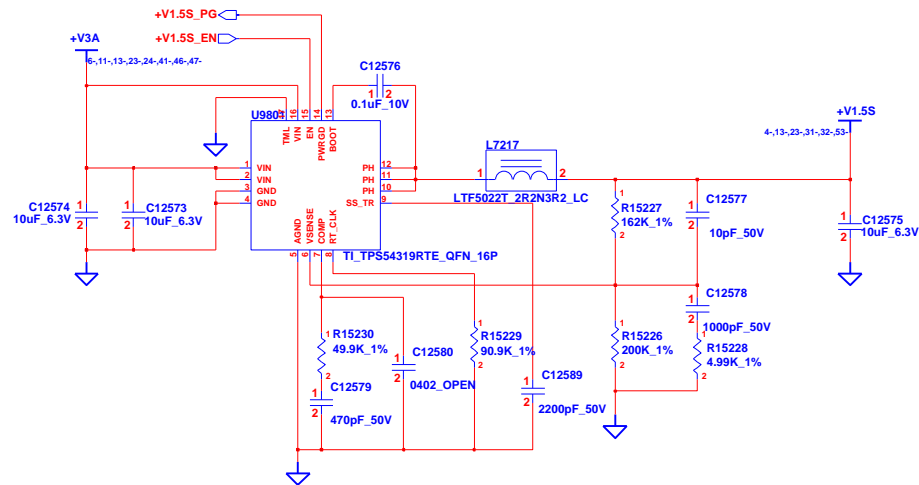
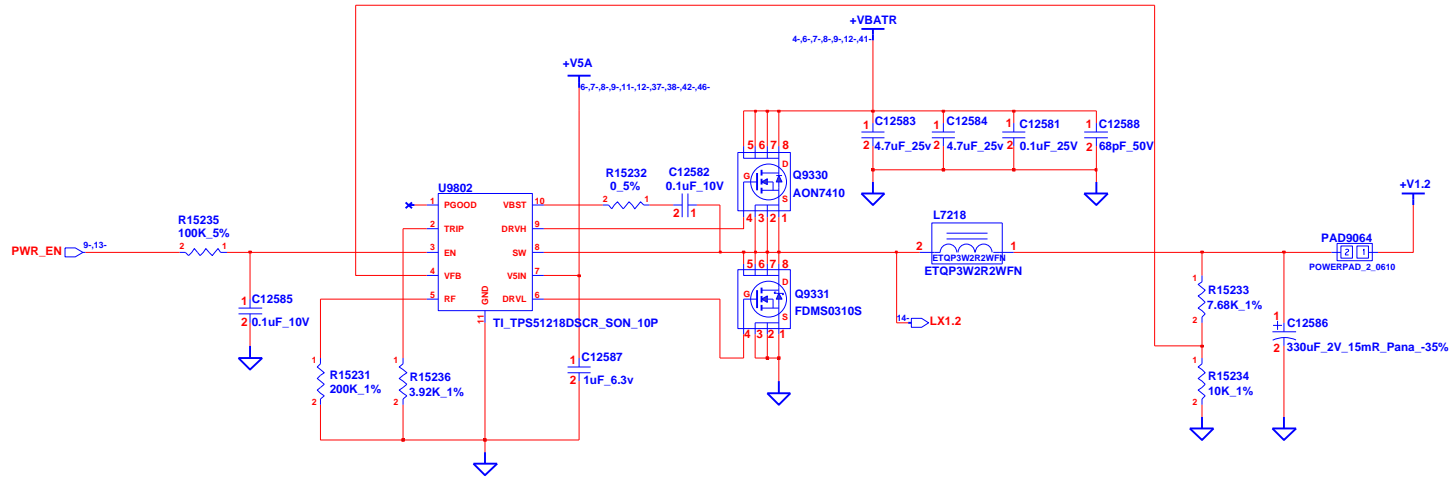
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**ROCKY-AMD  
BATTERY CONN**

SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <b>1234</b>	REV <b>X01</b>
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CHANGE by <b>Ken, Chiang</b>	9-Jun-2010	SHEET <b>8</b>	OF <b>58</b>
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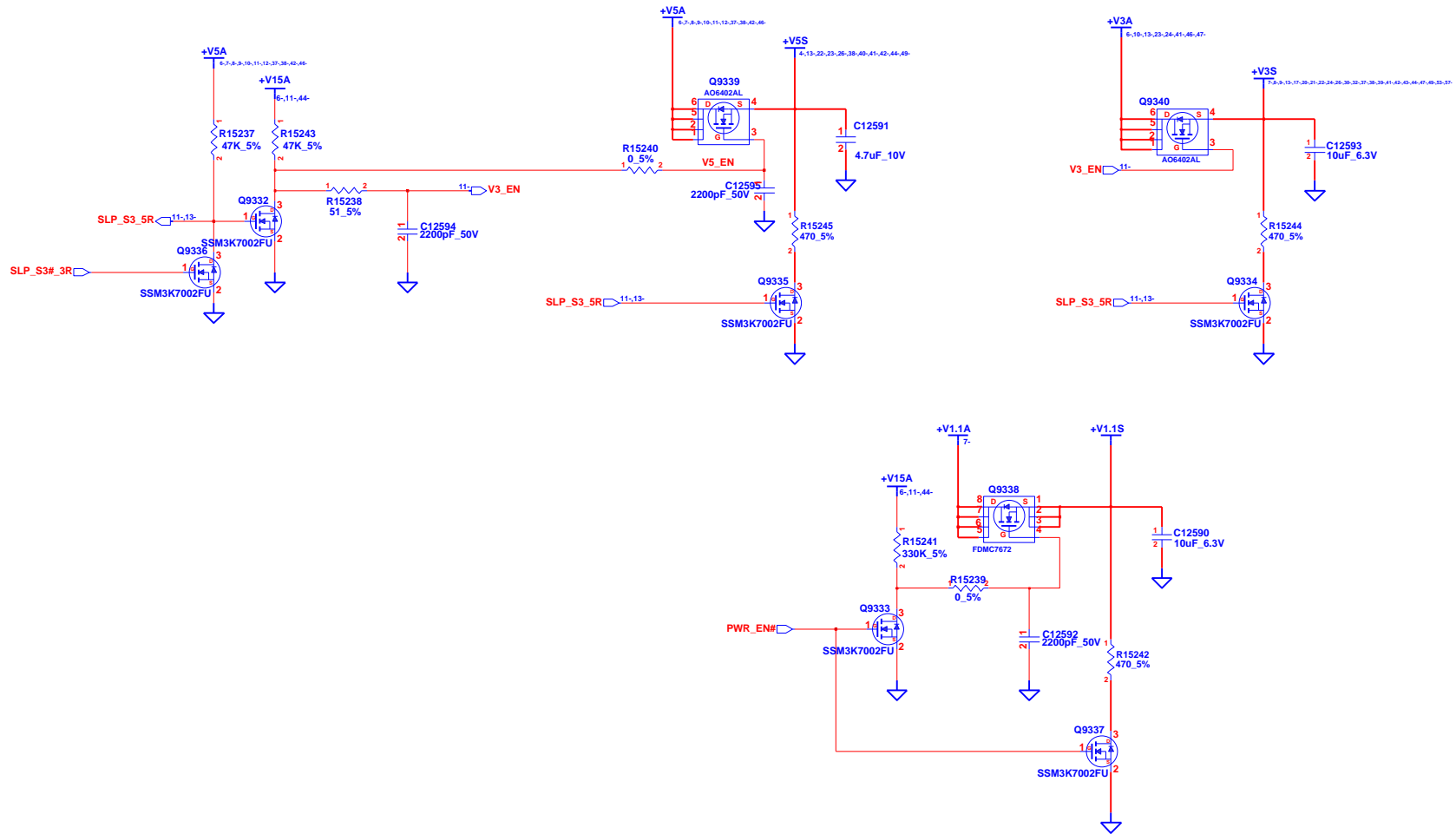
**INVENTEC**

TITLE  
**ROCKY-AMD**  
+V1.2A & VCC\_NB

SIZE CODE CS DOC. NUMBER REV  
**A3** **CS** **1234** **X01**

CHANGE by Ken Chiang 9-Jun-2010

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<b>INVENTEC</b>			
TITLE <b>ROCKY-AMD DDR3 POWER</b>			
SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <b>1234</b>	REV <b>X01</b>
SHEET <b>11</b>		OF <b>58</b>	

CHANGE by: Ken, Chiang 9-Jun-2010

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POW_SW1	POW_SW0	+VDDC_GPU	Rtop	Rbot
0	0	1.1V	R50	R55 // R54
0	1	1V	R50	R55
1	0	0.95V	R50 // R91	R55 // R54
1	1	0.9V	R50 // R91	R55

$V_{out} = (R_{top}/R_{bot} + 1) \times 0.75$

**INVENTEC**

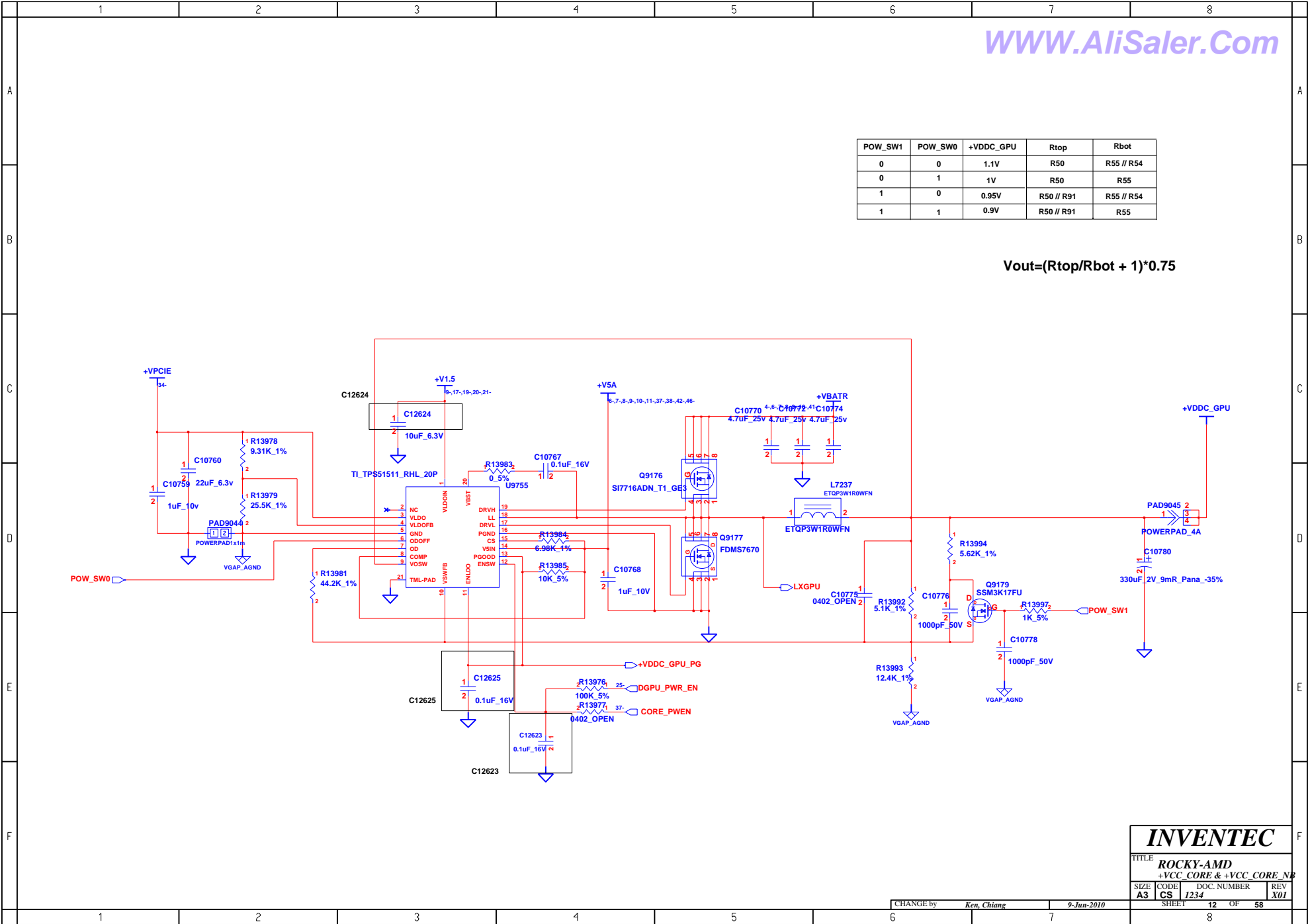
TITLE: **ROCKY-AMD**

+VCC\_CORE & +VCC\_CORE\_PG

SIZE	CODE	DOC. NUMBER	REV
A3	CS	1234	X01

CHANGE by: Ken, Chiang 9-Jun-2010

SHEET 12 OF 58



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POW_SW1	POW_SW0	+VDDC_GPU	Rtop	Rbot
0	0	1.1V	R50	R55 // R54
0	1	1V	R50	R55
1	0	0.95V	R50 // R91	R55 // R54
1	1	0.9V	R50 // R91	R55

$V_{out} = (R_{top}/R_{bot} + 1) \times 0.75$

**INVENTEC**

TITLE: **ROCKY-AMD**

+VCC\_CORE & +VCC\_CORE\_PG

SIZE	CODE	DOC. NUMBER	REV
A3	CS	1234	X01

CHANGE by: Ken, Chiang 9-Jun-2010

SHEET 12 OF 58

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POW_SW1	POW_SW0	+VDDC_GPU	Rtop	Rbot
0	0	1.1V	R50	R55 // R54
0	1	1V	R50	R55
1	0	0.95V	R50 // R91	R55 // R54
1	1	0.9V	R50 // R91	R55

$V_{out} = (R_{top}/R_{bot} + 1) \times 0.75$

**INVENTEC**

TITLE: **ROCKY-AMD**

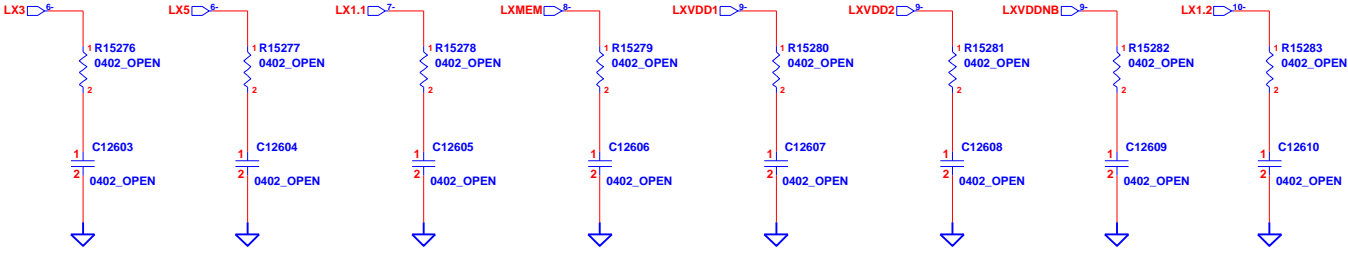
+VCC\_CORE & +VCC\_CORE\_PG

SIZE	CODE	DOC. NUMBER	REV
A3	CS	1234	X01

CHANGE by: Ken, Chiang 9-Jun-2010

SHEET 12 OF 58



	1	2	3	4	5	6	7	8	
A									A
B									B
C									C
D	<div>N/A</div> <div>+V1.5S_LDO</div>								D
E									E
F									F
	1	2	3	4	5	6	7	8	

INVENTEC			
TITLE: ROCKY-AMD			
+V5S & +V3.3S & +V1.5S & +V1.2S			
SIZE A3	CODE CS	DOC. NUMBER 1234	REV X01
CHANGE by: Ken, Chiang		9-Jun-2010	
SHEET		14	OF 58

	1	2	3	4	5	6	7	8	
A	WWW.AliSaler.Com								A
B									B
C									C
D									D
E									E
F									F
	1	2	3	4	5	6	7	8	

INVENTEC

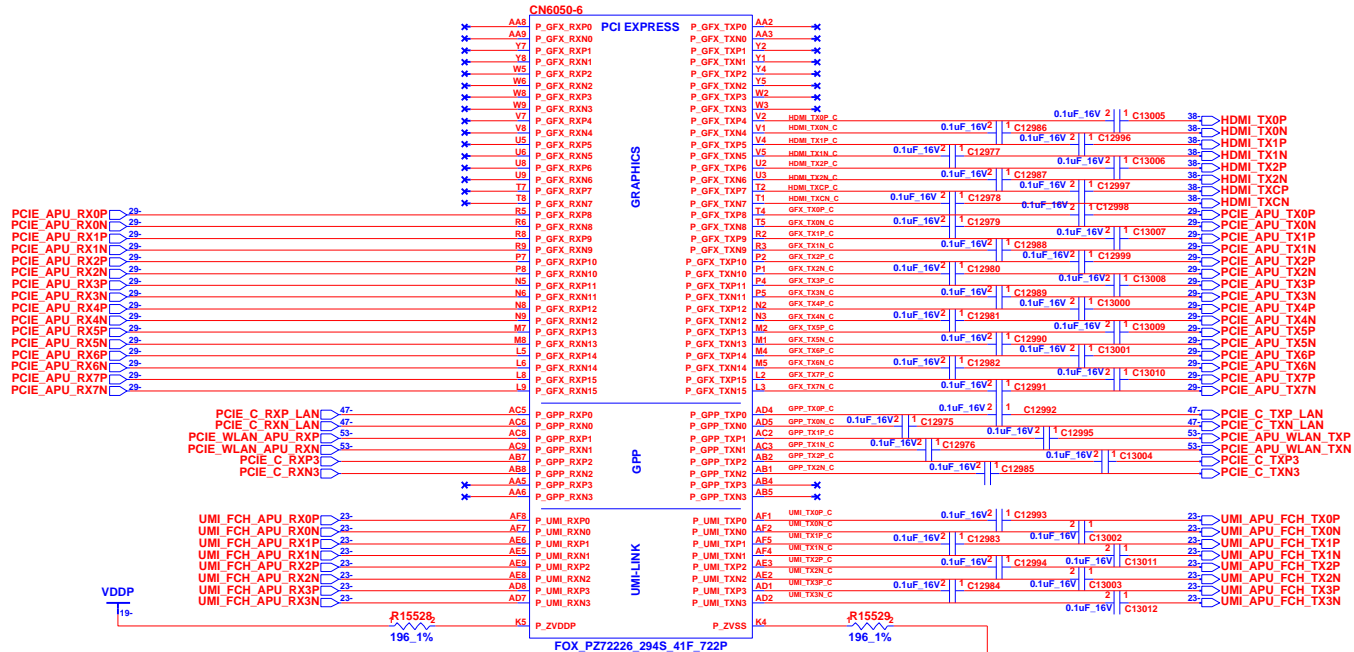
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ROCKY-AMD  
CLK GEN

SIZE A3	CODE CS	DOC. NUMBER 1234	REV X01
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CHANGE by Ken, Chiang 9-Jun-2010

SHEET 15 OF 58

# CPU



HDMI

PEG

LAN  
WLAN  
CardReader

INVENTEC

TITLE  
ROCKY-AMD  
RS880M - 1

SIZE CODE DOC. NUMBER REV  
A3 CS 1234 16 OF 58 X01

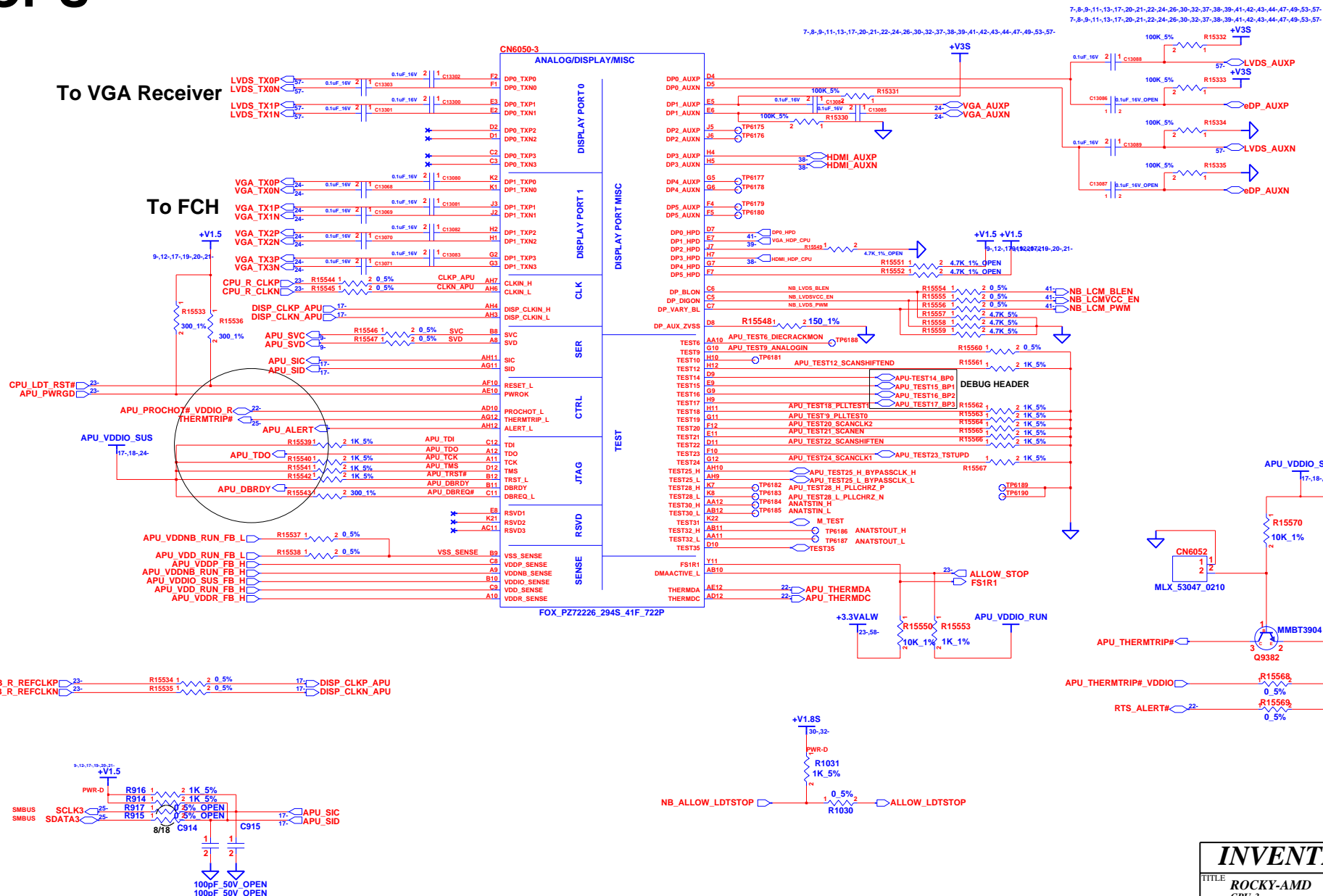
CHANGE by Ken, Chiang

9-Jun-2010



# CPU

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INVENTEC

TITLE **ROCKY-AMD**  
**CPU-3**

SIZE  
A

3	CS
---	----

DOC. NUMBER	1234
-------------	------

	R
	X

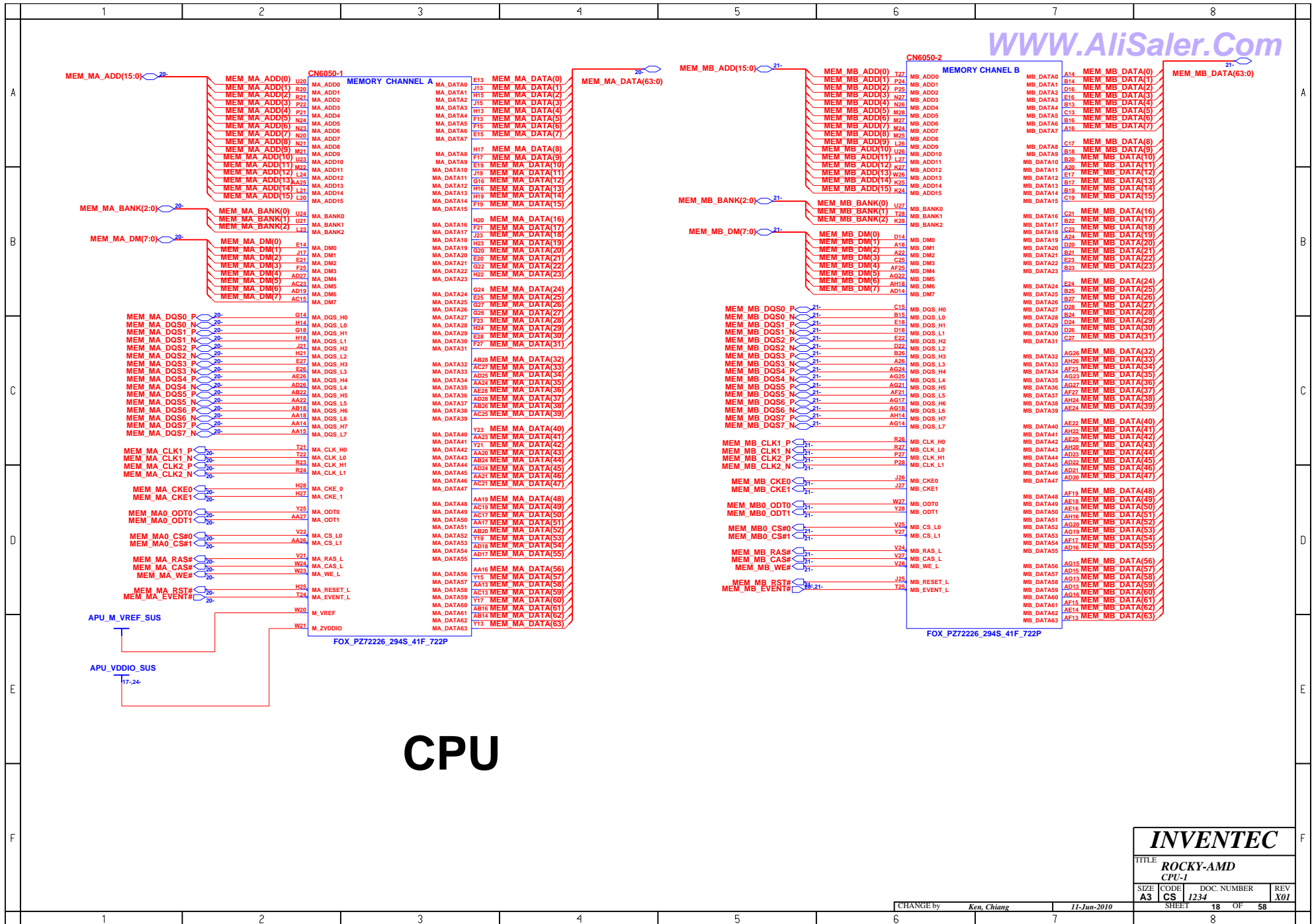
CHANGE by	<i>Ken, Chiang</i>
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9-Jun-2010

17 OF 58

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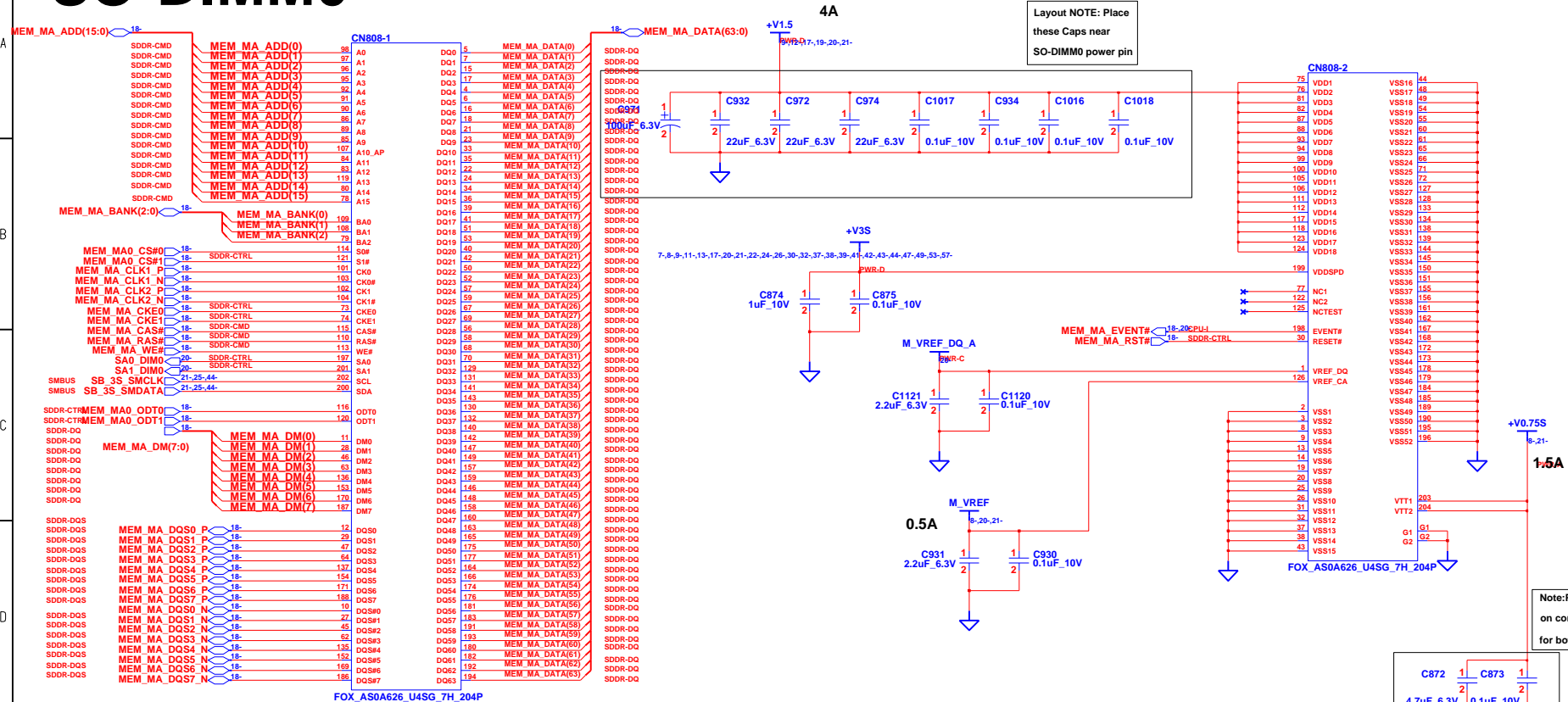
8





# SO-DIMM0

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## NOTE:

IF SA0\_DIM0=0, SA1\_DIM0=0  
SO-DIMMA SPD ADDRESS IS 0xA0  
SO-DIMMA TS ADDRESS IS 0x30

IF SA0\_DIM0=1, SA1\_DIM0=0  
SO-DIMMA SPD ADDRESS IS 0xA2  
SO-DIMMA TS ADDRESS IS 0x32

INVENTEC

TITLE ROCKY-AMD DDR3 DIMM0			
SIZE A3	CODE CS	DOC. NUMBER 1234	REV X01
SHEET		20	OF 58

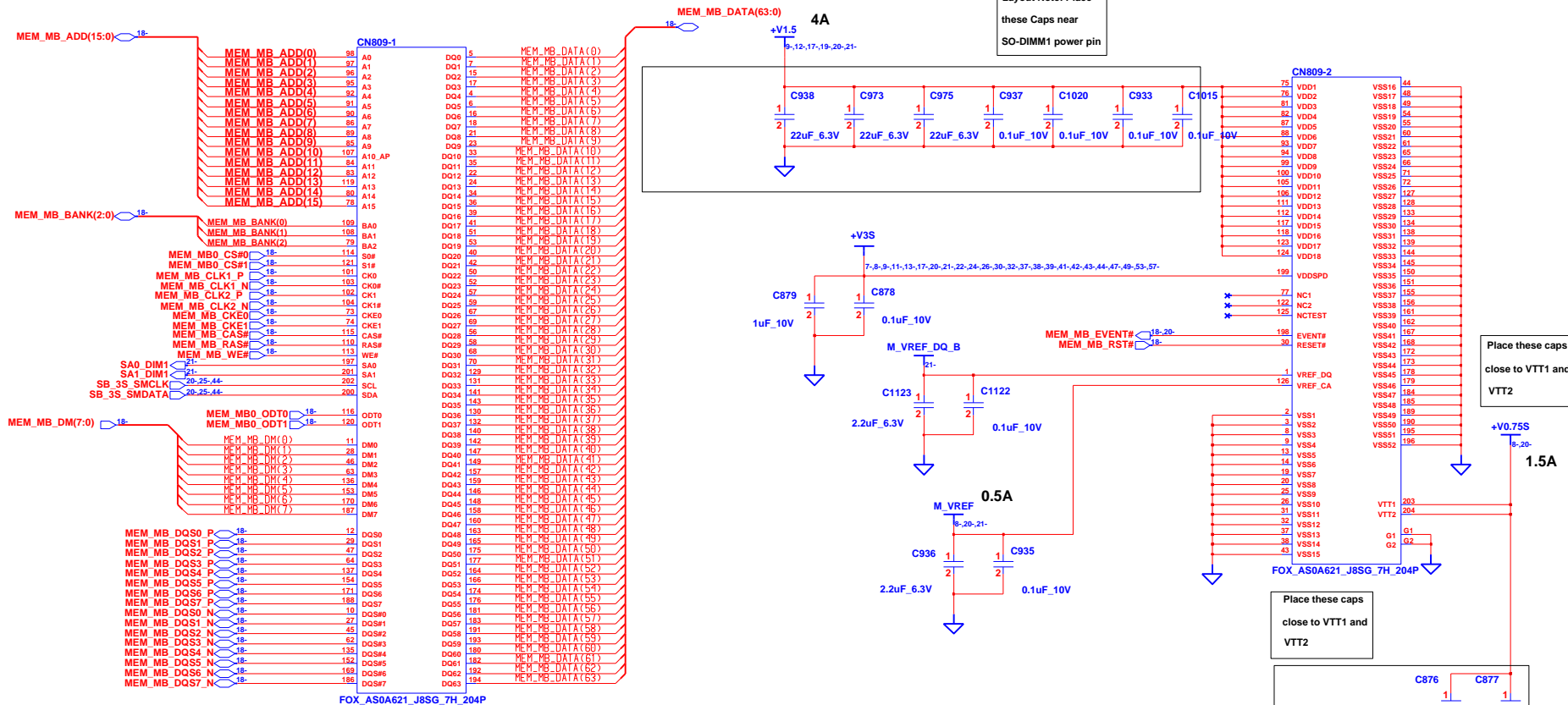
CHANGE by Ken, Chiang 9-Jun-2010

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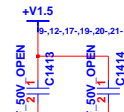
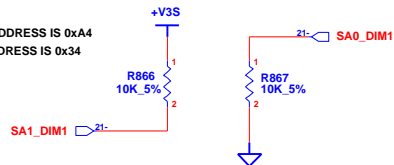
# SO-DIMM1

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Layout Note: Place  
these Caps near  
SO-DIMM1 power pin



NOTE:  
SO-DIMM SPD ADDRESS IS 0xA4  
SO-DIMM TS ADDRESS IS 0x34



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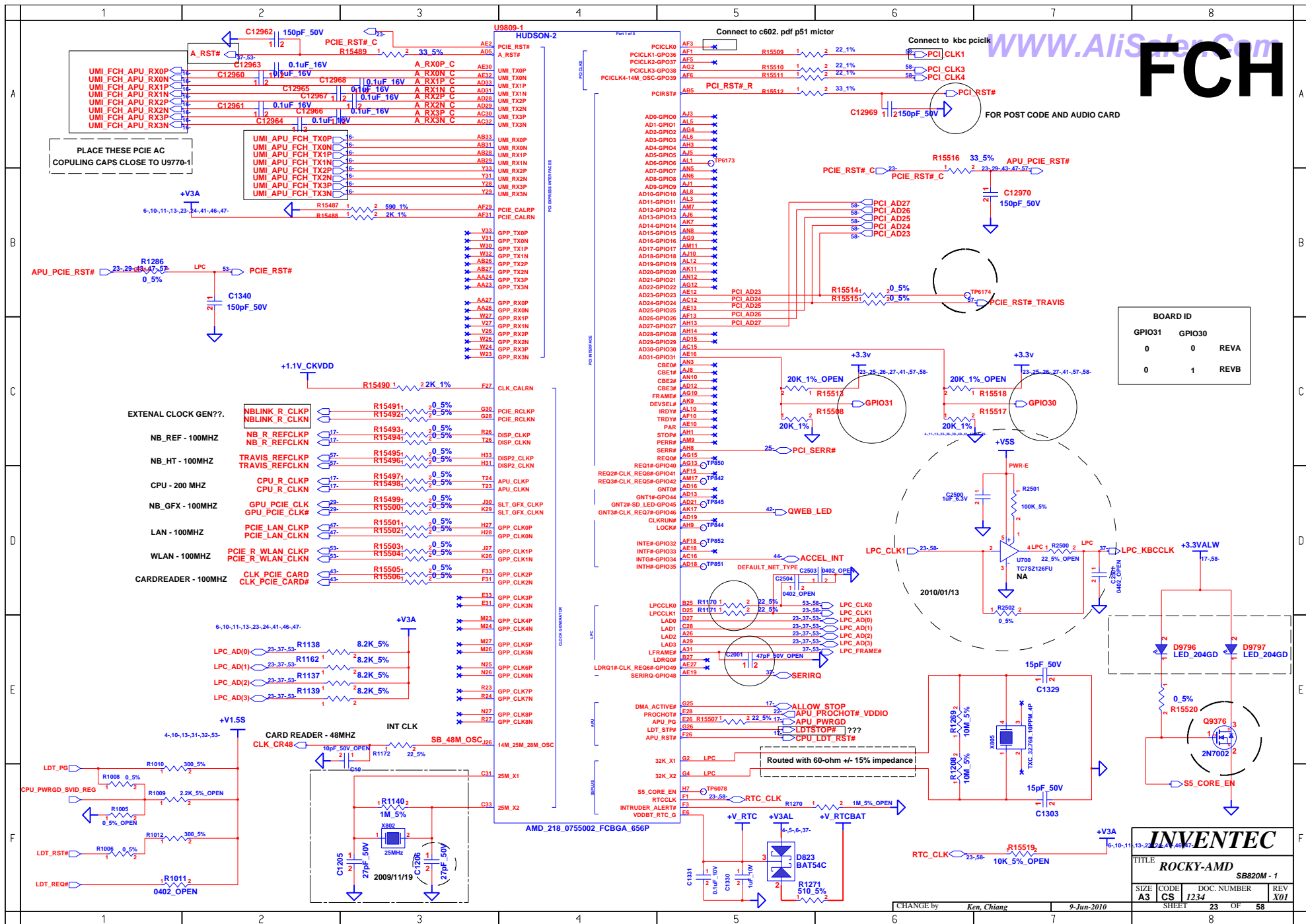
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SIZE A3	CODE CS	DOC. NUMBER 1234	REV X01
SHEET		21	OF 58

CHANGE by Ken, Chiang 9-Jun-2010

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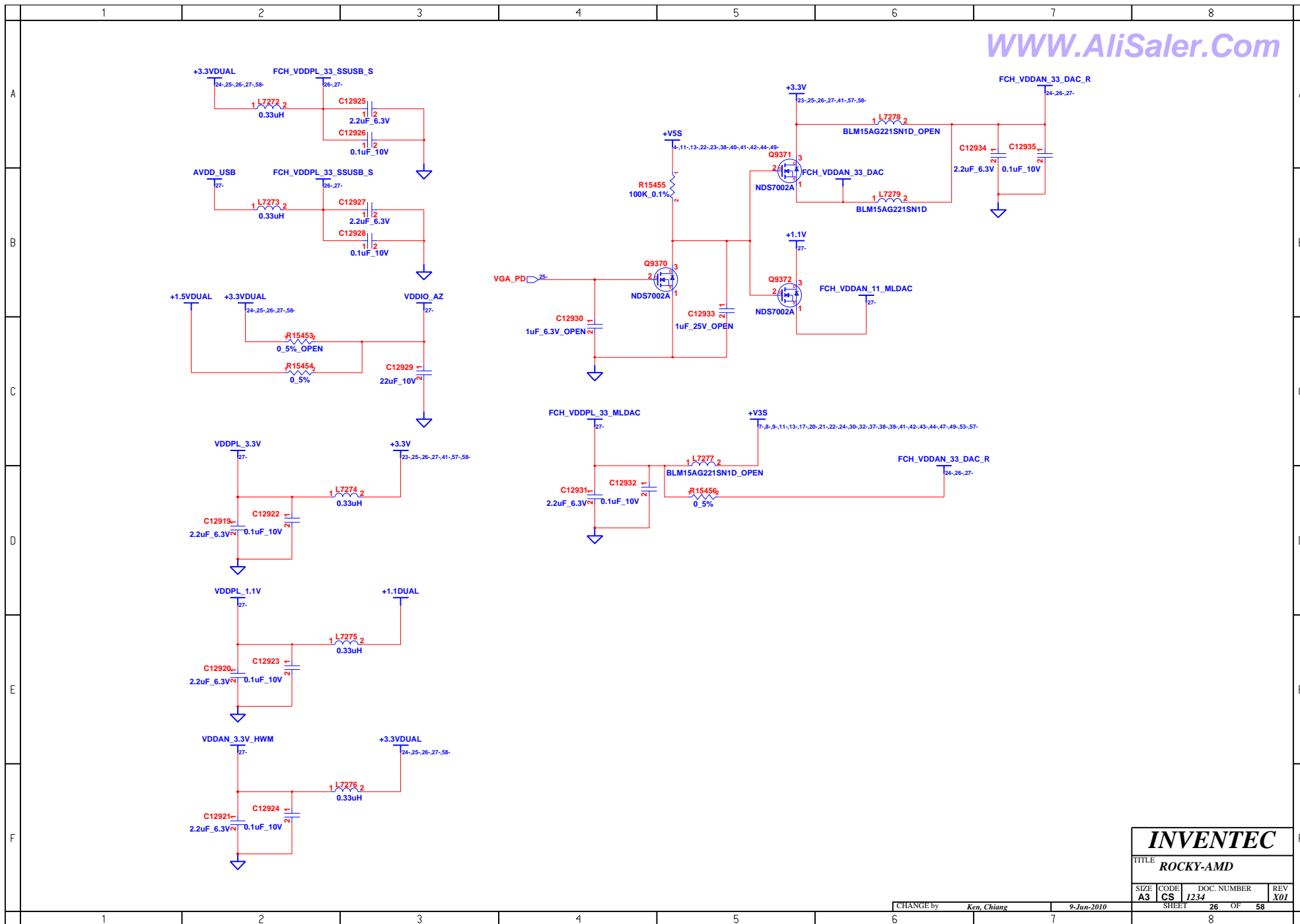
















# DGPU

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## CLOSE TO DGPU



INVENTEC

TITLE ROCKY-AMD

SIZE A3 CODE CS DOC. NUMBER 1234 REV X01

CHANGE by Ken Chiang 9-Jun-2010

SHEET 29 OF 58

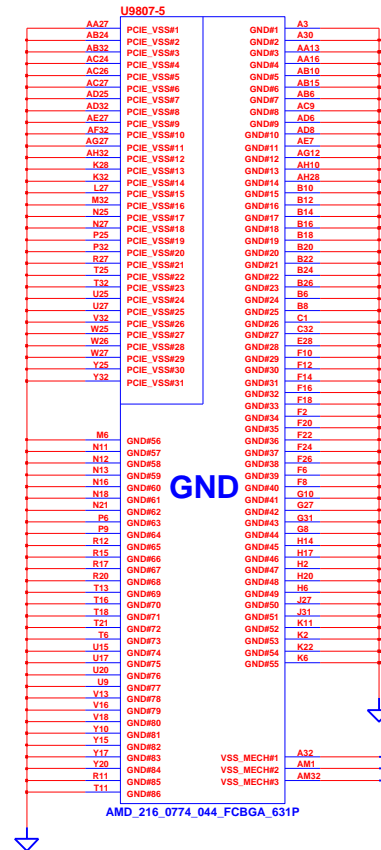
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# DGPU

## Channel B Memory

M92 - U906 / U18 - NA

M96 - U906 / U18 - Mount  
 HYNIX P/N = 6019B0581601  
 SAMSUNG P/N = 6019B0581801  
 QIMONDA P/N = 6019B0593501

**INVENTEC**

TITLE  
**ROCKY-AMD**

SIZE	CODE	DOC. NUMBER	REV
A3	CS	1234	X01

CHANGE by **Ken, Chiang**

9-Jun-2010

SHEET 36 OF 58

# KBC

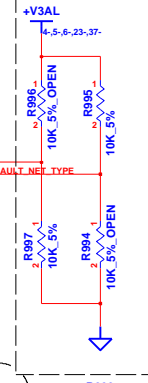
U805\_6019B0724201

U805  
ITE\_IT8502E-KX\_L\_LQFP\_128P

	UMA	DIS
R12841	NA	
R12842		NA

FOR ITE 8500

BOARD ID



INVENTEC

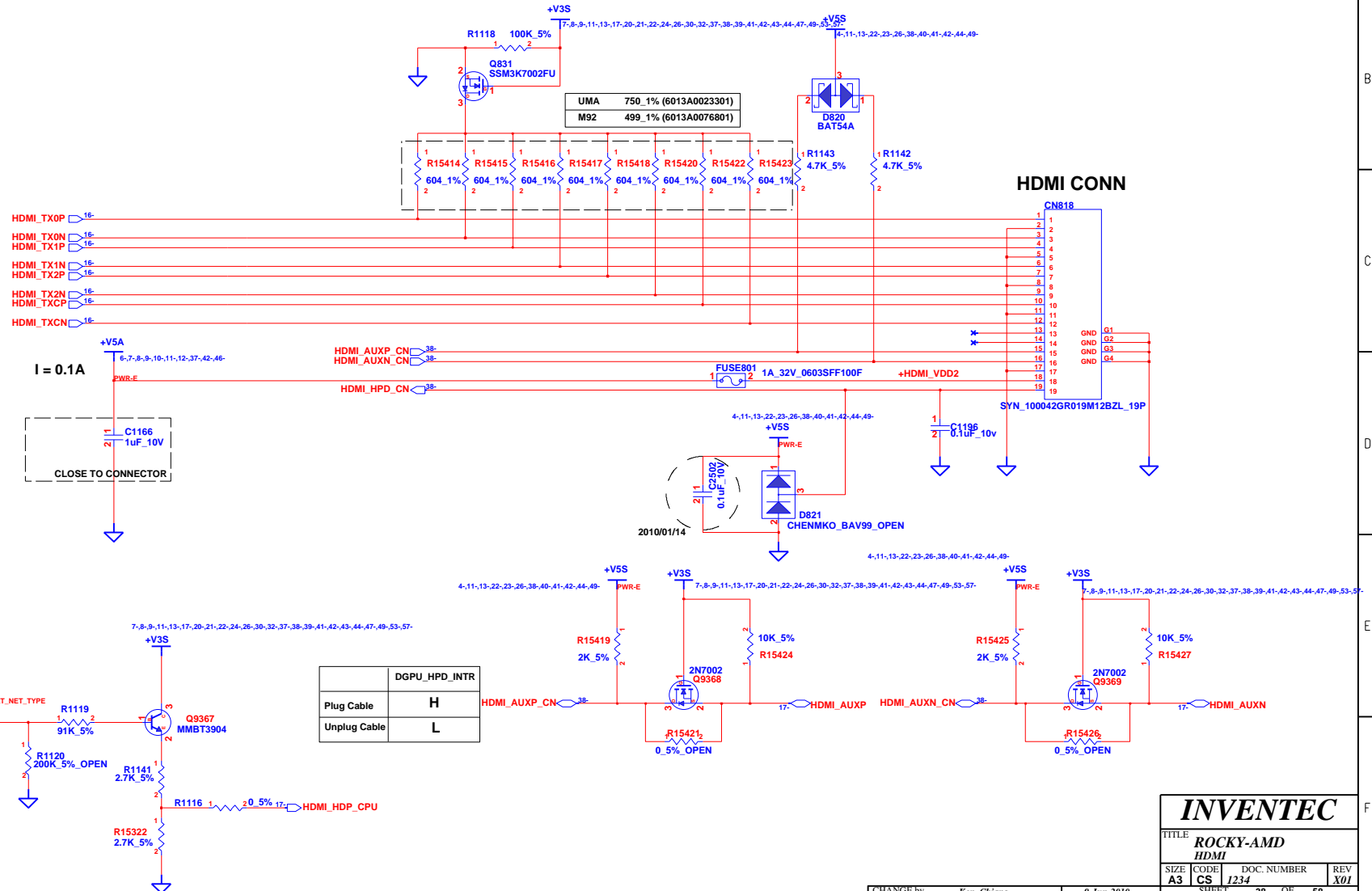
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SIZE A3	CODE CS	DOC. NUMBER 1234	REV X01
SHEET 37		OF 58	

CHANGE by Ken Chiang

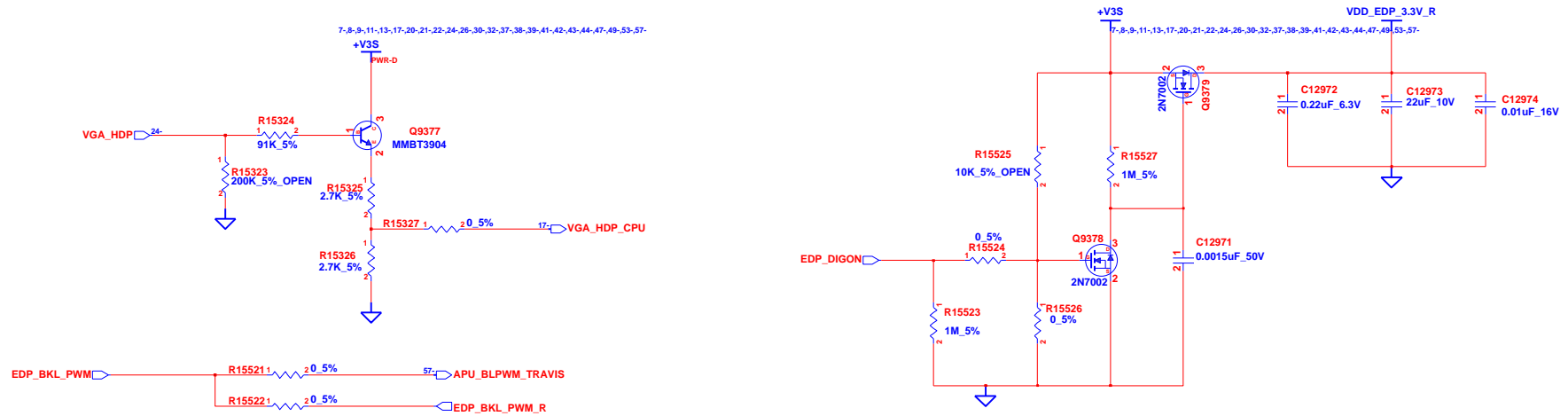
9-Jun-2010

8

# HDMI



INVENTEC			
TITLE ROCKY-AMD HDMI			
SIZE A3	CODE CS	DOC. NUMBER 1234	REV X01
CHANGE by Ken Chiang		9-Jun-2010	
SHEET 38		OF 58	

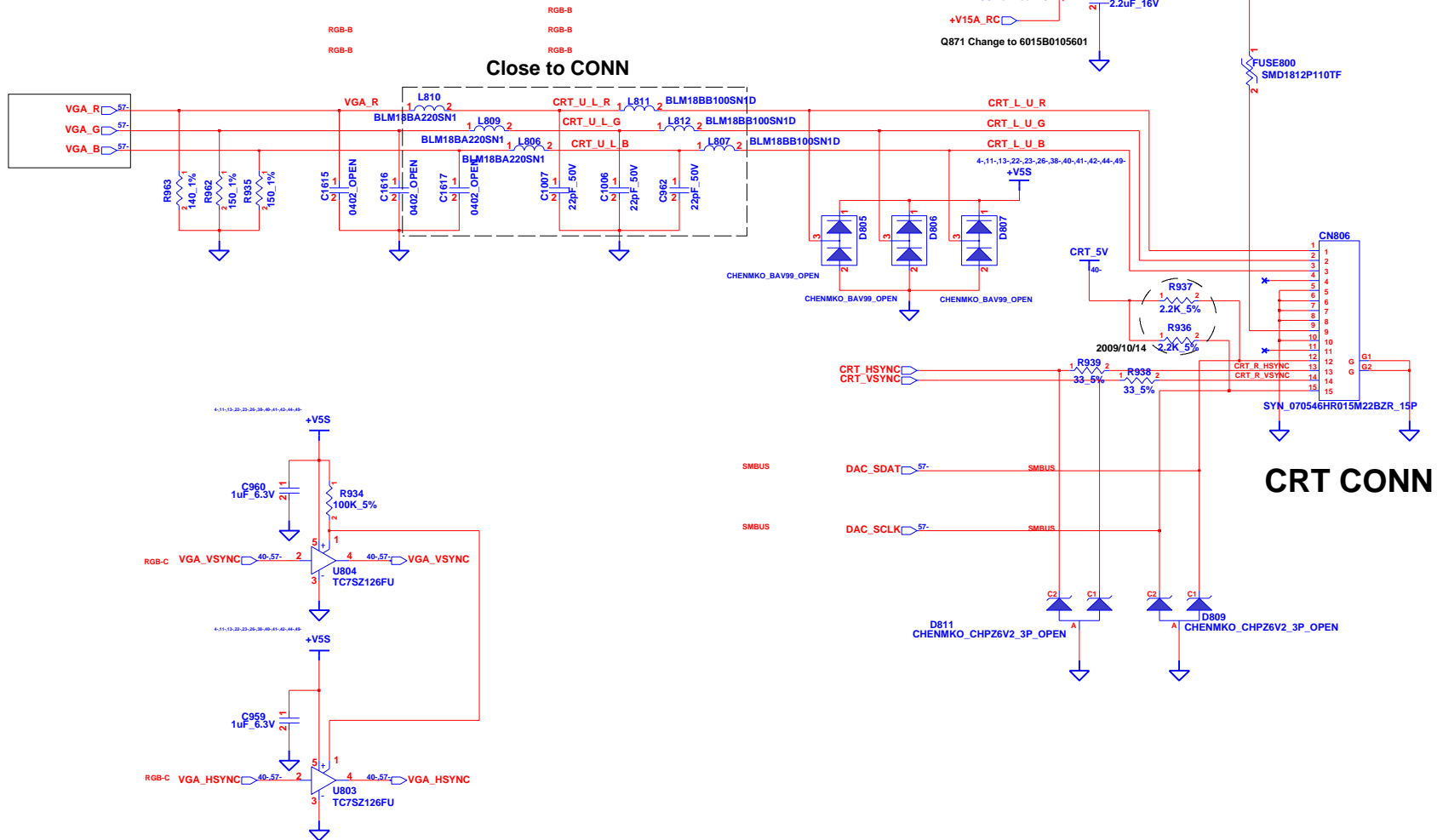


INVENTEC			
TITLE ROCKY-AMD DDR3 DAMPING			
SIZE A3	CODE CS	DOC. NUMBER 1234	REV X01
SHEET		39	OF 58

CHANGE by Ken, Chiang 9-Jun-2010

Circuit : Fixed  
2009/06/30

PE_GPIO2	S1	S2
L	ON	OFF
H	OFF	ON



INVENTEC

TITLE ROCKY-AMD CRT			
SIZE A3	CODE CS	DOC. NUMBER 1234	REV X01

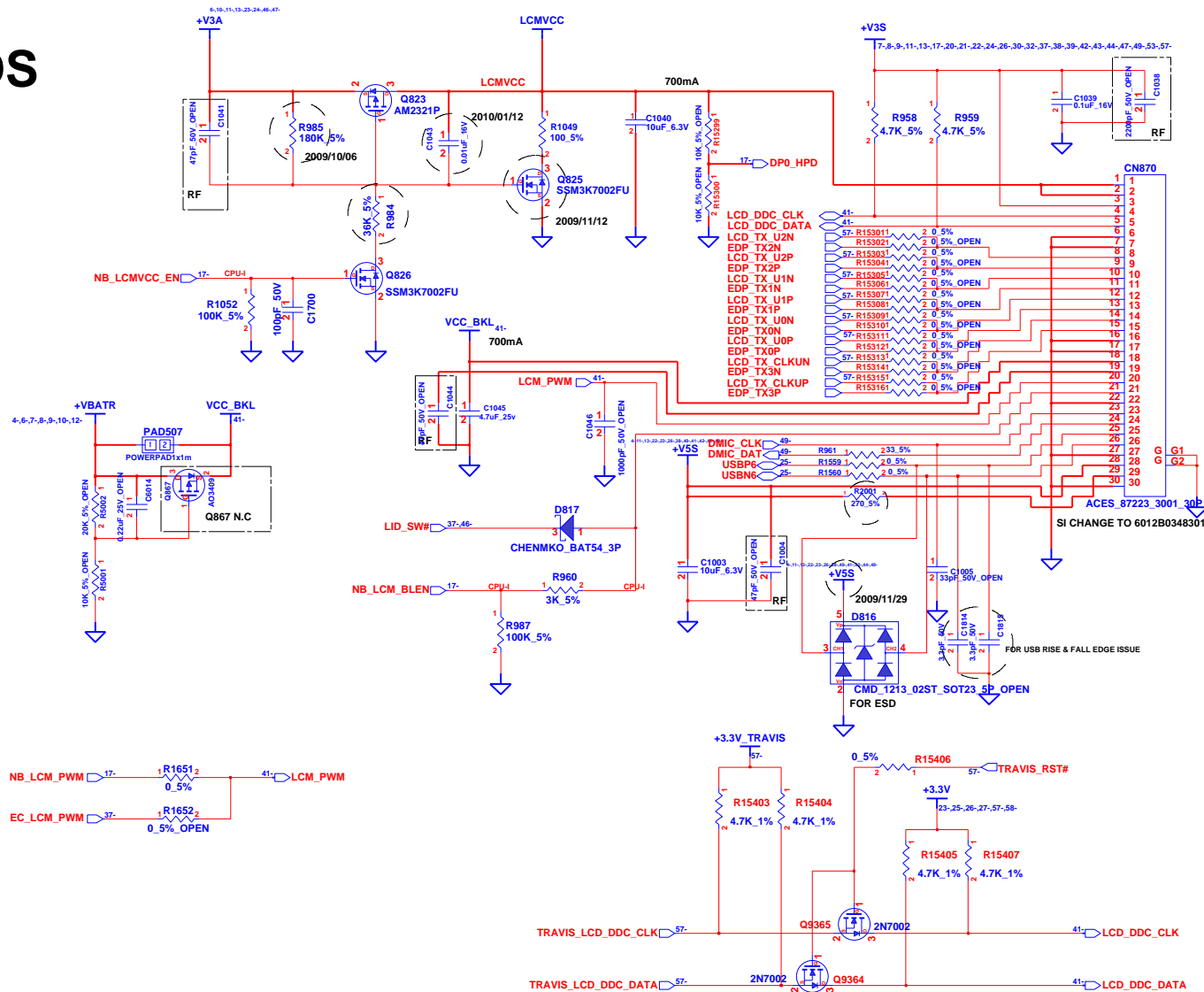
CHANGE by Ken Chiang 9-Jun-2010

SHEET 40 OF 58



# LVDS

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TITLE  
ROCKY-AMD  
LCM CONN

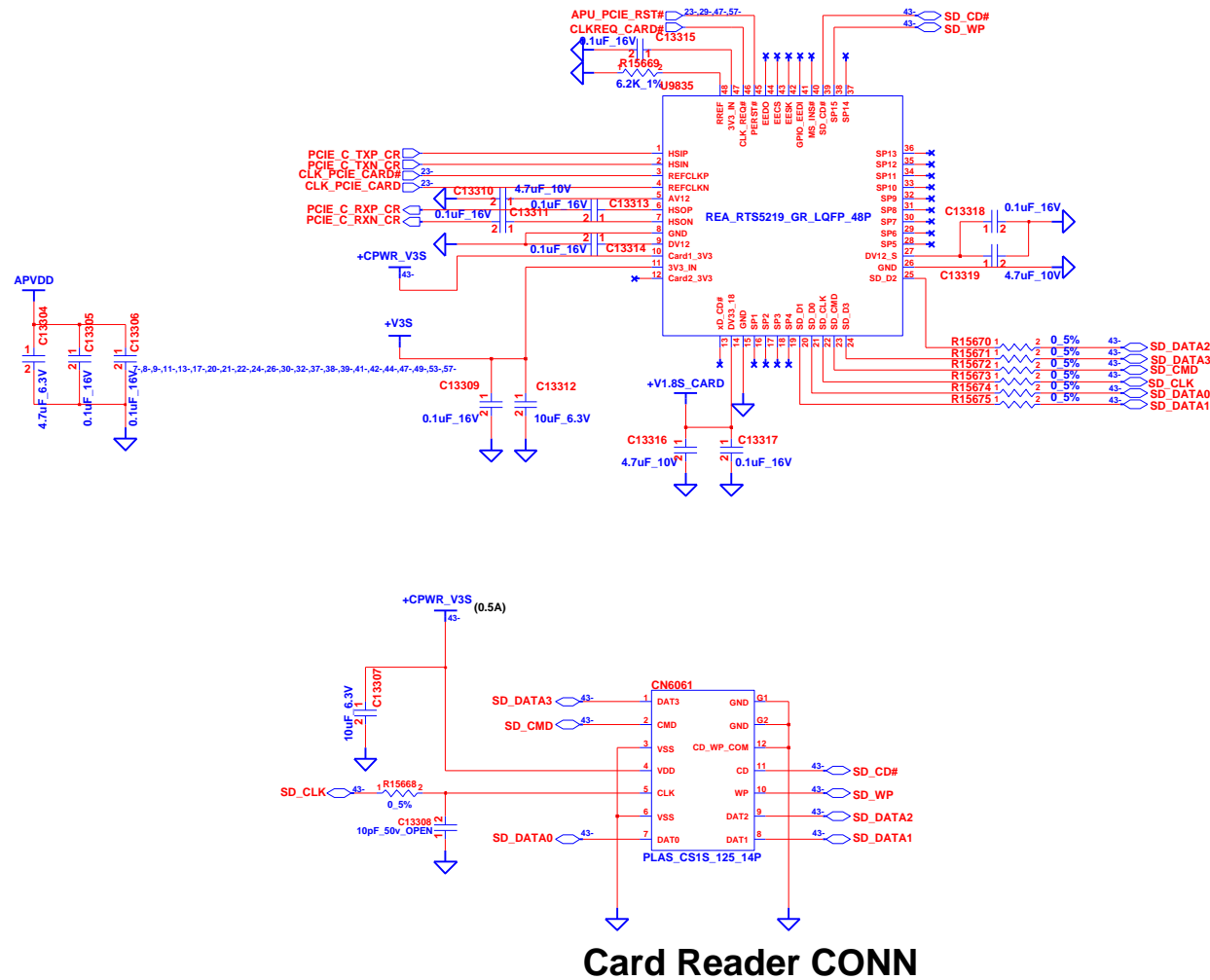
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A3 CS 1234 X01

CHANGE by Ken Chiang 9-Jun-2010

SHEET 41 OF 58

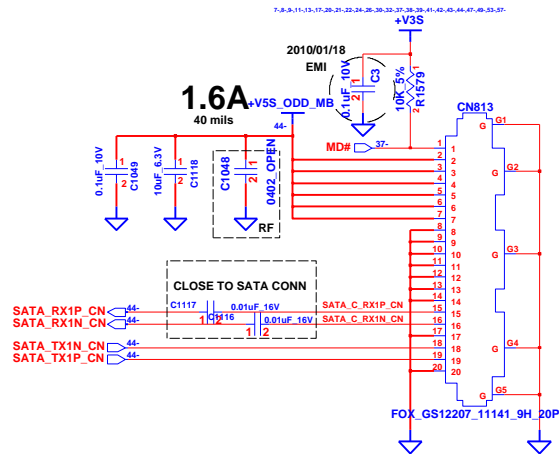
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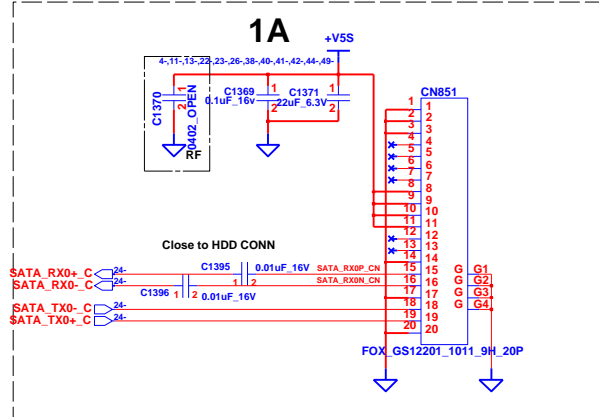


INVENTEC			
TITLE: ROCKY-AMD AU6433 & CARDREADER CONN			
SIZE A3	CODE CS	DOC. NUMBER 1234	REV X01
CHANGE by Ken Chiang		9-Jun-2010	
SHEET 43		OF 58	

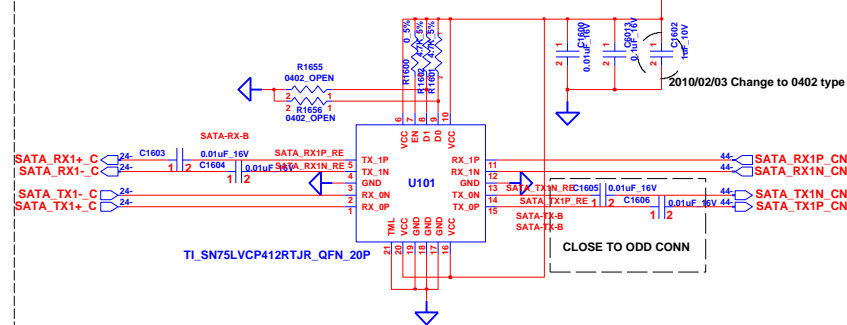
## SATA ODD FFC CONN on MB



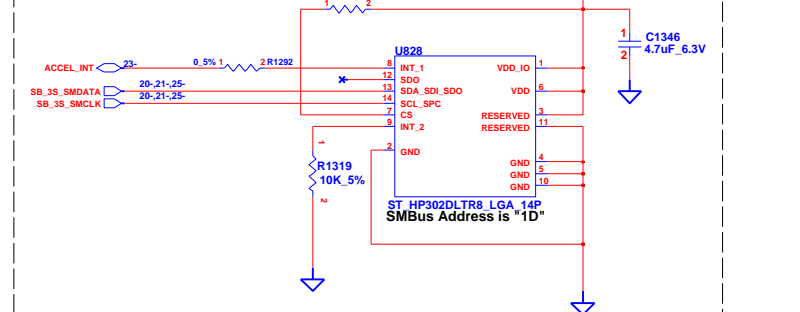
## SATA HDD FFC CONN on MB



## SATA RE-DRIVER



ST-HP302DLTR interrupt pin default is low / active Hi,  
BIOS need to programming 22h to change status  
from active Hi to low



**HARDDRIVE PROTECTION**  
**P/N = 6019B0505201**

**INVENTEC**

TITLE			
ROCKY-AMD SATA HDD & SATA ODD			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1234	X01

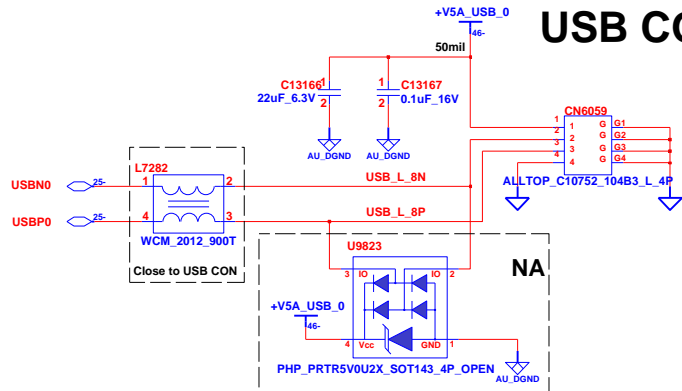
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SHEET 44 OF 58

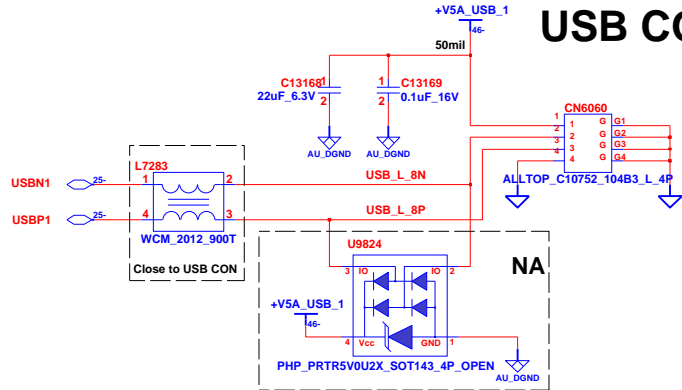
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B										B	
C										C	
D										D	
E										E	
F										F	
						CHANGE by		Ken, Chiang	9-Jun-2010		
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						CHANGE by		Ken, Chiang	9-Jun-2010		
						CHANGE by		Ken, Chiang	9-Jun-2010		
						CHANGE by		Ken, Chiang	9-Jun-2010		
						CHANGE by		Ken, Chiang	9-Jun-2010		
						CHANGE by		Ken, Chiang	9-Jun-2010		

# USB

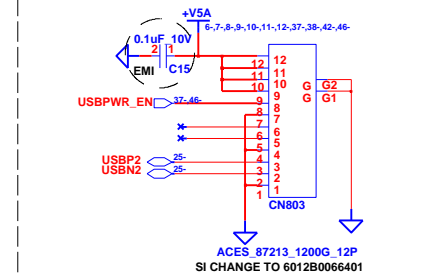
## USB CONN01



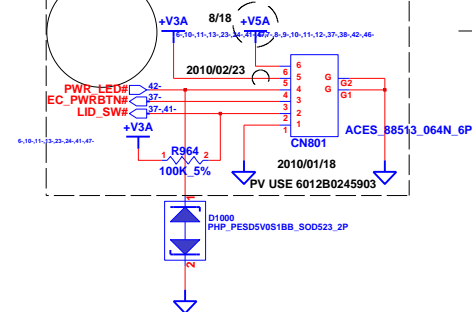
## USB CONN02



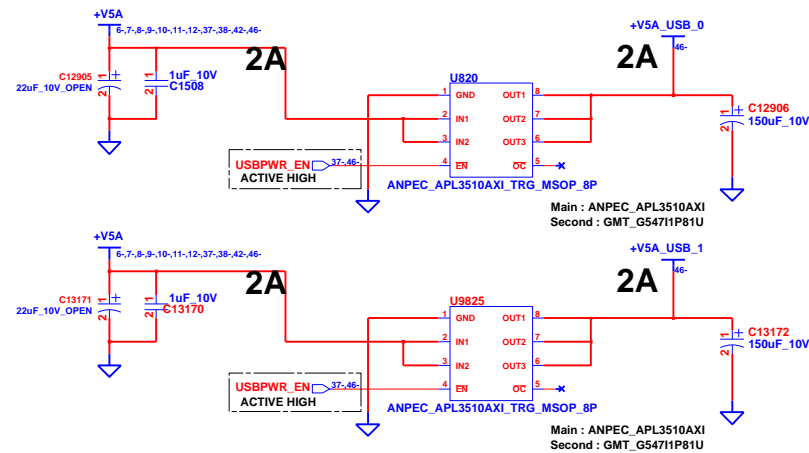
## USB BOARD Cable on MB



## PBN Cable CN on MB



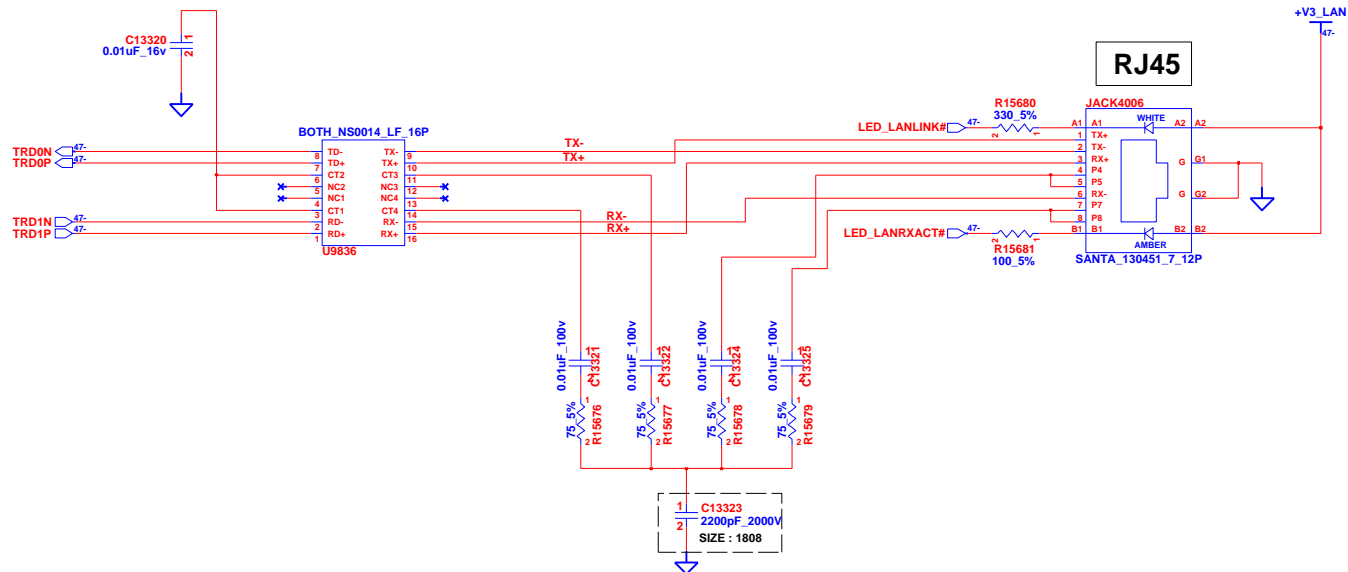
????



## INVENTEC

TITLE			
ROCKY-AMD			
ESATA & USB BOARD CABLE			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1234	X01
SHEET		46	OF 58



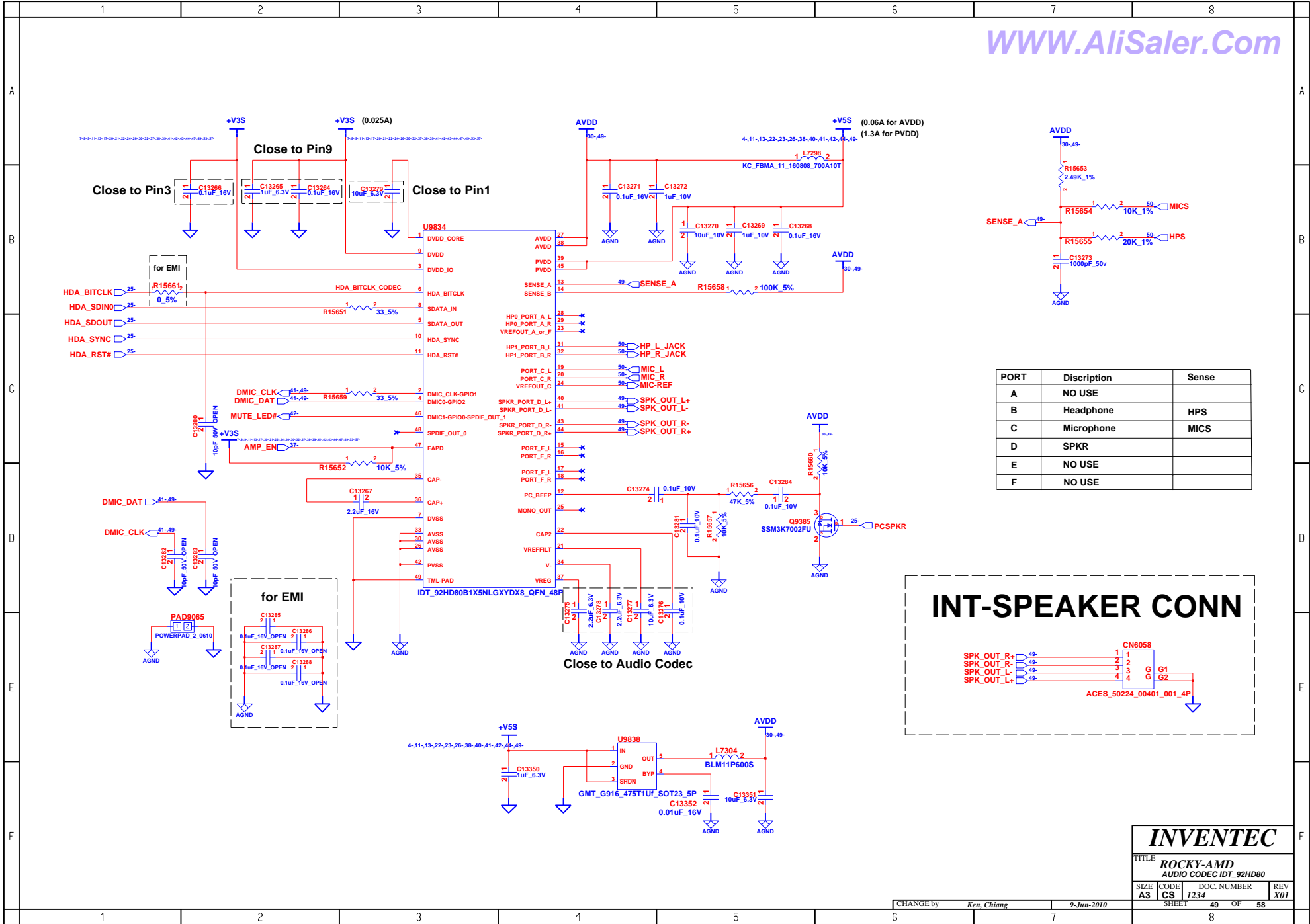


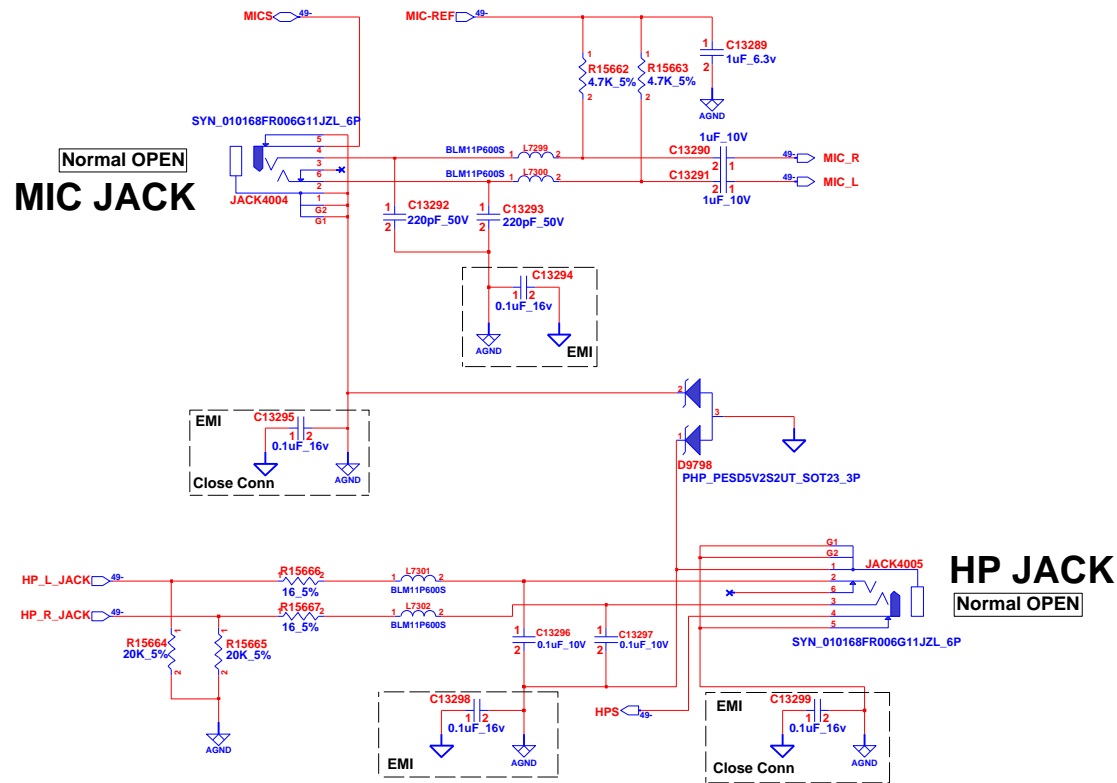
LAYOUT NOTE : Place termination resistors and caps as close to LAN controller as possible

INVENTEC			
TITLE ROCKY-AMD LAN & RJ45 CONN			
SIZE A3	CODE CS	DOC. NUMBER 1234	REV X01
SHEET		48	OF 58

CHANGE by Ken Chiang 9-Jun-2010



[illegible]



INVENTEC			
TITLE ROCKY-AMD AUDIO JACK			
SIZE A3	CODE CS	DOC. NUMBER 1234	REV X01
CHANGE by Ken, Chiang		9-Jun-2010	
SHEET		50 OF	58
		8	

	1	2	3	4	5	6	7	8	
A	<div>WWW.AliSaler.Com</div>								A
B									B
C									C
D									D
E									E
F									F
	1	2	3	4	5	6	7	8	

INVENTEC

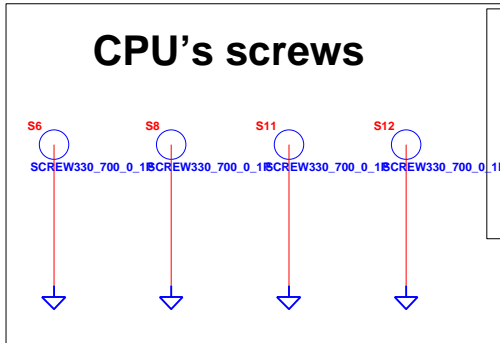
TITLE  
ROCKY-AMD  
SCREW

SIZE A3	CODE CS	DOC. NUMBER 1234	REV X01
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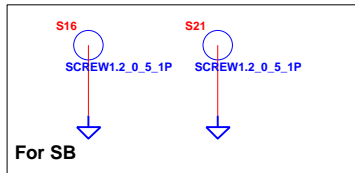
CHANGE by Ken, Chiang 9-Jun-2010

SHEET 51 OF 58

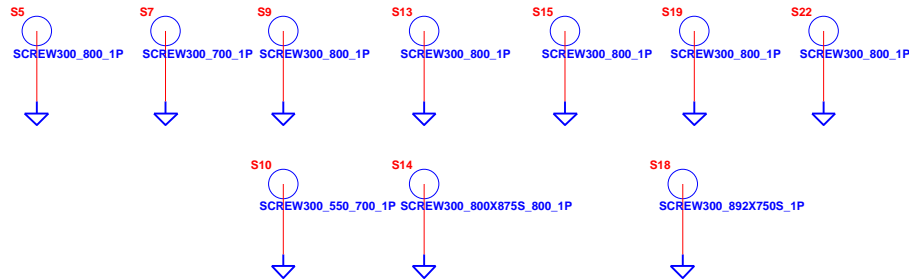
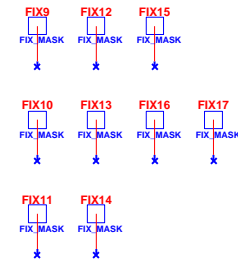
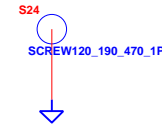
## CPU's screws



For NB



For SB



**INVENTEC**

TITLE **ROCKY-AMD**  
WLAN & BT CONN

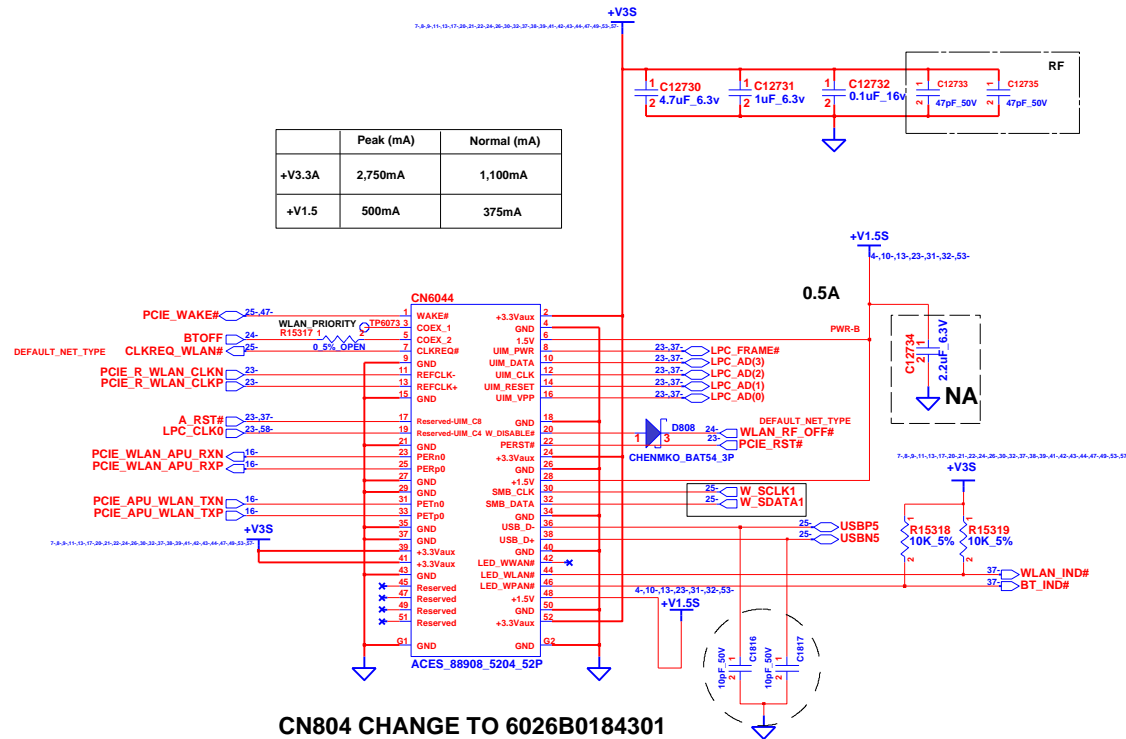
SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <b>1234</b>	REV <b>X01</b>
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CHANGE by *Ken, Chiang* 9-Jun-2010

SHEET 52 OF 58

# WLAN CONN (MINICARD)

	Peak (mA)	Normal (mA)
+V3.3A	2,750mA	1,100mA
+V1.5	500mA	375mA



CN804 CHANGE TO 6026B0184301

	PCH	SB800/820
WLAN_RF_OFF#	GPIO57	GPIO12
WLAN_POWER_OFF	GPIO35	GPIO176

**INVENTEC**

TITLE  
**ROCKY-AMD**  
**WWAN & SIM CARD CONN**

SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <b>1234</b>	REV <b>X01</b>
SHEET <b>53</b>		OF <b>58</b>	

CHANGE by **Ken Chiang** 9-Jun-2010

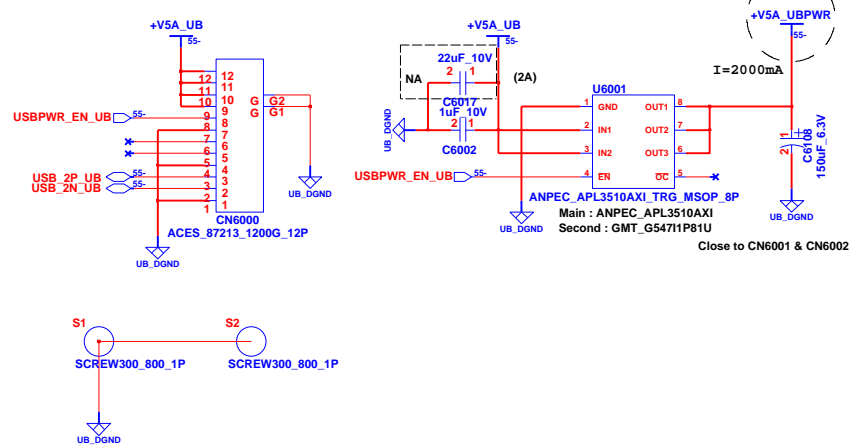
	1	2	3	4	5	6	7	8	
A	<div>WWW.AliSaler.Com</div>								A
B									B
C									C
D									D
E									E
F									F
	1	2	3	4	5	6	7	8	

INVENTEC			
TITLE ROCKY-AMD			
SIZE A3	CODE CS	DOC. NUMBER 1234	REV X01
CHANGE by Ken, Chiang		9-Jun-2010	
SHEET		54	OF 58

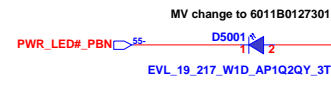
## USB BOARD

### USB BOARD Cable ON UB

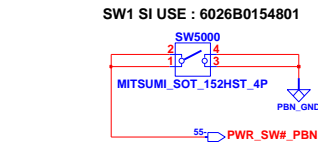
Circuit : Fixed  
2009/06/29



## POWER LED



## POWER BUTTON

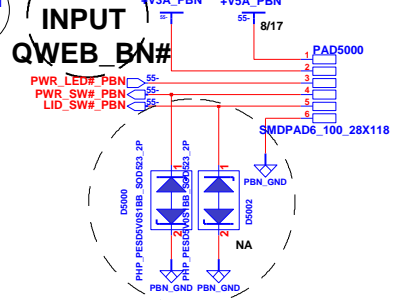


Circuit : Fixed  
2009/06/29

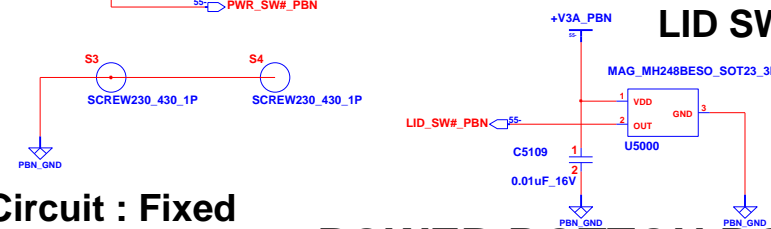
## POWER BOTTOM BOARD

WWW.AliSaler.Com

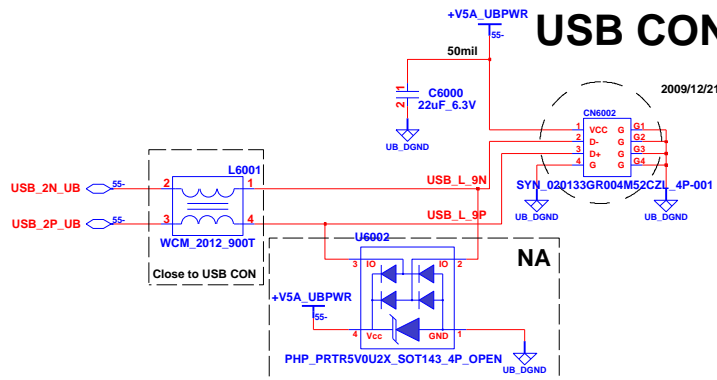
## PBN Cable CN on PBN



## LID SWITCH

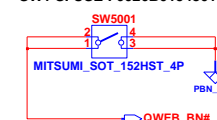


## USB CONN03



## QUICKWER BUTTON

SW1 SI USE : 6026B0154801



INVENTEC

TITLE ROCKY-AMD EXT USB CONN			
SIZE A3	CODE CS	DOC. NUMBER 1234	REV X01
SHEET		55 OF	58

CHANGE by Ken Chiang 9-Jun-2010

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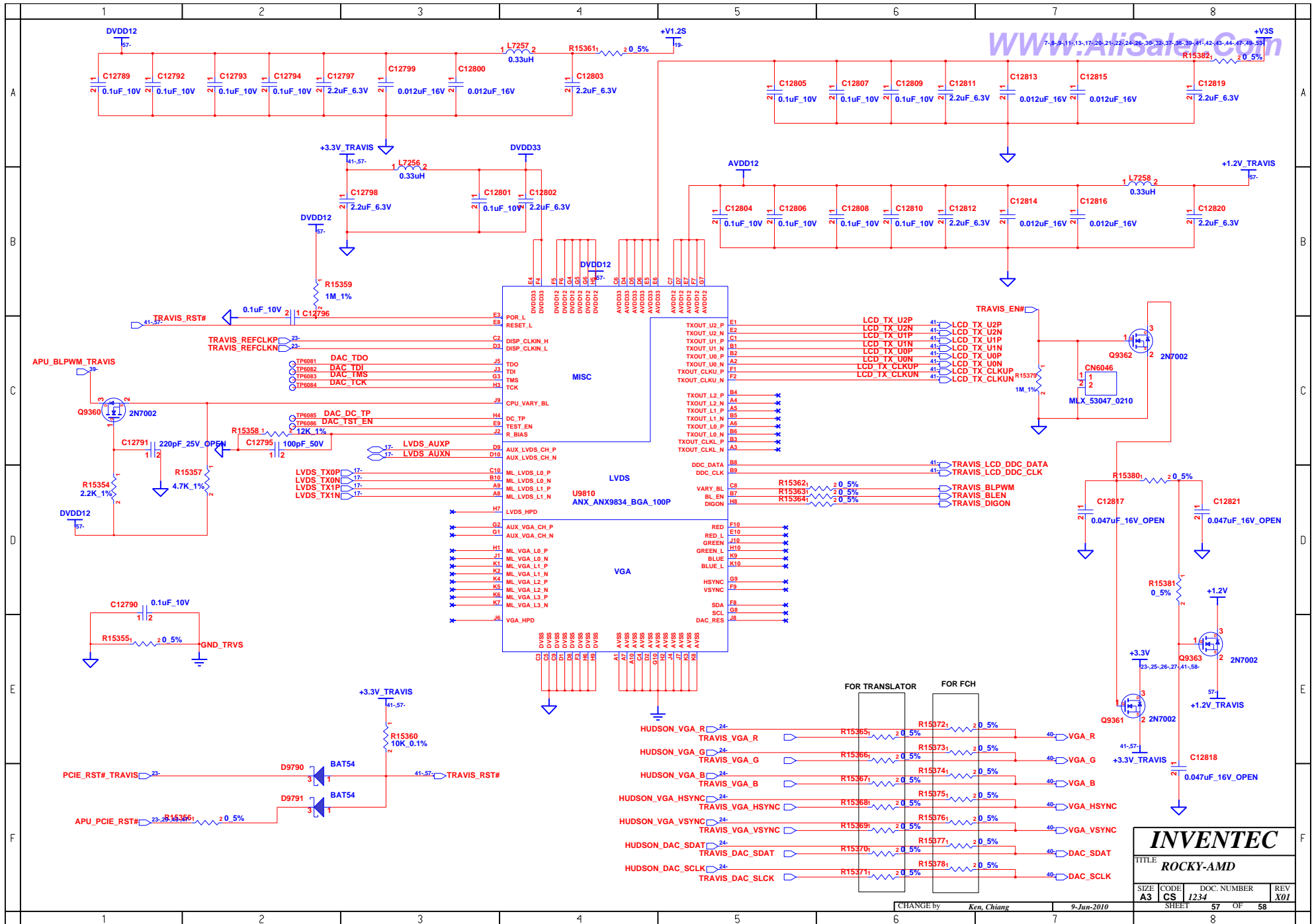
	1	2	3	4	5	6	7	8	
A	<div>WWW.AliSaler.Com</div>								A
B									B
C									C
D									D
E									E
F									F
	1	2	3	4	5	6	7	8	

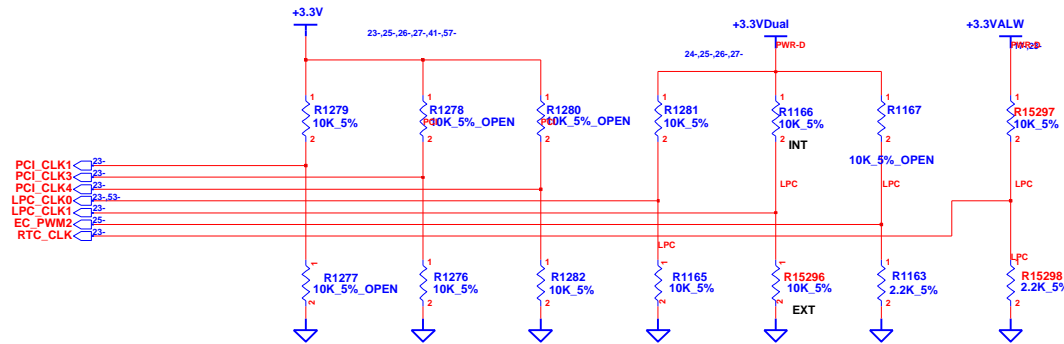
INVENTEC			
TITLE ROCKY-AMD			
SIZE A3	CODE CS	DOC. NUMBER 1234	REV X01

CHANGE by Ken, Chiang 9-Jun-2010

SHEET 56 OF 58

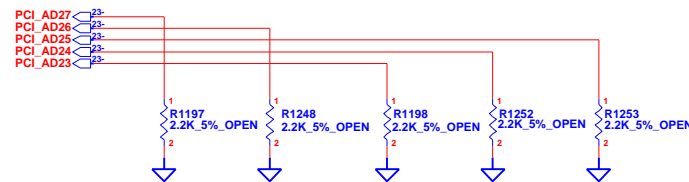






### Required Straps:

RTCLK		PCI_CLK1	PCI_CLK3	PCI_CLK4	PCI_CLK0	PCI_CLK1	EC_PWM2
S5 PLUS MODE DISABLE (DEFAULT)	<b>PULL HIGH</b>	ALLOW PCIE Gen2 DEFAULT	USE DEBUG STRAP	non Fusion CLOCK MODE APU_CLK AND DISP_CLK NO CLOCK OUTPUT	EC ENABLED	INTEGRATED CLKGEN ENABLED DEFAULT	LPC ROM DEFAULT
S5 PLUS MODE ENABLE	<b>PULL LOW</b>	FORCE PCIE Gen1	IGNORE DEBUG STRAPE DEFAULT	Fusion CLOCK MODE DEFAULT APU_CLK AND DISP_CLK CLOCK OUTPUT FROM INT CLOCK GEN	EC DISABLED DEFAULT	INTEGRATED CLKGEN DISABLED	SPI ROM



### Debug Straps

SB800 HAS 15K INTERNAL PU FOR PCI\_AD[27:23]

	PCI_3S_AD27	PCI_3S_AD24	PCI_3S_AD23
<b>PULL HIGH</b>	USE PCI PLL (default)	GETTING I2C ROM ENABLE	DISABLE PCI MEM BOOT (default)
<b>PULL LOW</b>	BYPASS PCI PLL	USE EEPROM PCIE STRAPS I2C ROM DISABLE	ENABLE PCI MEM BOOT

**INVENTEC**

TITLE  
**ROCKY-AMD**

SIZE	CODE	DOC. NUMBER	REV
A3	CS	1234	X01

CHANGE by Ken Chiang 9-Jun-2010

SHEET 58 OF 58