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HSF Property: ROHS or Halogen-Free(5L3?)

CALVIN DIS
SI BUILD
FINAL

2011.08.02

21.OCT.2002		
DATE	CHANGE NO.	REV

	EE	DATE	POWER	DATE	INVENTEC			
DRAWER					TITLE MODEL PROJECT FUNCTION PROJECT NAME			
DESIGN								
CHECK								
RESPONSIBLE								
SIZE				VER	SIZE	CODE	DOC NUMBER	REV
FILE NAME					2	05	1310KXXXX-0-0	2011
PIN	XXXX				SHEET 1 of 01			

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6. OCP	33. PANTER POINT_2 (PCI-E,SMBUS,CLK)	60. SMART CARD
7. SYSTEM POWER (P3V3 / P5V0)	34. PANTER POINT_3 (DMI,FDI,GPIO)	61. EXPRESS CARD CONN
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22. FAN & THERMAL	49. ACCELEMETOR	
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27. IVY BRIDGE_4 (DDR3)	54. LAN (NIC,TRANSFORMER,RJ45)	

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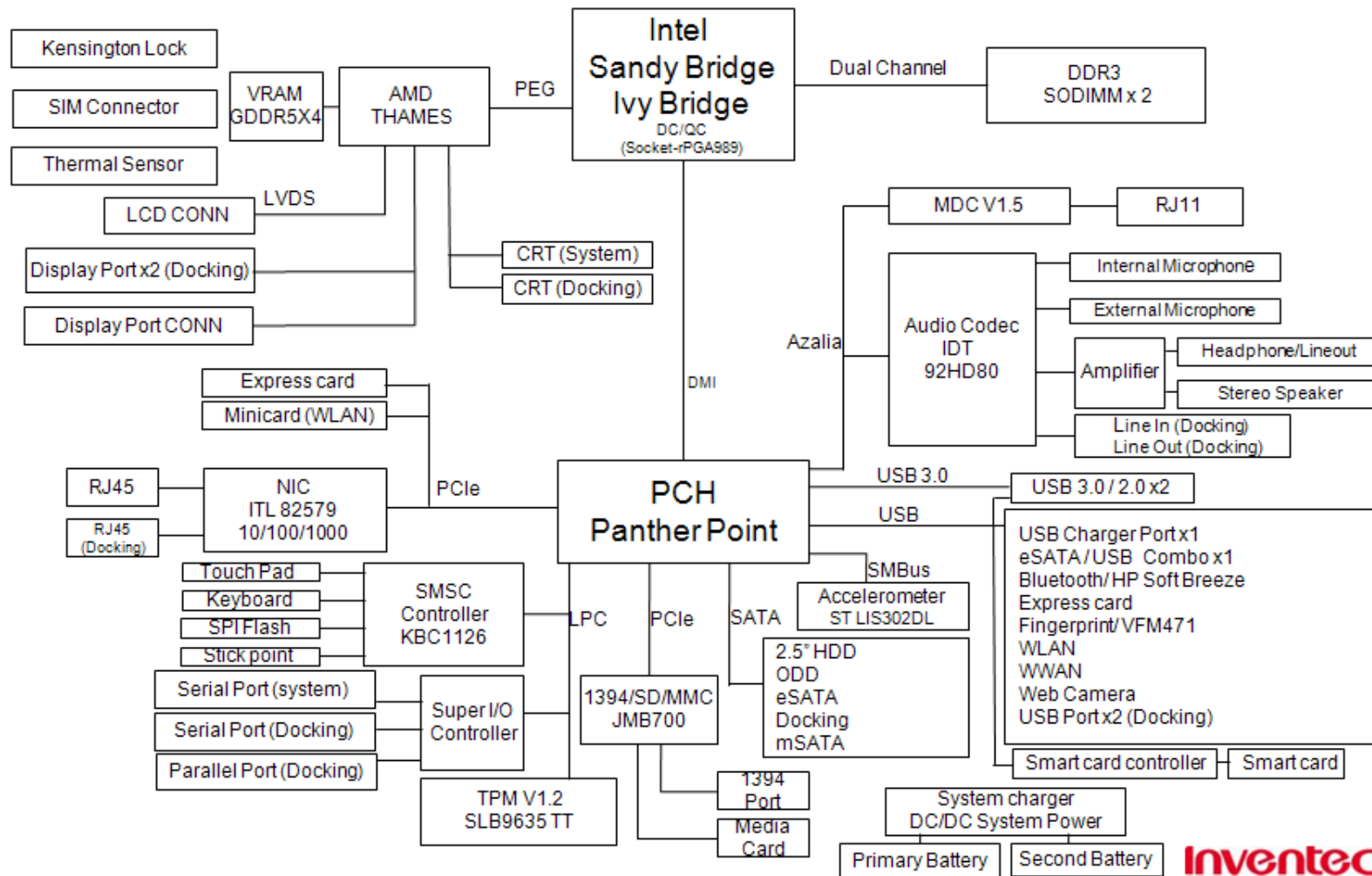
TITLE
MODEL, PROJECT, FUNCTION
TABLE OF CONTENTS

SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
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CALVIN Discrete Block Diagram



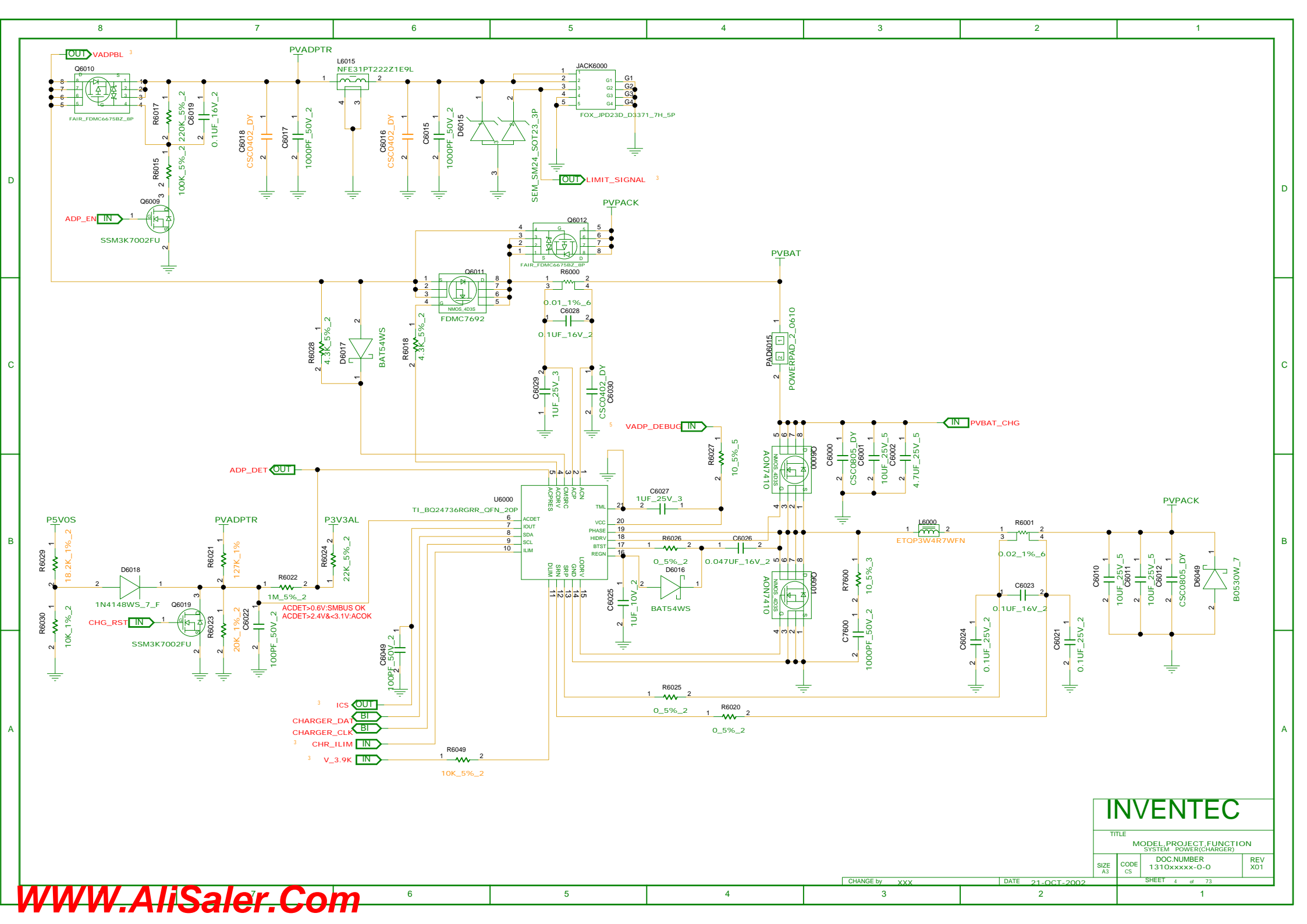
Inventec

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TITLE			
BLOCK DIAGRAM			
MODEL, PROJECT, FUNCTION			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01

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TITLE

MODEL PROJECT FUNCTION

SYSTEM POWER(CHARGER)

SIZE

A3

CODE

CS

DOC NUMBER

1310xxxxx-0-0

REV

X01

CHANGE by

XXX

DATE

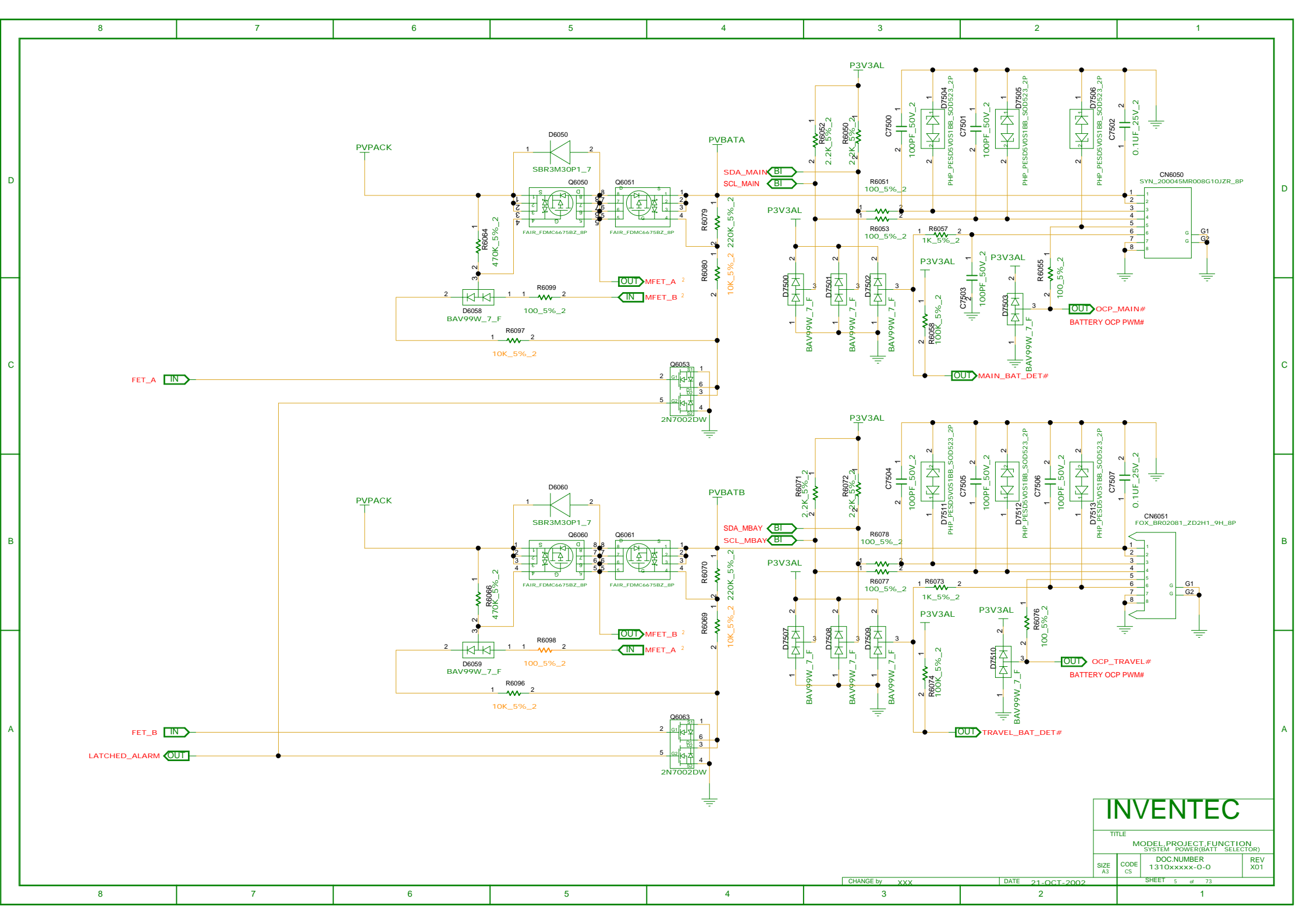
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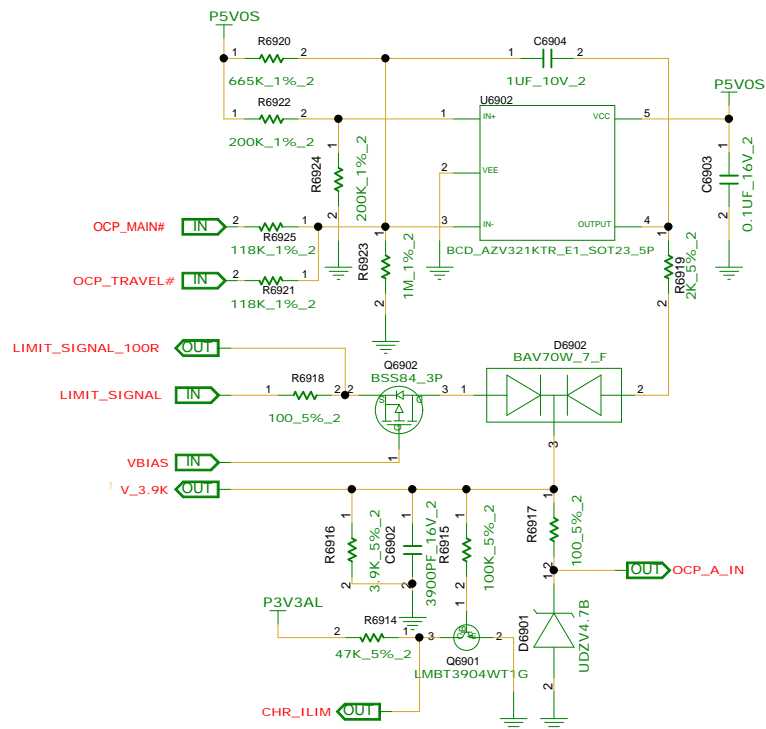
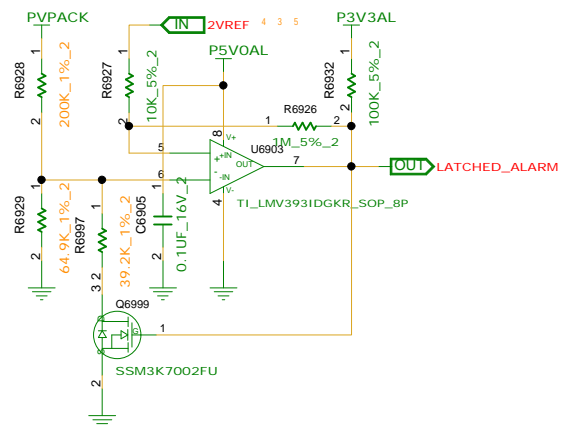
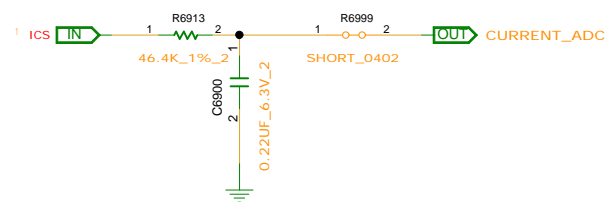
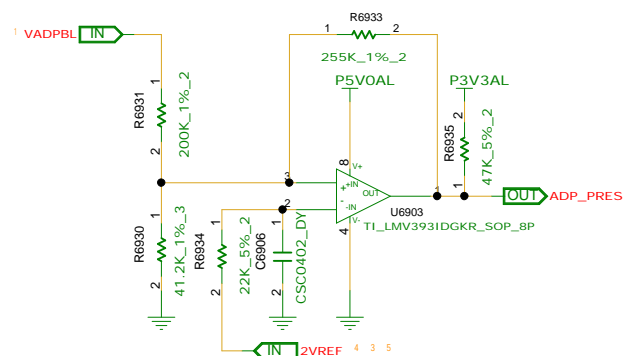
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of

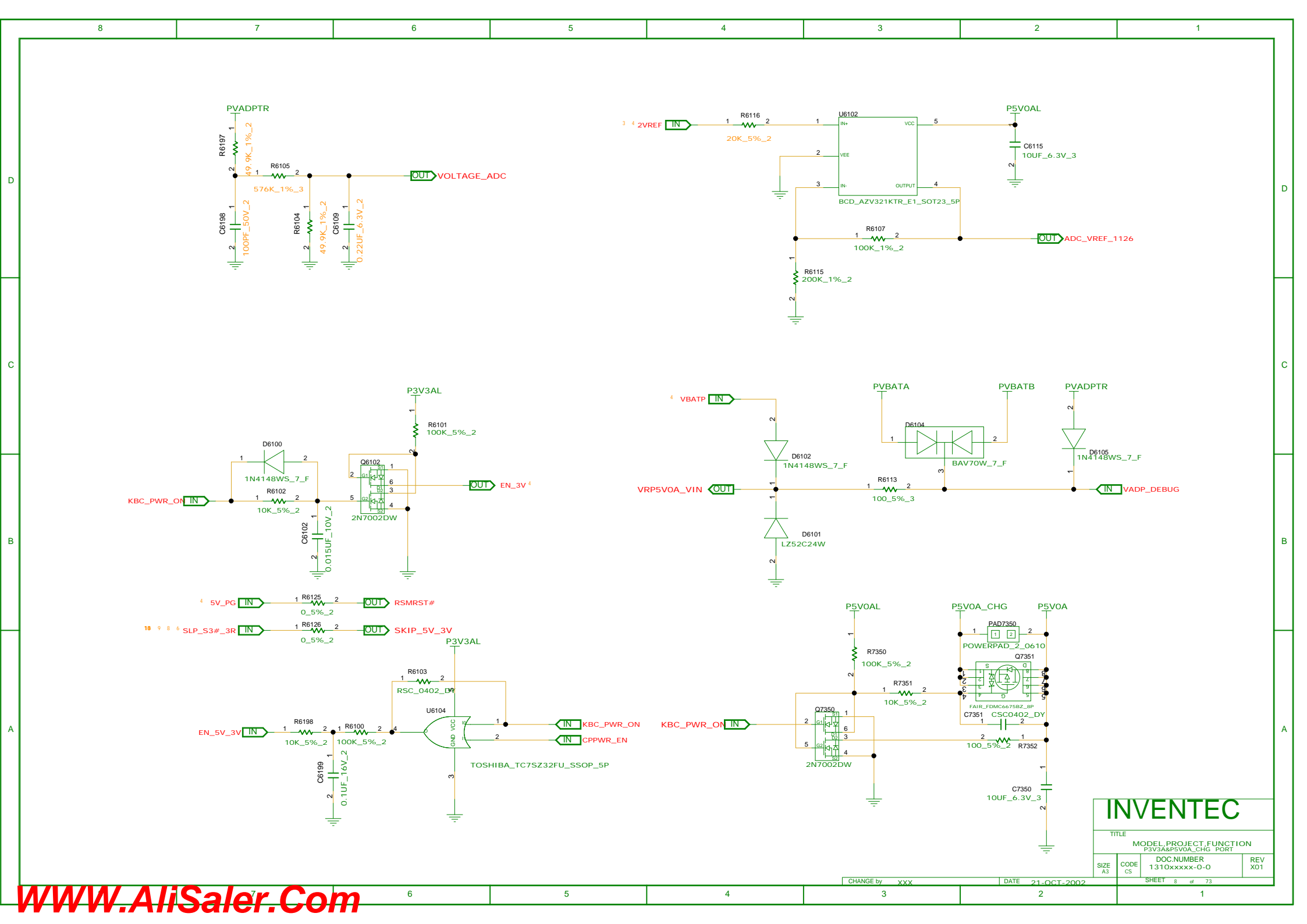
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INVENTEC

TITLE				
MODEL,PROJECT,FUNCTION SYSTEM POWER(OPC)				
SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0		REV X01
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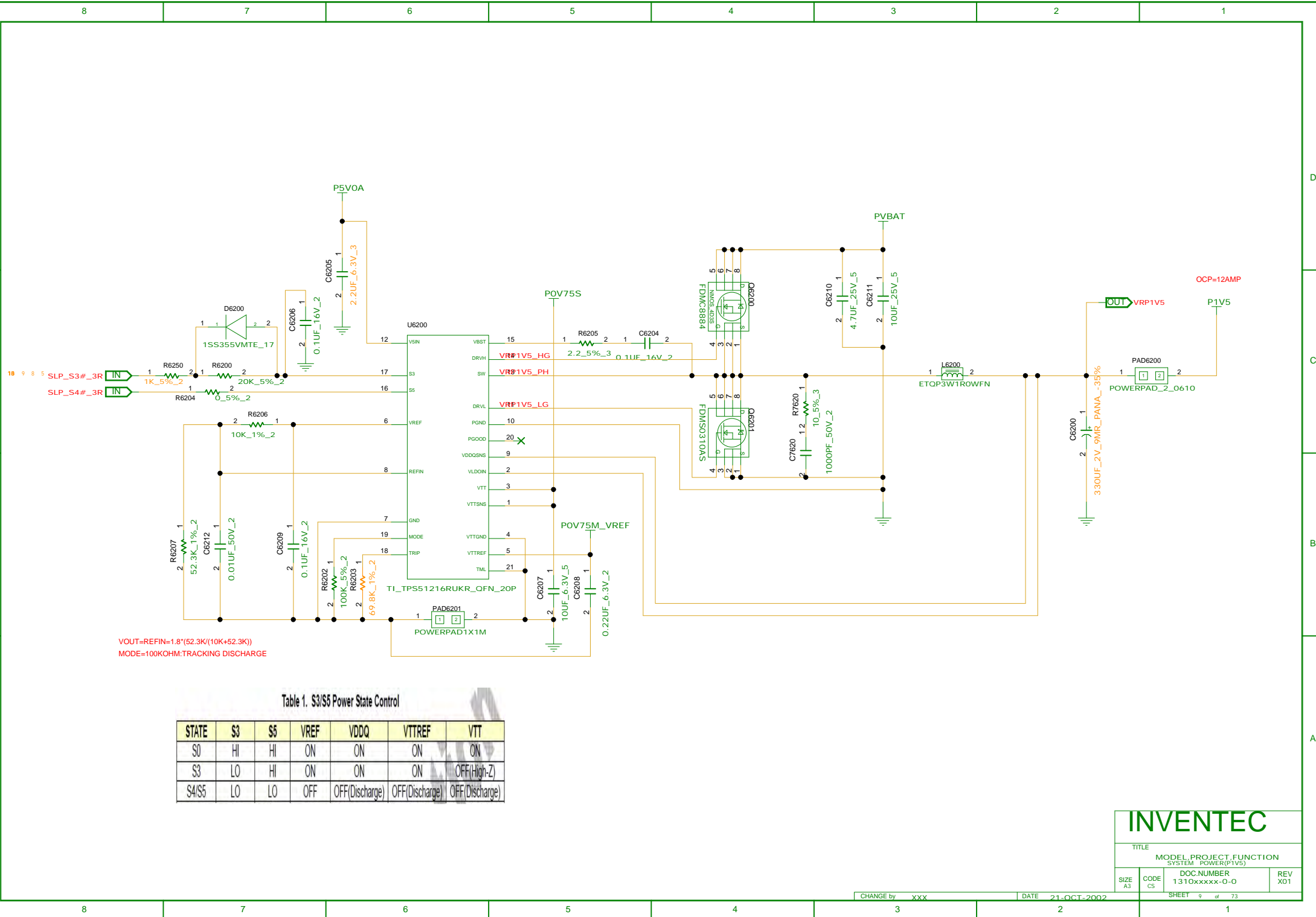
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TITLE
MODEL, PROJECT, FUNCTION
P3V3AL&P5VOA_CHG_PORT

SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01

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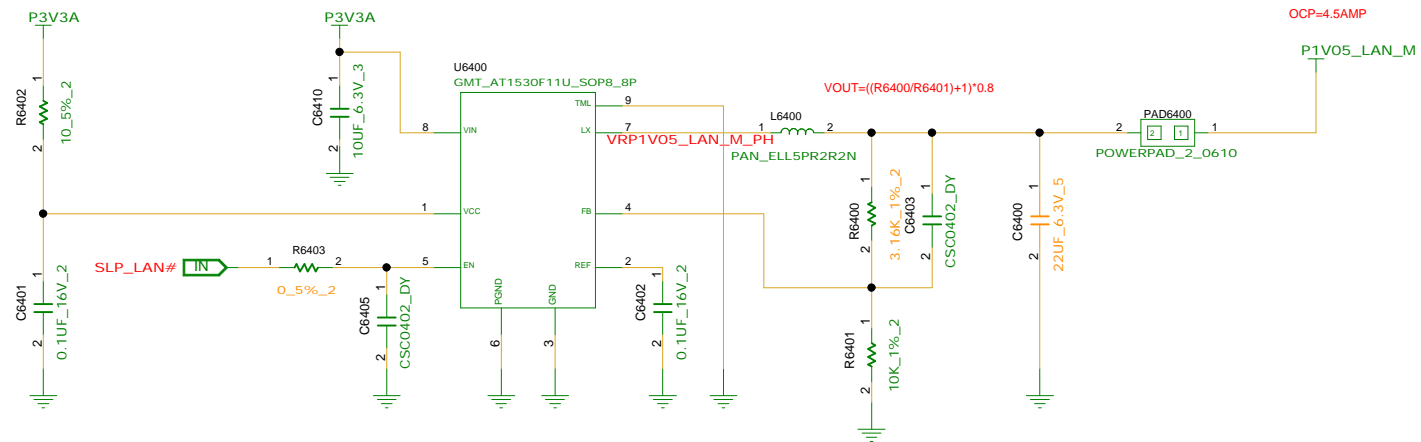
VOUT=REFIN=1.8*(52.3K/(10K+52.3K))
MODE=100KOHM:TRACKING DISCHARGE

Table 1. S3/S5 Power State Control

STATE	S3	S5	VREF	VDDQ	VTTREF	VTT
S0	HI	HI	ON	ON	ON	ON
S3	LO	HI	ON	ON	ON	OFF(High-Z)
S4/S5	LO	LO	OFF	OFF(Discharge)	OFF(Discharge)	OFF(Discharge)

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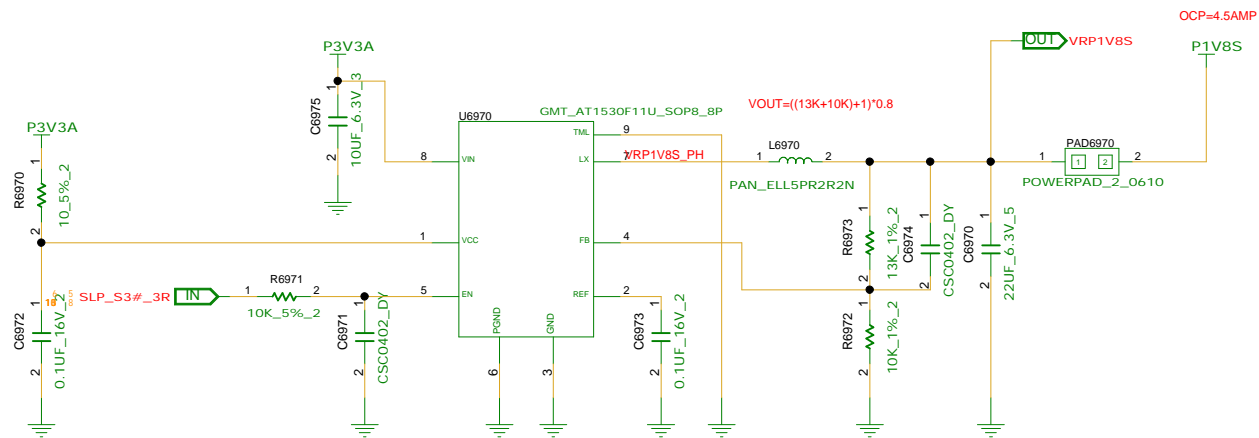
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MODEL PROJECT FUNCTION SYSTEM POWER(P1V5)			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01



INVENTEC

TITLE			
MODEL PROJECT FUNCTION SYSTEM POWER(P1V05_LAN_M)			
SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
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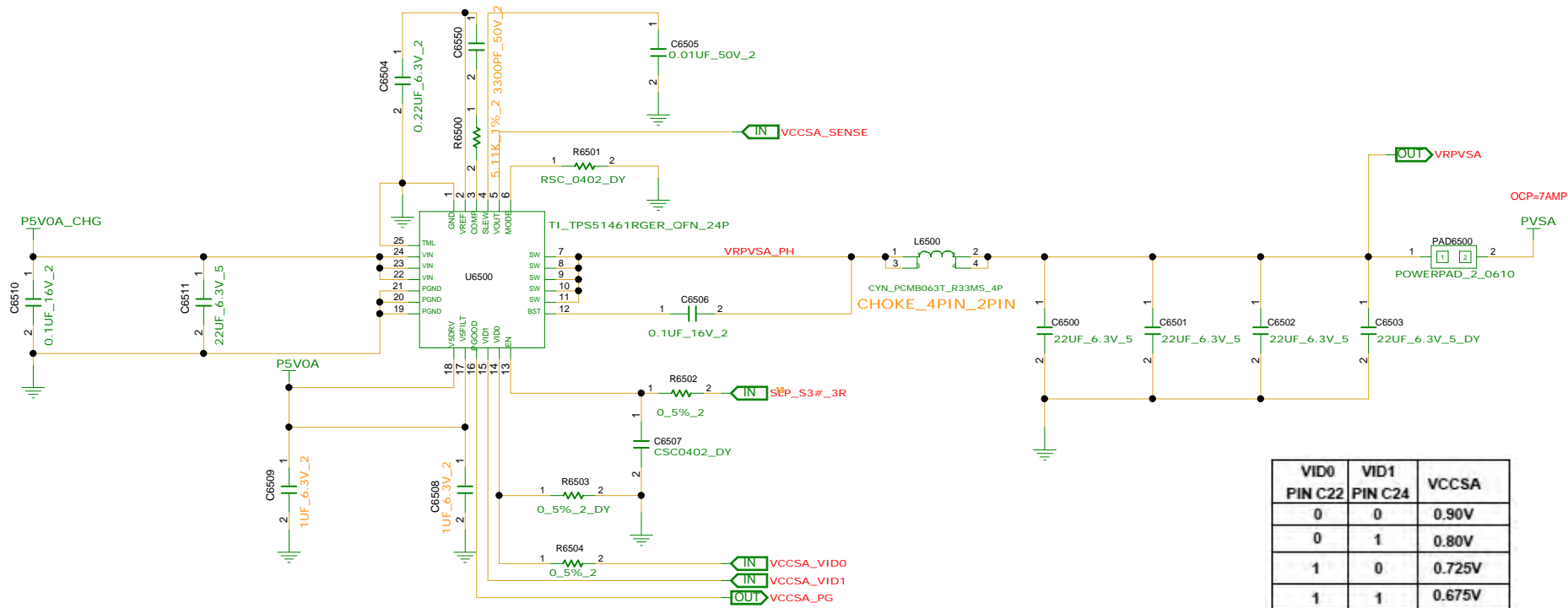
INVENTEC

TITLE MODEL,PROJECT,FUNCTION
SYSTEM POWER(P1V8S)

SIZE A3 CODE CS DOC NUMBER 1310xxxxx-0-0 REV X01

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VID0 PIN C22	VID1 PIN C24	VCCSA
0	0	0.90V
0	1	0.80V
1	0	0.725V
1	1	0.675V

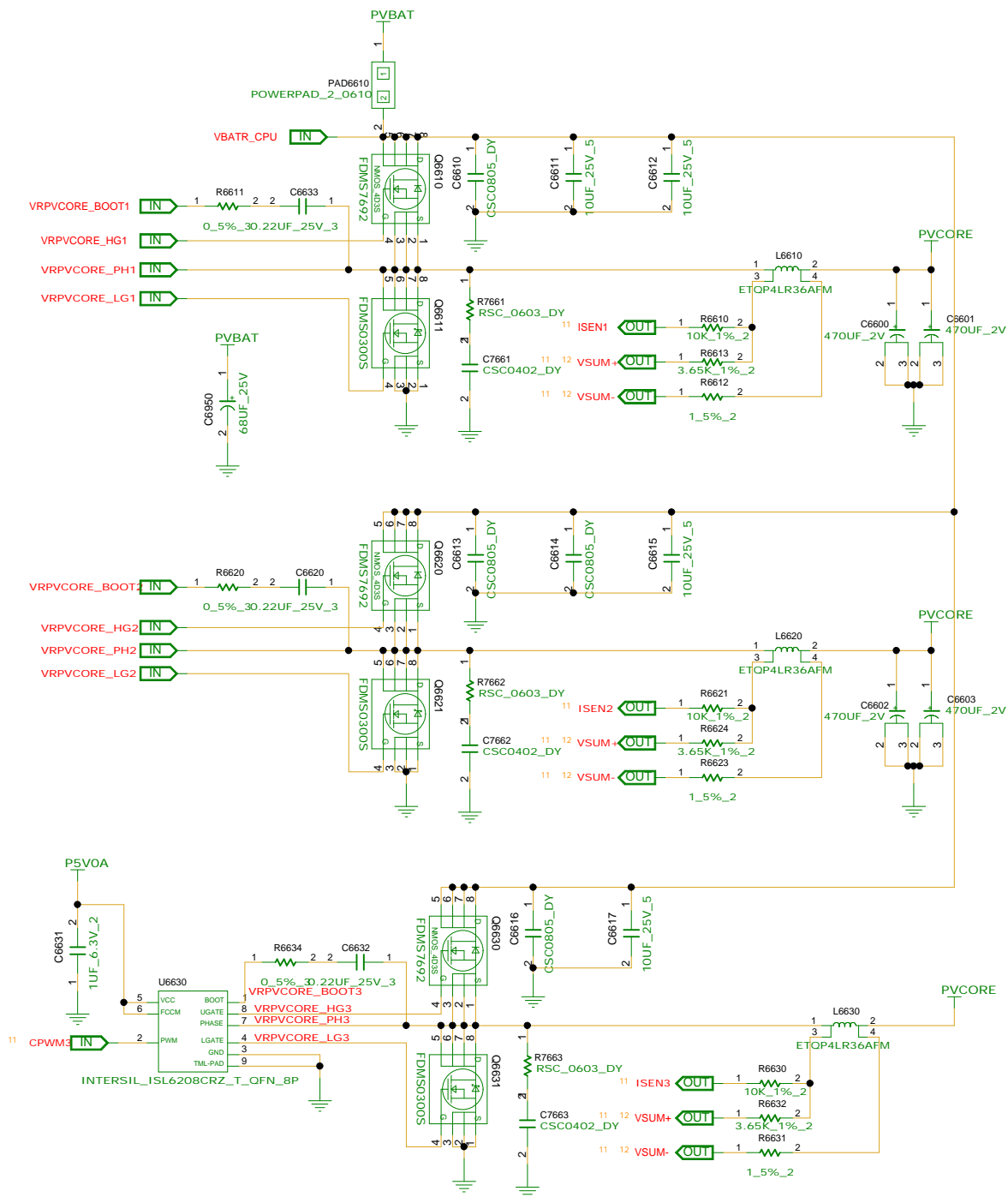
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TITLE
MODEL PROJECT FUNCTION
SYSTEM POWER(PVSA)

SIZE CODE DOC NUMBER REV
A3 CS 1310xxxxx-0-0 X01

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PV CORE OCP=112A

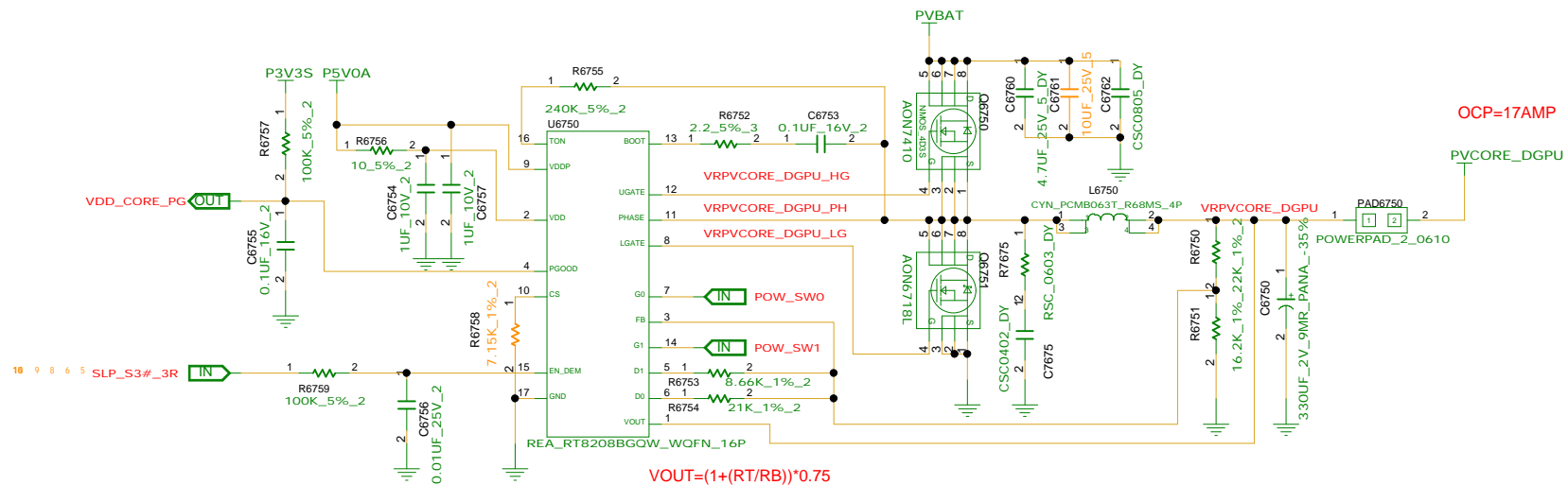
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TITLE MODEL,PROJECT,FUNCTION
SYSTEM POWER(PV CORE)

SIZE CODE DOC NUMBER REV
A3 CS 1310xxxxx-0-0 X01

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OCP=17AMP

PVCORE_DGPU

VOUT	SW1	SW0
1.10	0	0
1.00	0	1
0.90	1	0
0.85	1	1

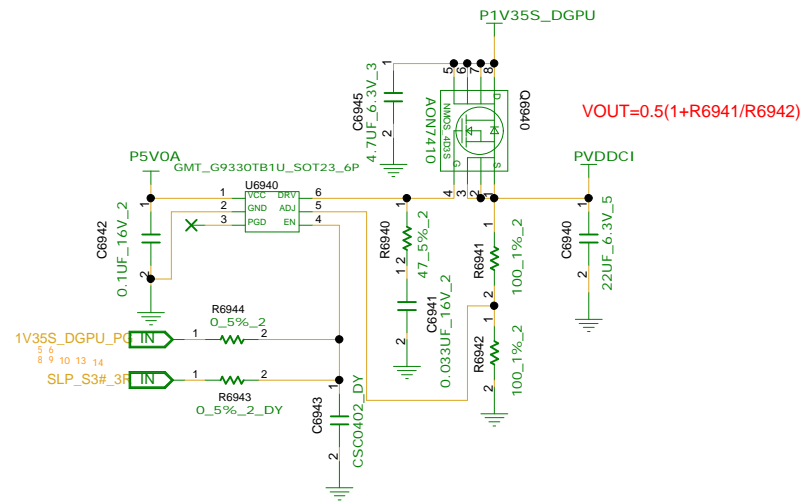
INVENTEC

TITLE MODEL,PROJECT,FUNCTION
SYSTEM POWER(PVCORE_DGPU)

DOC NUMBER 1310xxxxx-0-0 REV X01

CHANGE by XXX DATE 21-OCT-2002

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INVENTEC

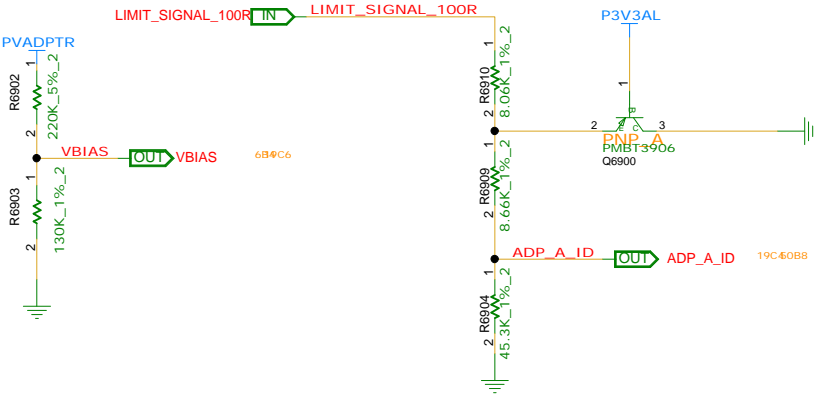
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MODEL, PROJECT, FUNCTION
Block Diagram

SIZE A3 CODE CS DOC NUMBER 1310xxxxx-0-0 REV X01

CHANGE by XXX DATE 21-OCT-2002

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REFERENCE NUMBER : 6900~6999



INVENTEC

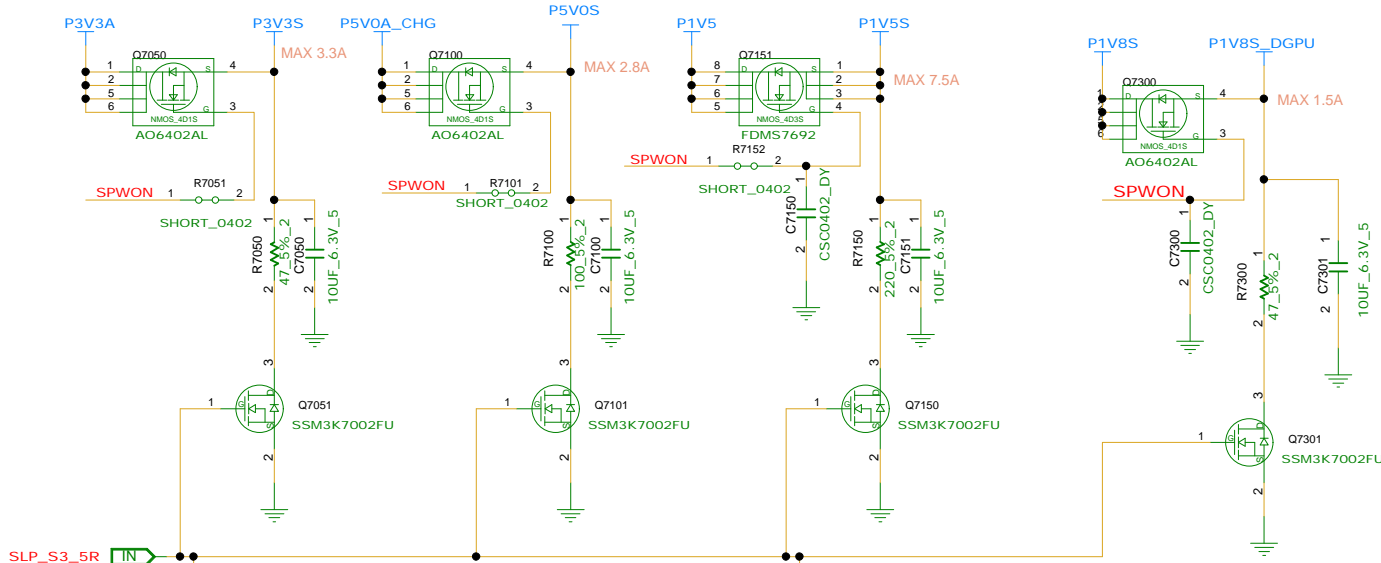
TITLE
MODEL, PROJECT, FUNCTION
HP_OCP

SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
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CHANGE by XXX DATE 21-OCT-2002

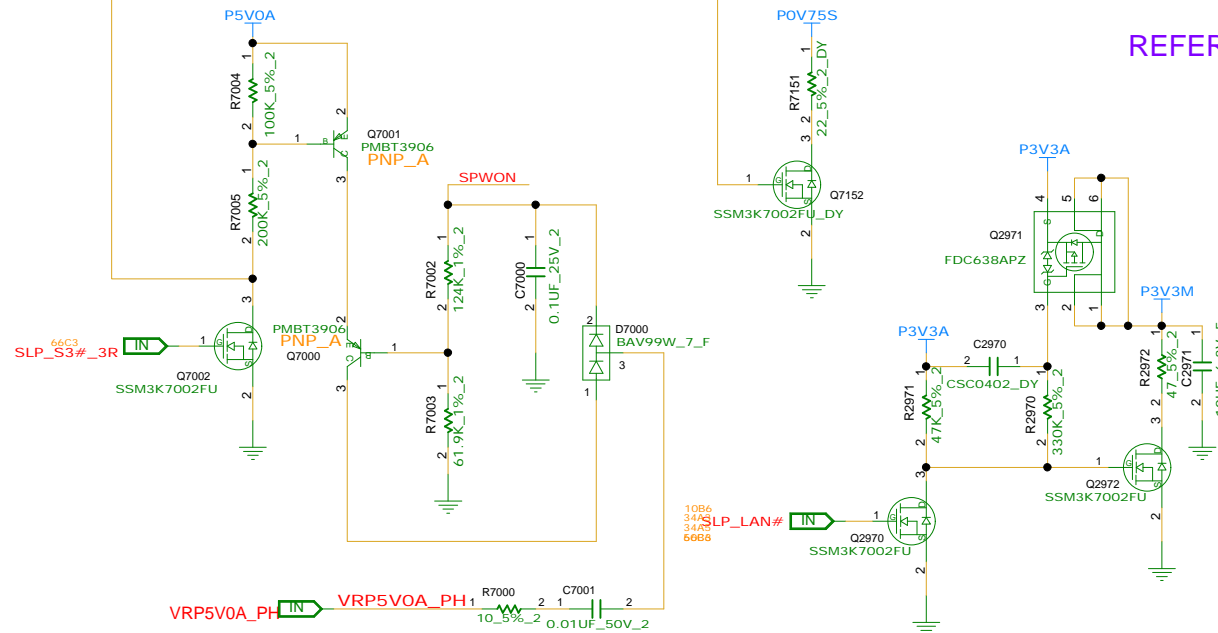
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REFERENCE NUMER : 7000~7350



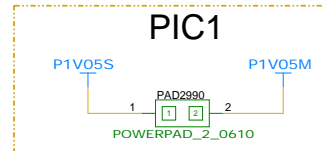
FOR DGPU

REFERENCE NUMER : 2950~2999



FOR IAMT

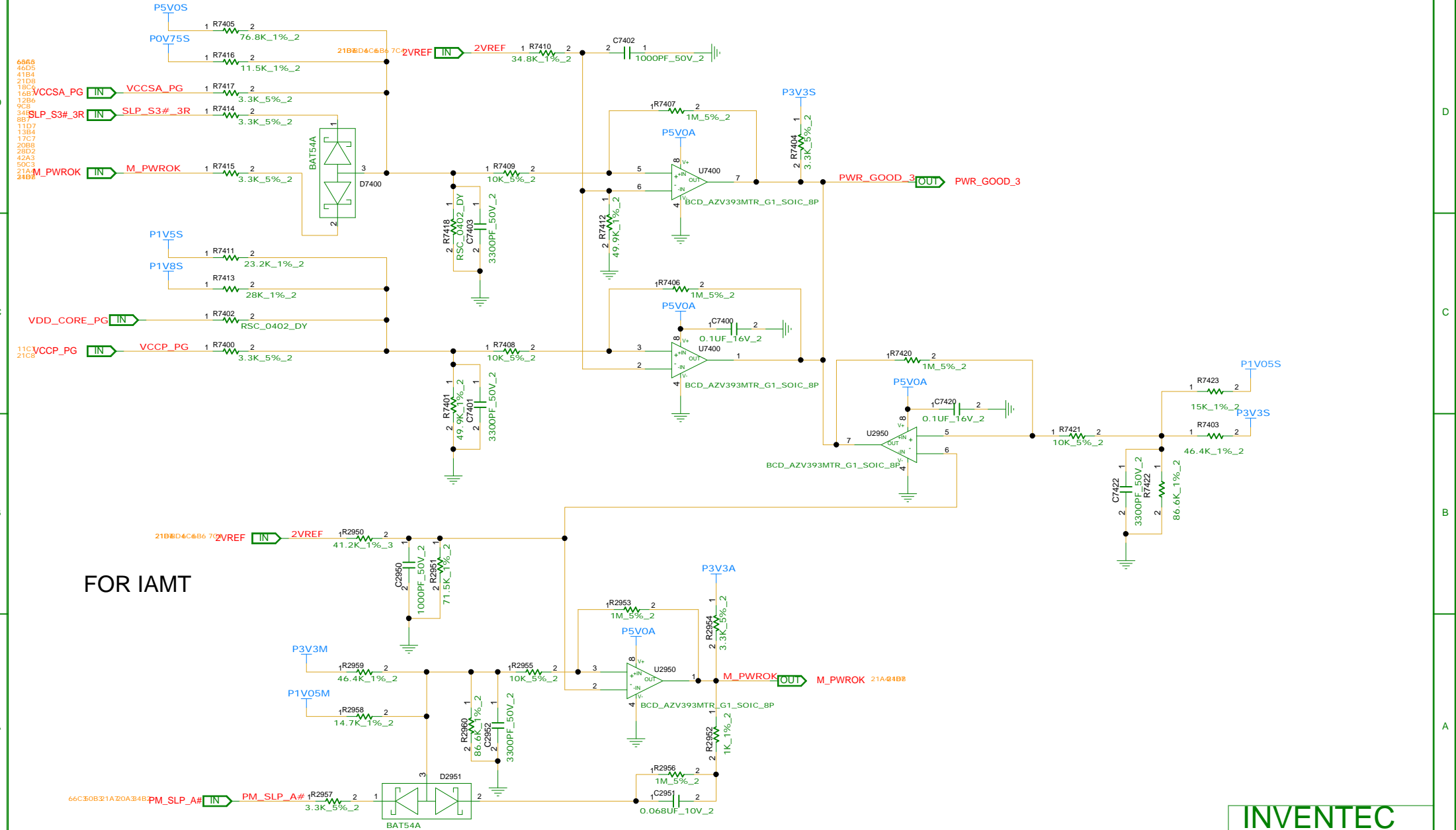
	PIC1	PIC2
NON-AMT	INSTALL	UN-INSTALL
I-AMT	UN-INSTALL	INSTALL



INVENTEC

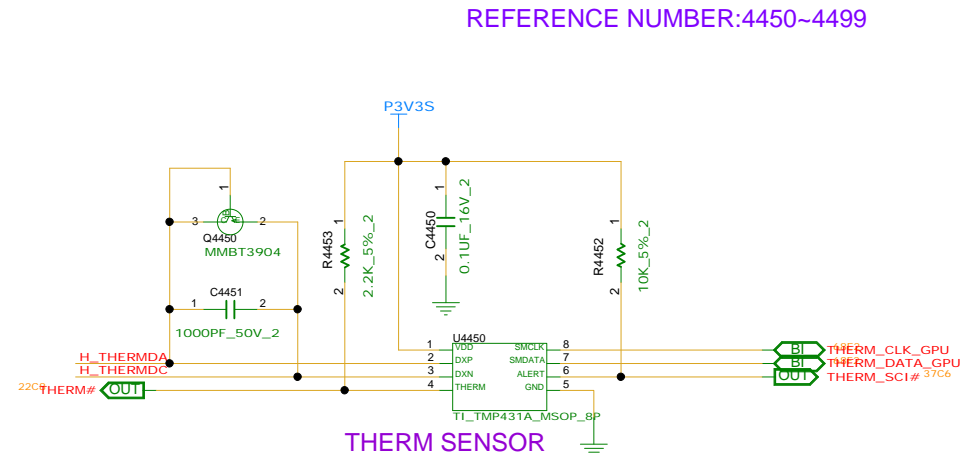
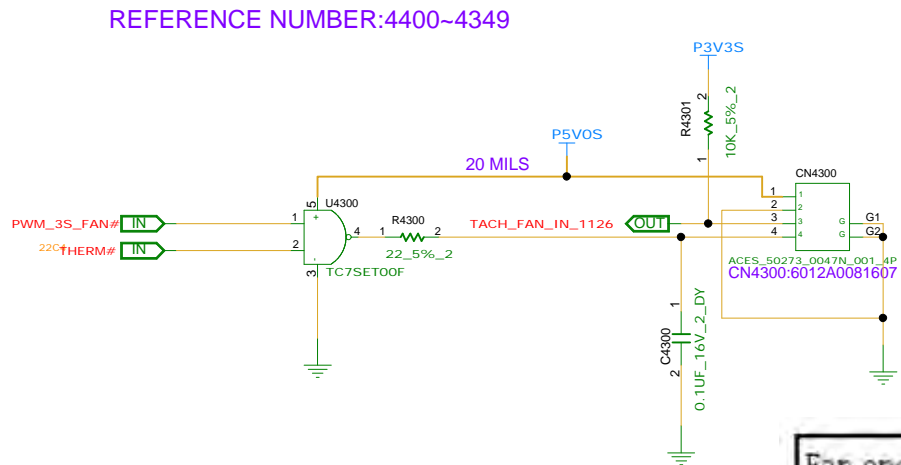
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MODEL PROJECT,FUNCTION			
POWER(SLEEP)			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01

REFERENCE NUMER : 7400~7450

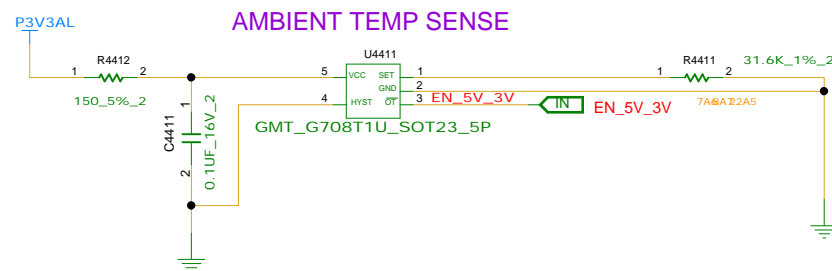


INVENTEC

TITLE			
MODEL PROJECT FUNCTION POWER (SEQUENCE)			
SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01



Fan speeds (RPM) :	2500	3300	3800	4300	5000	0	0	0
CPU temperature trip points :	45	57	68	80	91	0	0	0

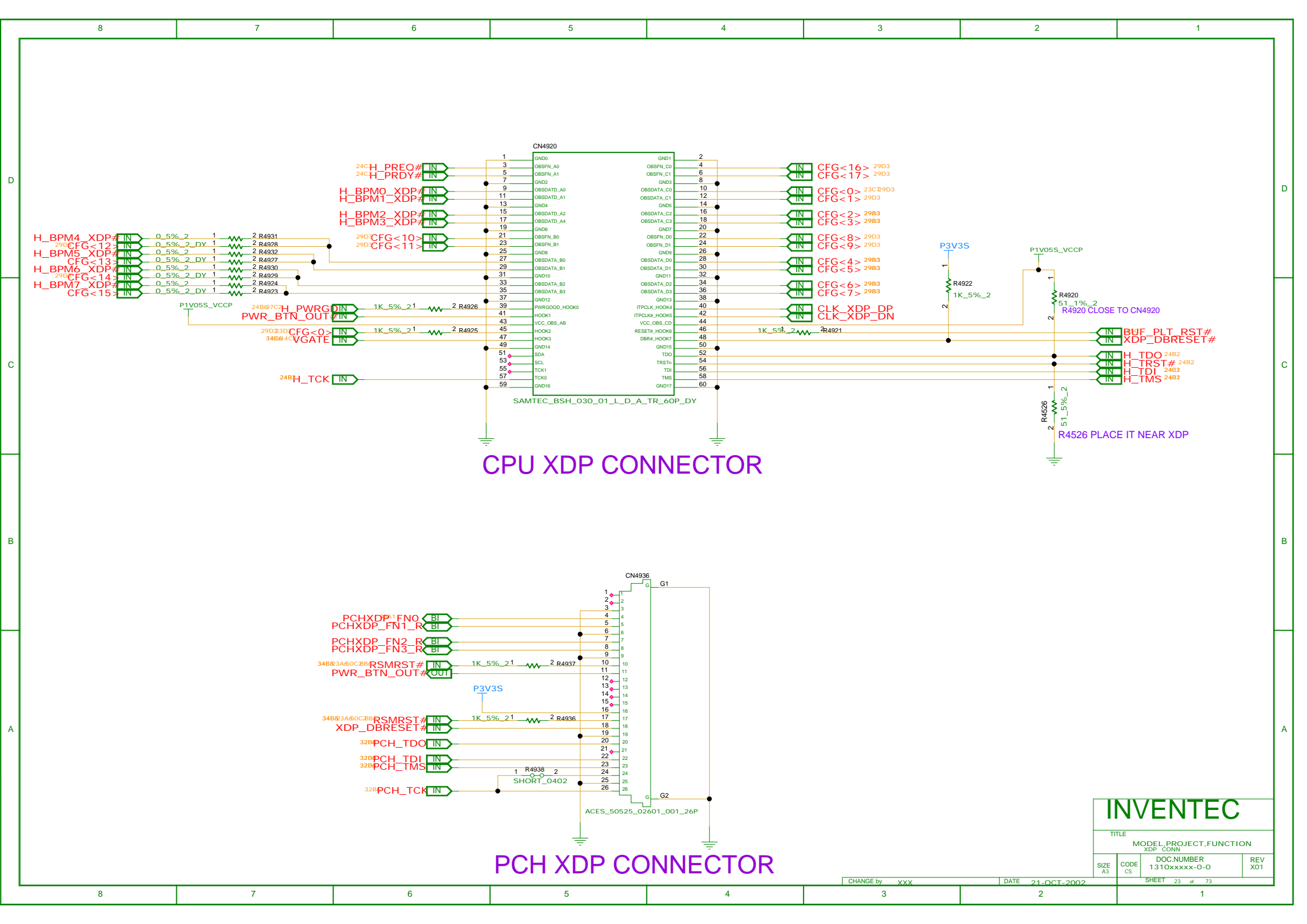


RSET (K OHM) = 0.0012T^2 - 0.9308T + 96.147
THERMAL SHUTDOWN AT 77 DEG. FOR B SERIES

REFERENCE NUMBER:4411~4449

INVENTEC			
TITLE			
MODEL PROJECT FUNCTION			
FAN & THERMAL			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
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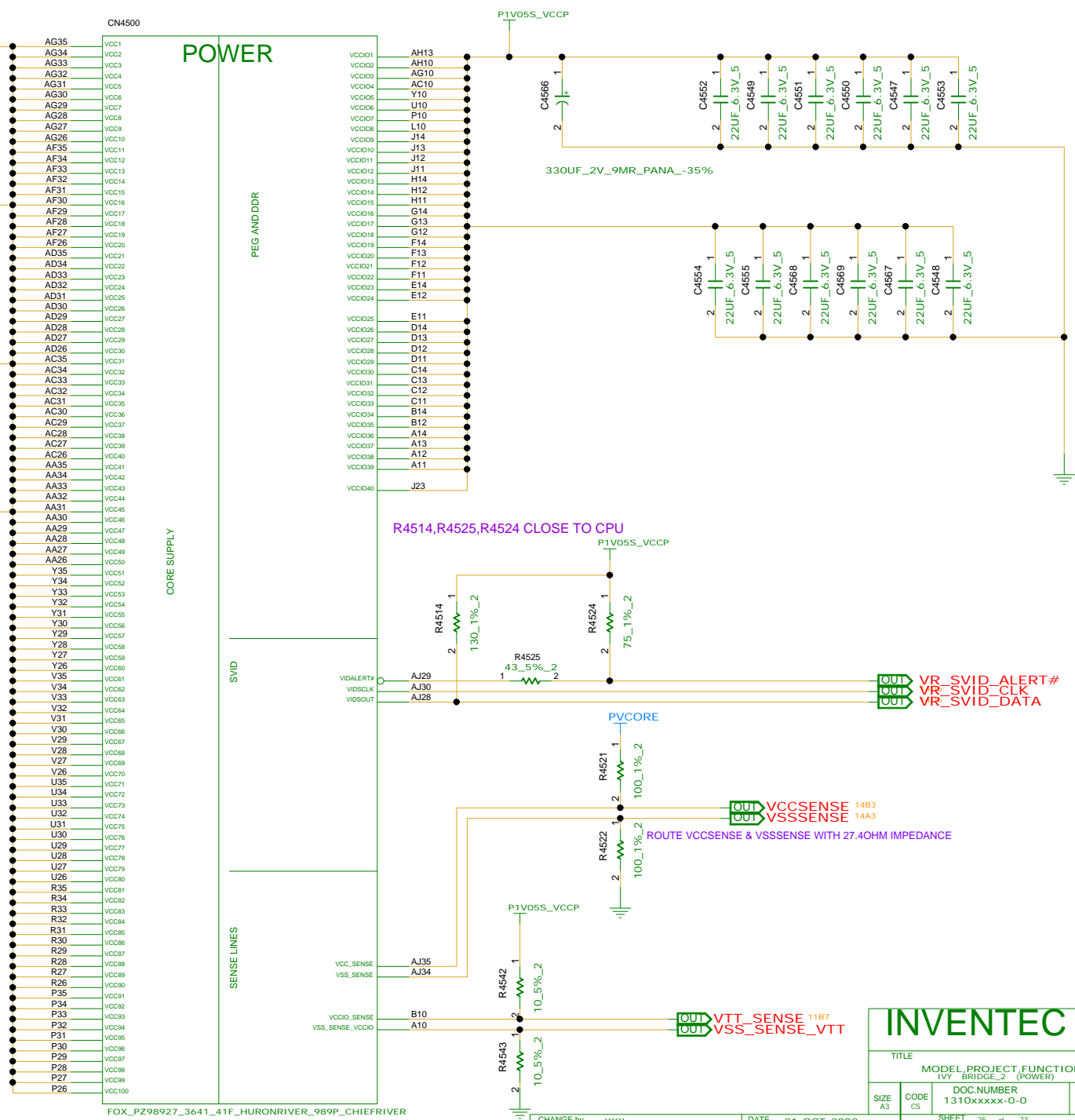
CHANGE by XXX DATE 21-OCT-2002

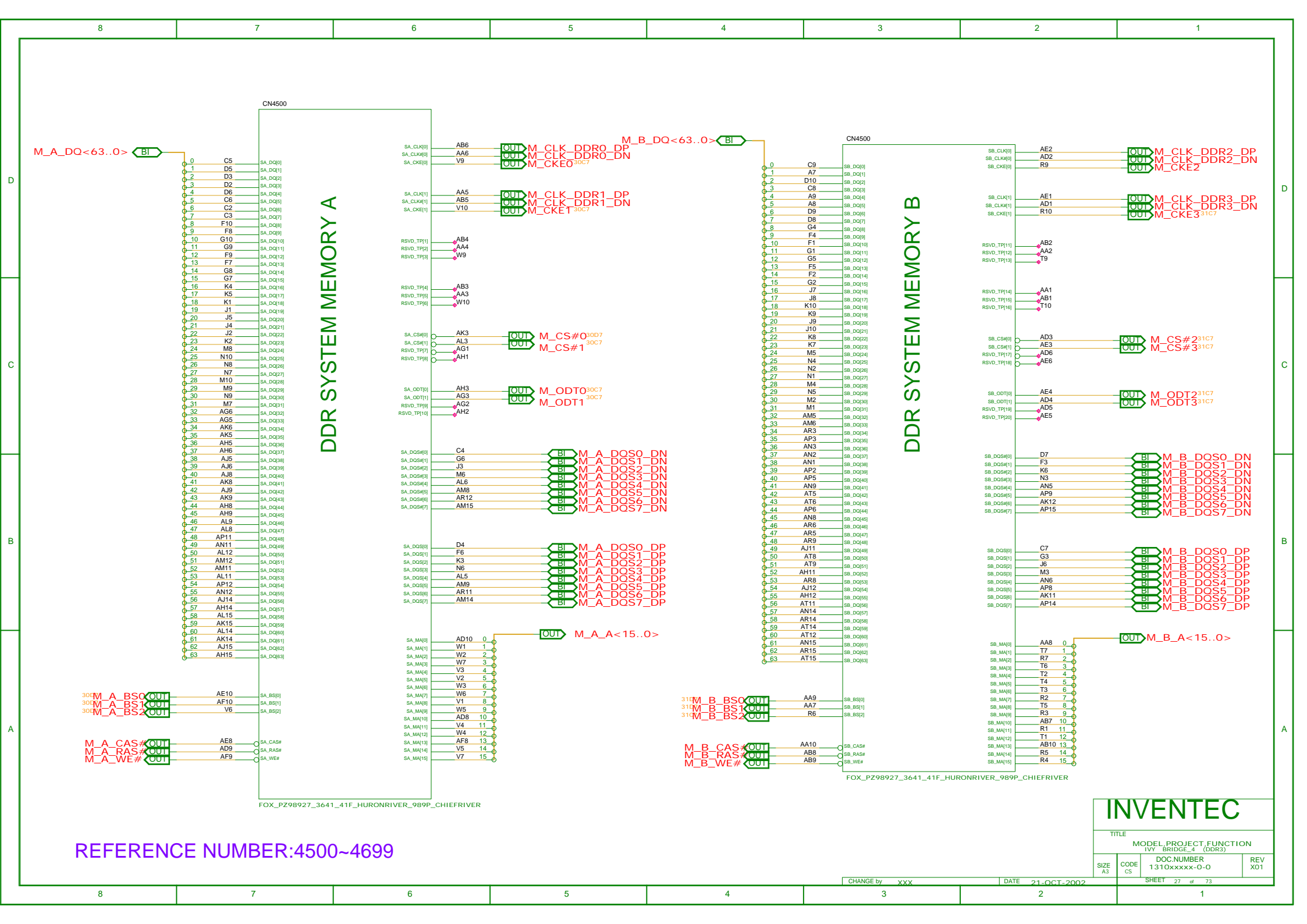


INVENTEC			
TITLE			
MODEL,PROJECT,FUNCTION			
XDP_CONN			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01

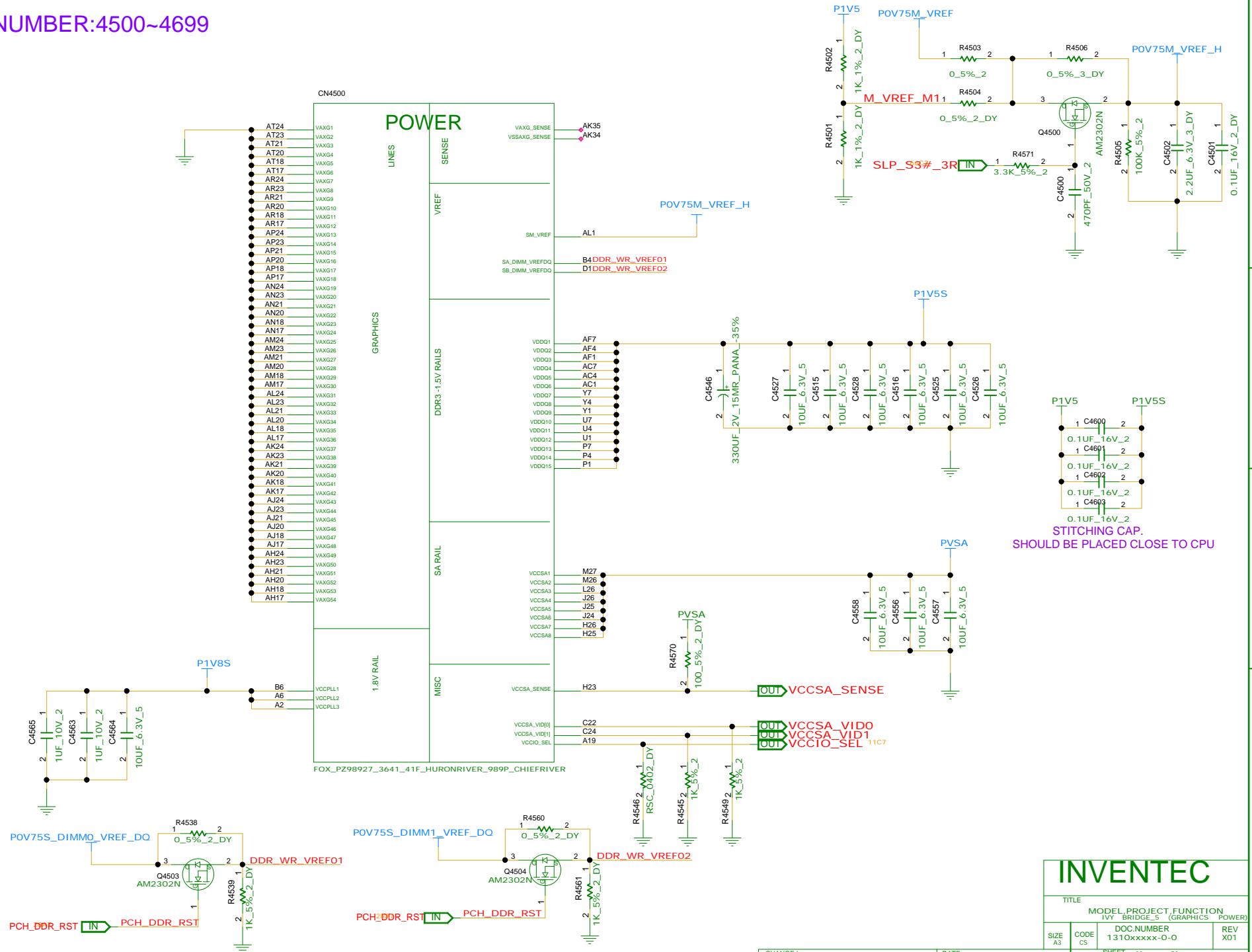


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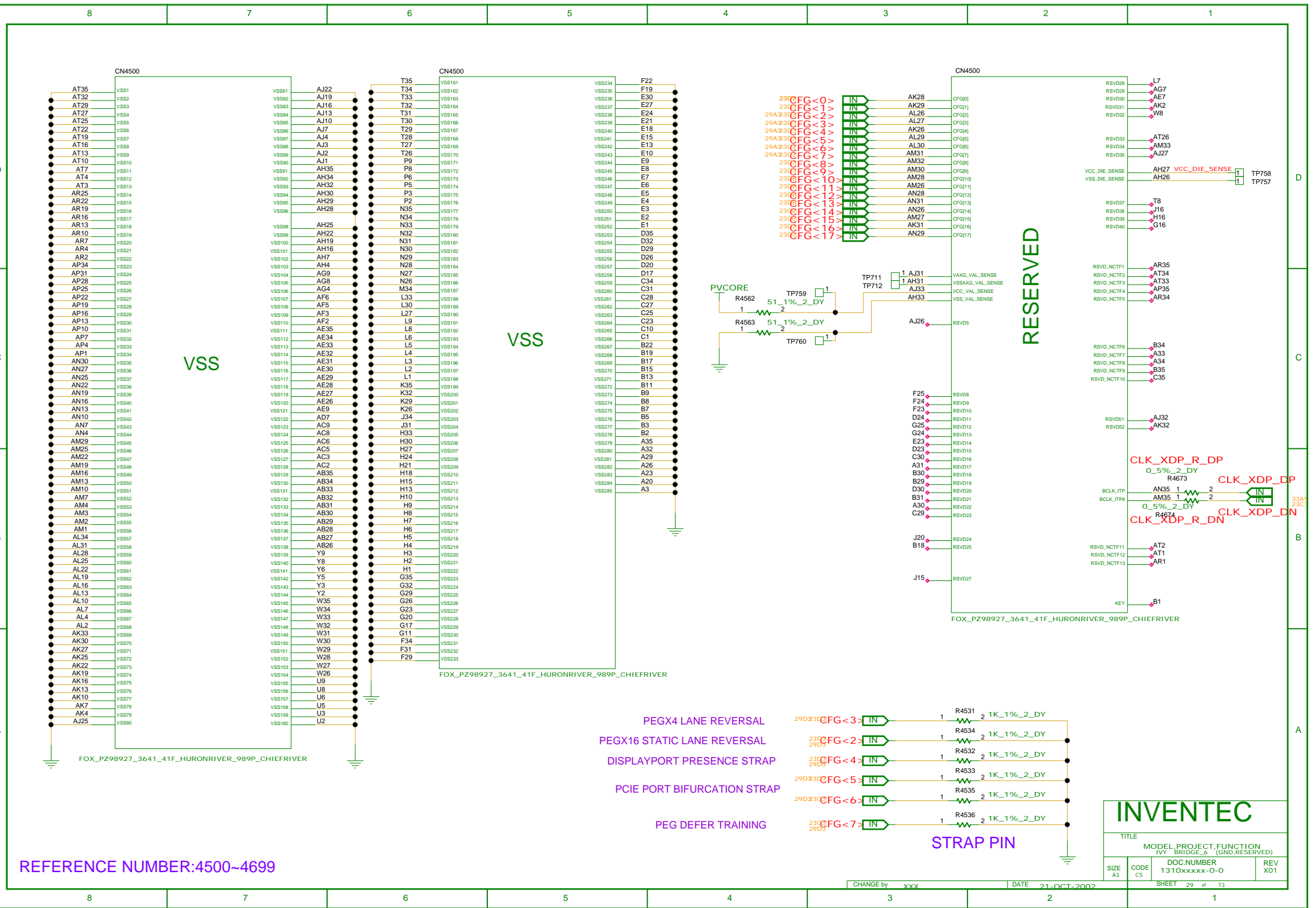


REFERENCE NUMBER:4500~4699



STITCHING CAP.
SHOULD BE PLACED CLOSE TO CPU

INVENTEC			
TITLE			
MODEL	PROJECT	FUNCTION	IVY BRIDGE-5 (GRAPHICS POWER)
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
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REFERENCE NUMBER:4500~4699

INVENTEC			
TITLE			
MODEL PROJECT.FUNCTION			
IVY BRIDGE_6 (GND,RESERVED)			
SIZE	CODE	DOC.NUMBER	REV
A3	CS	1310xxxxx-0-0	X01

M_B_A<15..0> BI M_B_DQ<63..0> BI

M_B_BS0
M_B_BS1
M_B_BS2
27CM_CS#2
27CM_CS#3
M_CLK_DDR2_DP
M_CLK_DDR2_DN
M_CLK_DDR3_DP
M_CLK_DDR3_DN
M_CKE2
M_CKE3
27CM_CAS#
M_B_RAS#
M_B_WE#
SA0_DIM1
SA1_DIM1
PCH_3S_SMCLK
PCH_3S_SMDATA

M_B_DQS0
M_B_DQS1
M_B_DQS2
M_B_DQS3
M_B_DQS4
M_B_DQS5
M_B_DQS6
M_B_DQS7
M_B_DQS8
M_B_DQS9
M_B_DQS10
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M_B_DQS552

FOX_AS0A626_N2R6_7H_204P
CN4675:6026B0182502

NOTE:
SO-DIMMB SPD ADDRESS IS 0XA4
SO-DIMMB TS ADDRESS IS 0X34

REFERENCE NUMBER:4100~4299

P3V3S

POV75S_DIMM1_VREF_DQ

POV75S_DIMM1_VREF_CA

PM_EXTTTS#1 ROUT
DDR3_DRAMRST ROUT

POV75M_VREF

POV75M_VREF
POV75S_DIMM1_VREF_DQ

INVENTEC

TITLE
MODEL PROJECT,FUNCTION
DDR3_SO-DIMM1

DOC NUMBER
1310xxxxx-0-0

REV
X01

REFERENCE NUMER : 4700~4949

LAYOUT NOTE: JTAG_TMS TERMINATIONS NEED TO BE PLACED NEAR PCH
LAYOUT NOTE: JTAG_TDI TERMINATIONS NEED TO BE PLACED NEAR PCH
LAYOUT NOTE: JTAG_TDO TERMINATIONS NEED TO BE PLACED NEAR XDP

D

C

C6

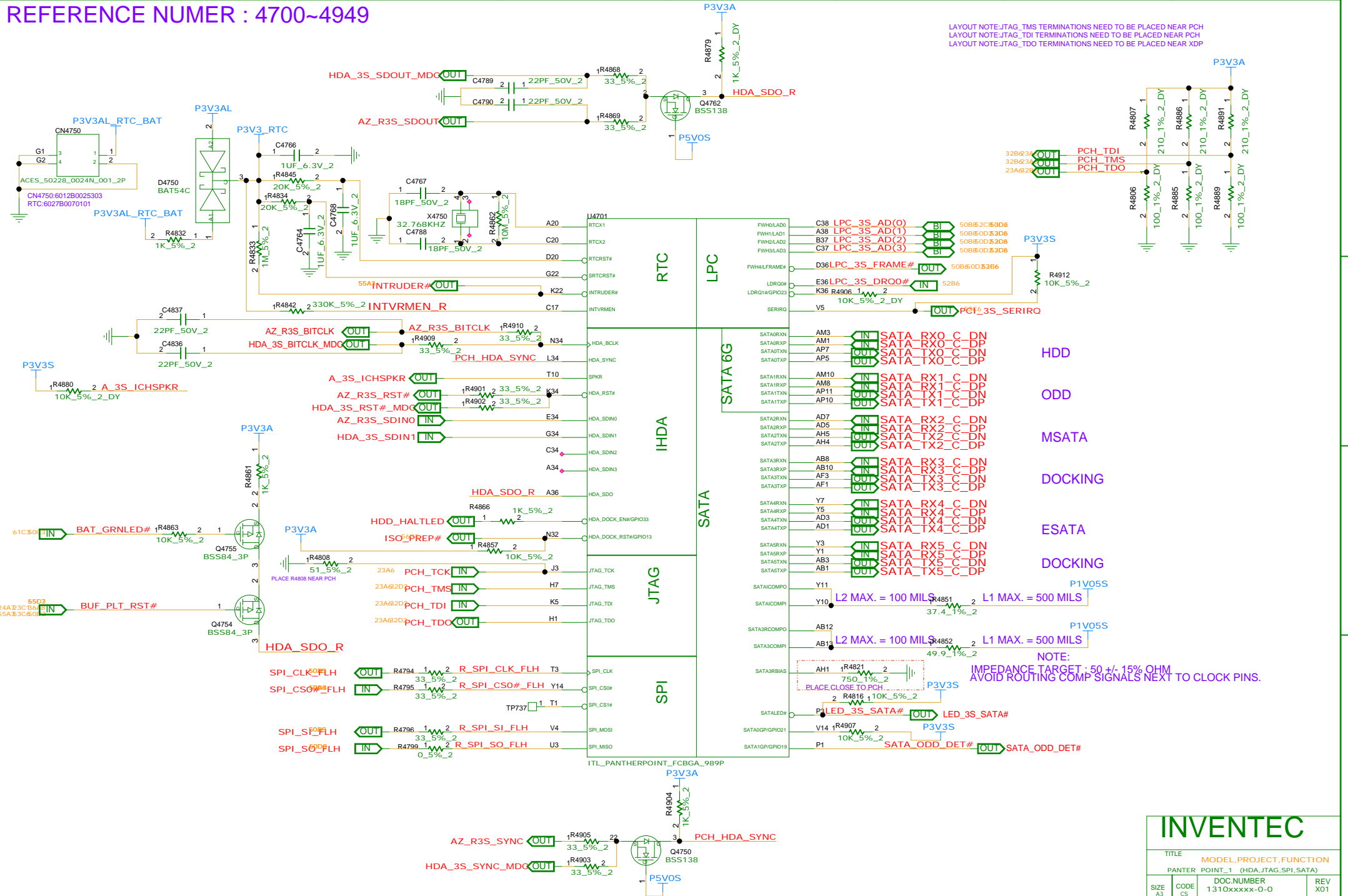
A

D

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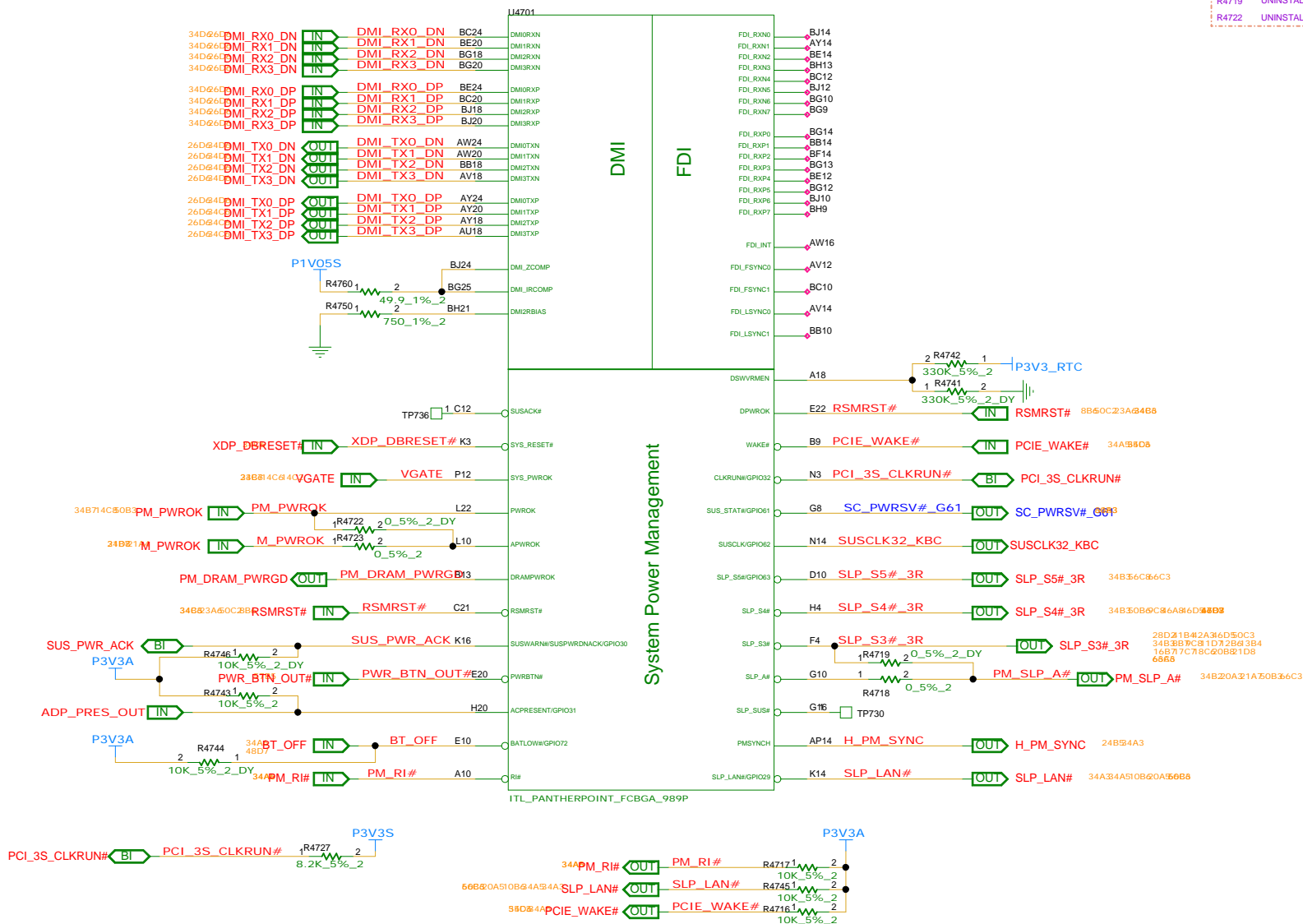


REFERENCE NUMBER : 4700~4949

NOTE:

- 1.SLP_SUS AND SUSACK# ARE NC IF DSW IS NOT SUPPORTED
- 2.DPWROK SHOULD CONNECT TO RSMRST# IF DSW NOT SUPPORTED
- 3.PCH_DPWROK PULL UP TO +V3S ENABLES DSW WUPPORT. NO INSTALL R5261 TO DISABLE DSW

	IAMT	NON-IAMT
R4718	INSTALL	UNINSTALL
R4723	INSTALL	UNINSTALL
R4719	UNINSTALL	INSTALL
R4722	UNINSTALL	INSTALL



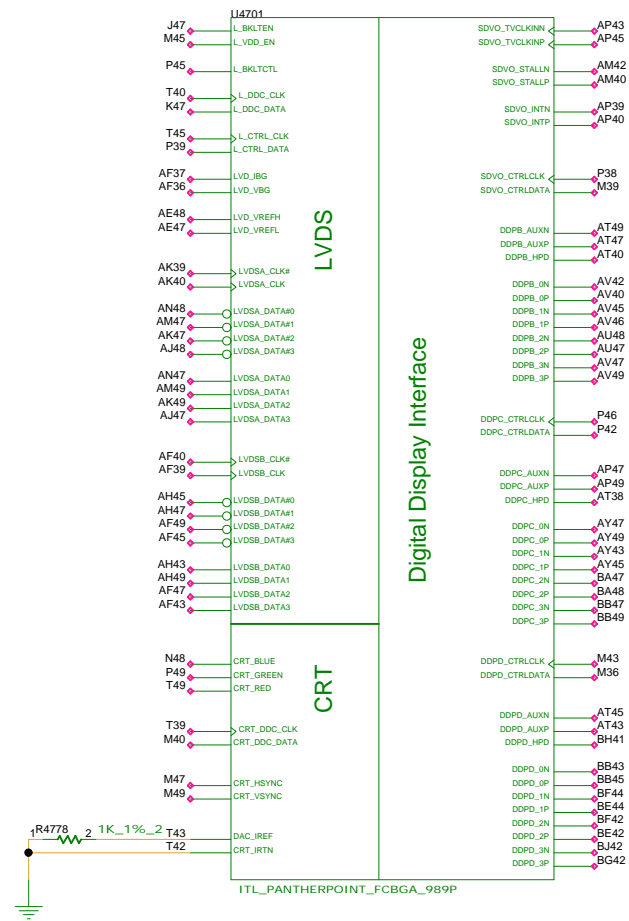
INVENTEC

TITLE			
MODEL PROJECT FUNCTION PANTER POINT-3 (DMI,FDI,GPIO)			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01

CHANGE by XXX DATE 21-OCT-2002

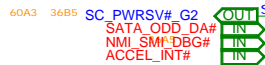
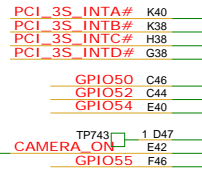
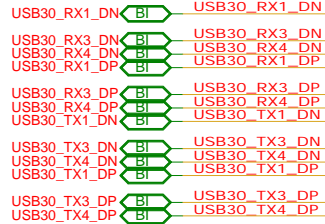
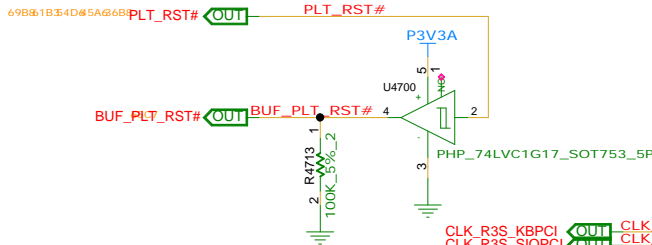
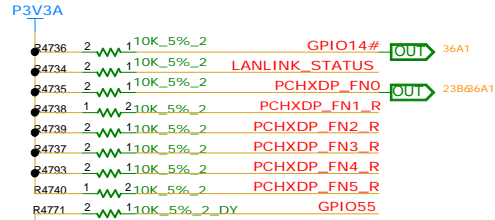
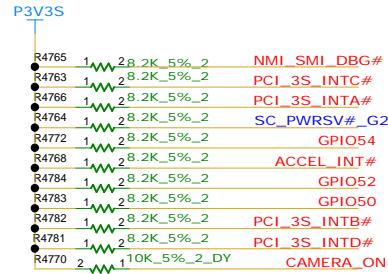
SHEET 34 of 73

REFERENCE NUMER : 4700~4949



INVENTEC			
TITLE			
MODEL PROJECT FUNCTION PANTHER POINT_4 (LVDS,DDI)			
SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
SHEET 35 of 73			

REFERENCE NUMER : 4700~4949

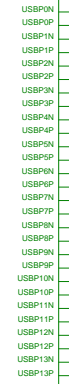


RSVD

NVRAM

PCI

USB



INVENTEC

TITLE

MODEL PROJECT FUNCTION

PANTER POINT-5 (PCI,USB,NVRAM)

SIZE A3

CODE CS

DOC NUMBER 1310xxxxx-0-0

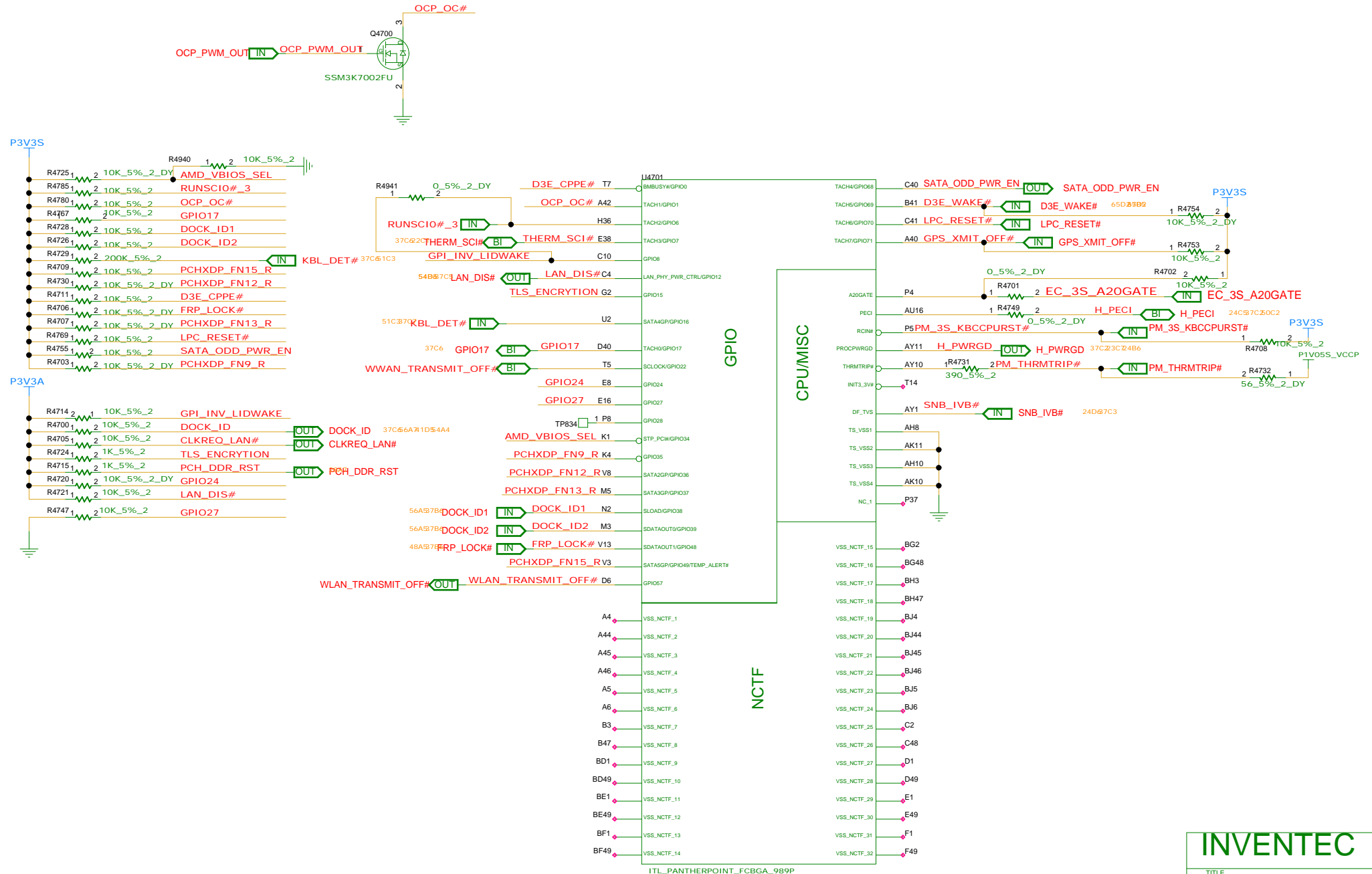
REV X01

CHANGE by XXX

DATE 21-OCT-2002

SHEET 36 of 73

REFERENCE NUMBER : 4700~4949



INVENTEC

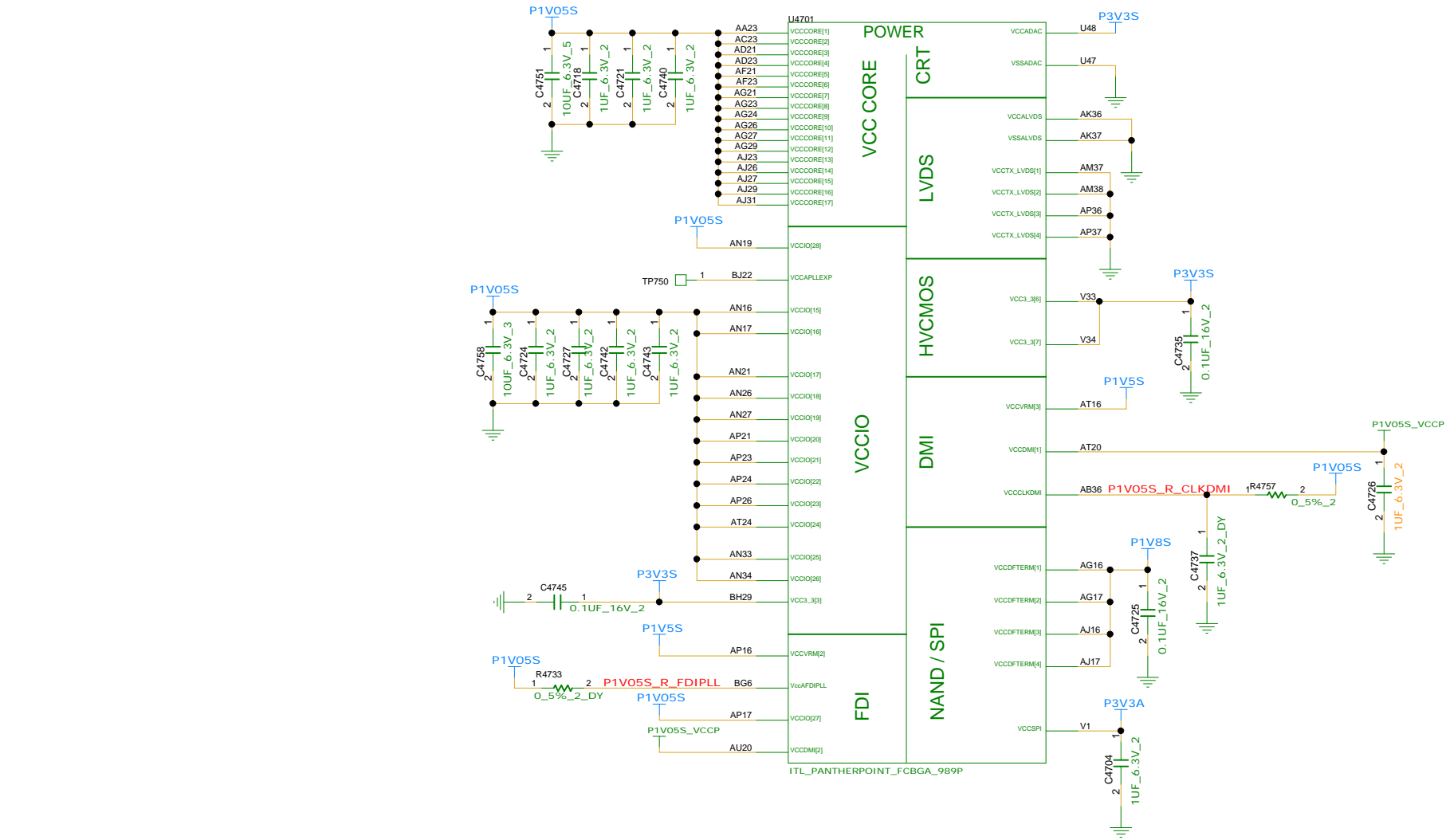
TITLE
MODEL,PROJECT,FUNCTION PANTER POINT_6 (GPIO,VSS_NCFT,RSVD)

SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-O-O	REV X01
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AS	CS	
SHEET 37 of 73		

CHANGE by	XXX	DATE	21-OCT-2002
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SHEET 37 of 73



INVENTEC

TITLE

MODEL PROJECT FUNCTION
PANTHER POINT_7 (POWER)

SIZE
A3

CODE
CS

DOC NUMBER
1310xxxxx-0-0

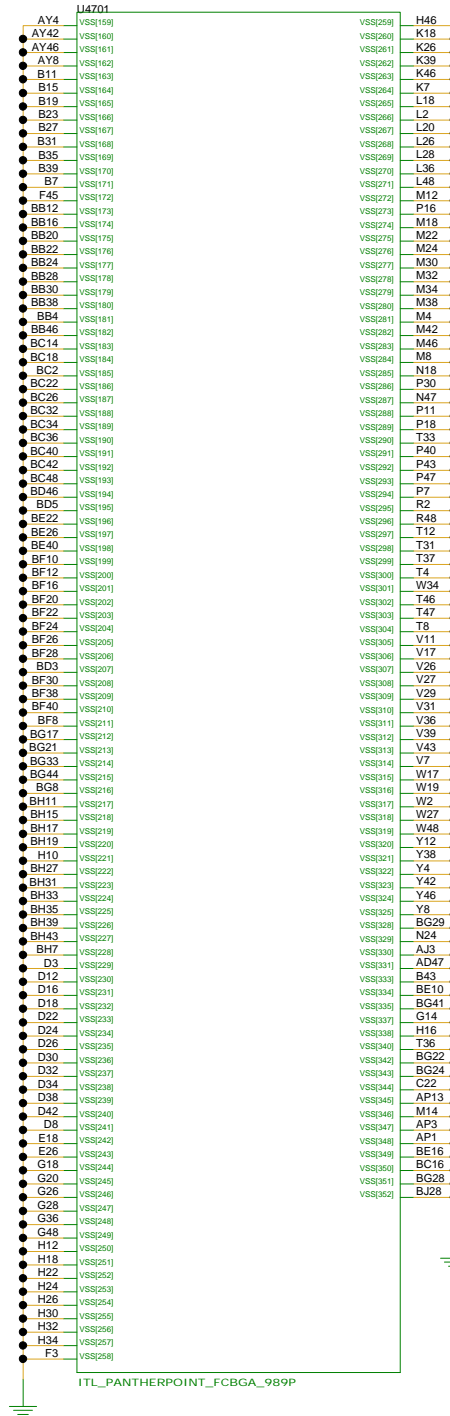
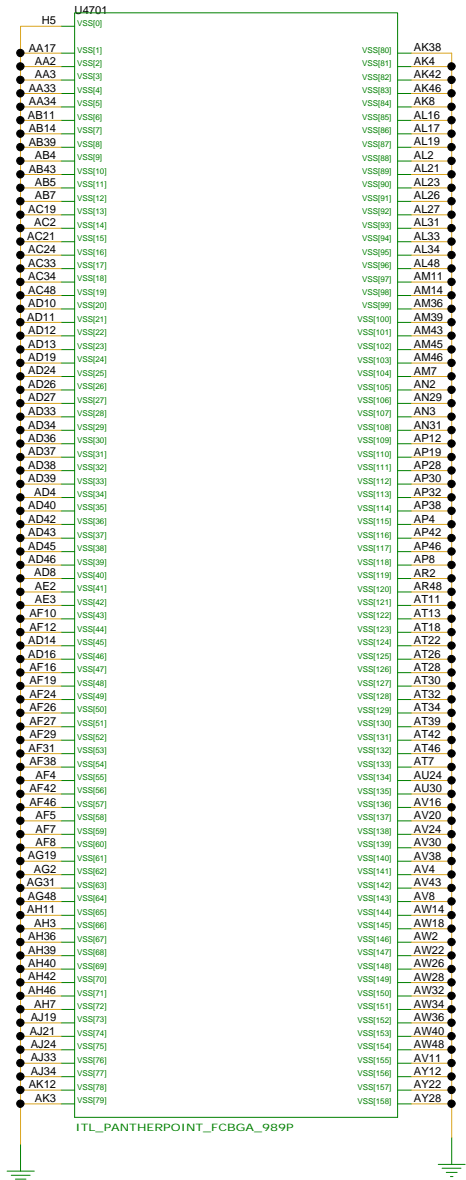
REV
X01

SHEET 38 of 73

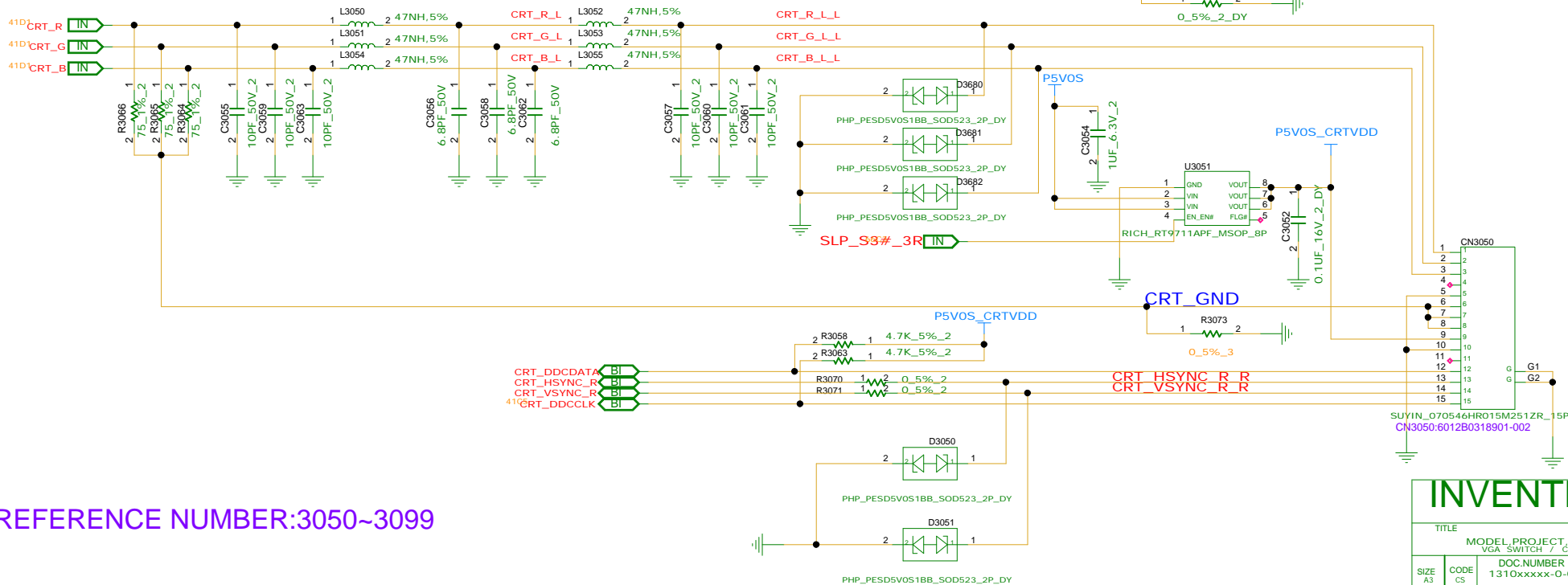
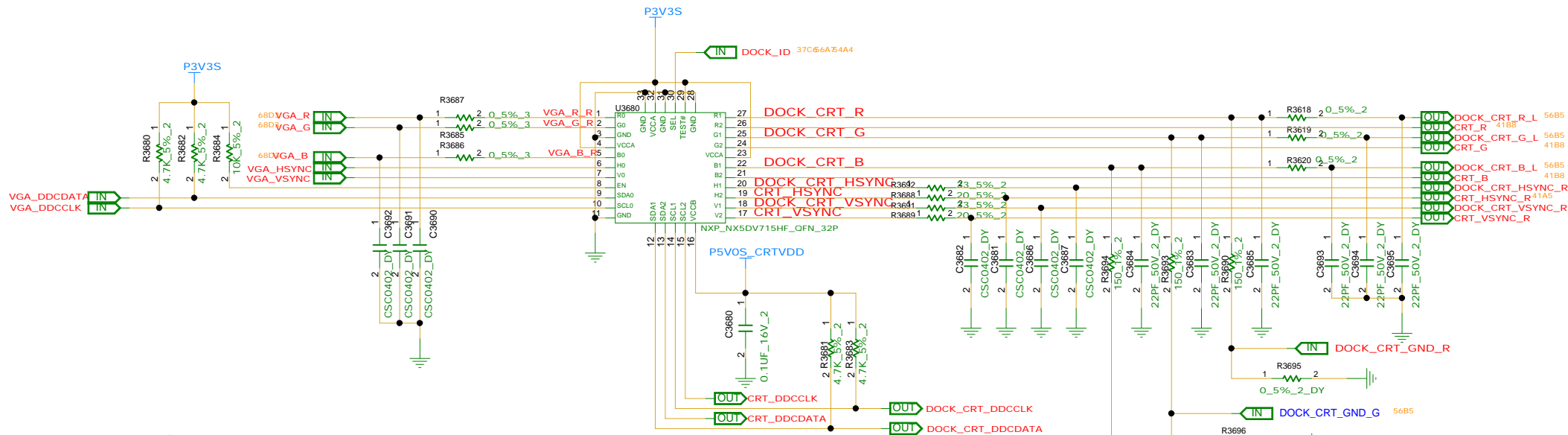


CHANGE by	XXX	DATE	21-OCT-2002
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REFERENCE NUMBER : 4700~4949

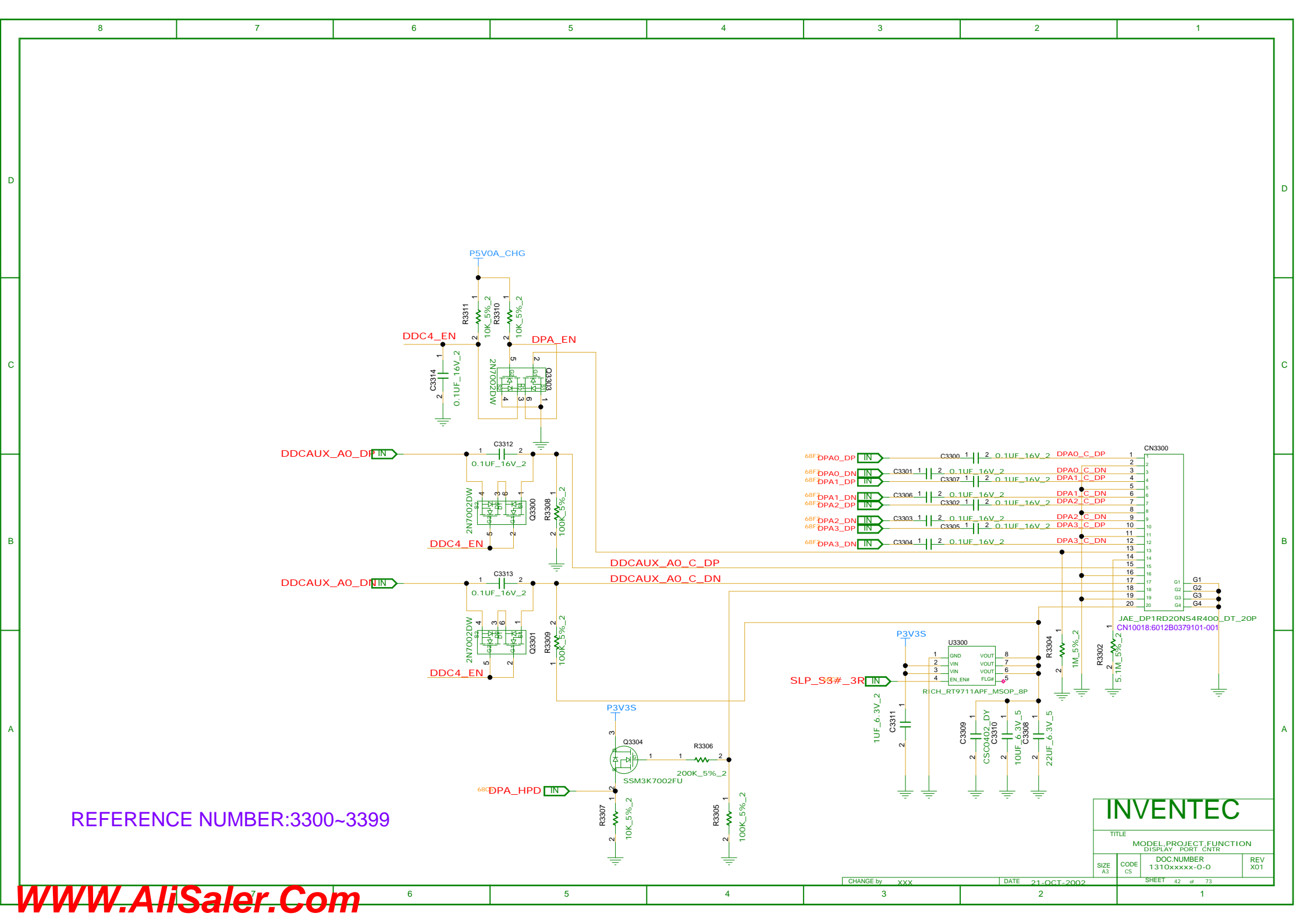


INVENTEC			
TITLE			
MODEL PROJECT FUNCTION			
PANTHER POINT-9 (GND)			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-O-O	X01
SHEET 40 of 73			



INVENTEC

TITLE			
MODEL PROJECT FUNCTION			
VGA SWITCH / CRT			
DOC NUMBER			
1310xxxxx-0-0			
REV			
X01			
SIZE	CODE	SHEET	
A3	CS	41	of 73



REFERENCE NUMBER:3300~3399

INVENTEC

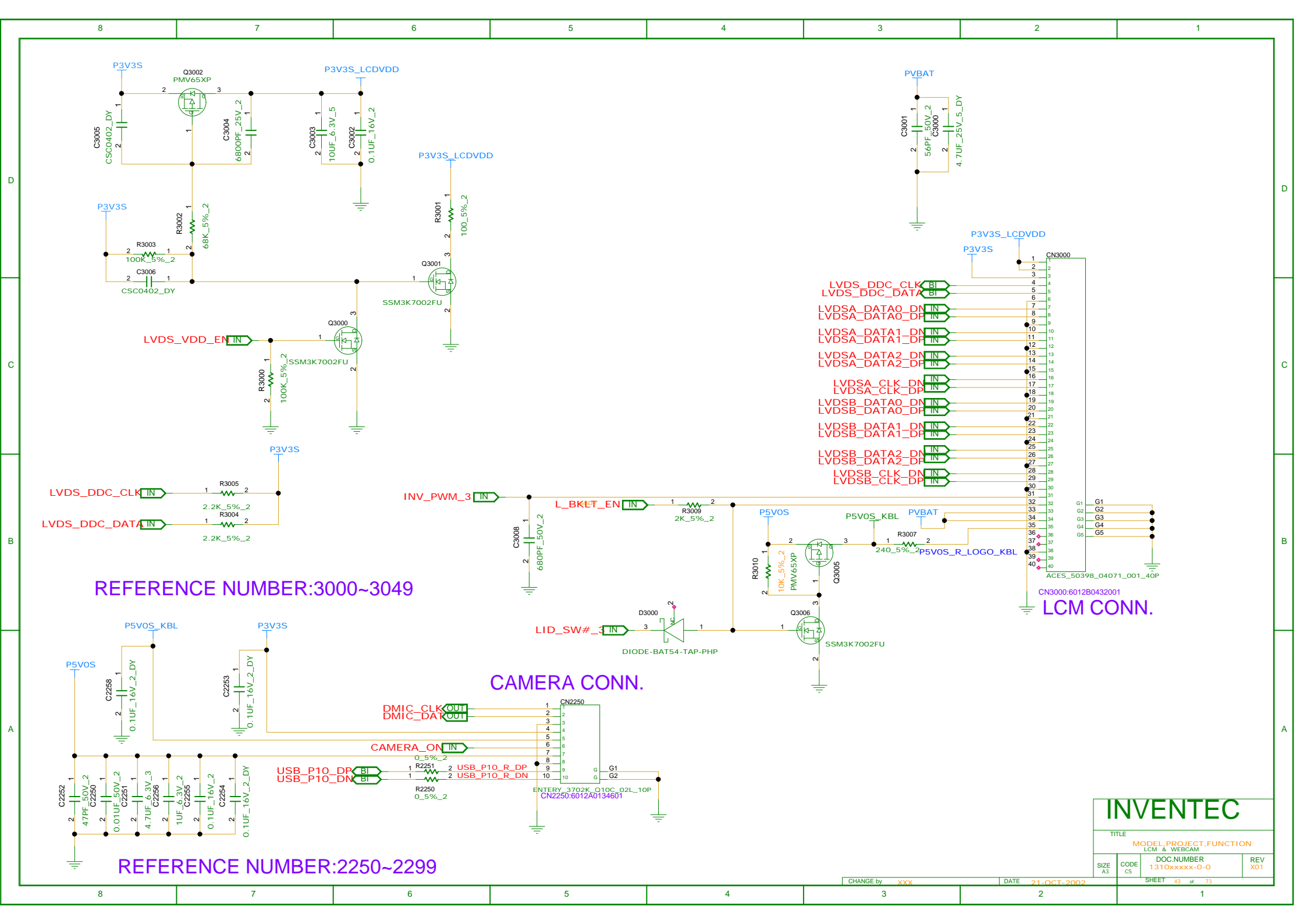
TITLE
MODEL PROJECT FUNCTION
DISPLAY PORT CNTR

DOC NUMBER
1310xxxxx-0-0

REV
X01

CHANGE by XXX DATE 21-OCT-2002

SHEET 42 of 73



REFERENCE NUMBER:3000~3049

CAMERA CONN.

LCM CONN.

REFERENCE NUMBER:2250~2299

INVENTEC			
TITLE MODEL,PROJECT,FUNCTION LCM & WEBCAM			
SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
CHANGE by XXX DATE 21-OCT-2002 SHEET 43 of 73			

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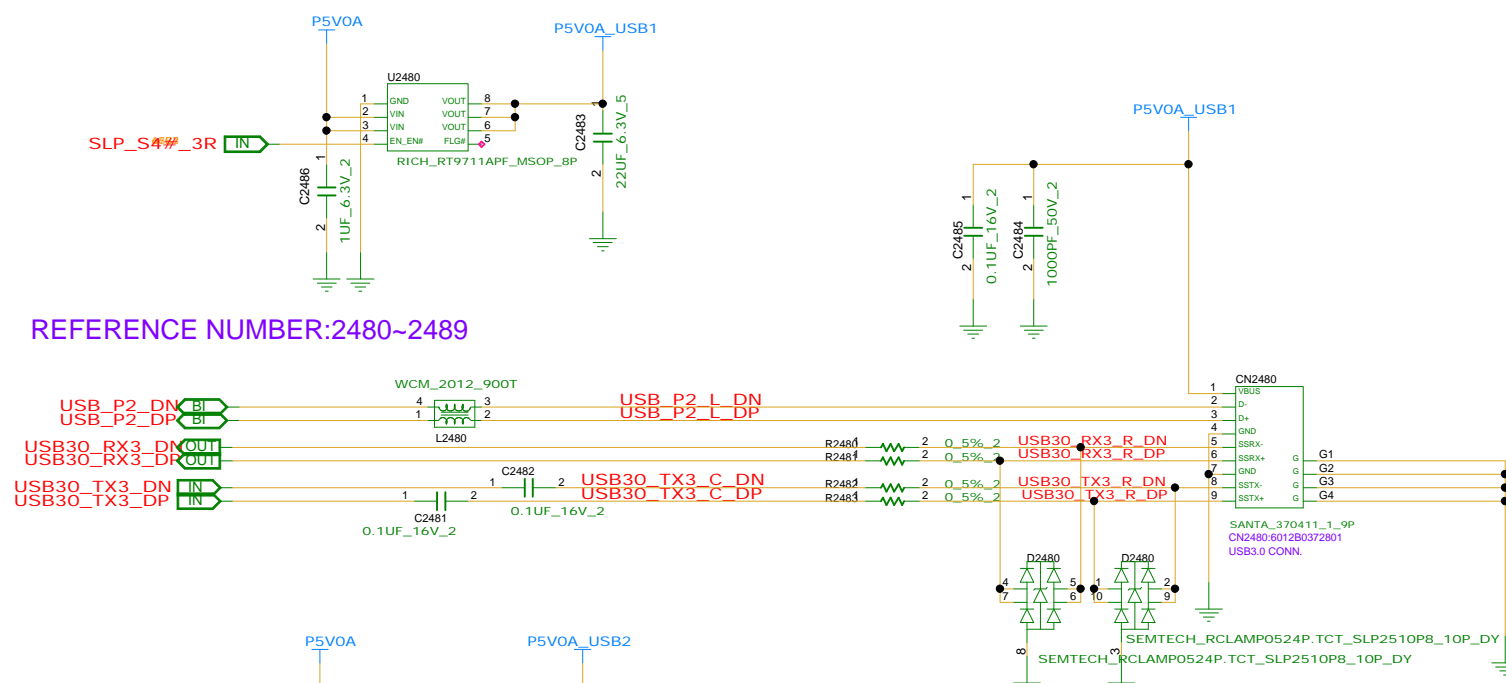
REFERENCE NUMBER:3400~3499

INVENTEC

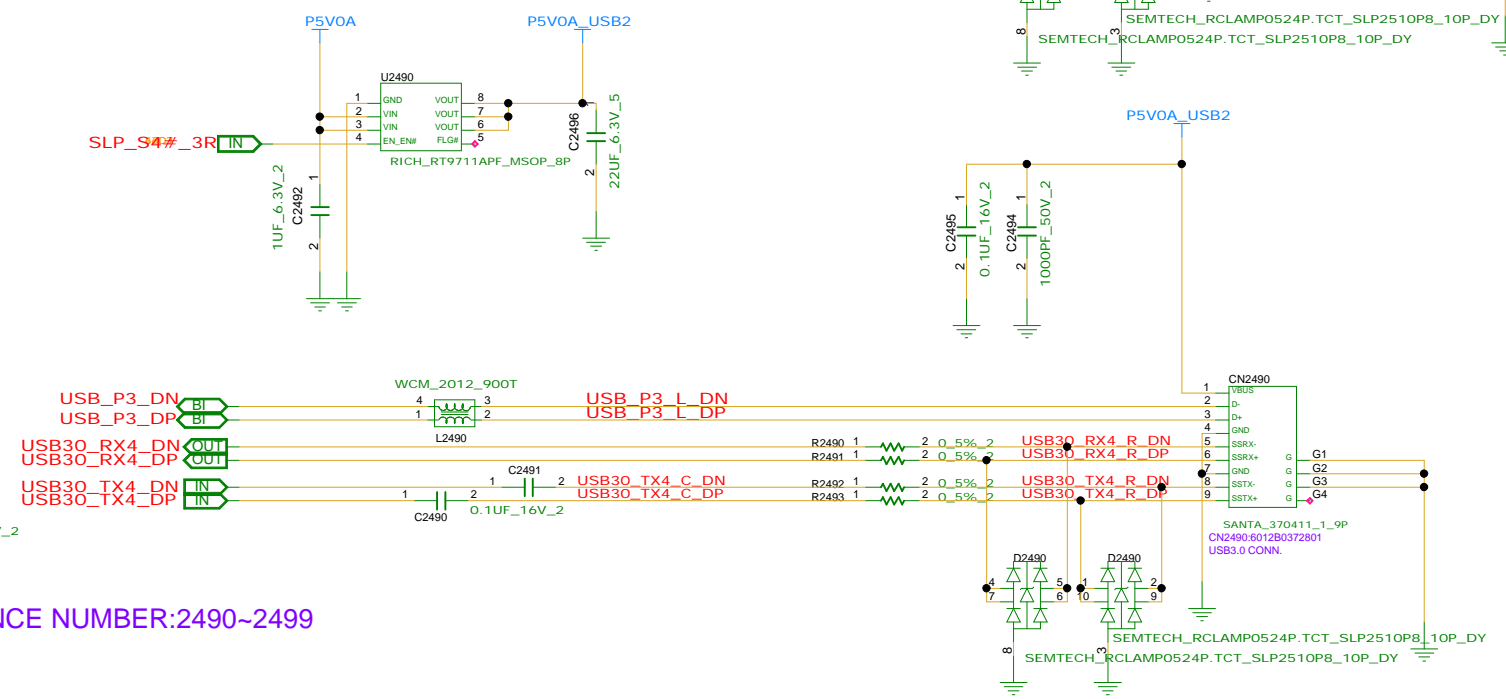
TITLE	MODEL, PROJECT, FUNCTION
	EDR TO LVDS

SIZE A2	CODE CS	DOC.NUMBER 1310xxxxx-0-0	REV X01
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CHANGE by	XXX	DATE	21-OCT-2002	SHEET	44	of	73
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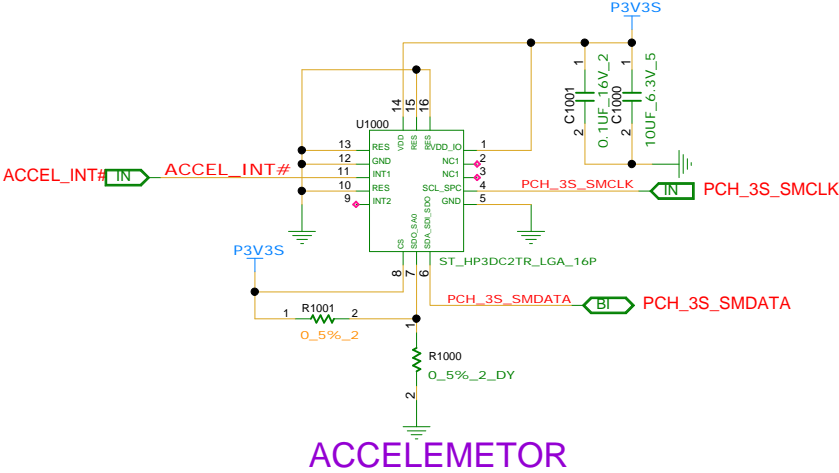
REFERENCE NUMBER:2480~2489



REFERENCE NUMBER:2490~2499

INVENTEC			
TITLE			
MODEL PROJECT FUNCTION			
USB CONN.			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
SHEET 47 of 73			

REFERENCE NUMER : 1000~1099



INVENTEC

TITLE
MODEL PROJECT FUNCTION
ACCELEMETER

SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
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CHANGE by XXX DATE 21-OCT-2002

SHEET 49 of 73

REFERENCE NUMBER : 300~399

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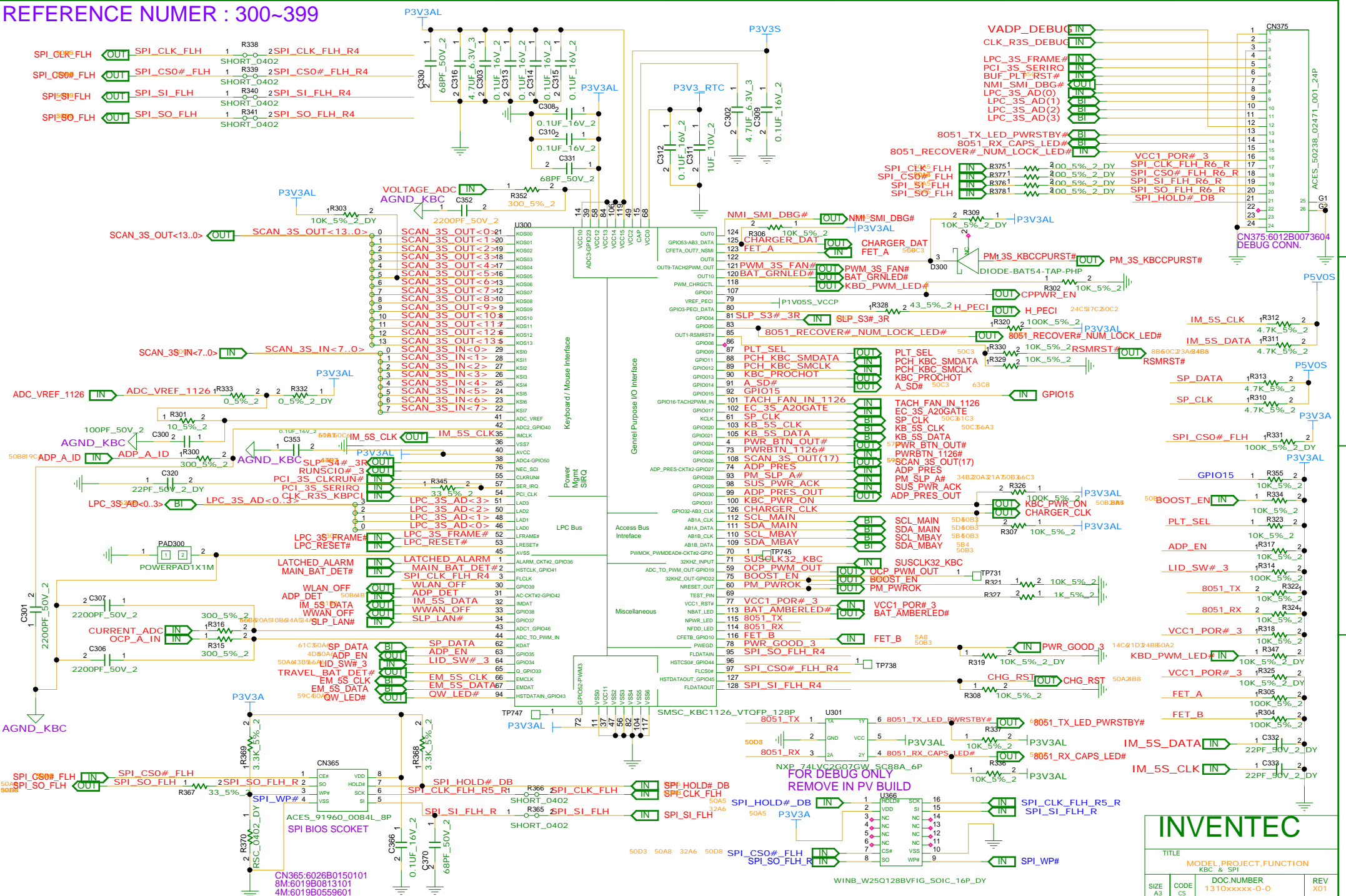
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INVENTEC			
TITLE			
MODEL PROJECT FUNCTION			
KBC & SPI			
DOC NUMBER			
1310xxxxx-0-0			
REV			
X01			
SHEET			
50 of 73			

REFERENCE NUMER : 300~389

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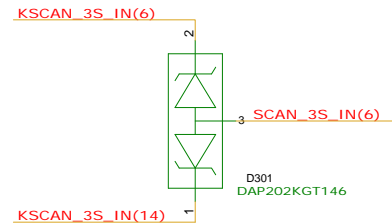
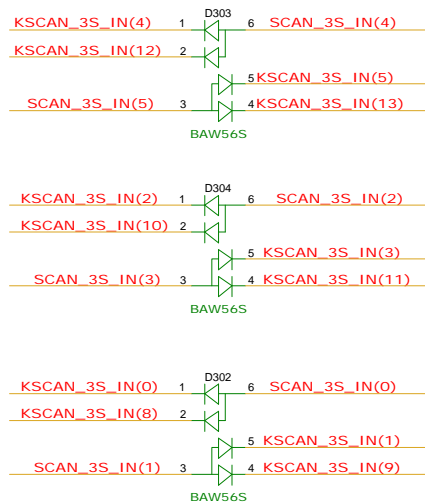
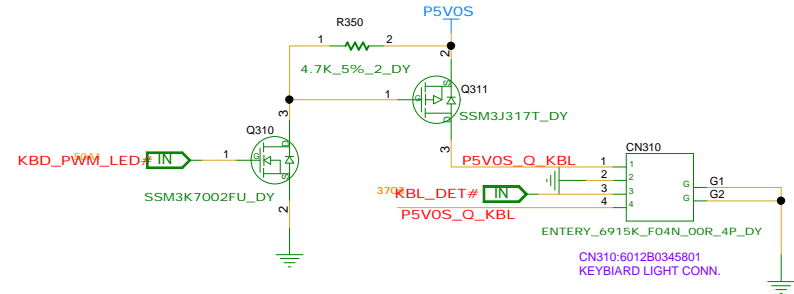
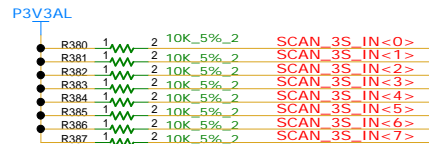
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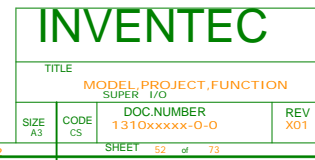
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INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION			
KEYBOARD			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01

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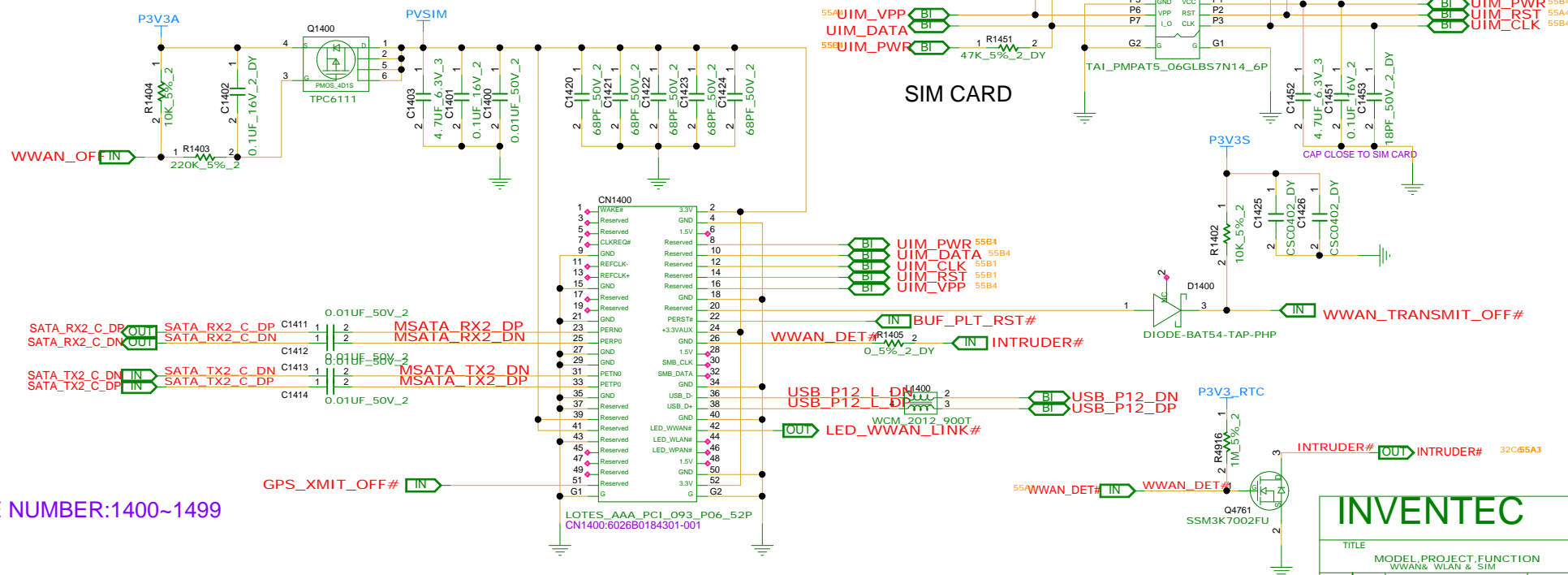
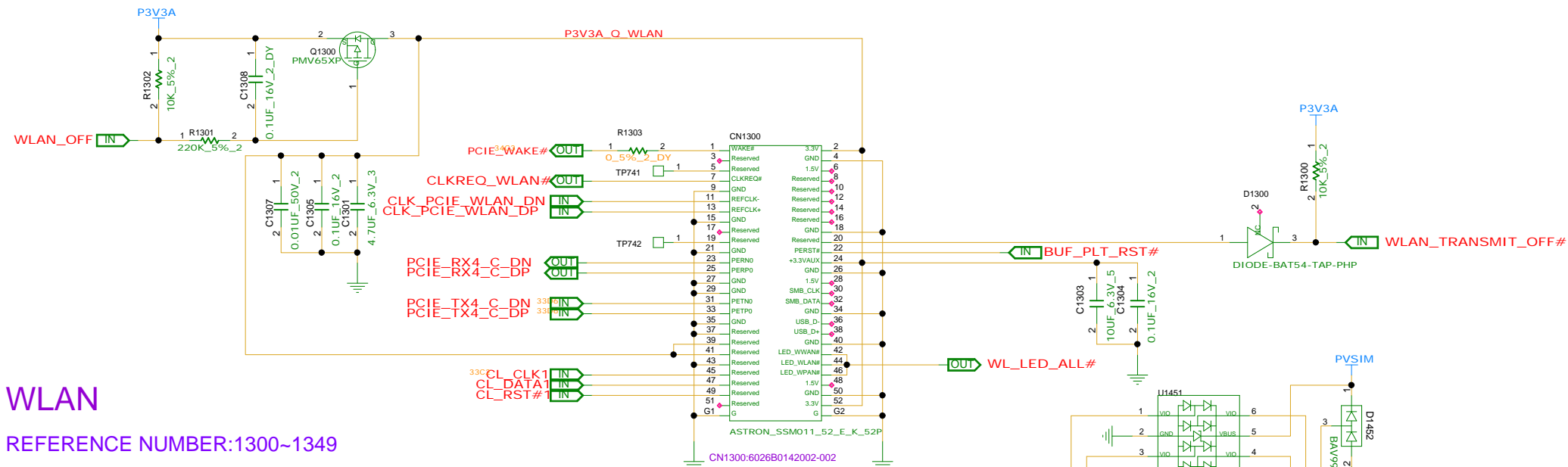


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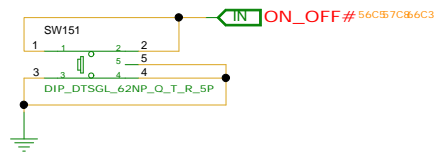


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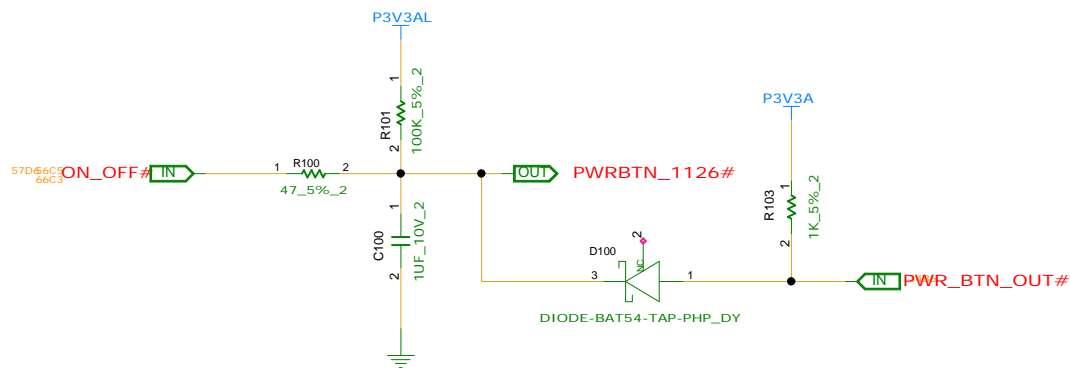
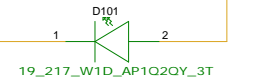
CHANGE by	XXX	DATE	21-OCT-2002
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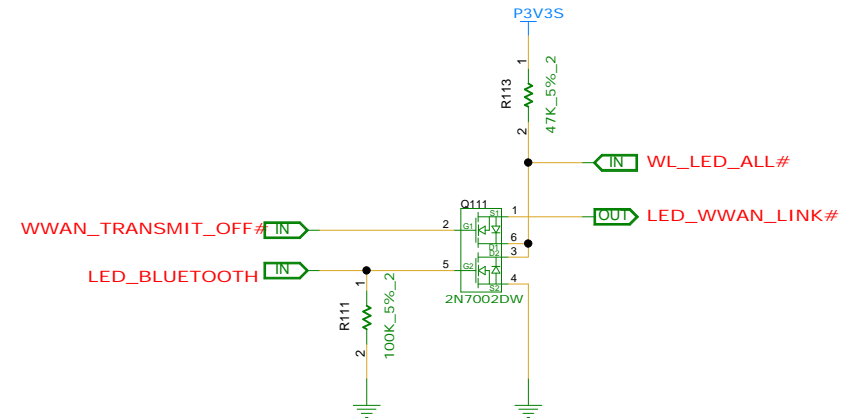




8051_TX_LED_PWRSTBY#



POWER / STANDBY LED



WLAN_WWAN_BLUETOOTH_LED

REFERENCE NUMBER:100~199

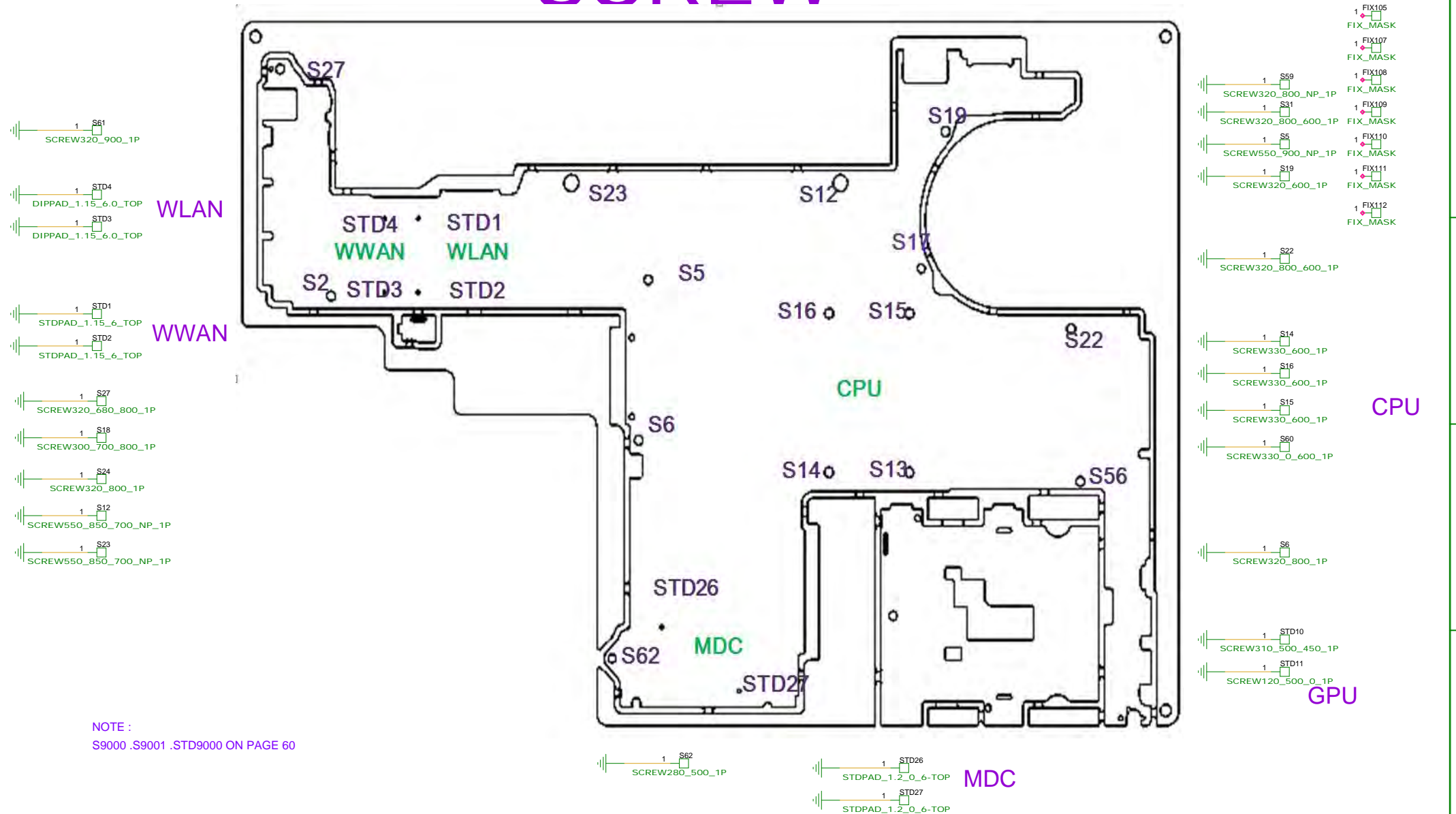
INVENTEC

TITLE			
MODEL PROJECT FUNCTION POWER & WLAN_LED			
SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01

CHANGE by XXX DATE 21-OCT-2002

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SCREW



INVENTEC

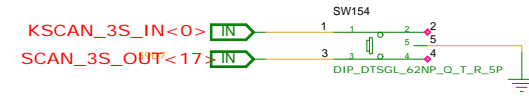
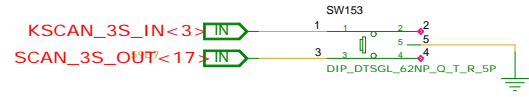
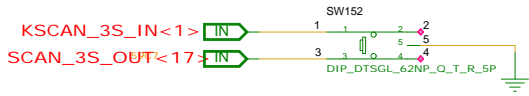
TITLE MODEL,PROJECT,FUNCTION

SIZE CODE DOC NUMBER REV

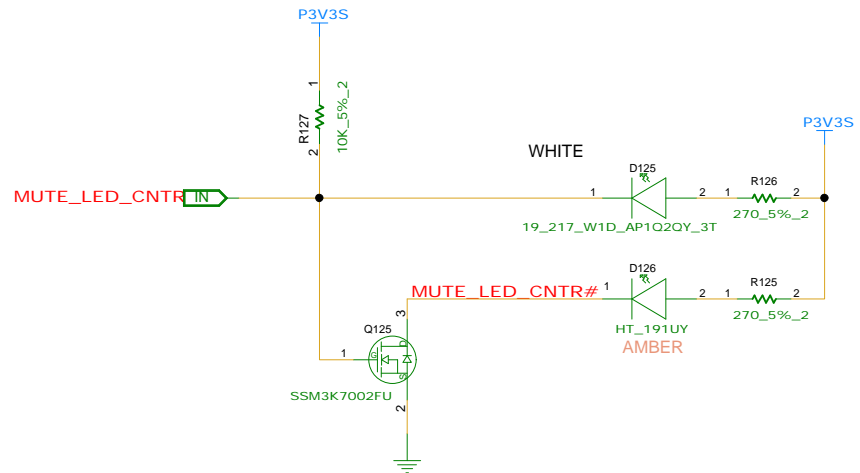
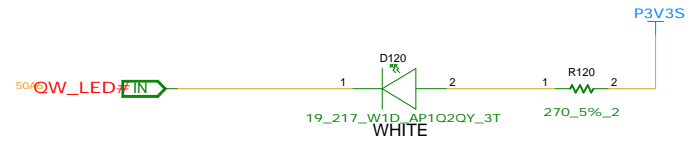
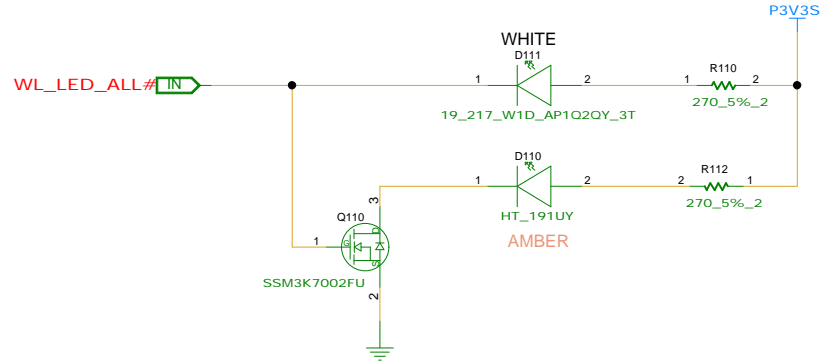
A3 CS 1310xxxxx-0-0 X01

SHEET 58 of 73

CHANGE by XXX DATE 21-OCT-2002



REFERENCE NUMBER:100~199



INVENTEC			
TITLE			
MODEL PROJECT FUNCTION			
WIRELESS / QUICK WEB / MUTE LED			
DOC NUMBER		REV	
1310xxxxx-0-0		X01	
SIZE	CODE	SHEET	
A3	CS	59 of 73	

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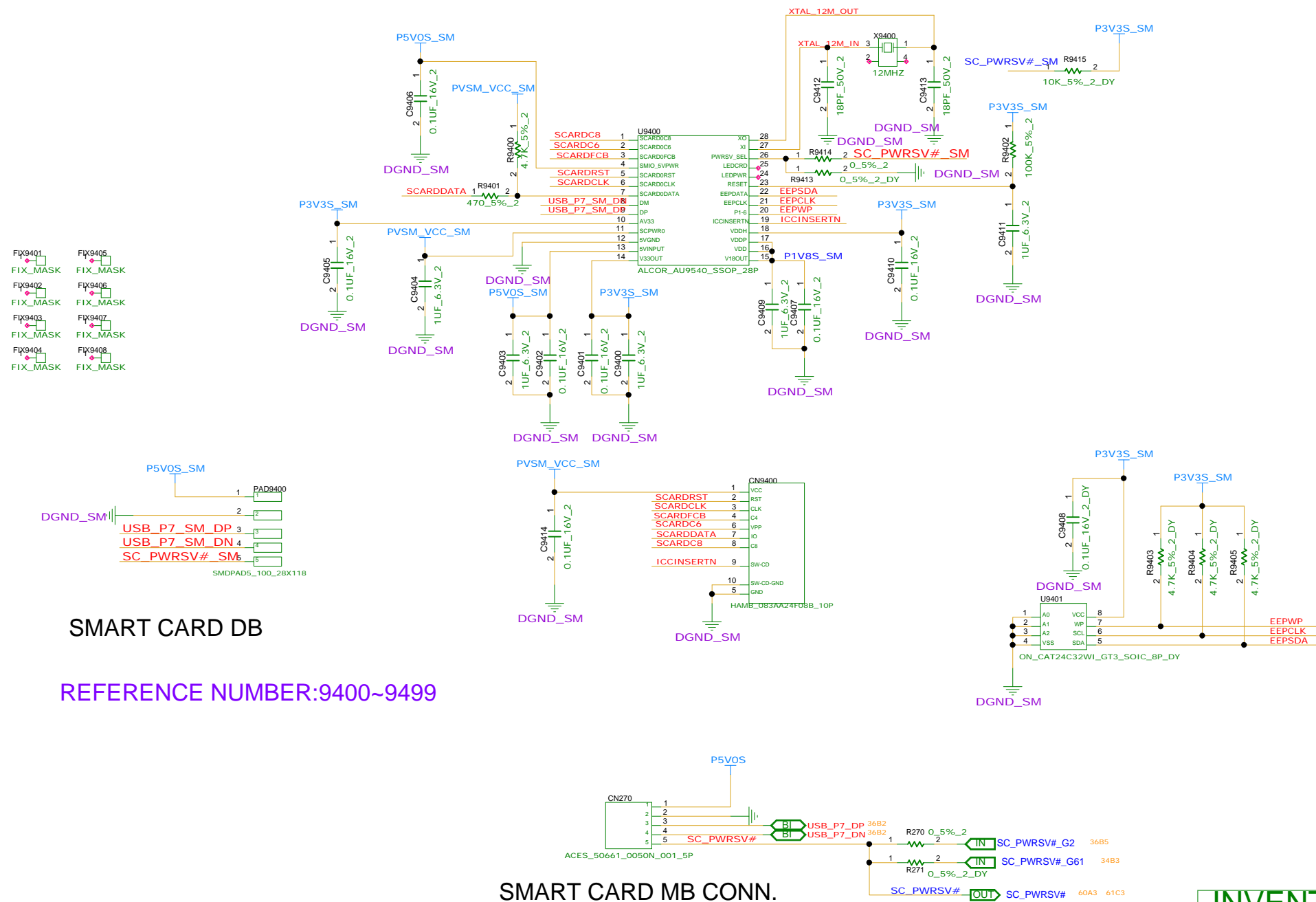
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INVENTEC

TITLE MODEL PROJECT,FUNCTION

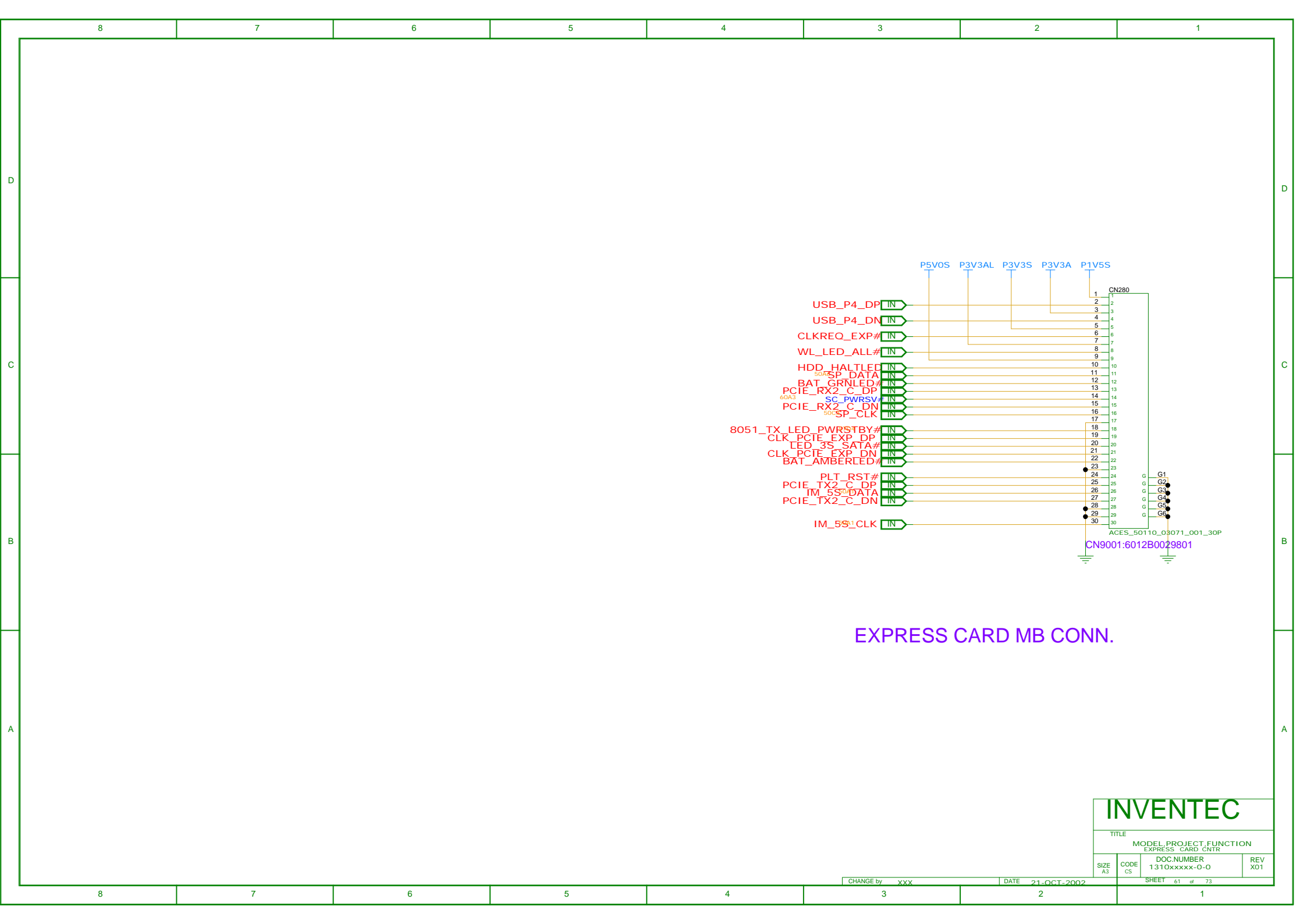
SMART CARD

DOC NUMBER 1310xxxxx-0-0

REV X01

SHEET 60 of 73

CHANGE by XXX DATE 21-OCT-2002



EXPRESS CARD MB CONN.

INVENTEC

TITLE
MODEL PROJECT FUNCTION
EXPRESS_CARD_CNTR

SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
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CHANGE by XXX DATE 21-OCT-2002

SHEET 61 of 73

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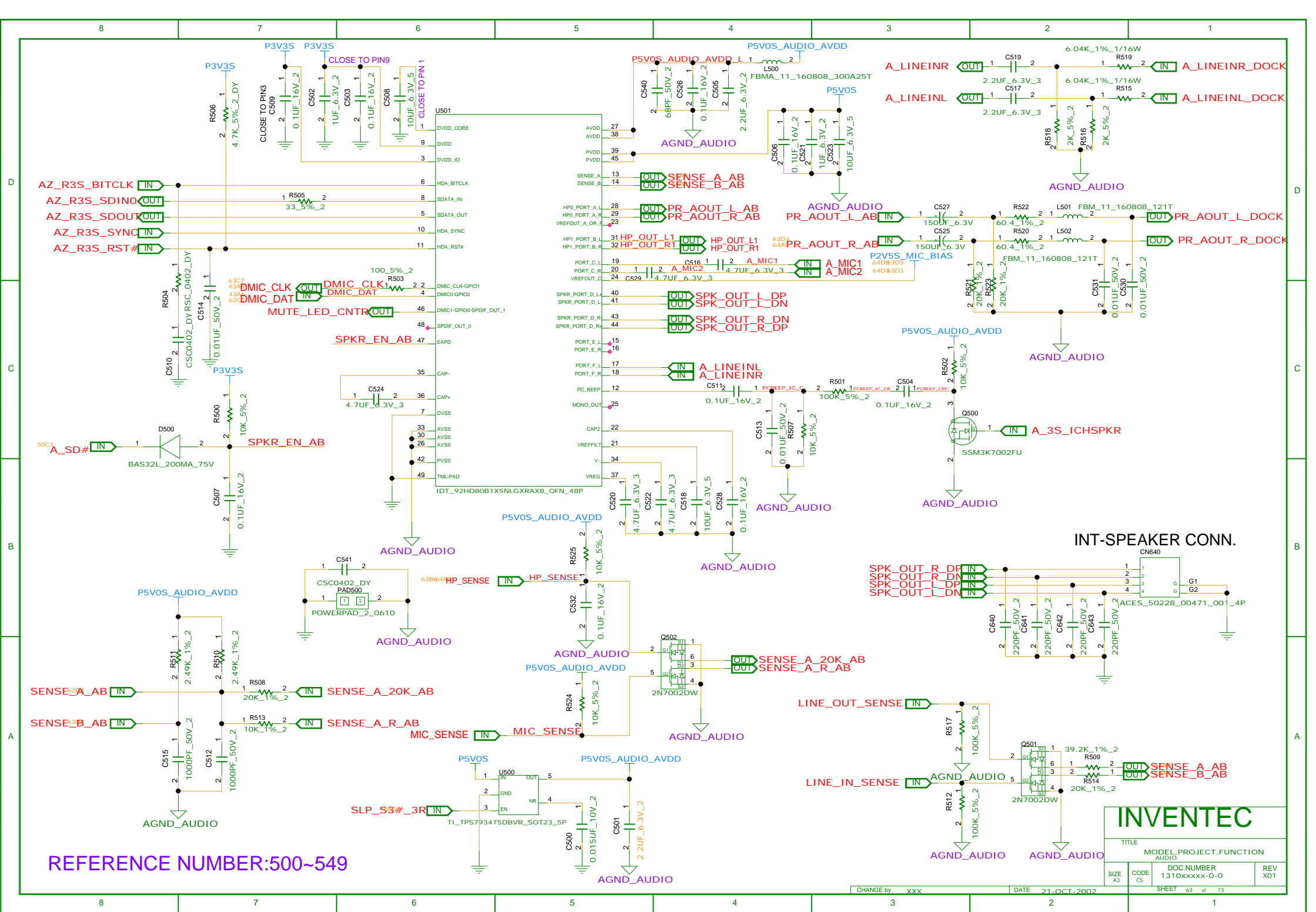
INVENTEC

TITLE
MODEL, PROJECT, FUNCTION

SIZE A3	CODE CS	DOC. NUMBER 1310xxxxx-0-0	REV X01
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CHANGE by XXX DATE 21-OCT-2002

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REFERENCE NUMBER:800~899

SD/MMC CONN

1394 CONN.

INVENTEC

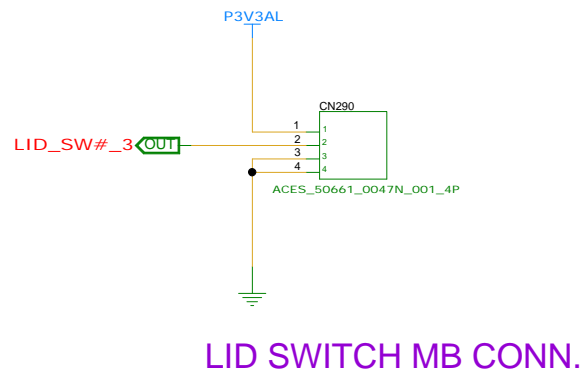
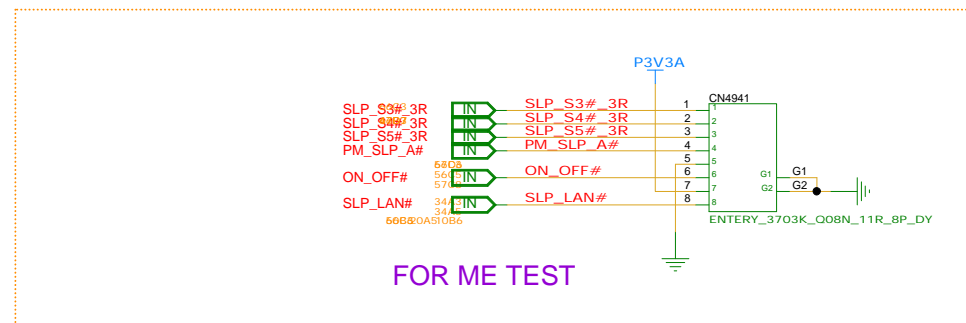
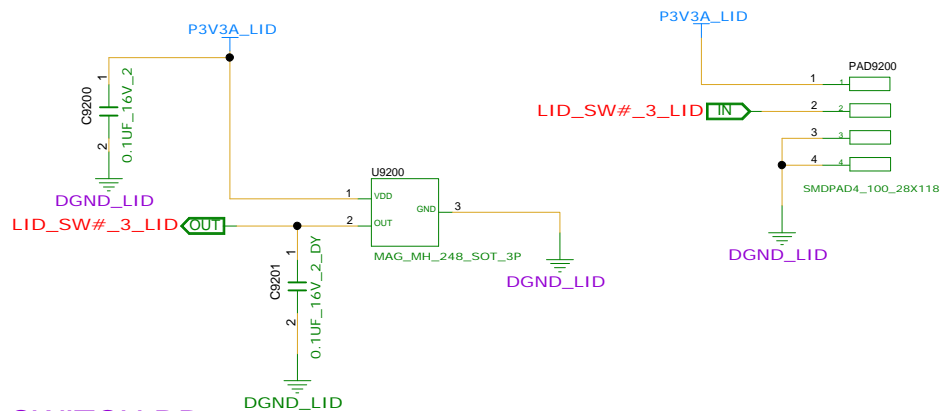
TITLE			
MODEL PROJECT FUNCTION		CARD READER CTRL / 1394 CONN / SD CONN	
SIZE A3		DOC NUMBER 1310xxxxx-0-0	
CODE CS		REV X01	

CHANGE by XXX DATE 21-OCT-2002

SHEET 65 of 73

LID SWITCH DB

REFERENCE NUMBER:9200~9299

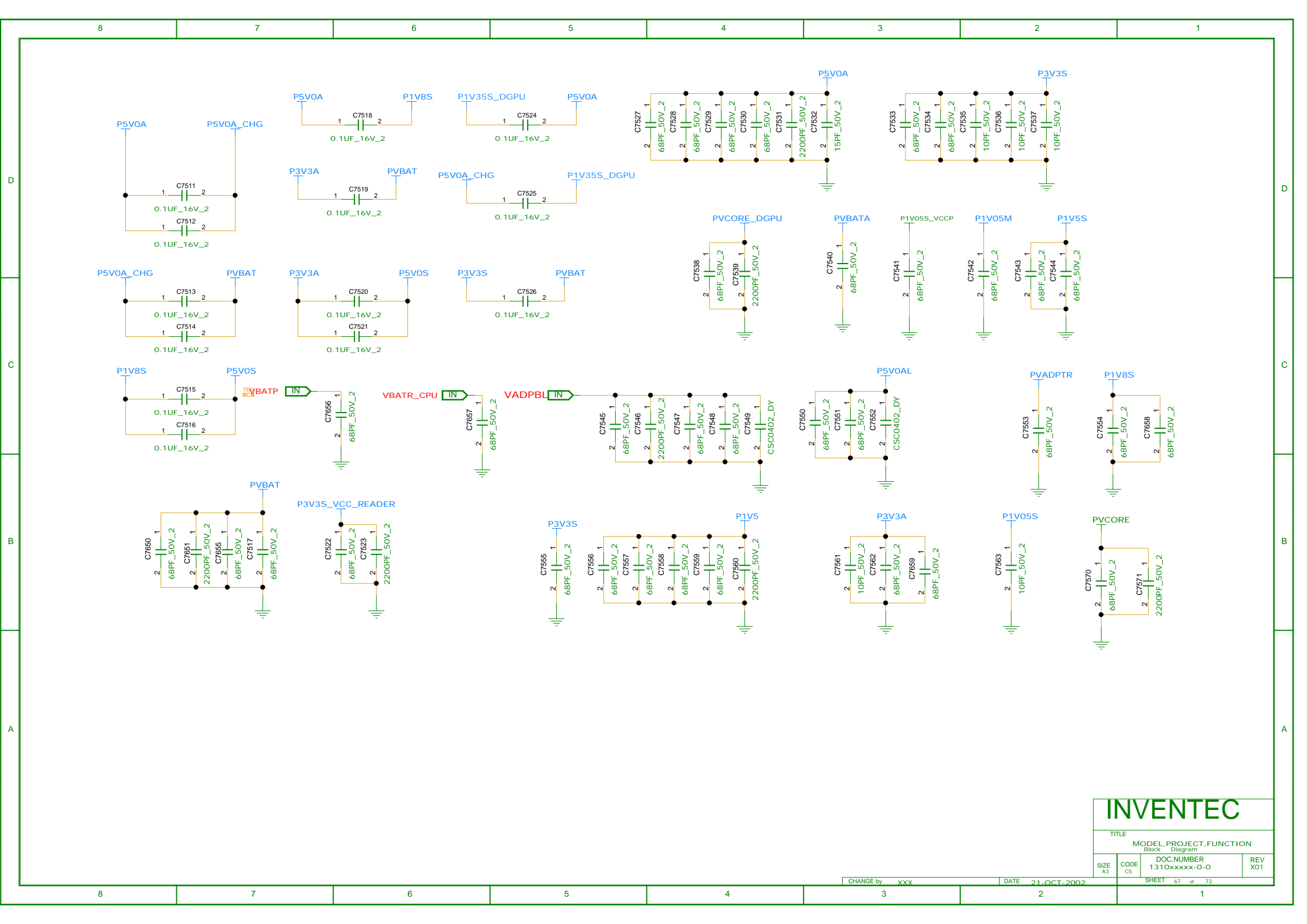


INVENTEC

TITLE
MODEL PROJECT,FUNCTION
LID SWITCH

SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
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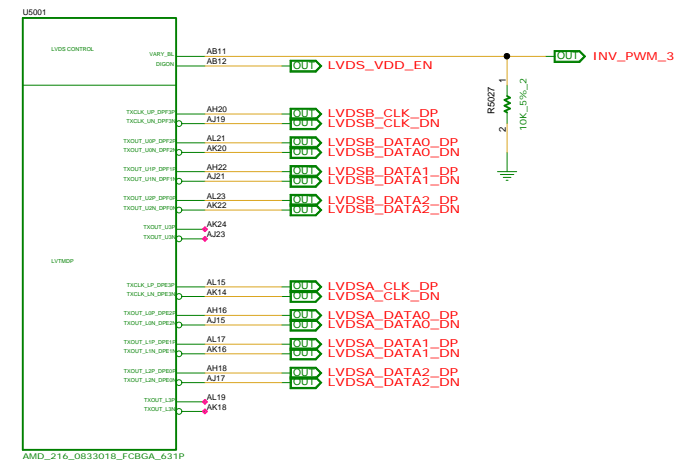
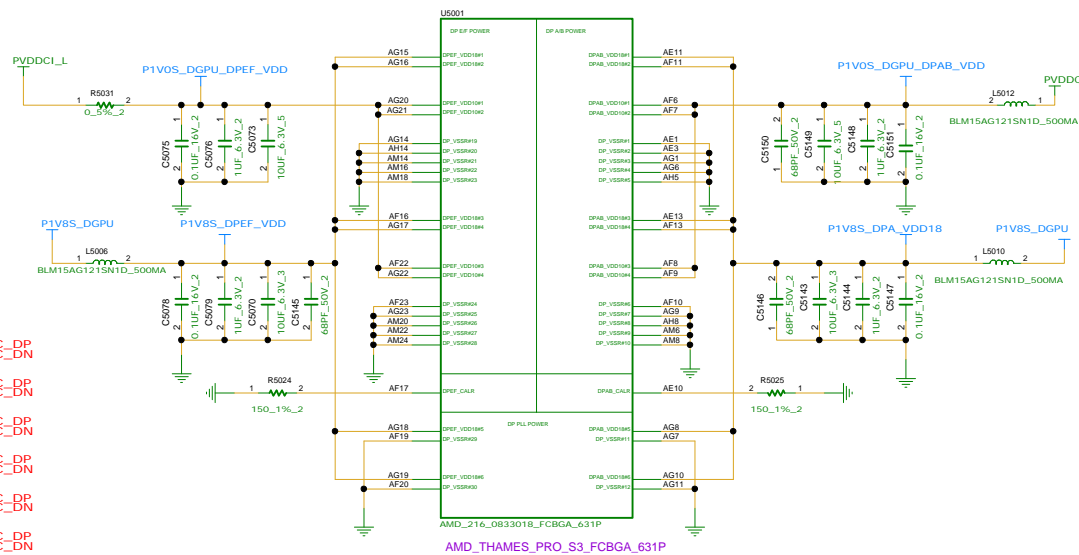


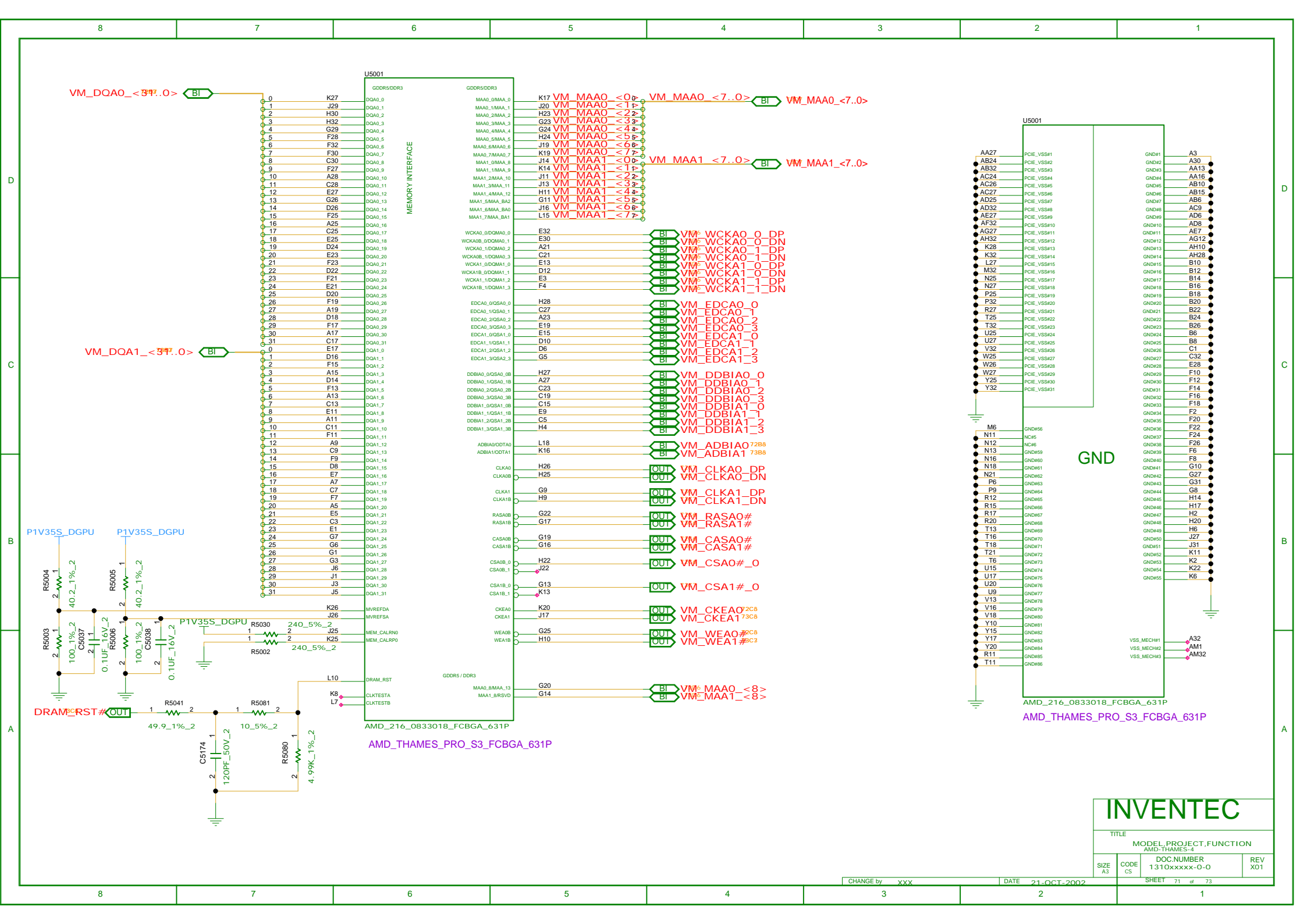
INVENTEC

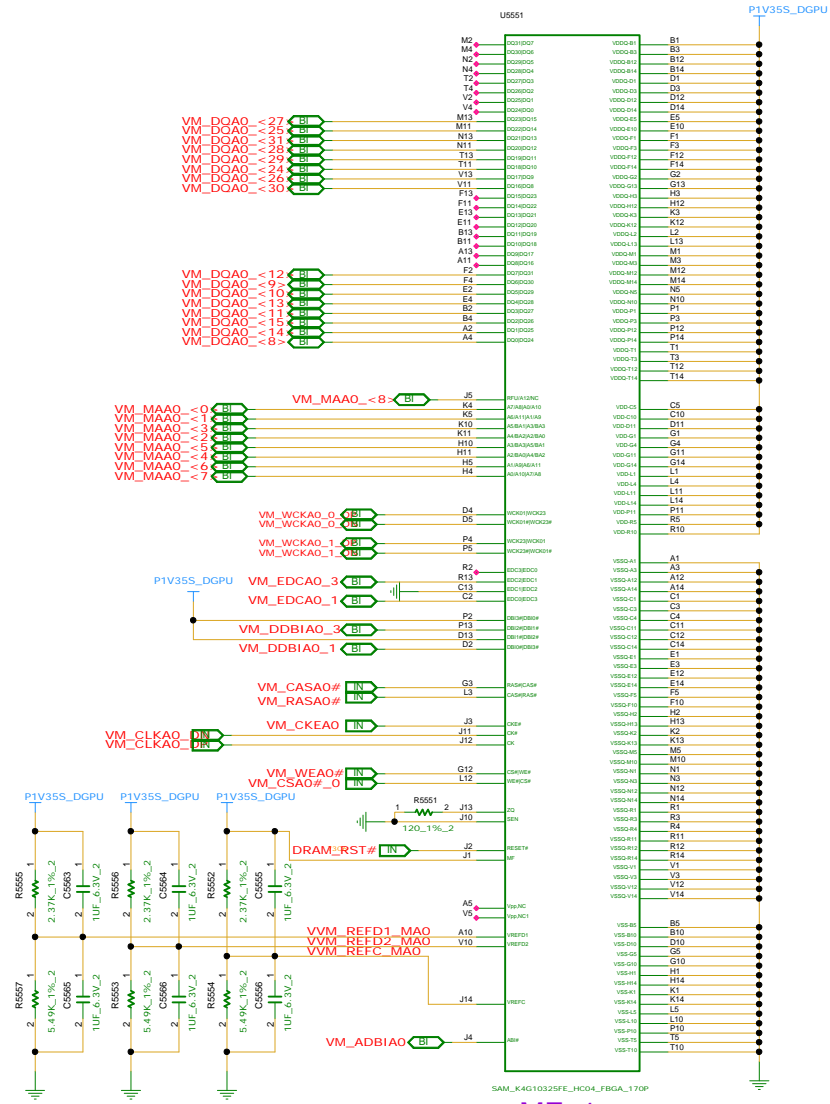
TITLE			
MODEL, PROJECT, FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01

CHANGE by XXX DATE 21-OCT-2002

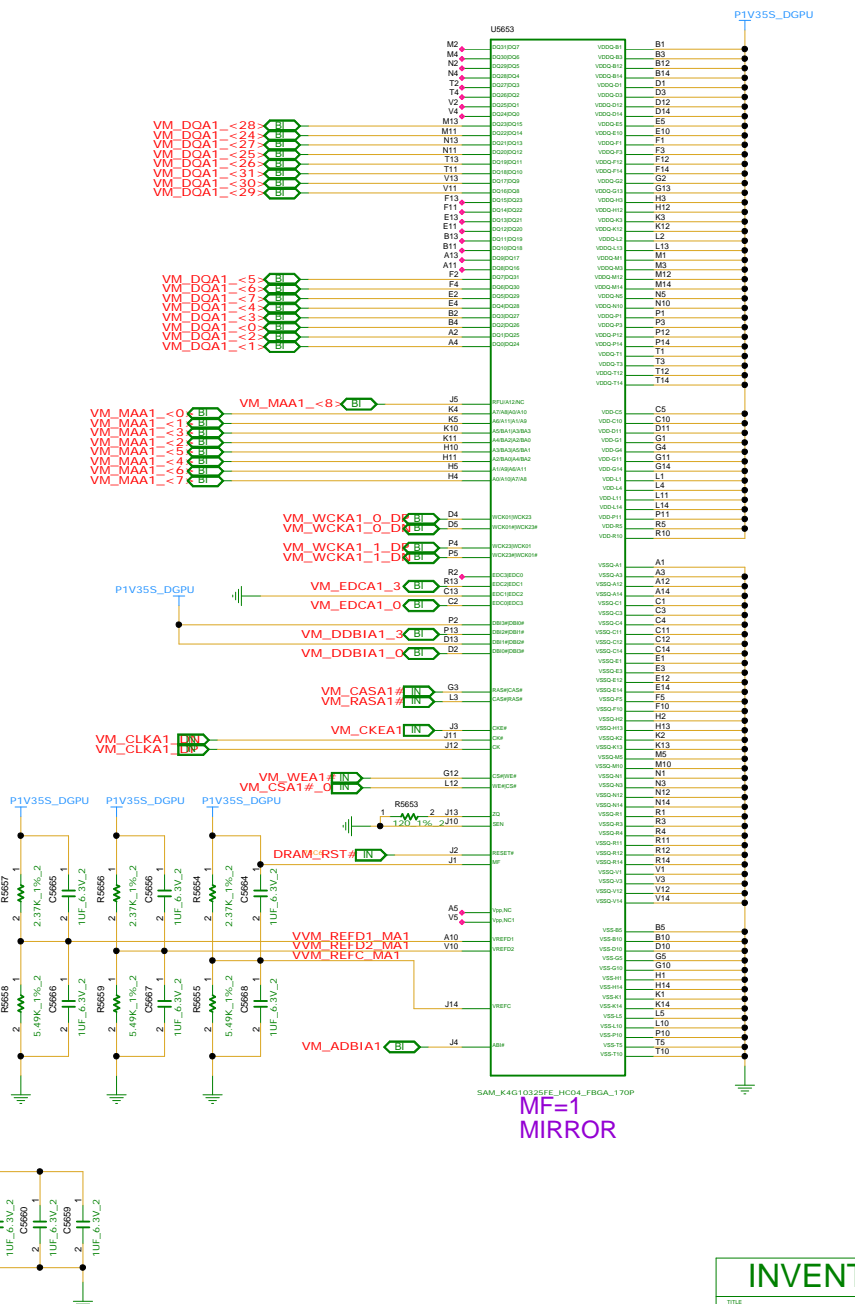
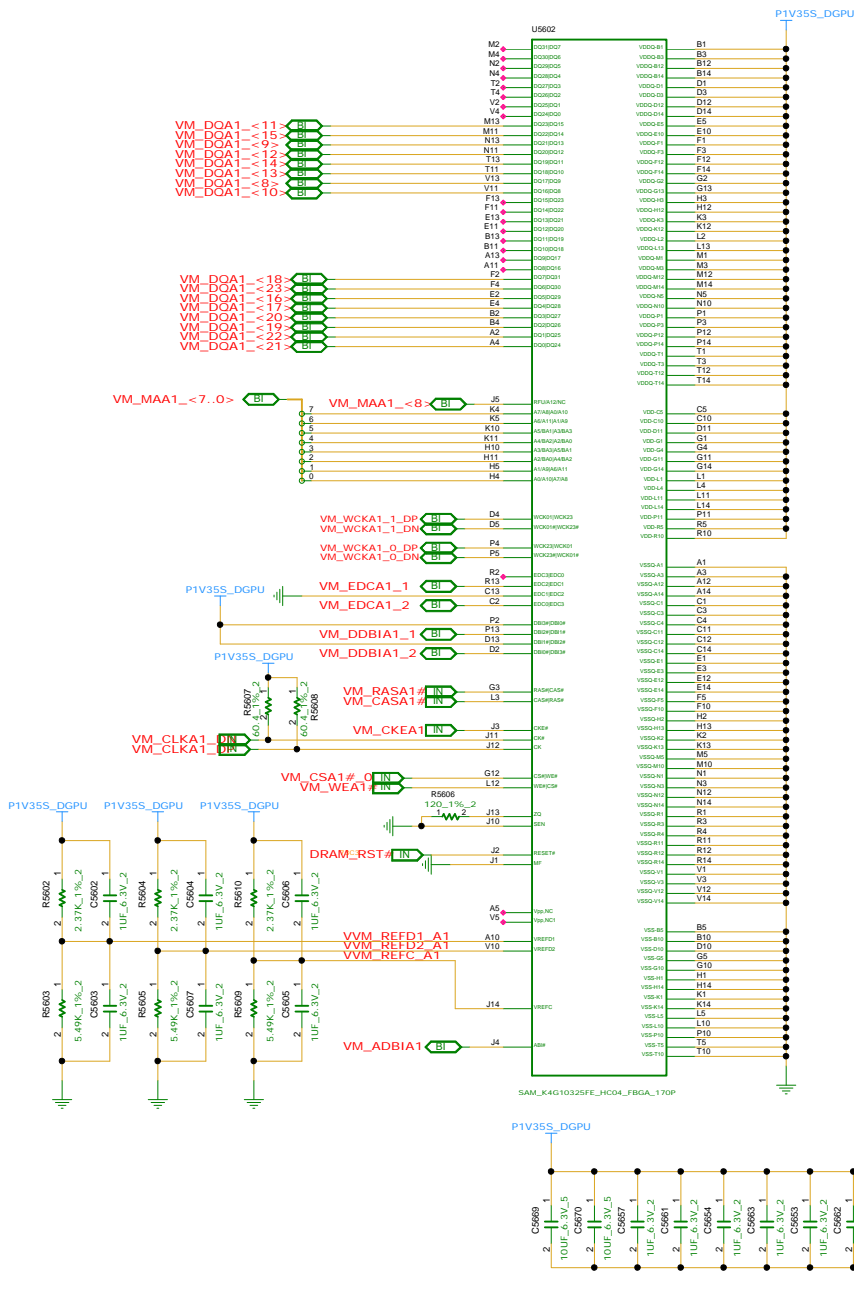
SHEET 67 of 73







TITLE			
MODEL,PROJECT,FUNCTION VRAM-1			
SIZE C	CODE CS	DOC NUMBER 1310xxxxxx-0-0	REV X01
SHEET 72 of 73			



MF=1
MIRROR

INVENTEC			
TITLE			
MODEL,PROJECT,FUNCTION			
VRAM-2			
SIZE	CODE	DOC NUMBER	REV
E	CS	1310XXXXX-0-0	201
SHEET		23	of 23