

Compal Confidential

Schematics Document

Intel CLARKSFIELD/ARRANDALE
with IBEX PEAK-M core logic

Cartier DIS

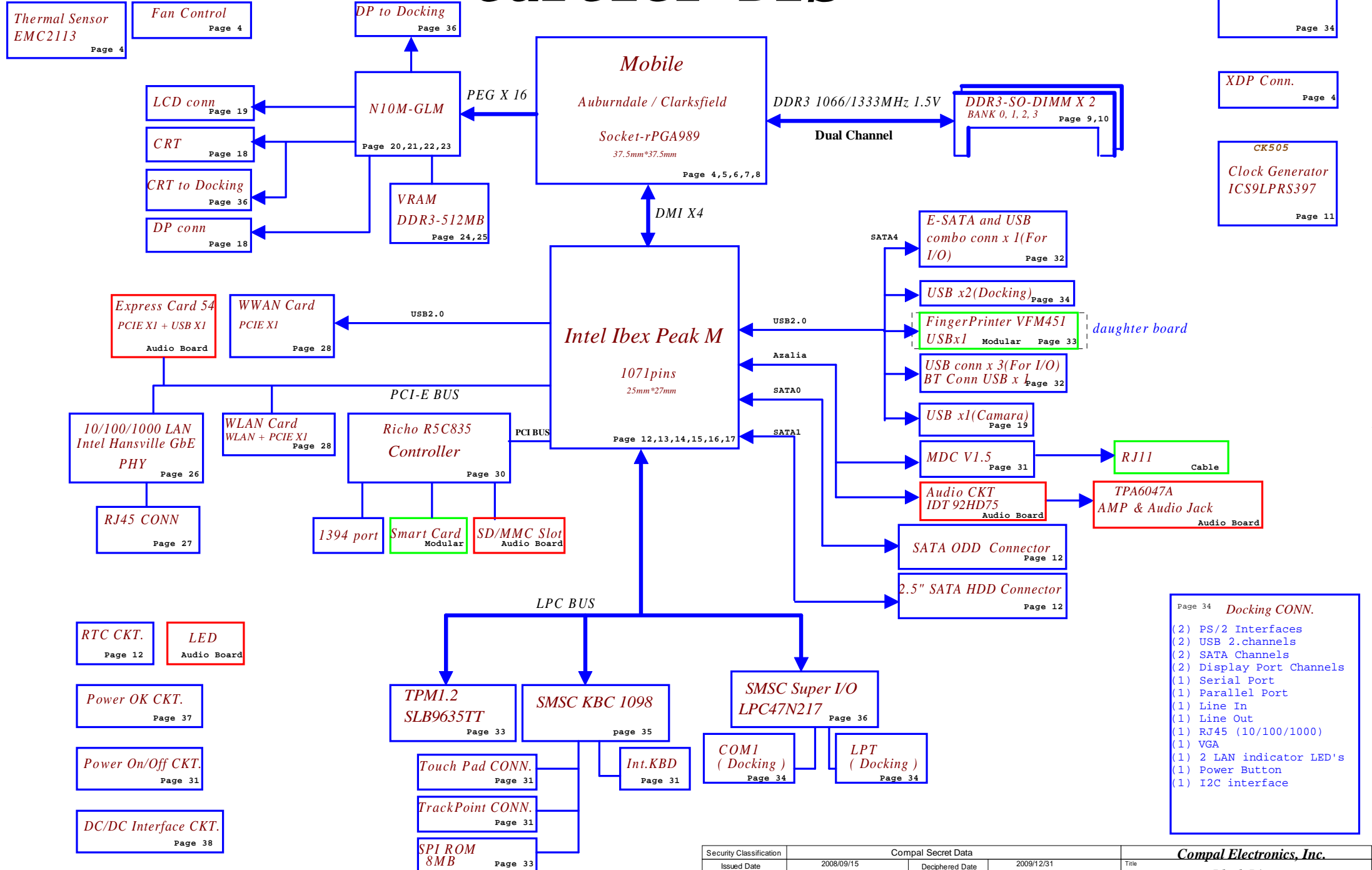
LA-4901P

2009-12-01

REV:1.0

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Cartier DIS



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Voltage Rails (O MEANS ON X MEANS OFF)

<div>power plane</div> <div>State</div>	+RTCVCC	+B +3VL	+5VALW +3VALW	+3VM +1.05VM	+1.5V +0.75V	+5VS +3VS +1.5VS +VGA_CORE +VCCP +CPU_CORE +1.05VS +1.8VS
s0	O	O	O	O	O	O
s1	O	O	O	O	O	O
s3	O	O	O	O	O	X
s5 S4/AC	O	O	O	O	X	X
s5 S4/ Battery only	O	O	X	X	X	X
s5 S4/AC & Battery don't exist	O	X	X	X	X	X

Symbol Note :



: means Digital Ground



: means Analog Ground

Install below 45 level BOM structure

45@ : means just put it in the BOM of 45 level.

Install below 43 level BOM structure

@ : means just reserve , no build

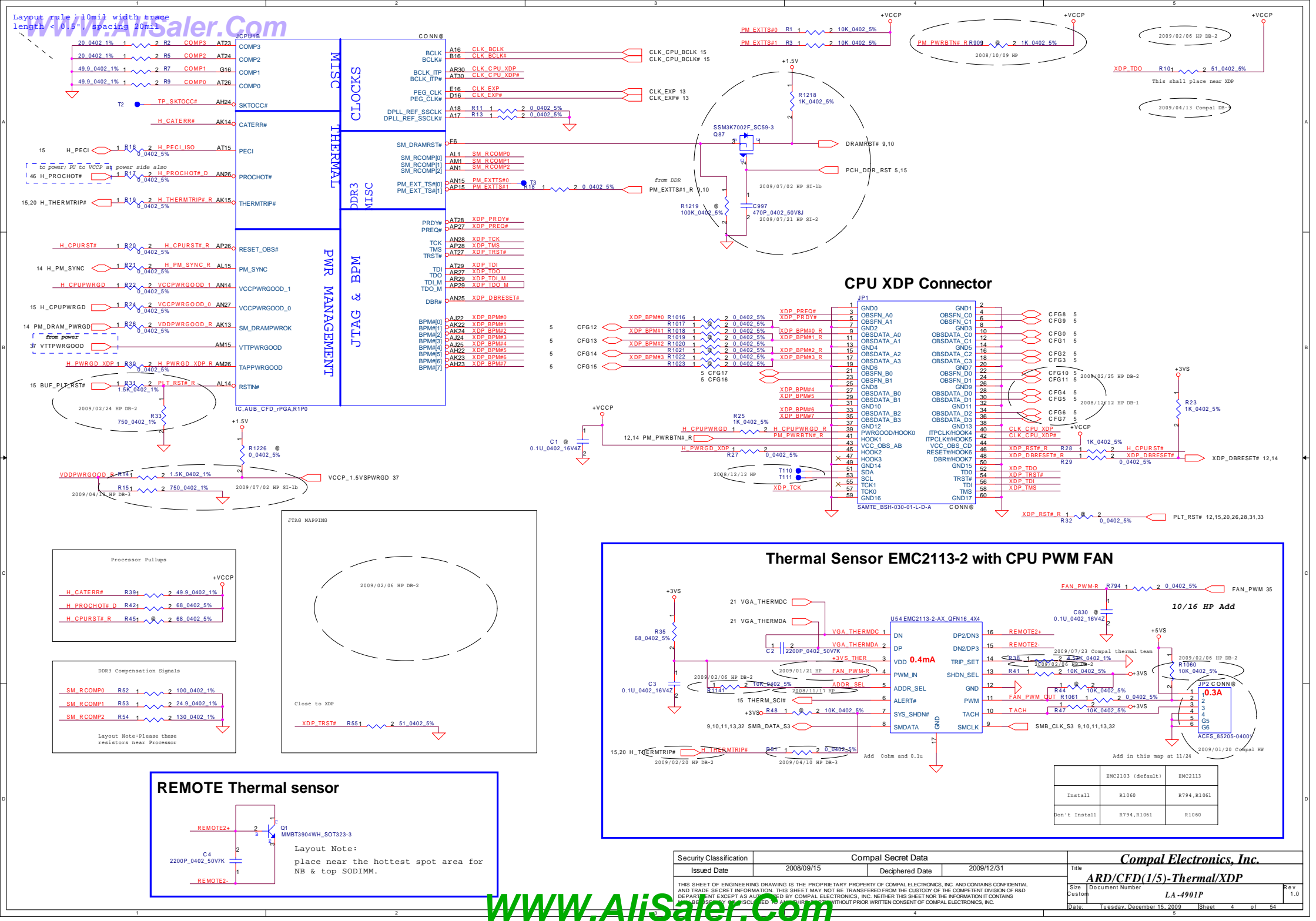
CONN@ : means ME part.

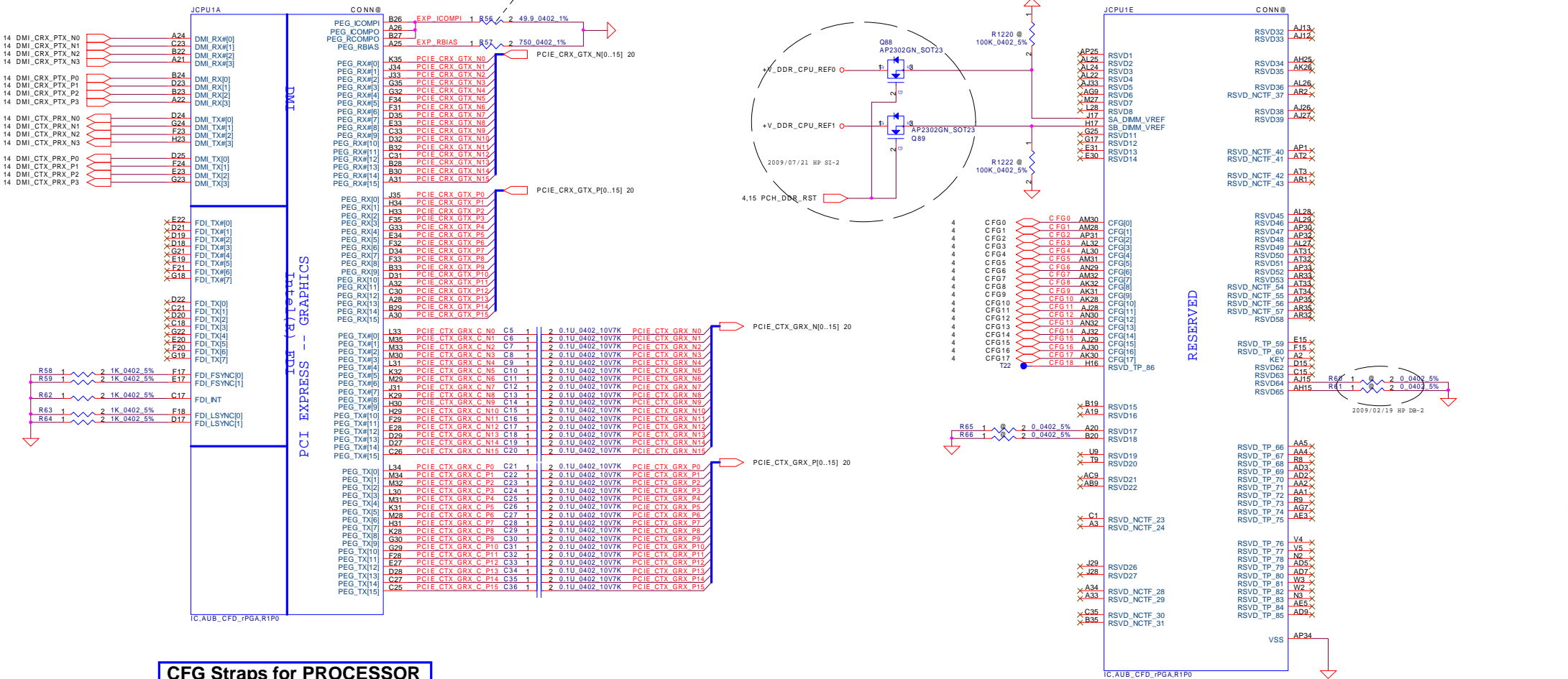
VRAM@ : means VRAM strip pin part.

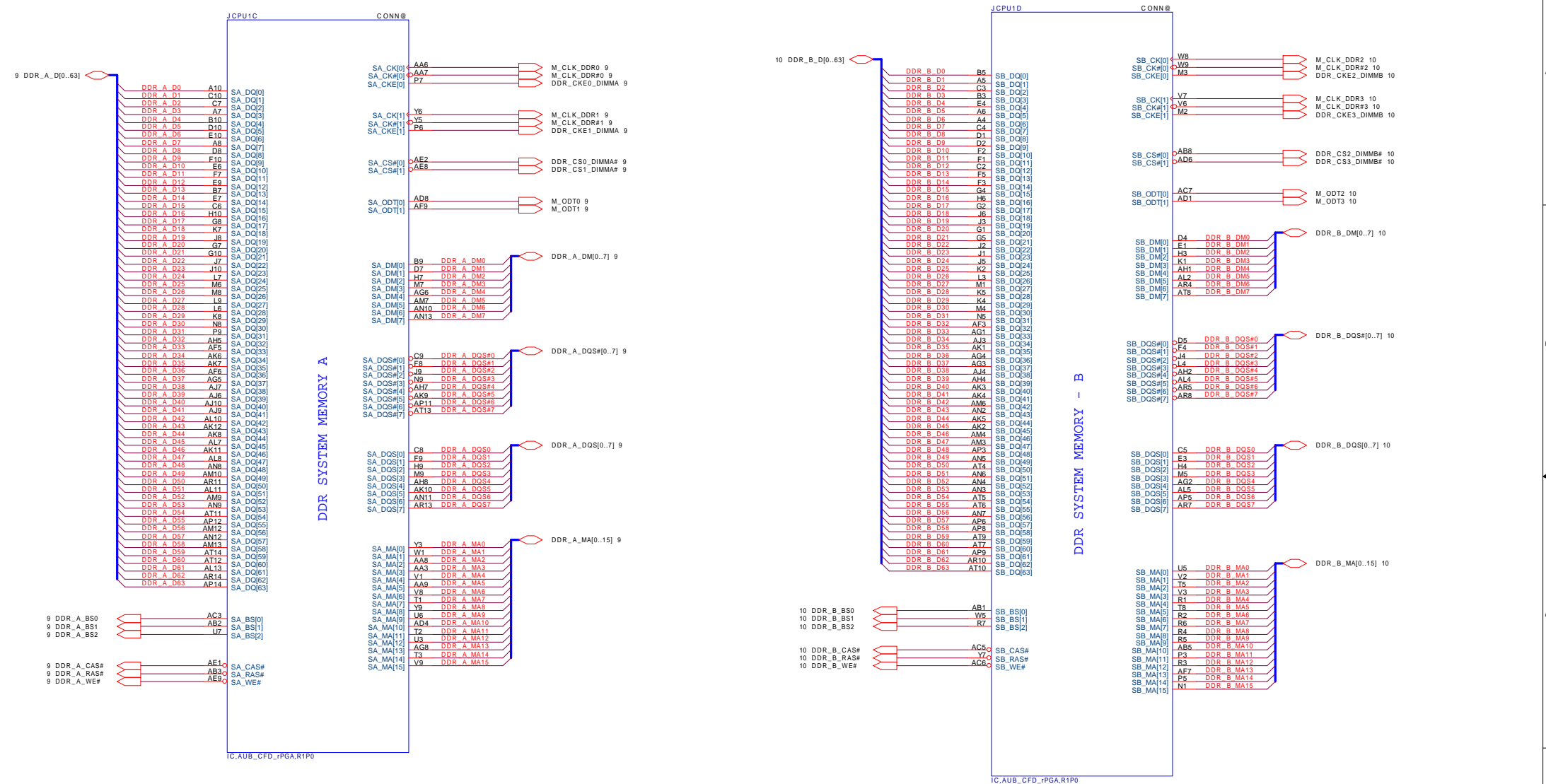
SMBUS Control Table

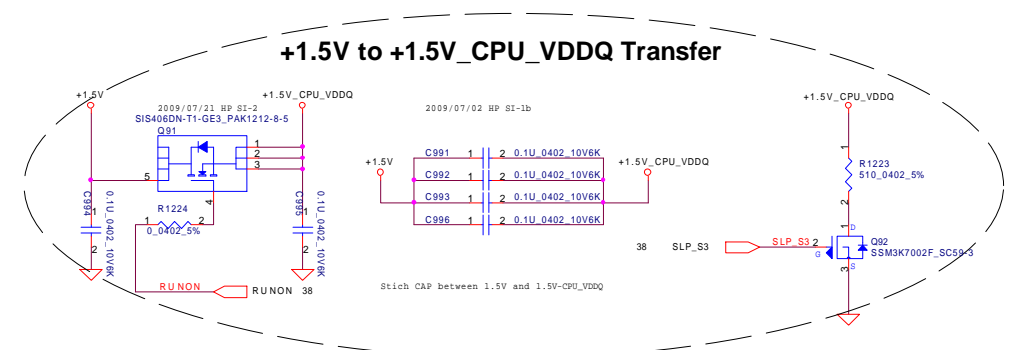
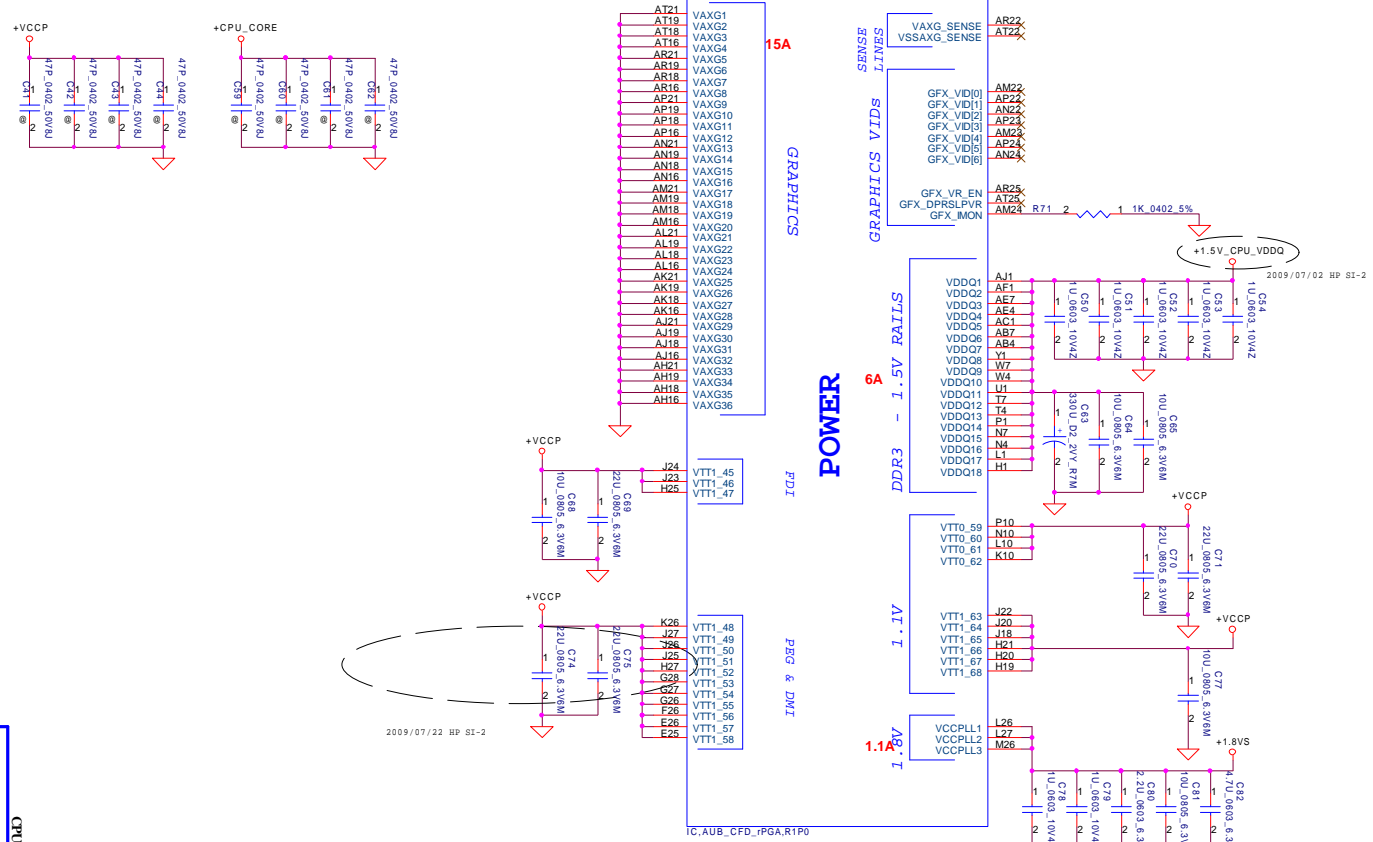
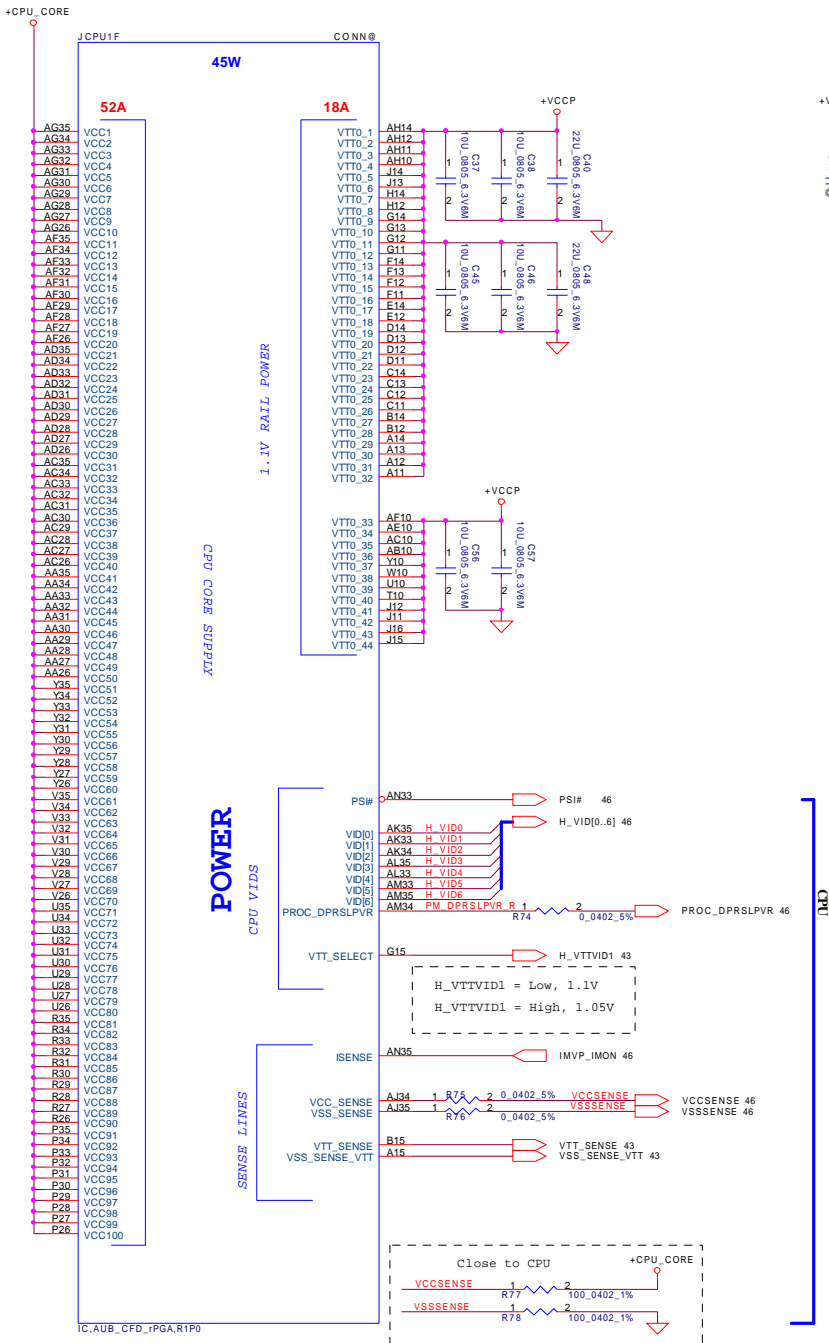
	SOURCE	BATT	XDP	SODIMM	CLK CHIP	MINI CARD	DOCK	NIC	THERMAL SENSOR	G-SENSOR
SMB_EC_CK1 SMB_EC_DA1	SMSC1098	V	X	X	X	X	X	X	X	X
SMBCLK SMBDATA	Calpella	X	V	V	V	V	V	X	X	V
SML0CLK SML0DATA	Calpella	X	X	X	X	X	X	V	X	X
SML1CLK SML1DATA	Calpella	X	X	X	X	X	X	X	V	X

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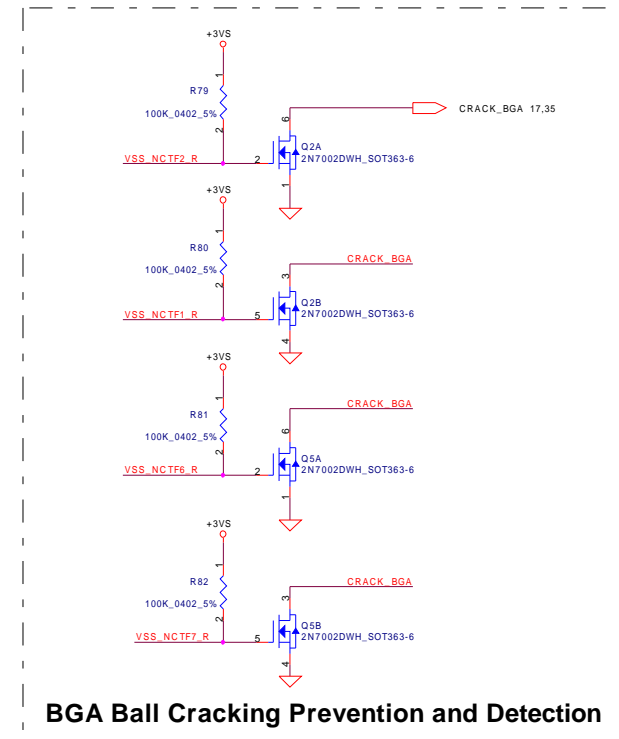
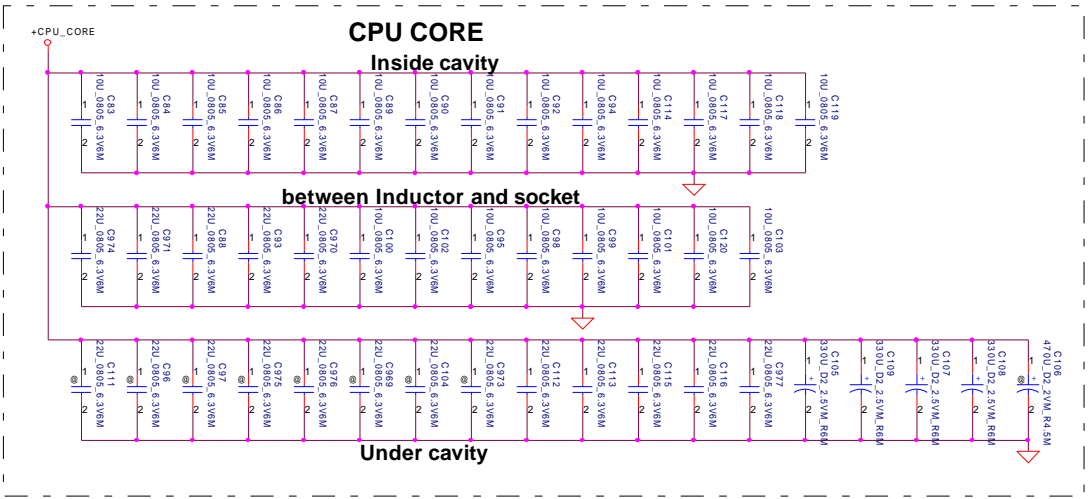
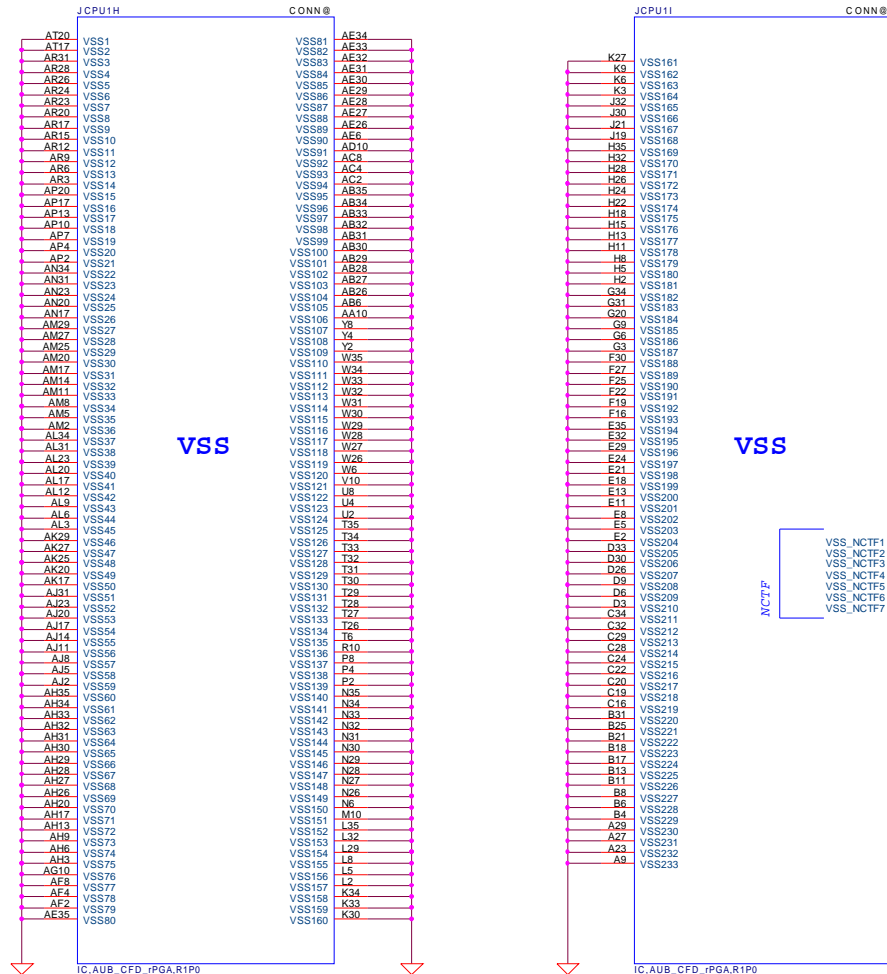




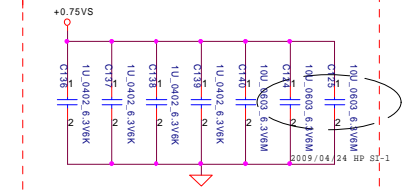




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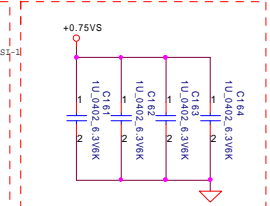
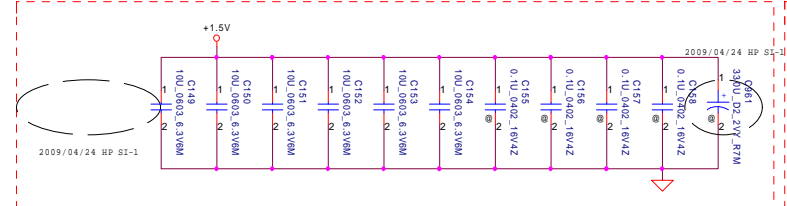
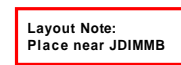
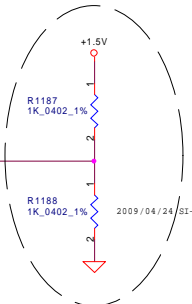
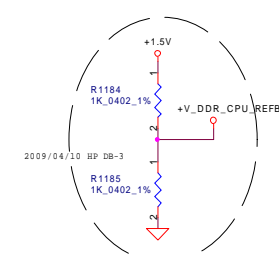
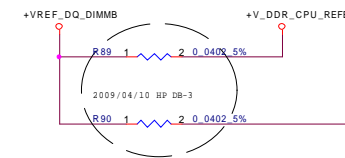
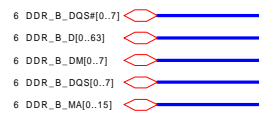


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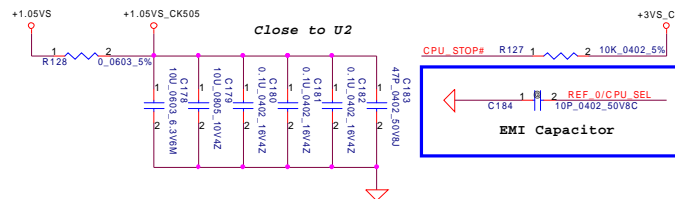
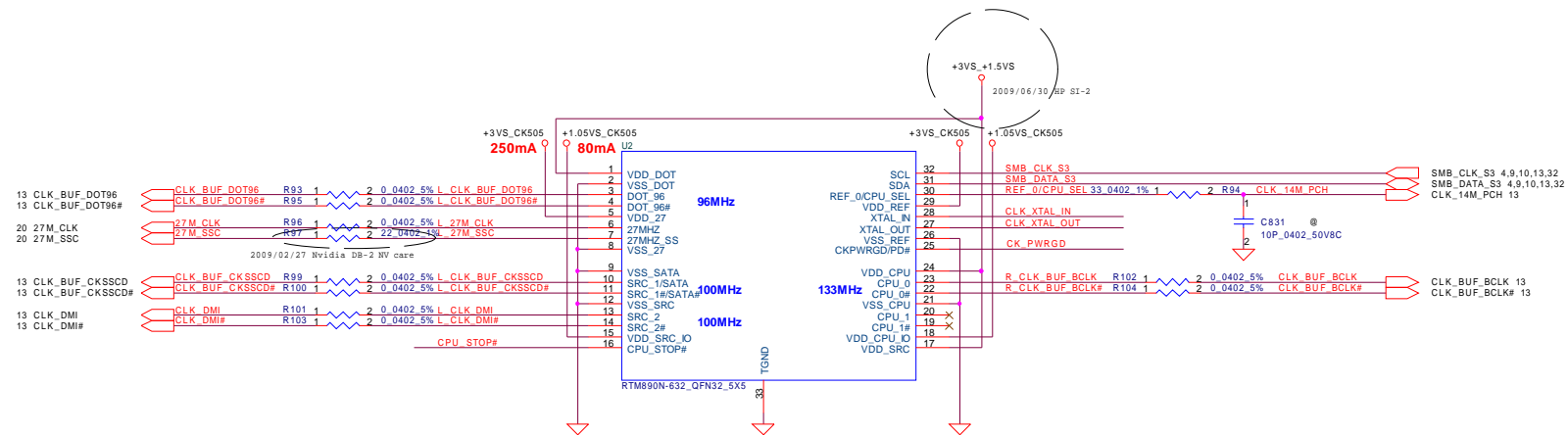
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				Custom	LA-4901P	1.0
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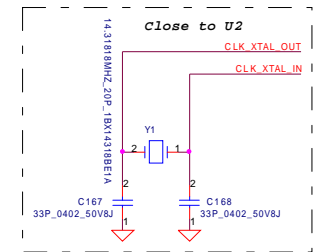
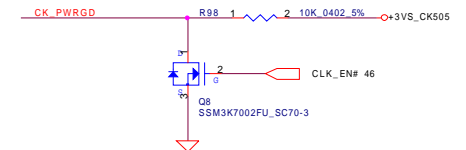
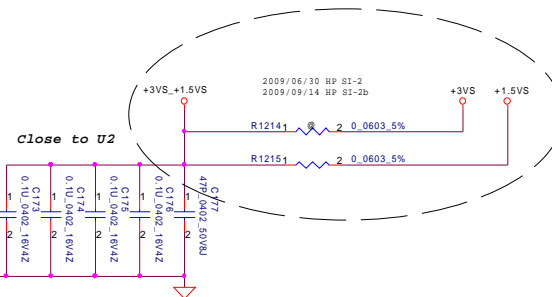
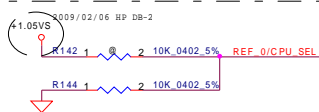


BOT SLOT

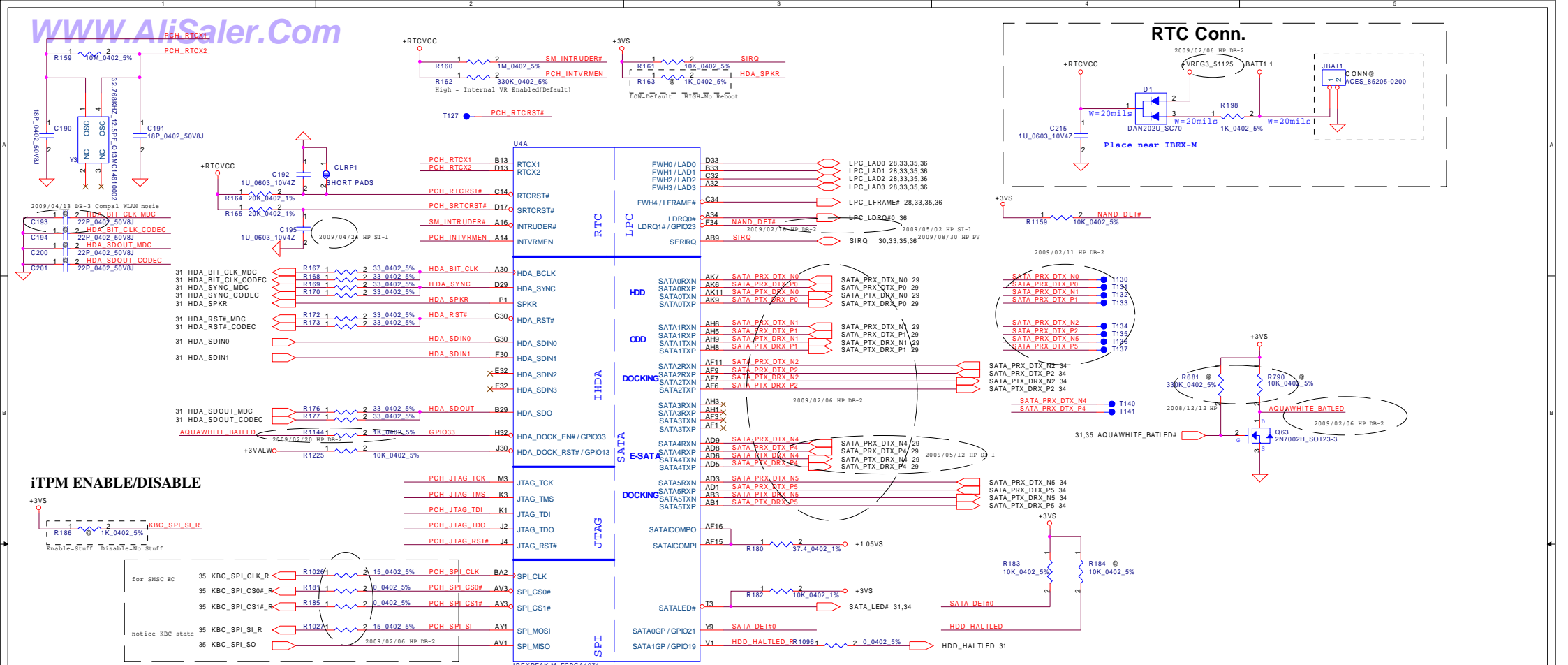
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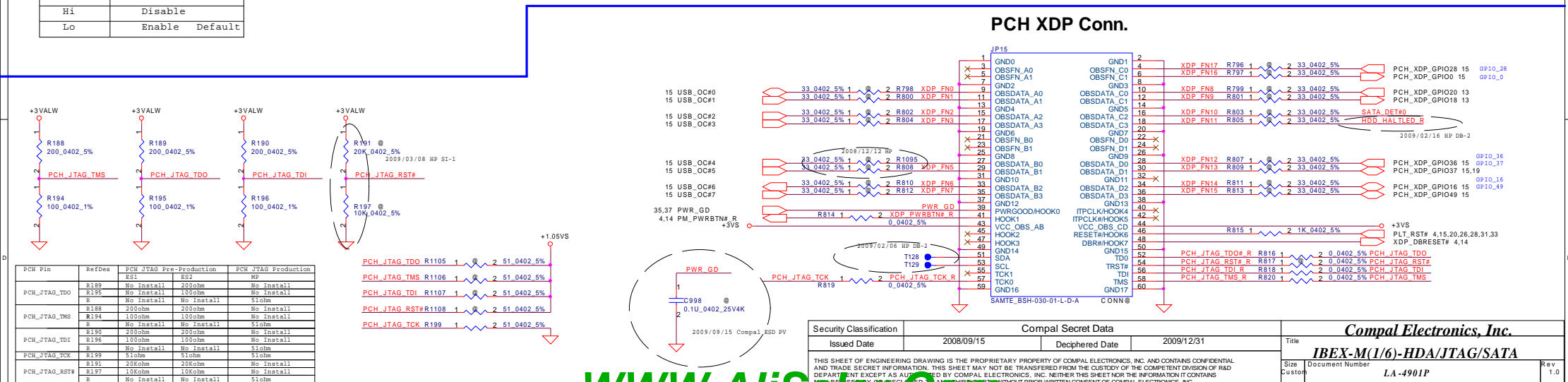
PIN 30	CPU_0	CPU_1
0 (Default)	133MHz	133MHz
1	100MHz	100MHz



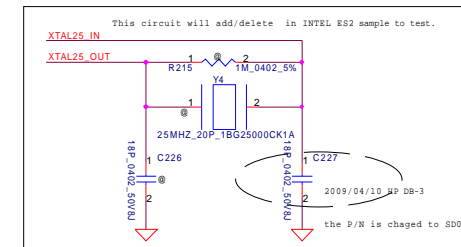
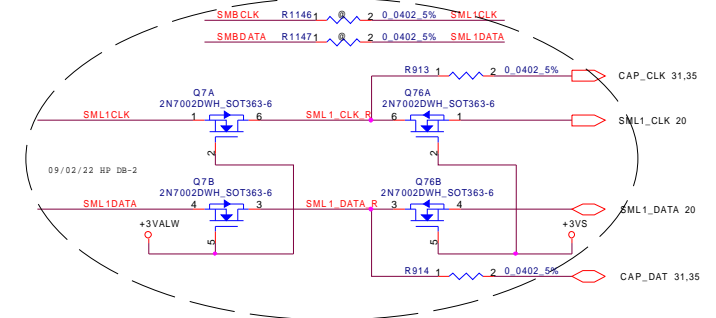
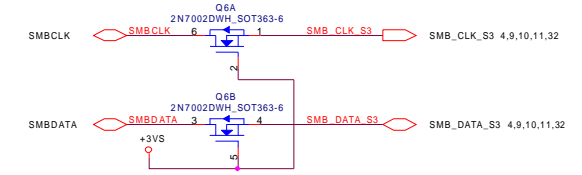
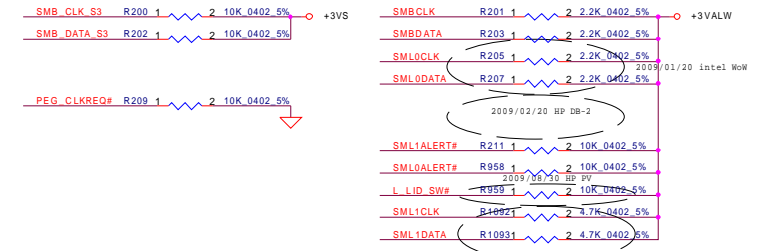
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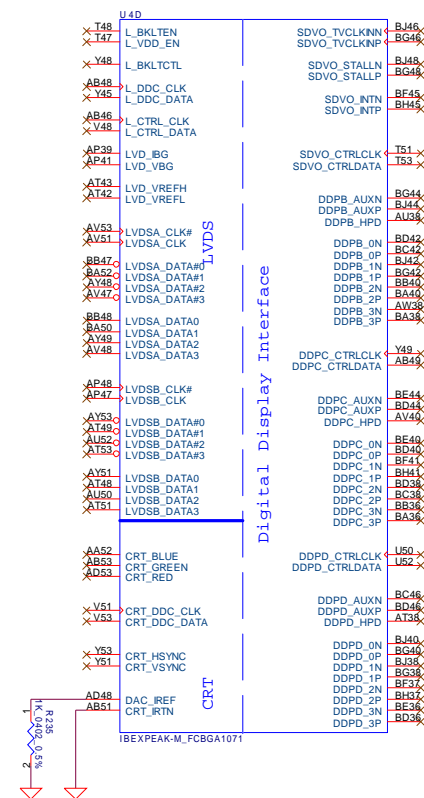
GPIO33	iAMT Enable /Disable
Hi	Disable
Lo	Enable Default



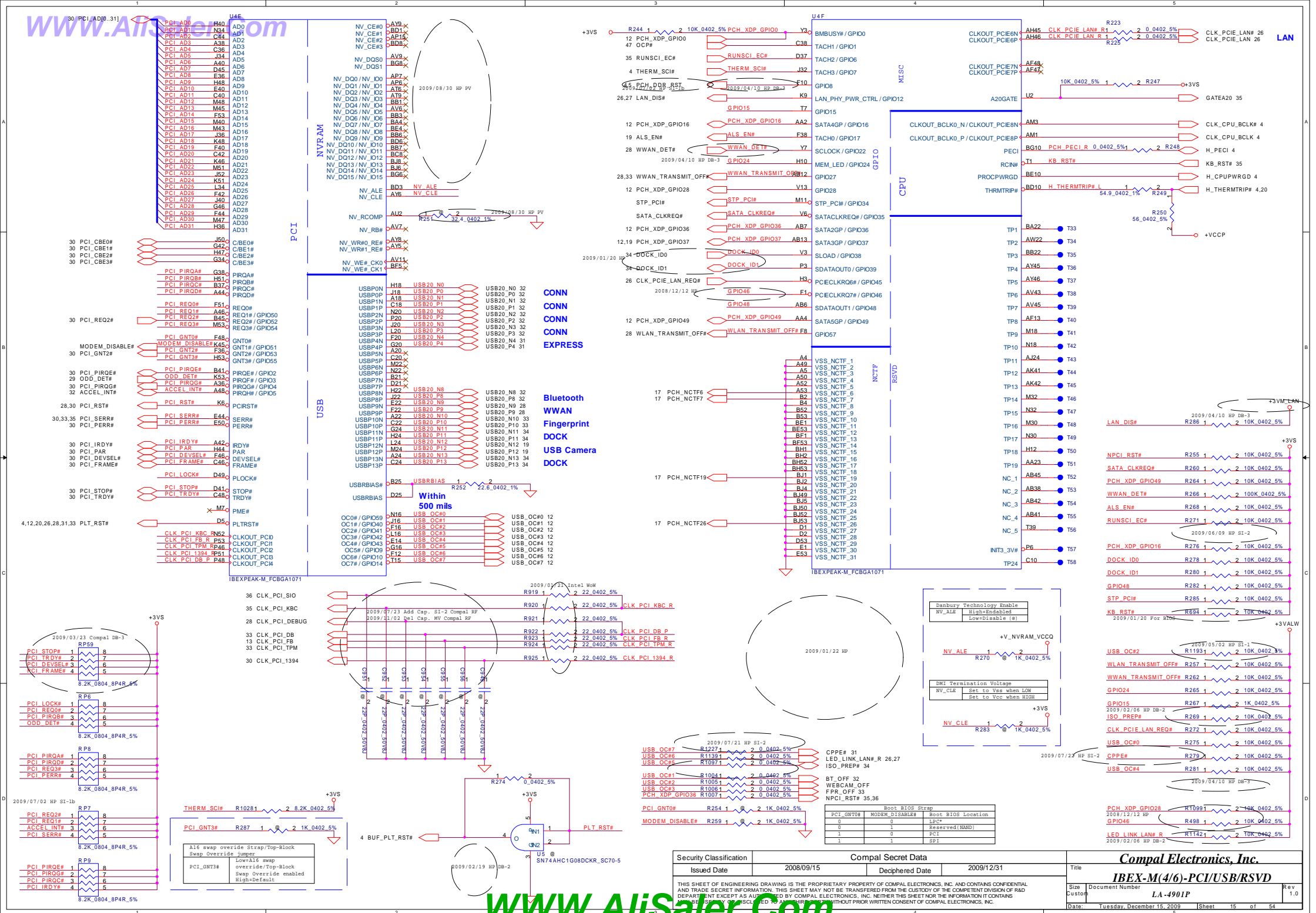
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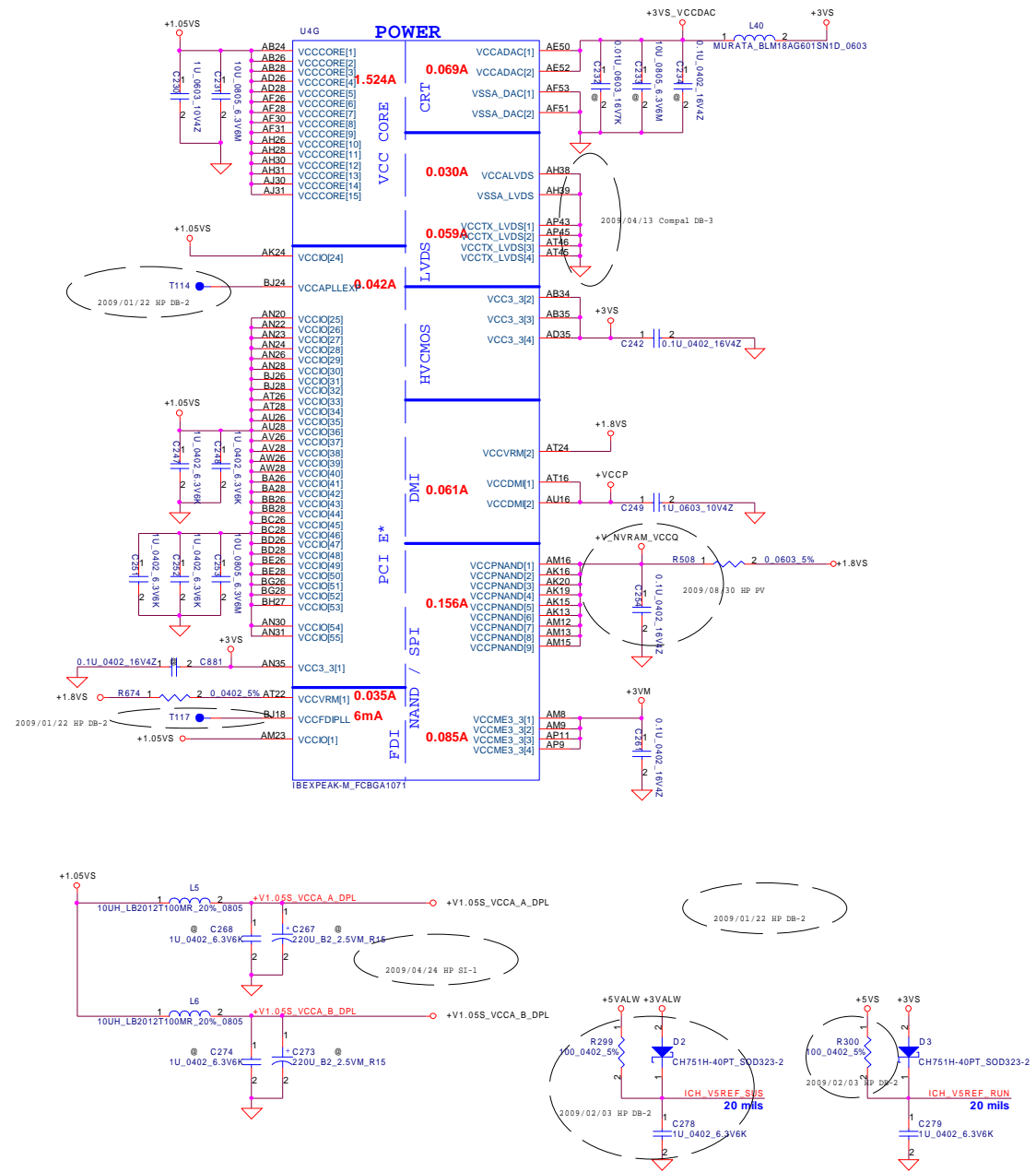
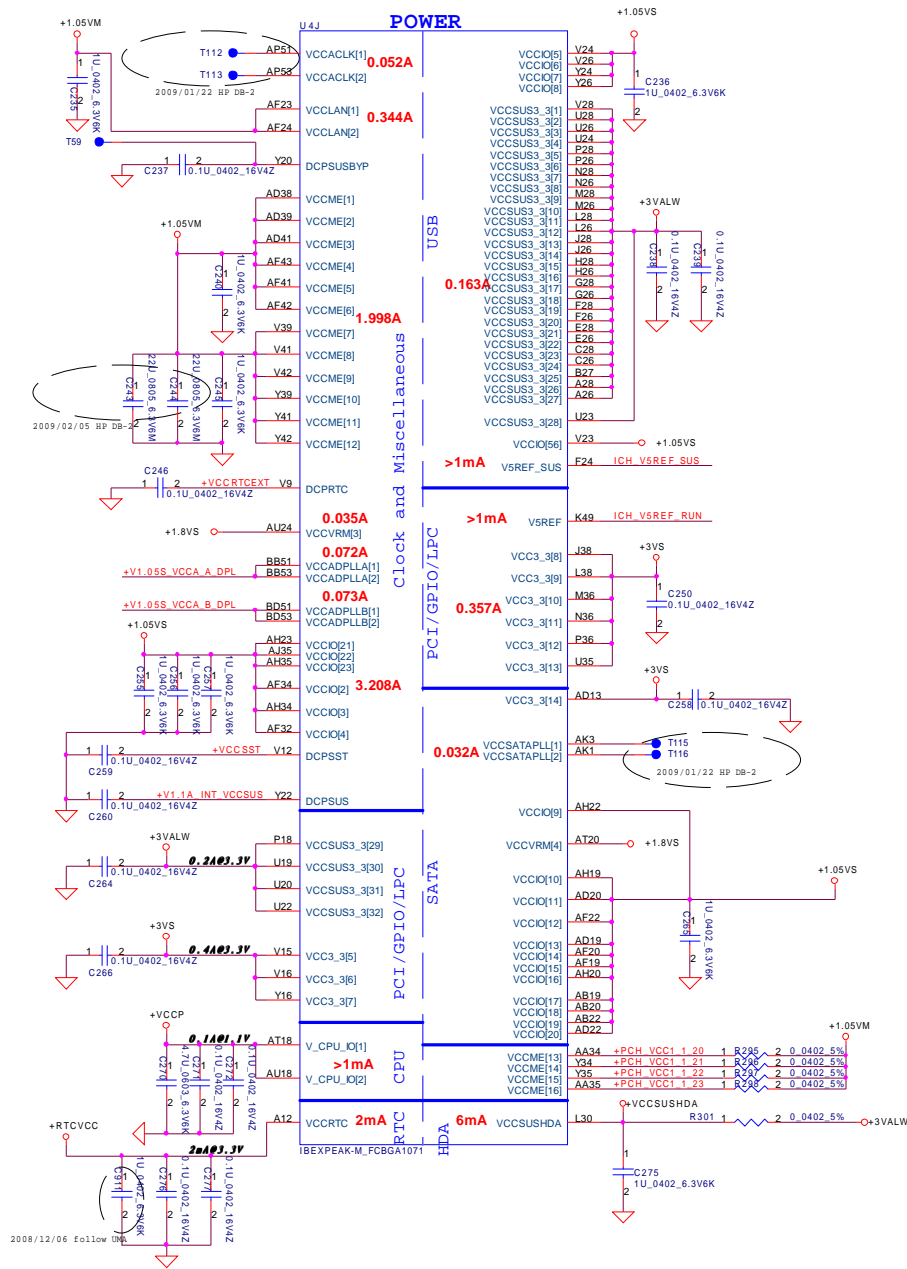


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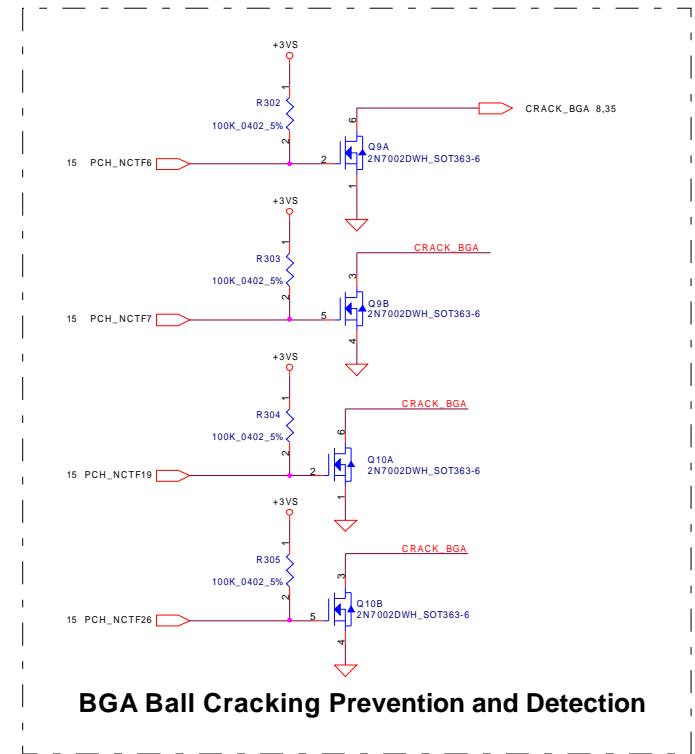
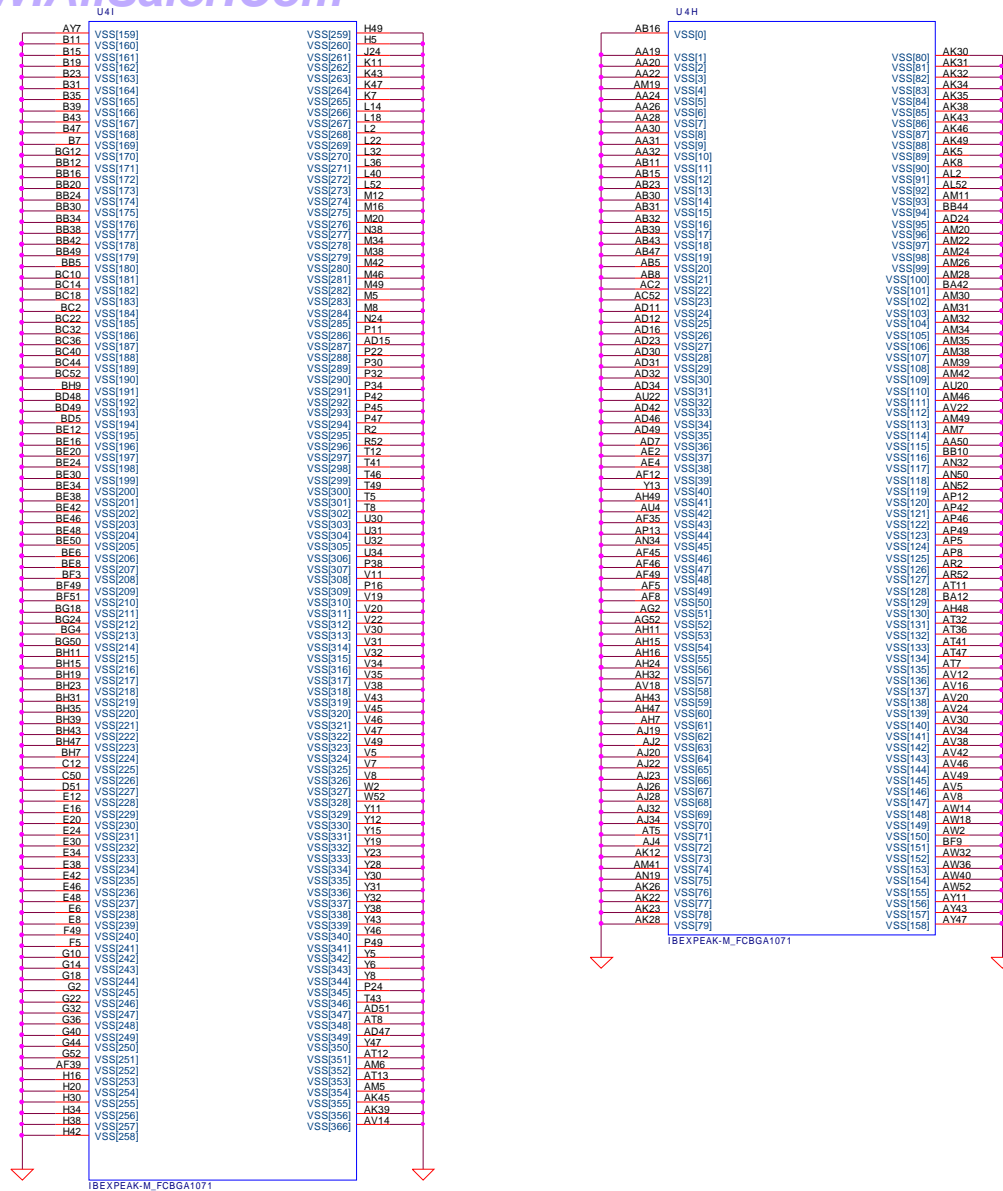


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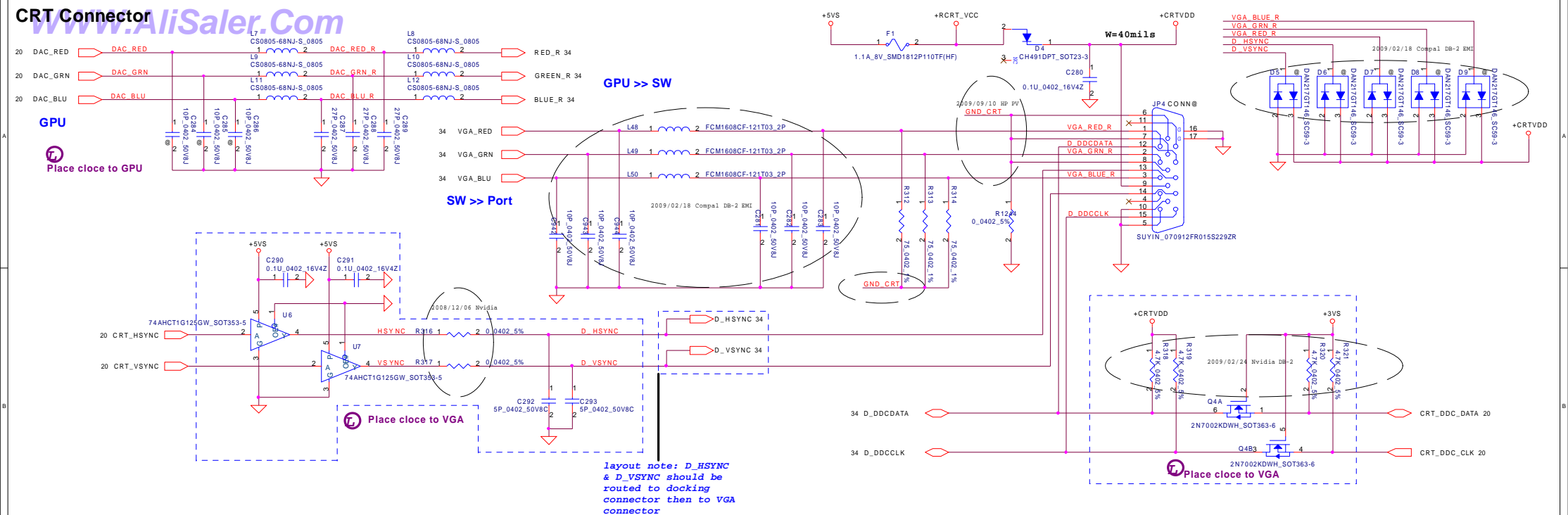
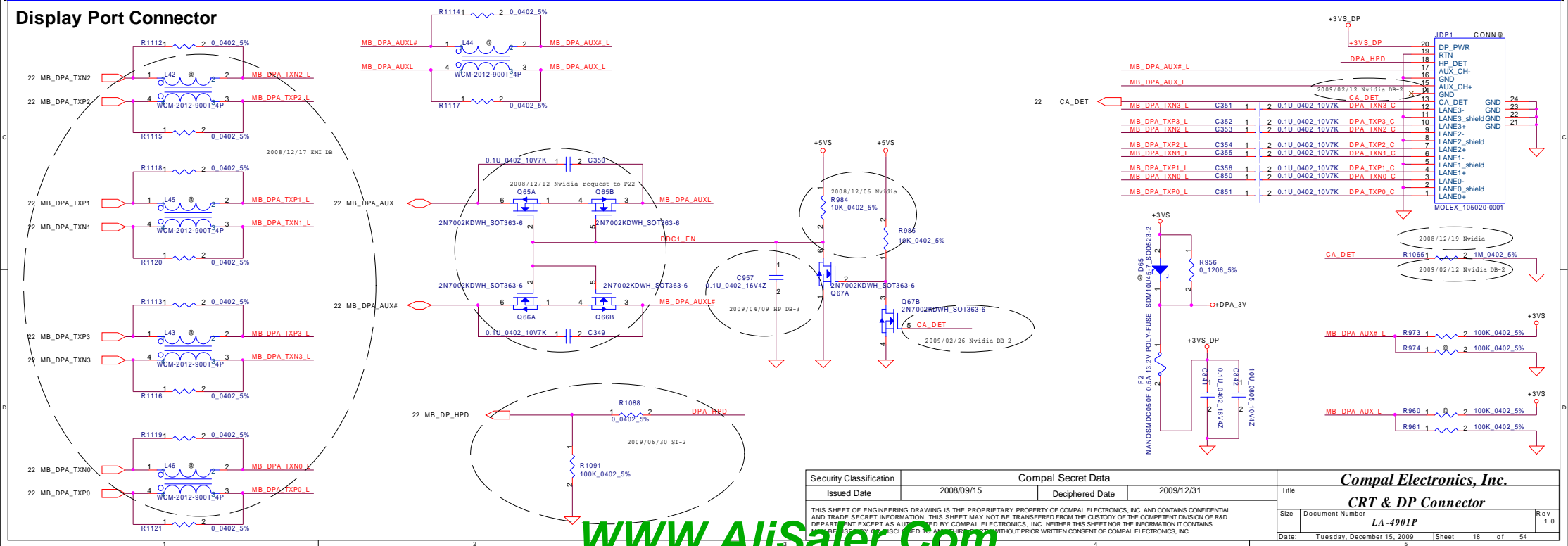


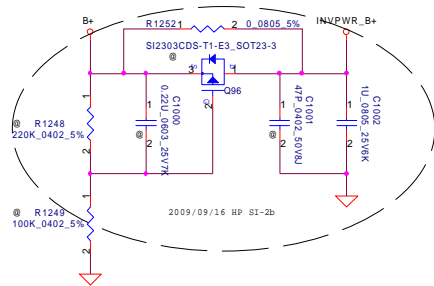
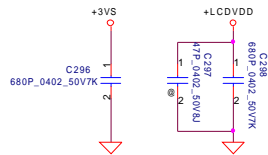


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				Size	Document Number	Rev
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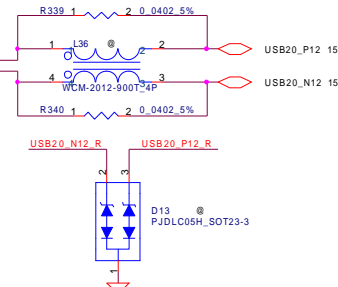
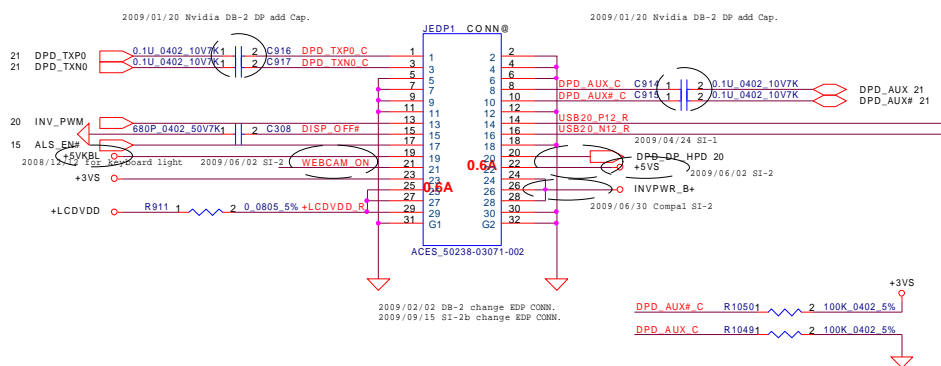
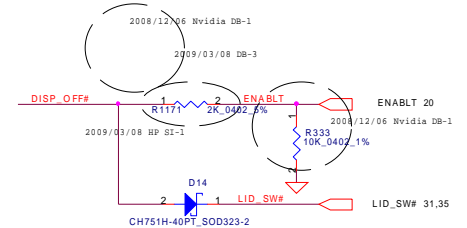
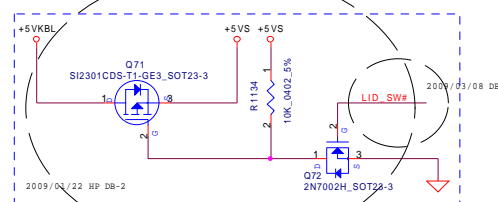


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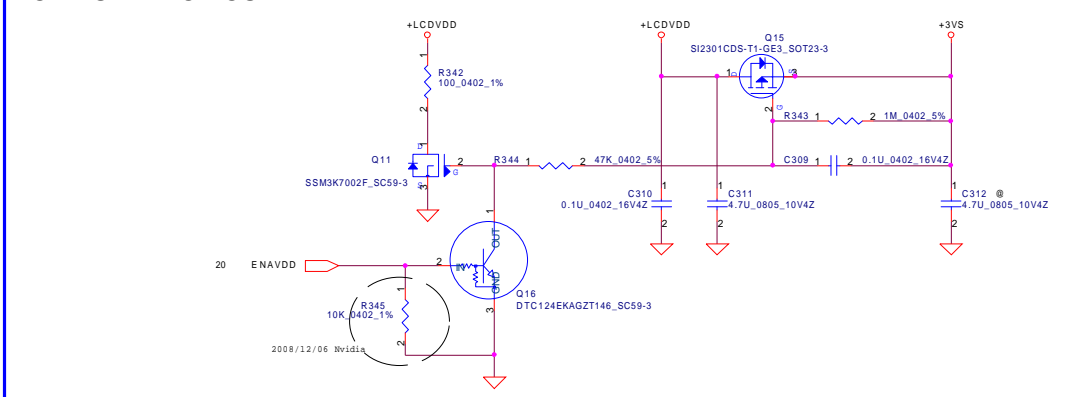
**Display Port Connector**



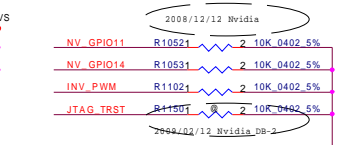
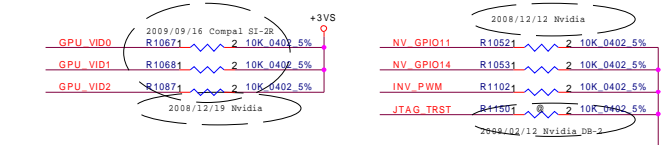
Key_Board_Light power Control



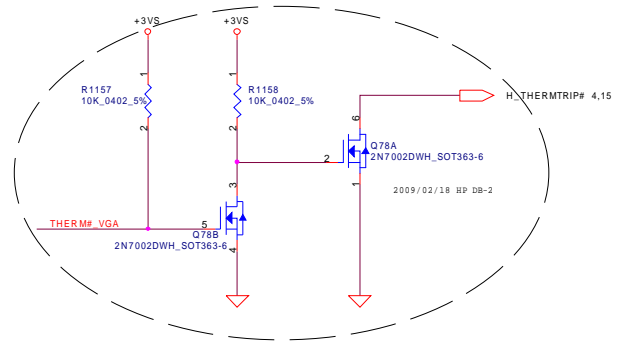
LCD POWER CIRCUIT



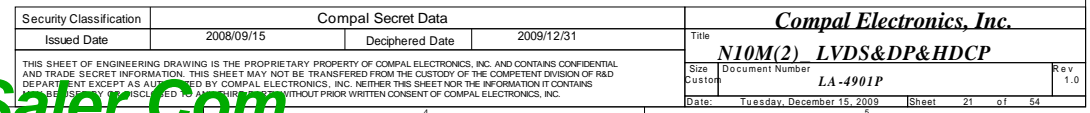
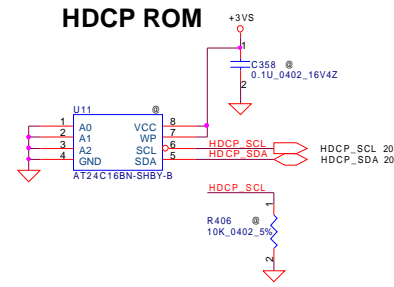
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
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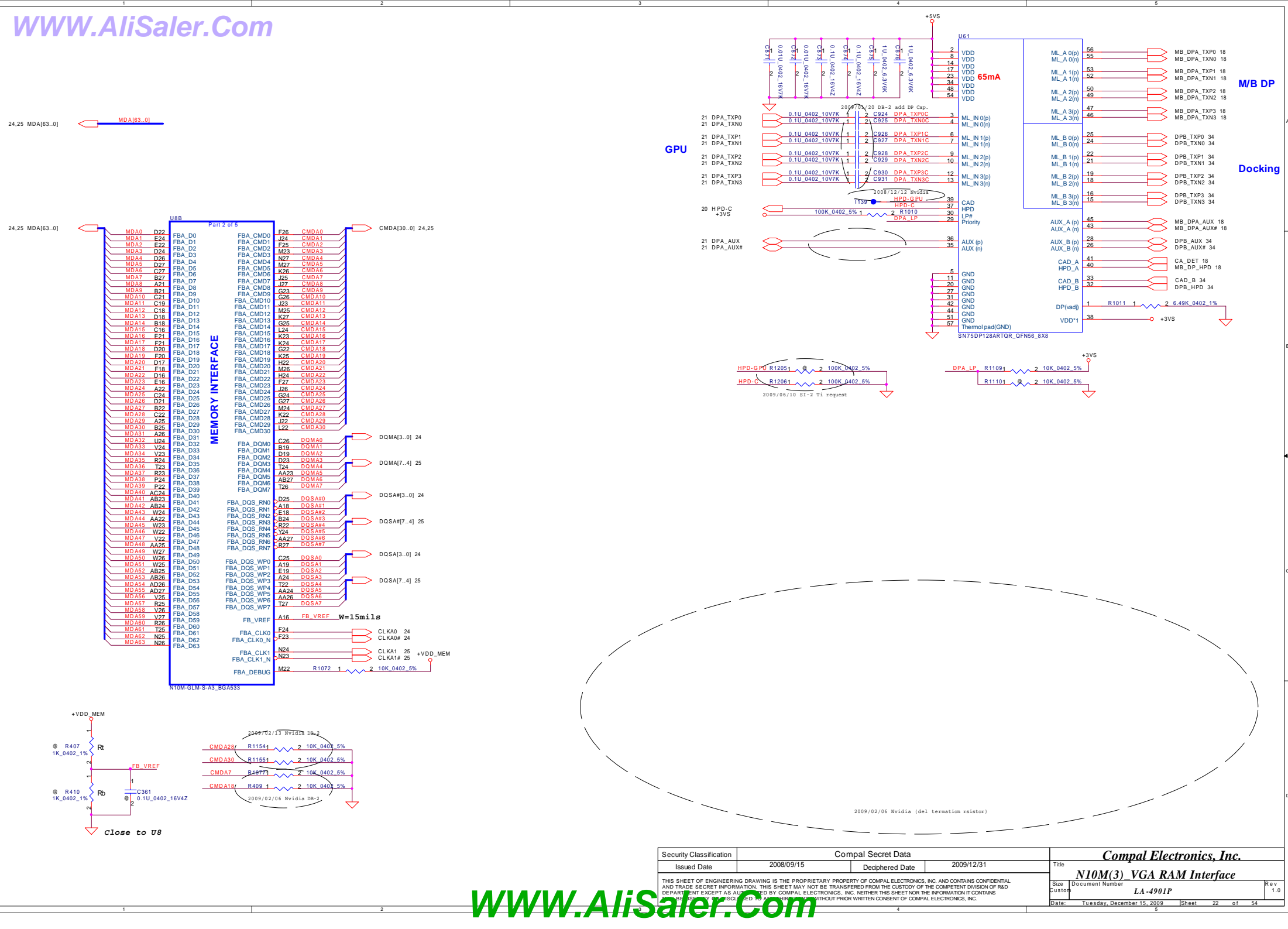
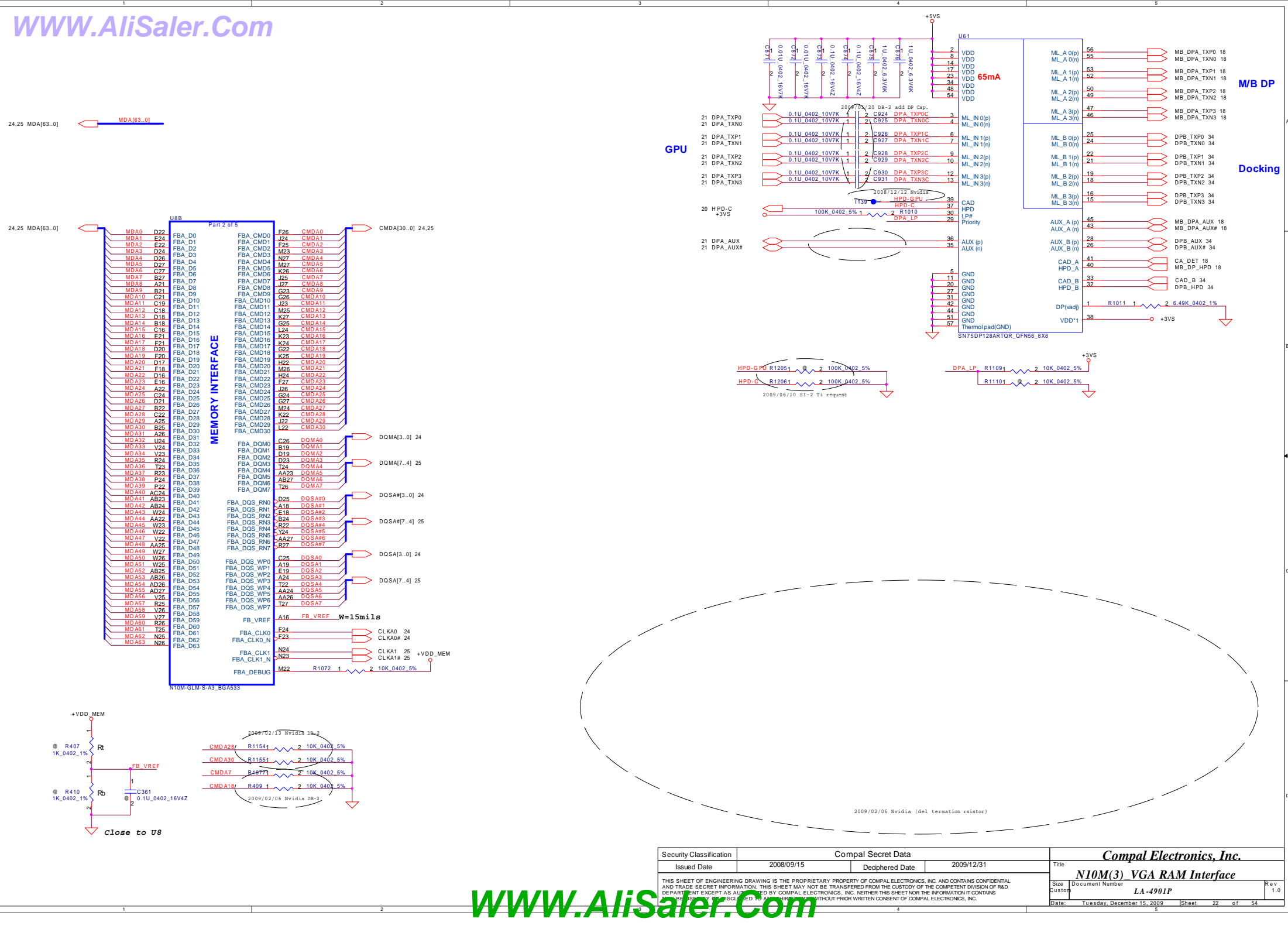


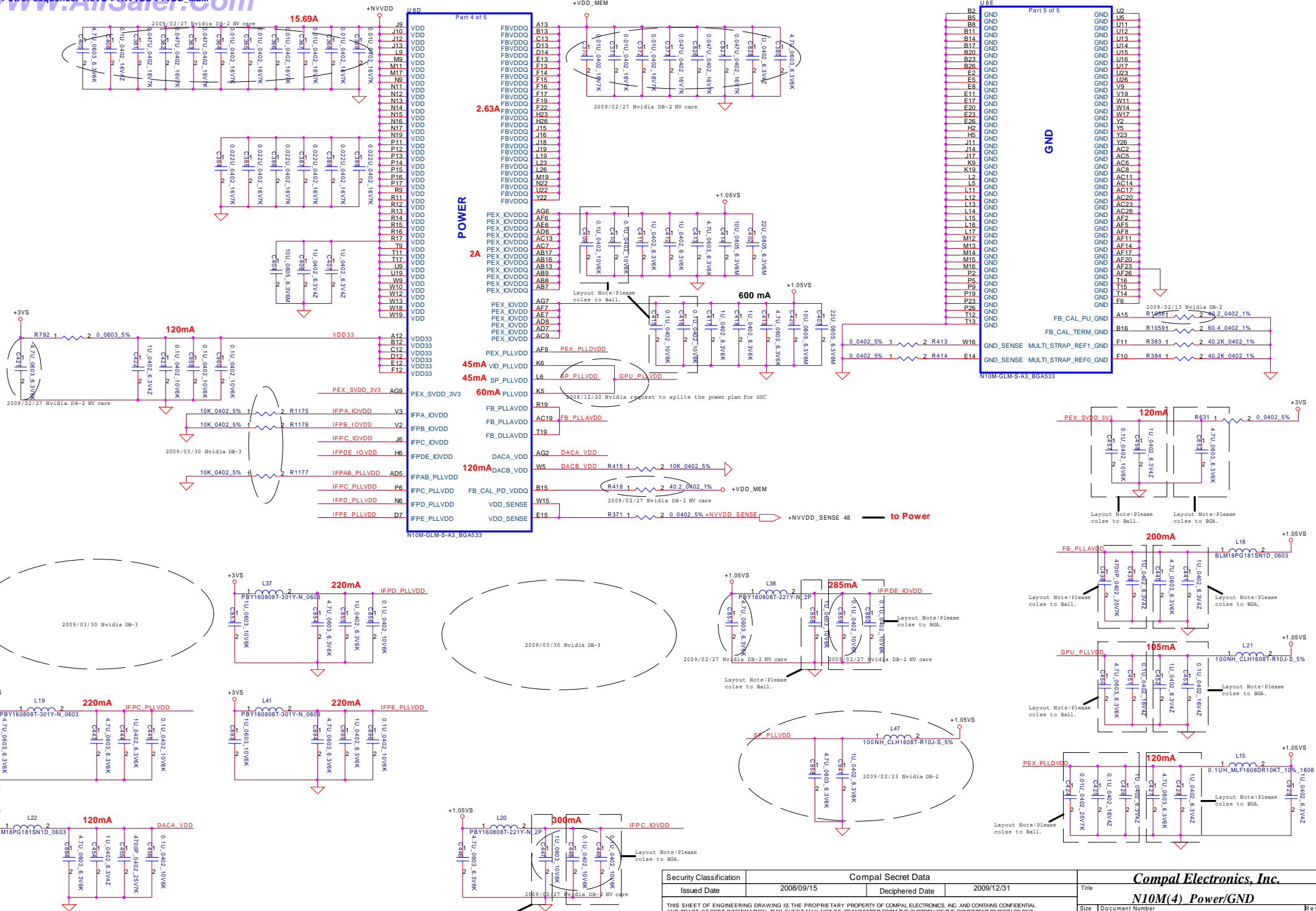
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Title			
PEG Interface			
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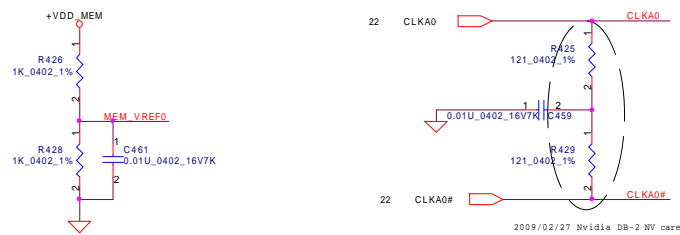
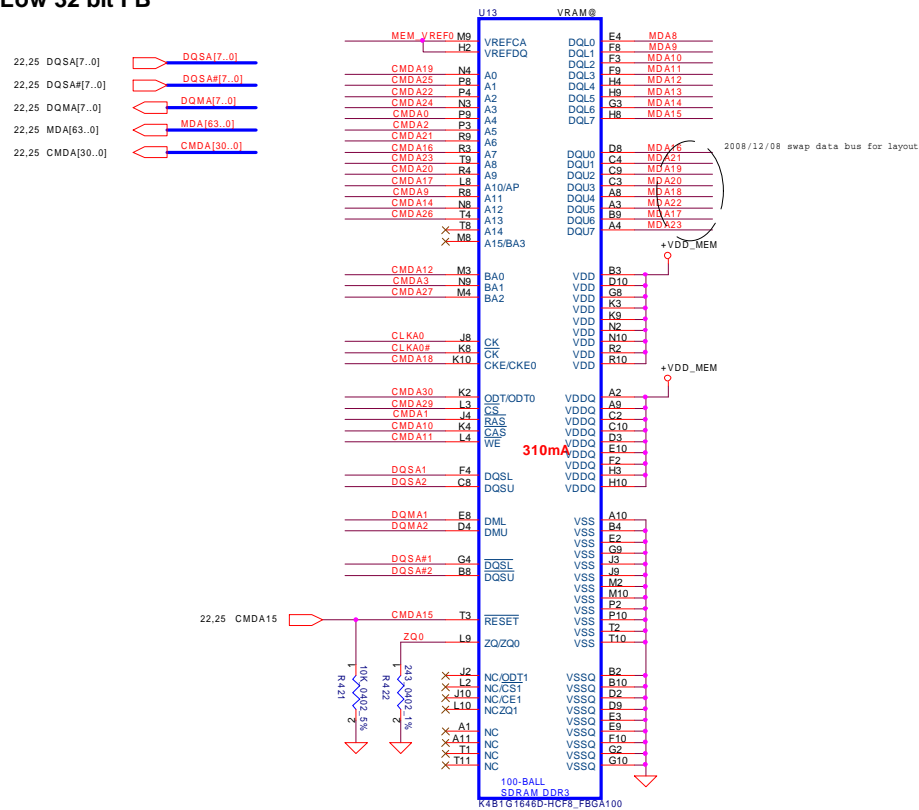
[illegible][illegible][illegible]



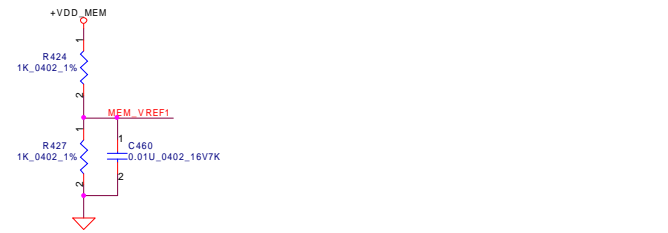
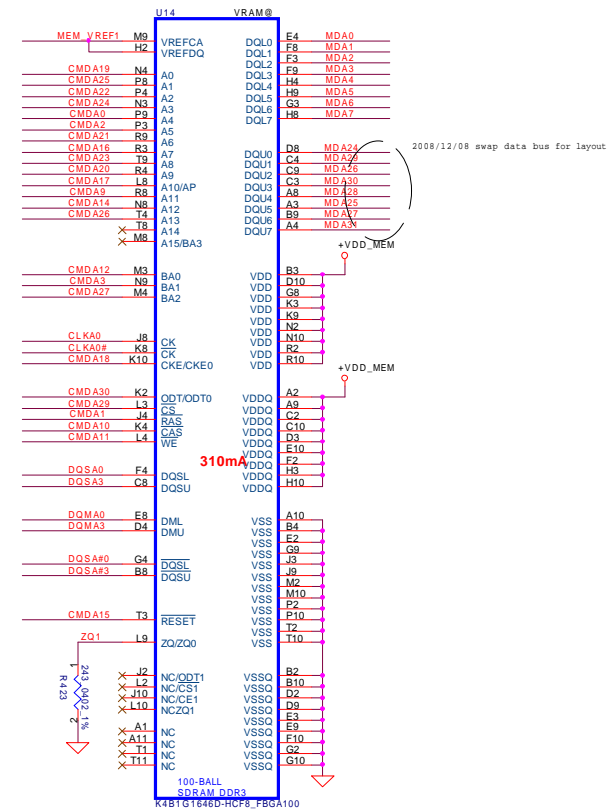
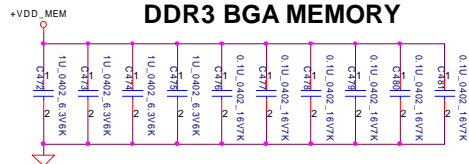
Low 32 bit FB

Hynix : H5TQ1G63BFR-12C-->SA000032400

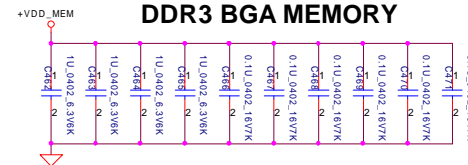
Samsung : K4W1G1646E-HC12-->SA000035700



DDR3 BGA MEMORY



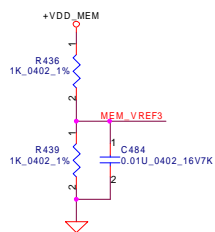
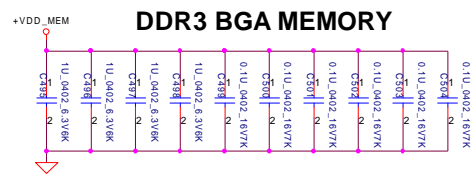
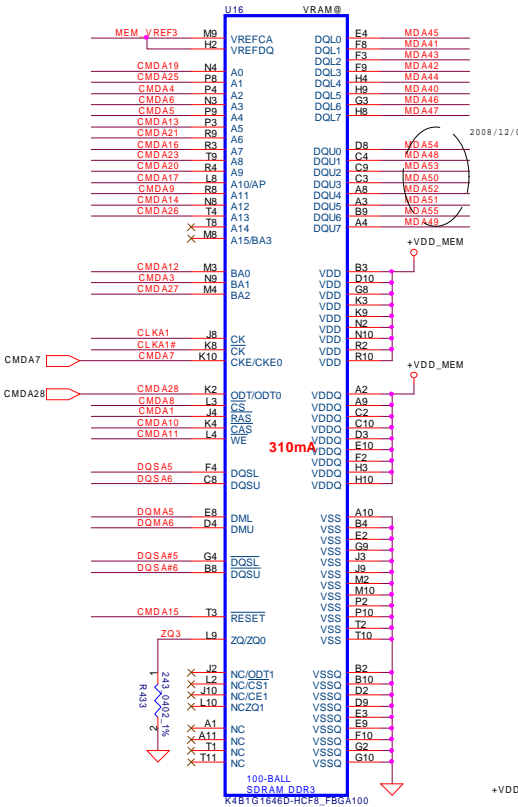
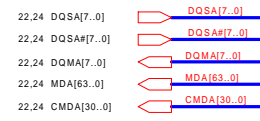
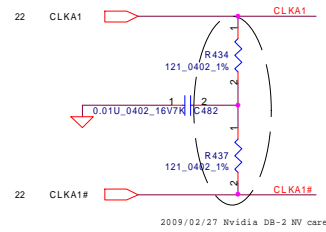
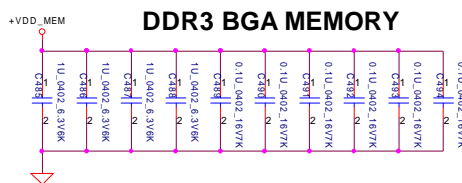
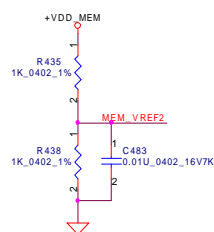
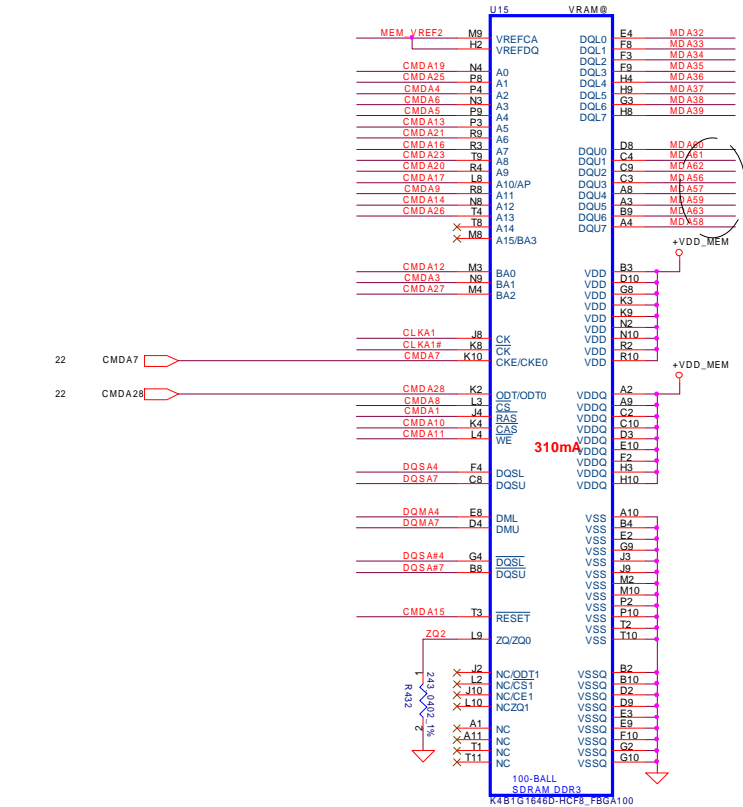
DDR3 BGA MEMORY



	DATA Bus	
Address	0..31	32..63
CMD0	A4	
CMD1	RAS#	RAS#
CMD2	A5	
CMD3	BA1	BA1
CMD4		A2
CMD5		A4
CMD6		A3
CMD7		CKE
CMD8		CS#
CMD9	A11	A11
CMD10	CAS#	CAS#
CMD11	WE#	WE#
CMD12	BA0	BA0
CMD13		A5
CMD14	A12	A12
CMD15	RST	RST
CMD16	A7	A7
CMD17	A10	A10
CMD18	CKE	
CMD19	A0	A0
CMD20	A9	A9
CMD21	A6	A6
CMD22	A2	
CMD23	A8	A8
CMD24	A3	
CMD25	A1	A1
CMD26	A13	A13
CMD27	BA2	BA2
CMD28		ODT
CMD29	CS#	
CMD30	ODT	

LOW	HIGH
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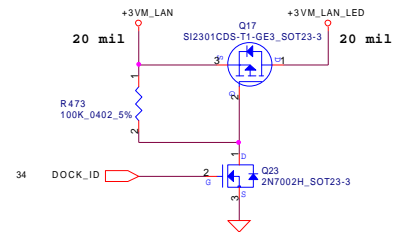
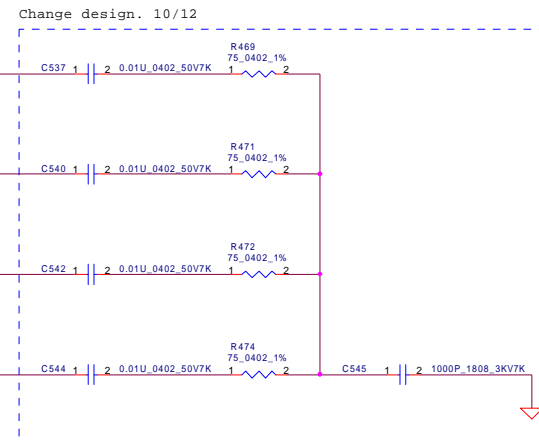
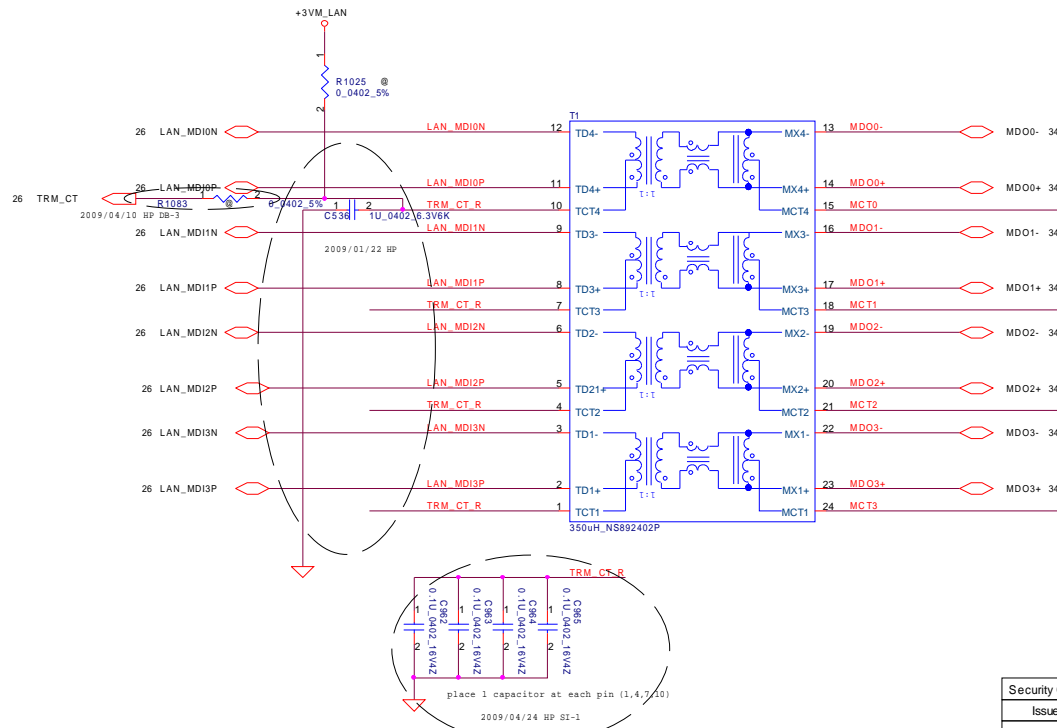
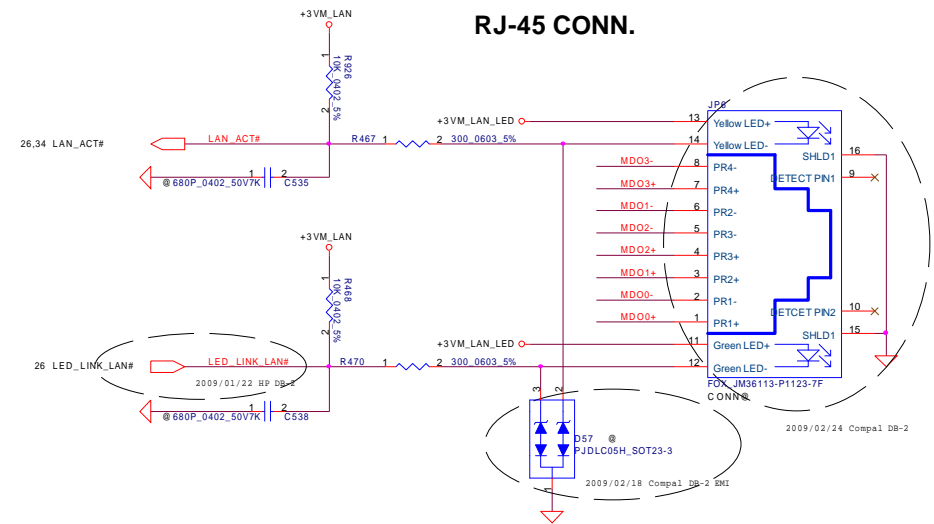
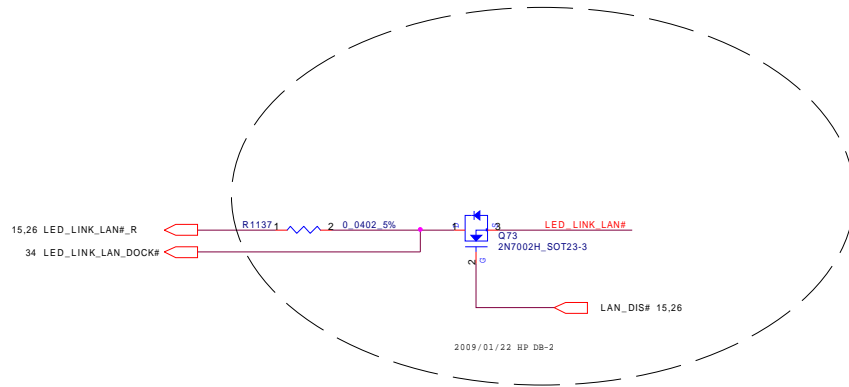
VRAM DDR3 chips (512MB)
64Mx16 DDR3 800MHz*4==>512MB
High 32 bit FB



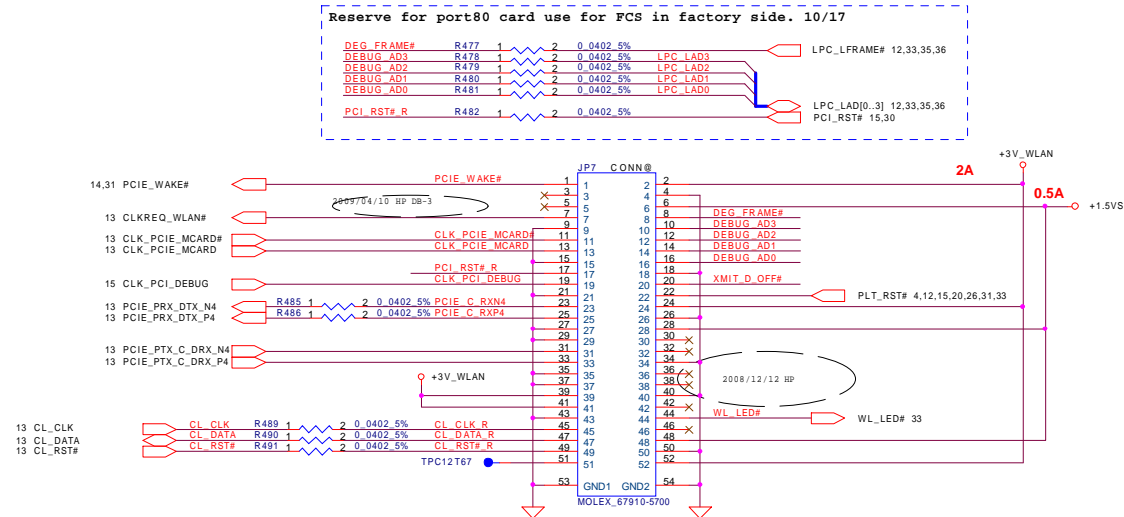
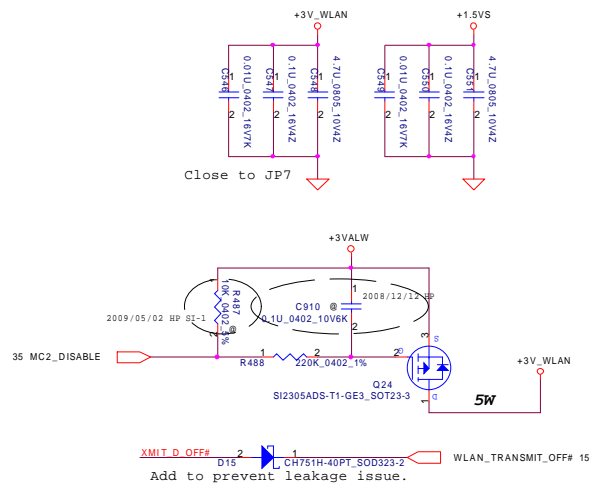
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Issued Date	2008/09/15	Deciphered Date	2009/12/31	Title	VRAM DDR3
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				Sheet	25 of 54



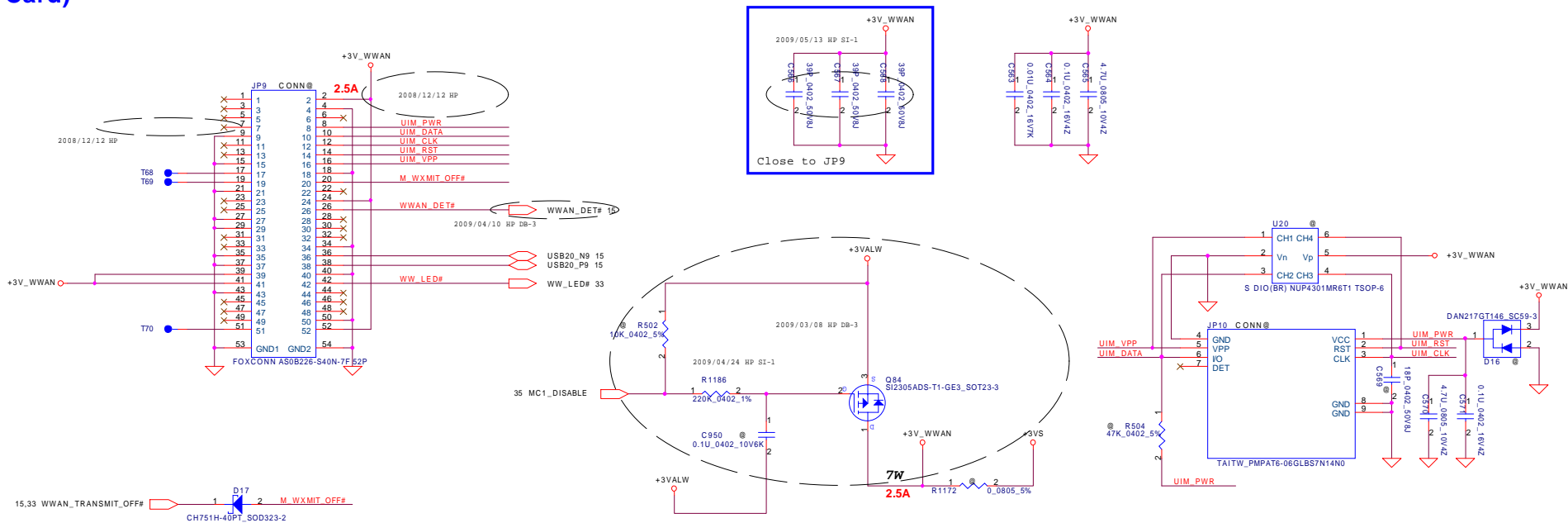
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Size	Document Number	Rev		Date	
LA-4901P	1.0	Tuesday, December 15, 2009		Sheet 27 of 54	

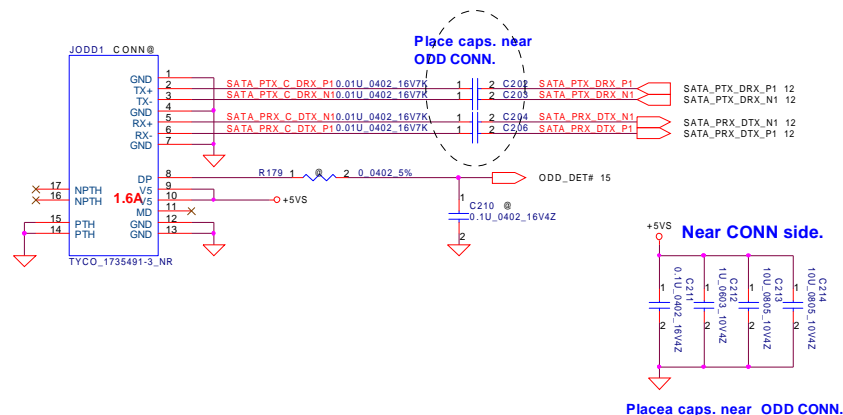


WWAN (Full mini Card)

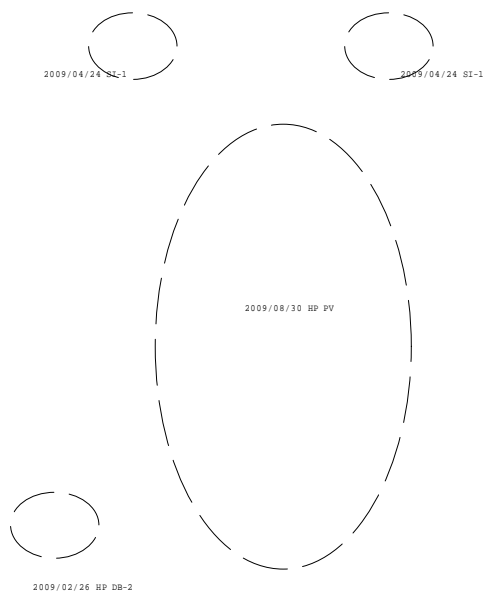


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					LA-4901P	1.0
				Date:	Tuesday, December 15, 2009	Sheet 28 of 54

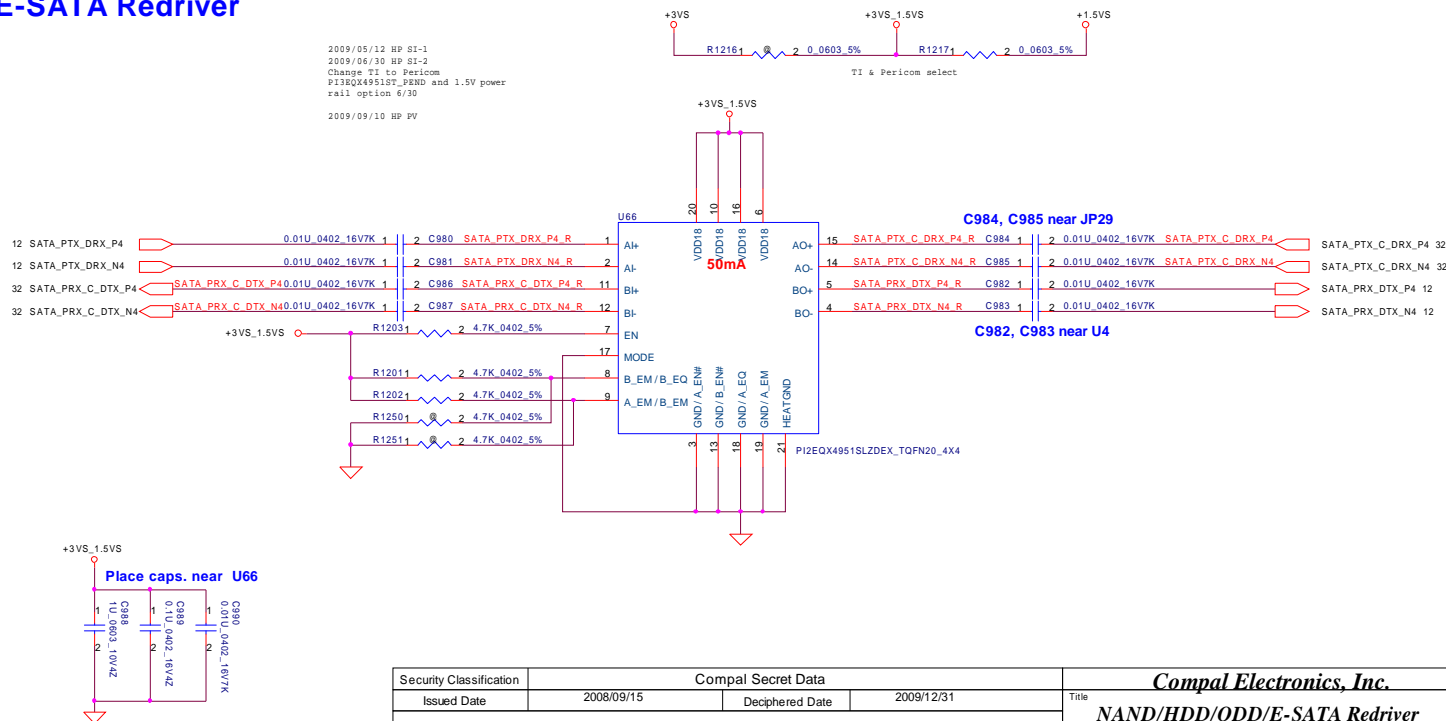
SATA ODD CONN.



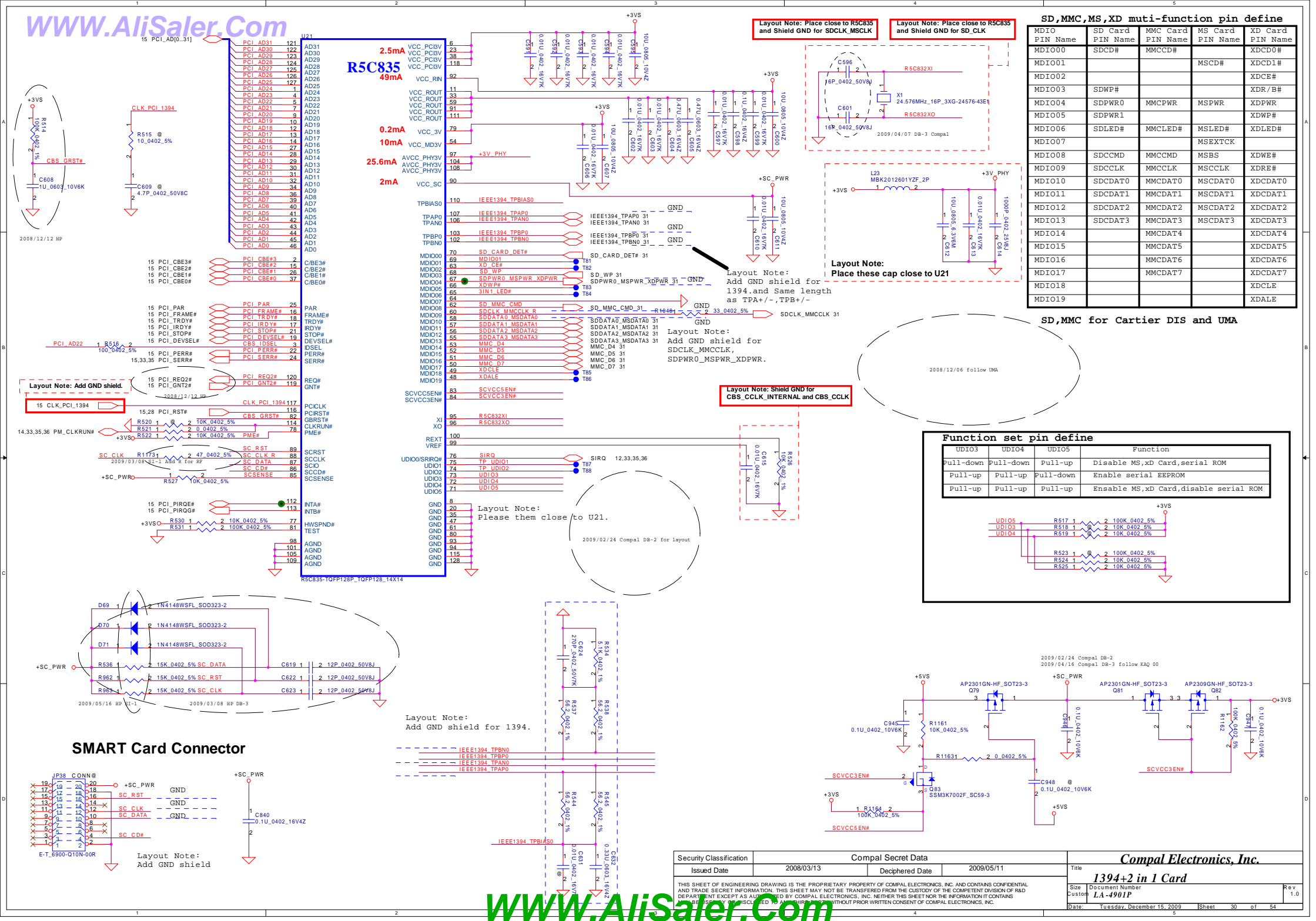
NAND FLASH



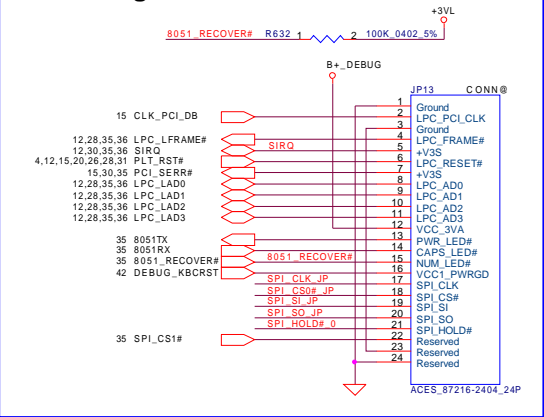
E-SATA Redriver



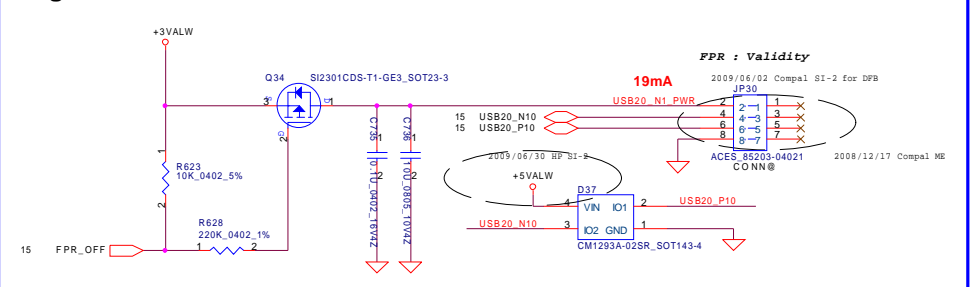
Security Classification		Compal Secret Data		Compal Electronics, Inc. NAND/HDD/ODD/E-SATA Redriver	
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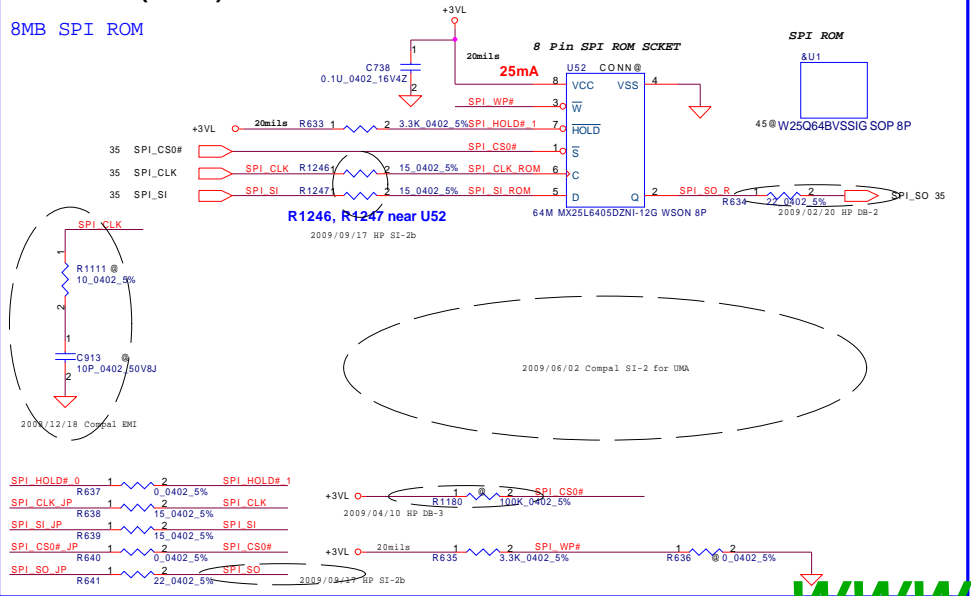
LPC Debug Port



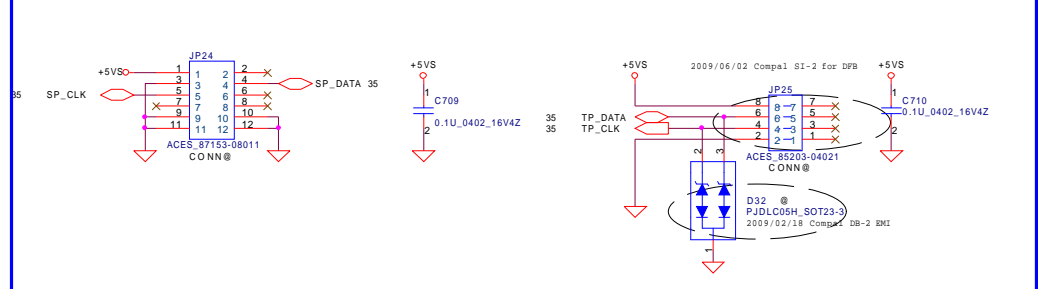
Finger Printer



BIOS ROM(16MB)

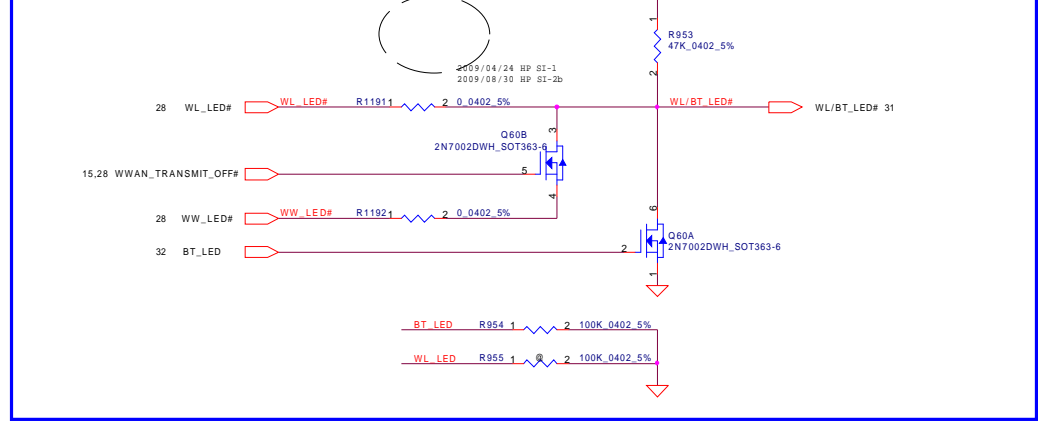


TrackPoint CONN.

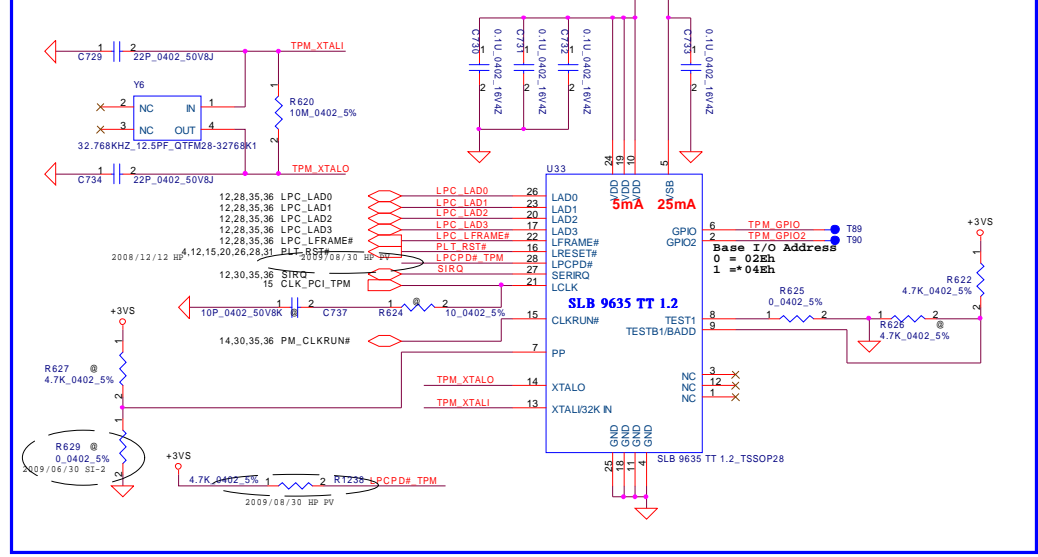


T/P CONN.

WLAN/WWAN/BT LED



TPM1.2 on board



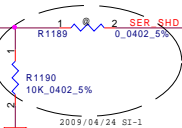
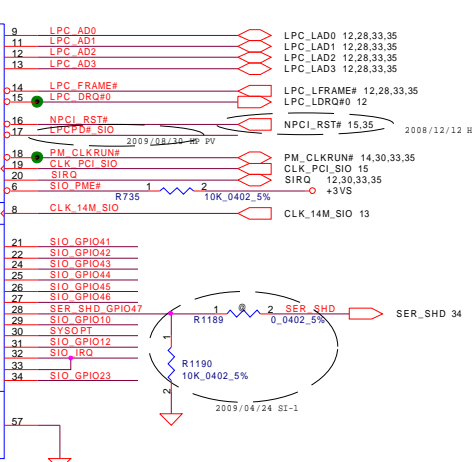
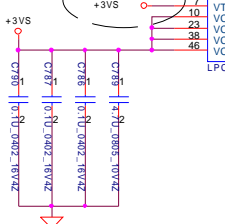
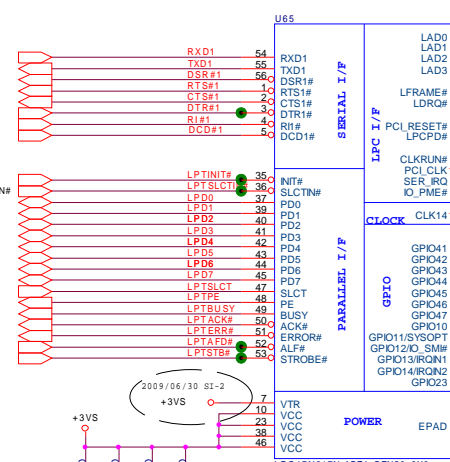
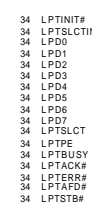
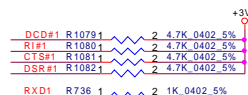
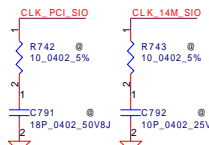
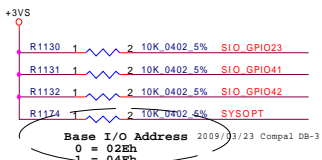
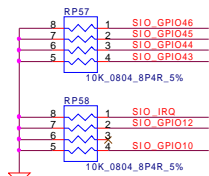
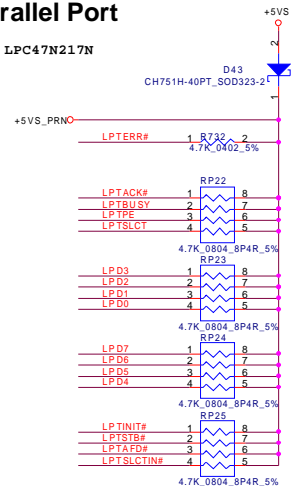
Security Classification		Compal Secret Data		Title	
Issued Date		Deciphered Date		FPR/BIOS/LPC DEBUG/LED/TPM/TP	
2008/09/15		2009/12/31		Document Number	
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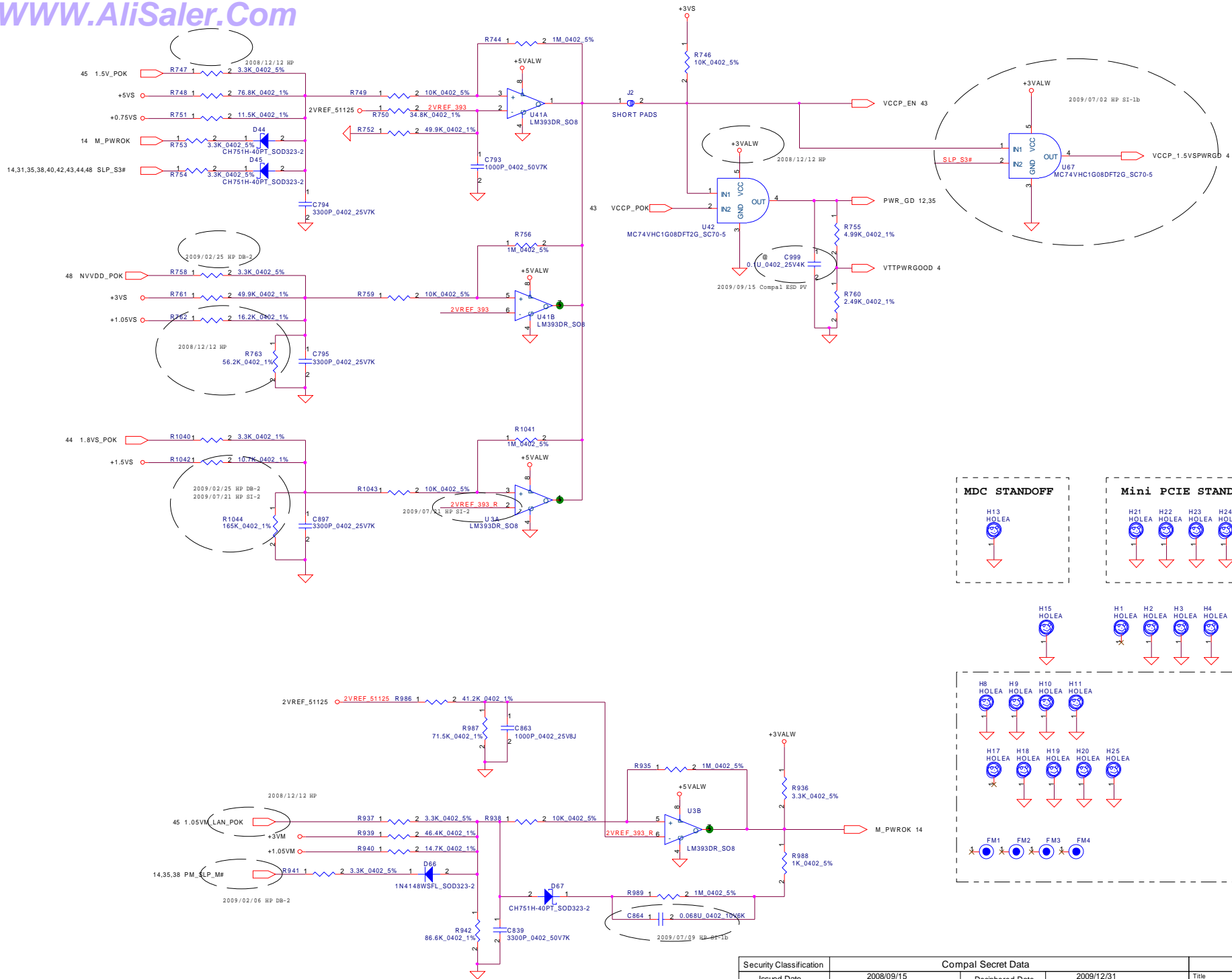


Parallel Port

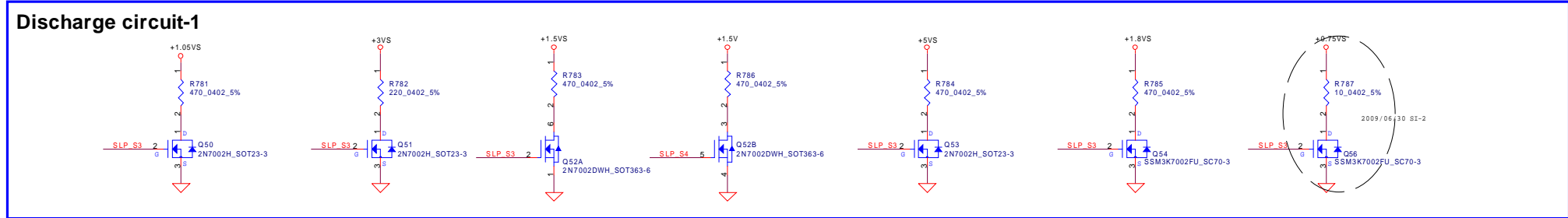
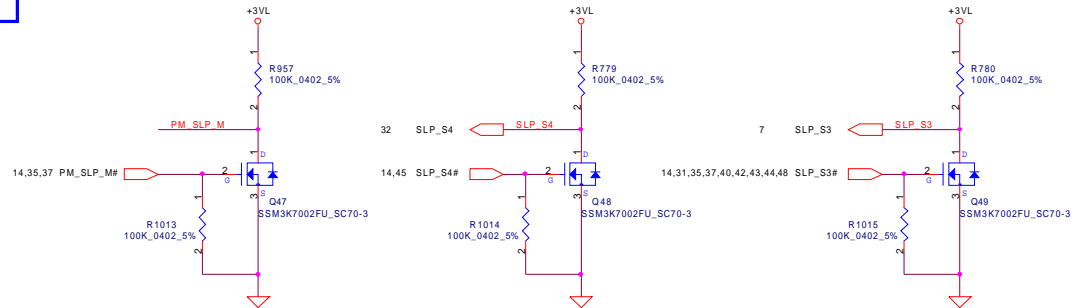
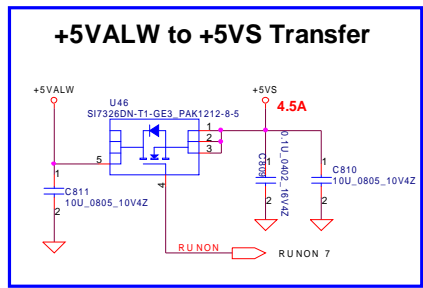
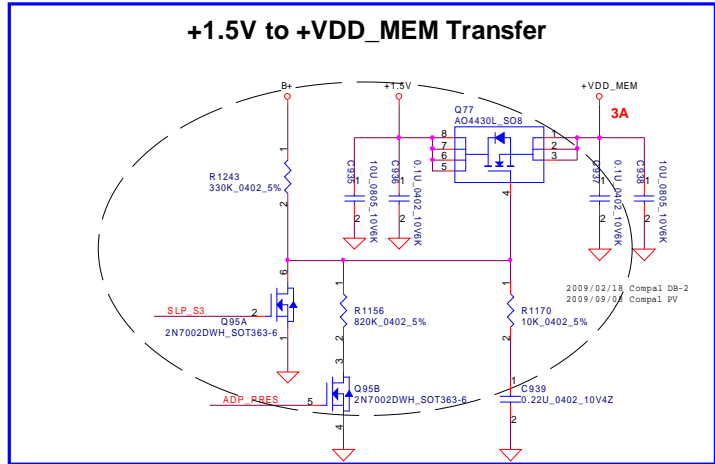
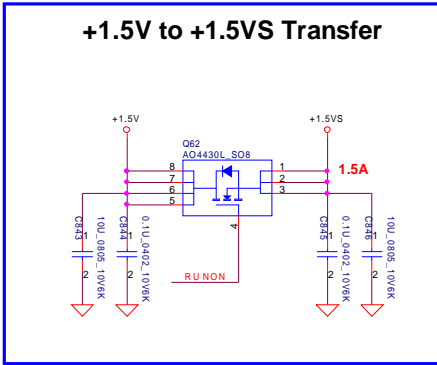
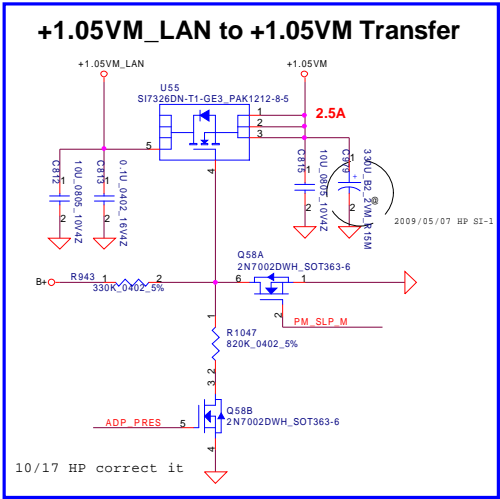
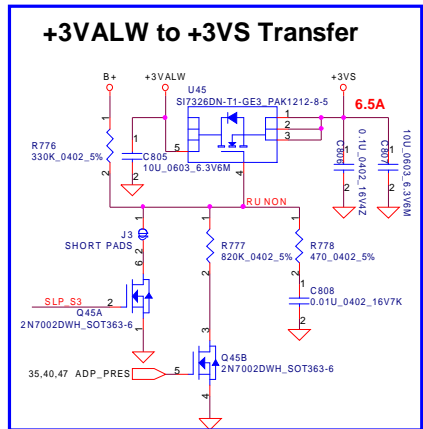
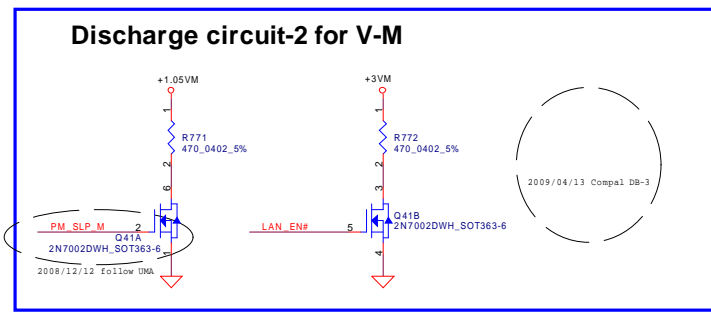
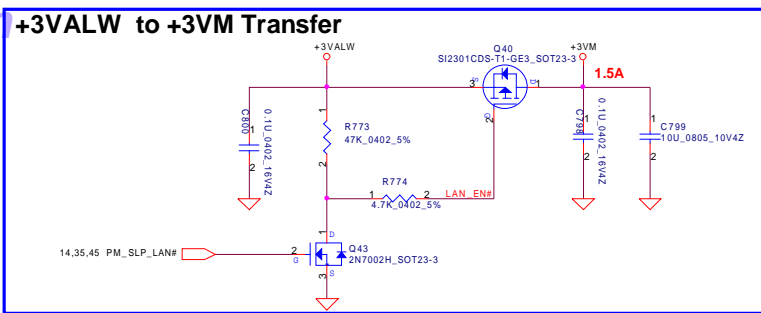
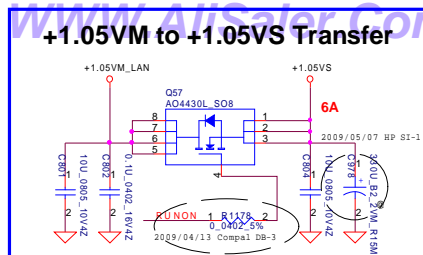
TO LPC47N217N



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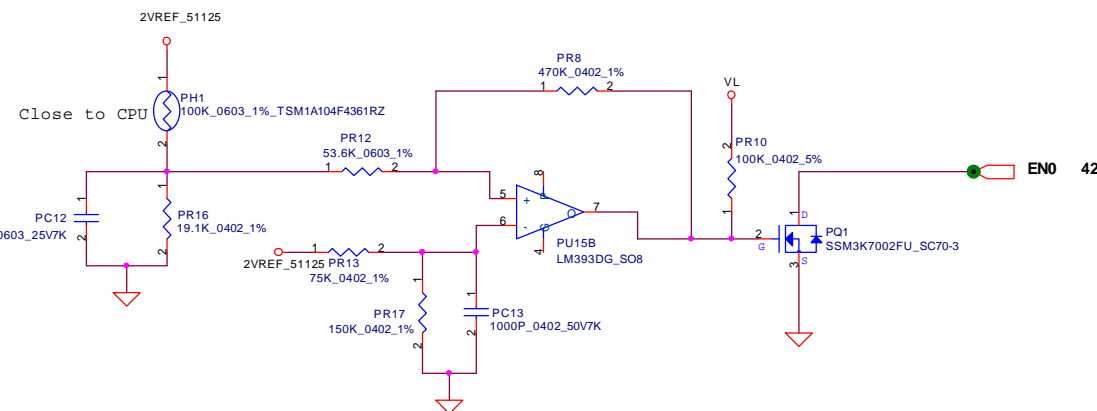
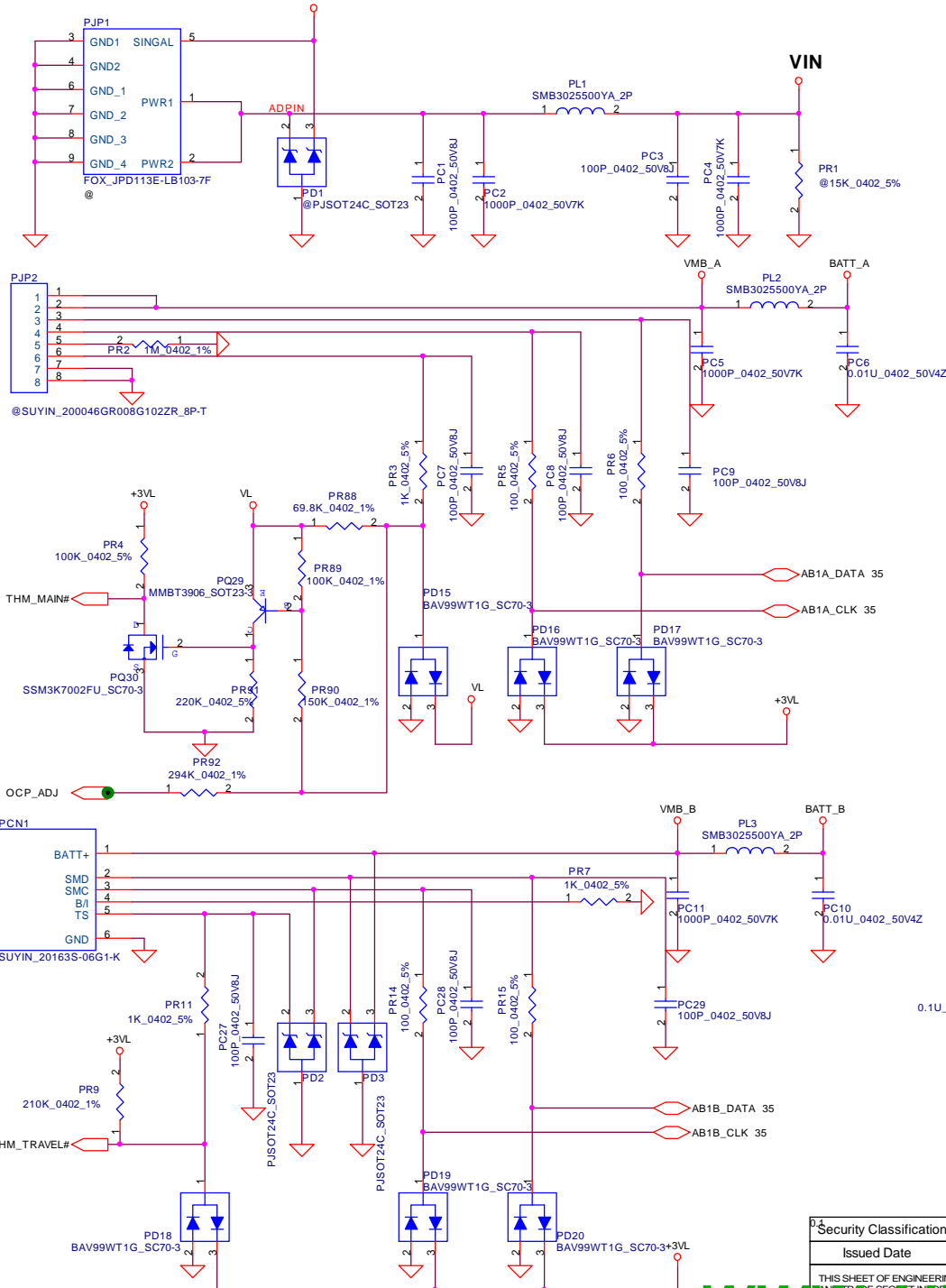


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Size	Document Number	Rev	1.0	Date	Tuesday, December 15, 2009
				Sheet	37 of 54

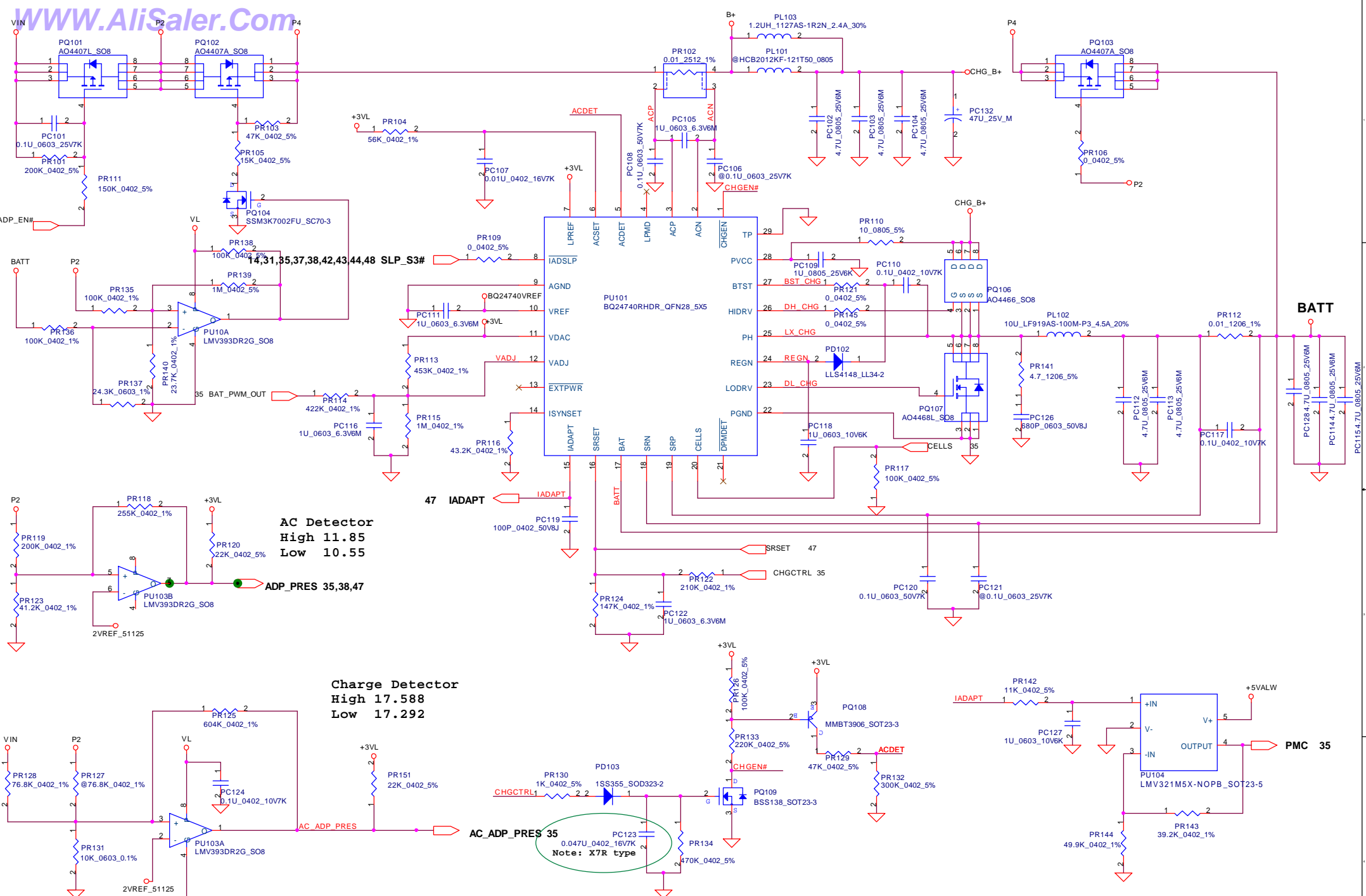


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				DC/DC Circuits	
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ADP_SIGNAL



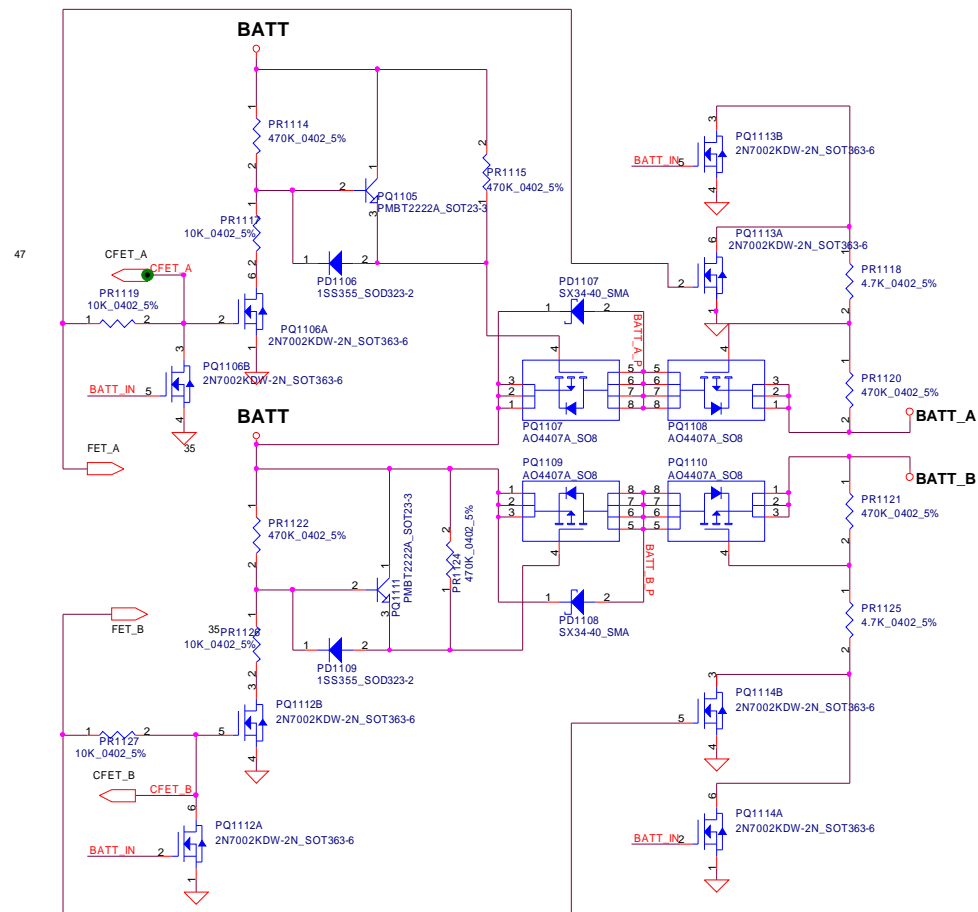
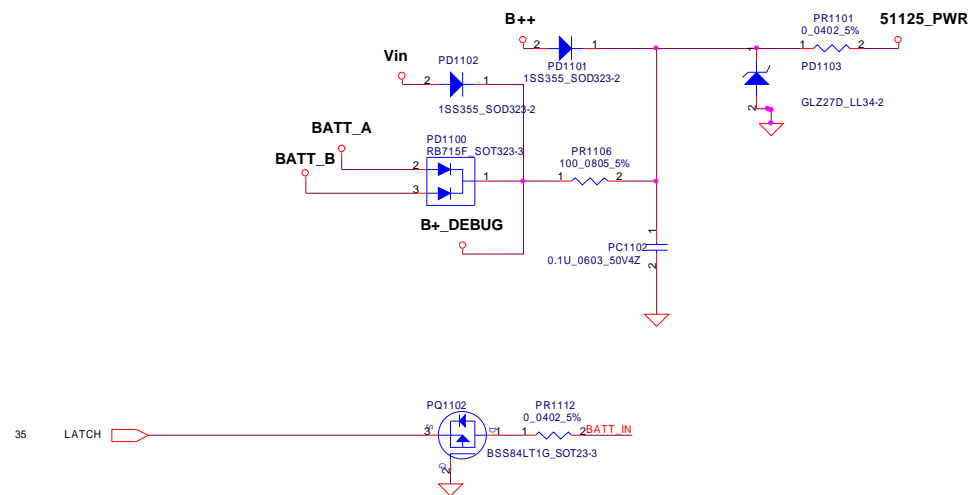
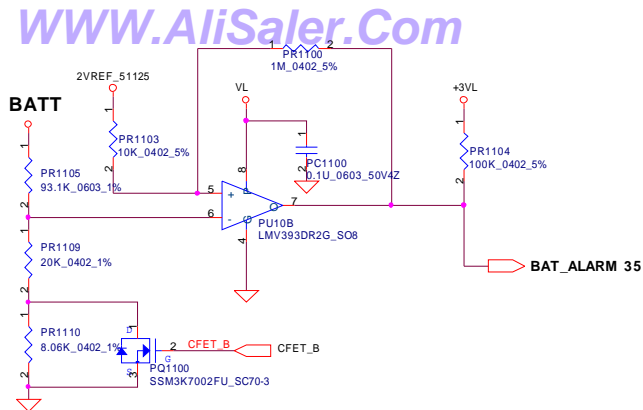
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2008/09/15	Deciphered Date	2009/09/15	Title	DC-IN/ BATTERY CONN
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				Custom	LA-4902P
				Date	Tuesday, December 15, 2009
				Sheet	39 of 54



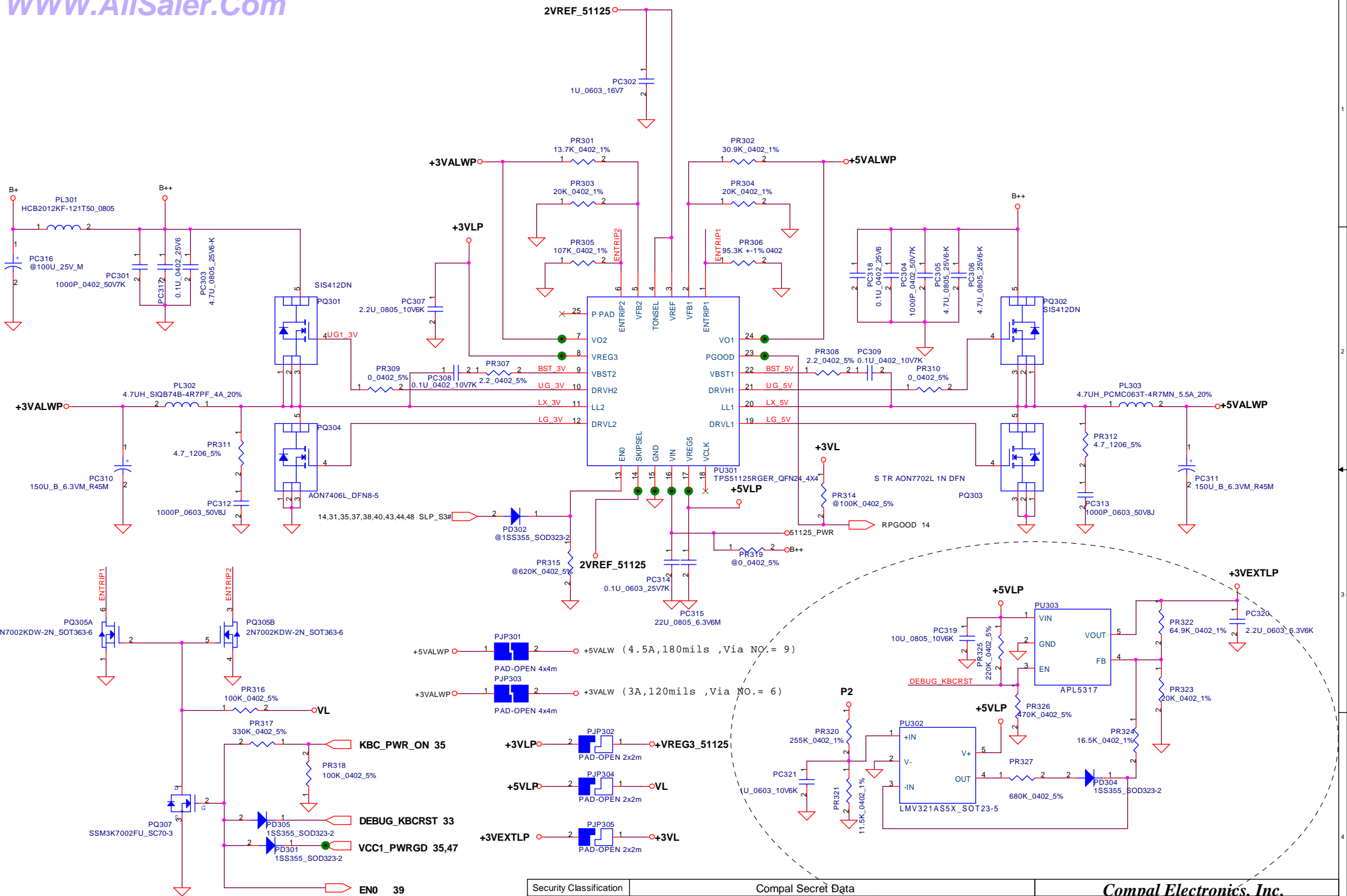
AC Detector
High 11.85
Low 10.55
ADP_PRES 35,38,47

Charge Detector
High 17.588
Low 17.292
AC_AD_PRES 35

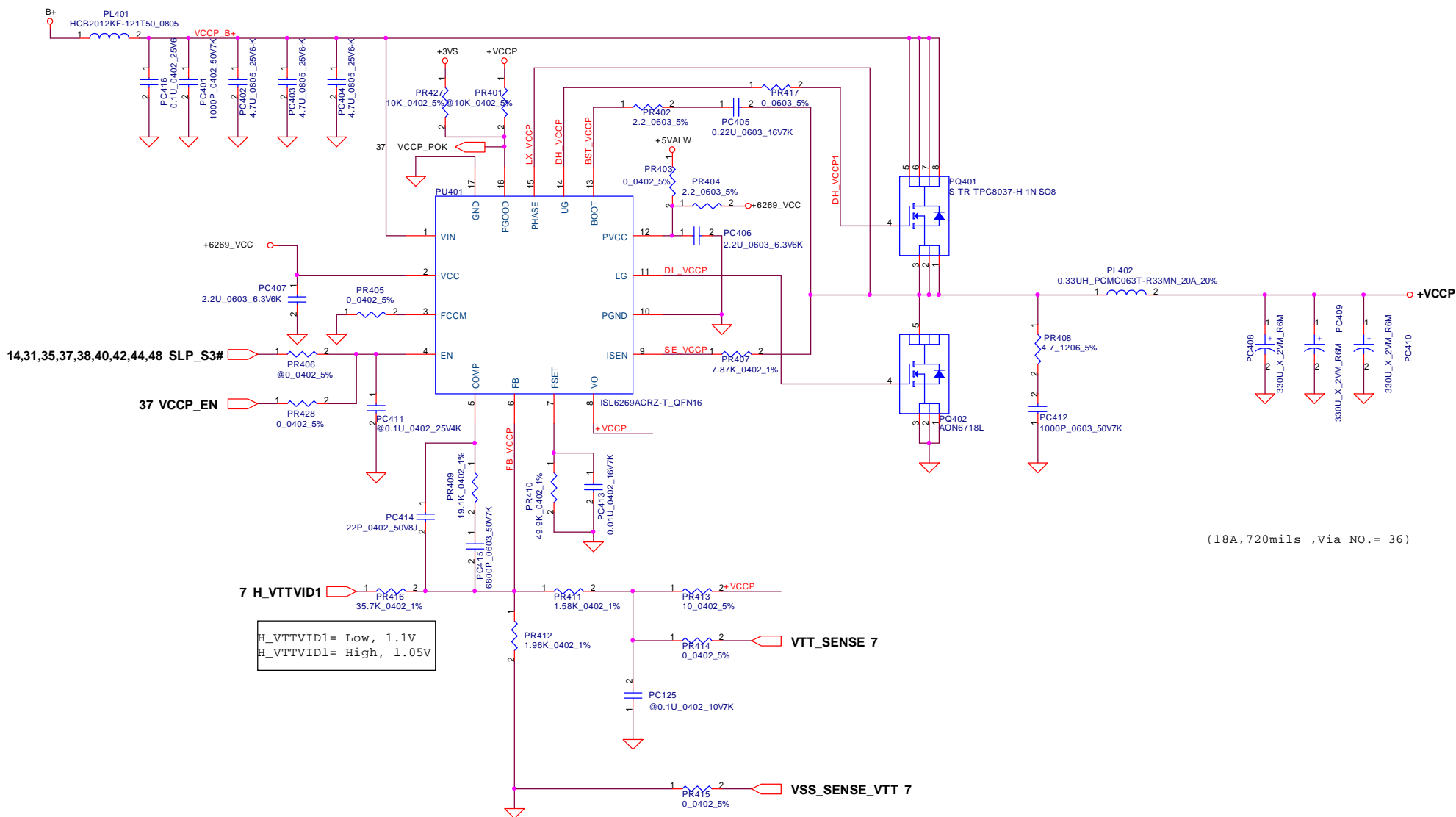
Security Classification		Compal Secret Data		Title	
Issued Date	2008/09/15	Deciphered Date	2009/09/15	Charger	
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				LA-4902P	Rev 0.1
				Date:	Tuesday, December 15, 2009
				Sheet	40 of 54



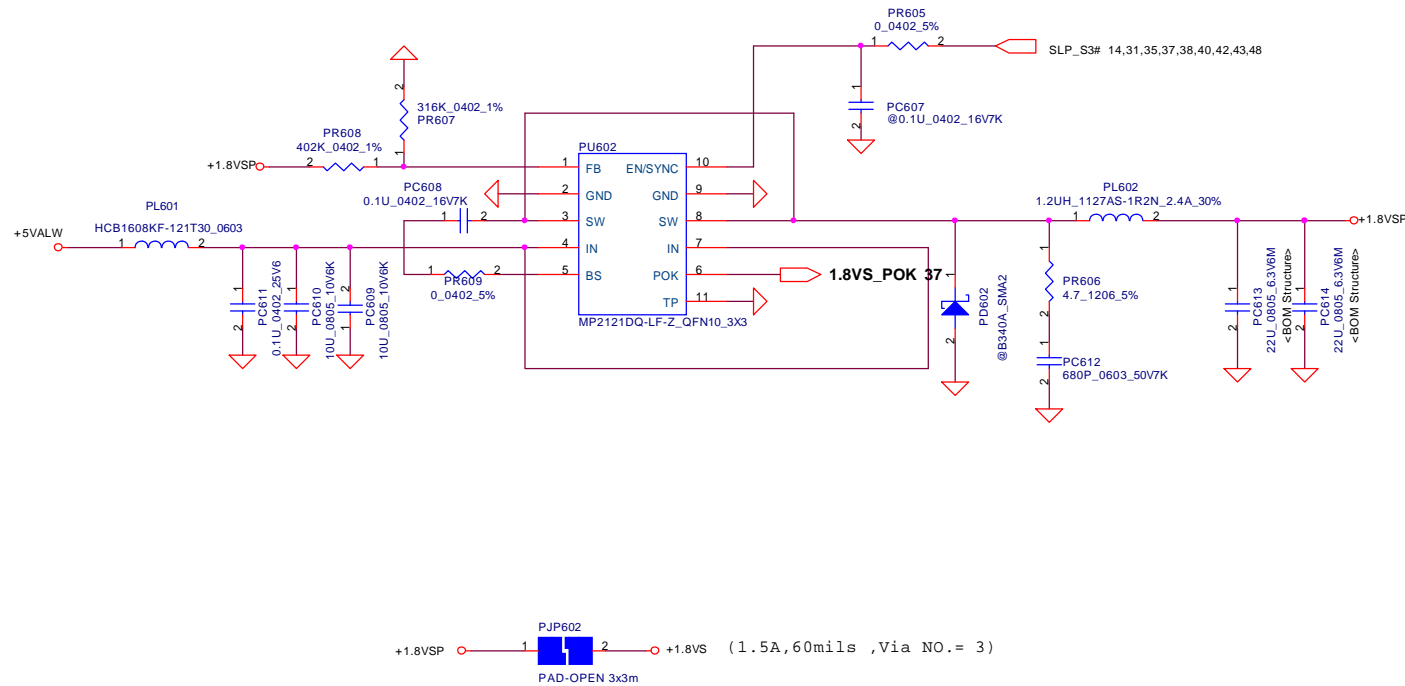
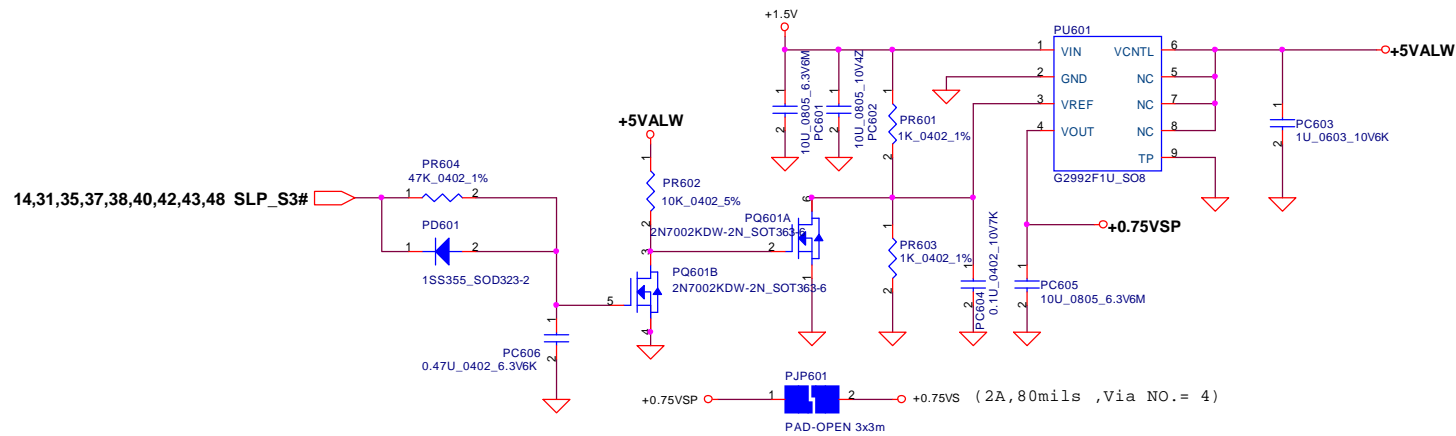
Security Classification		Compal Secret Data		Compal Electronics, Inc. Battery selector	
Issued Date	2008/09/15	Deciphered Date	2009/09/15	Title	
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				Date:	Tuesday, December 15, 2009
				Sheet	41 of 54



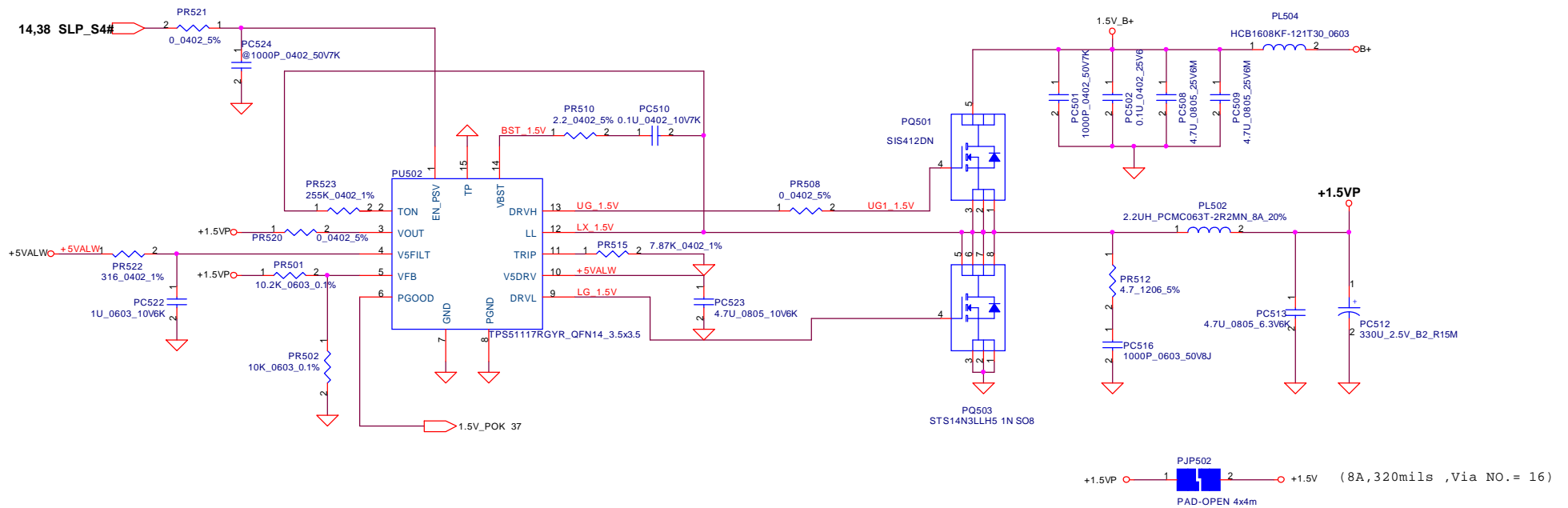
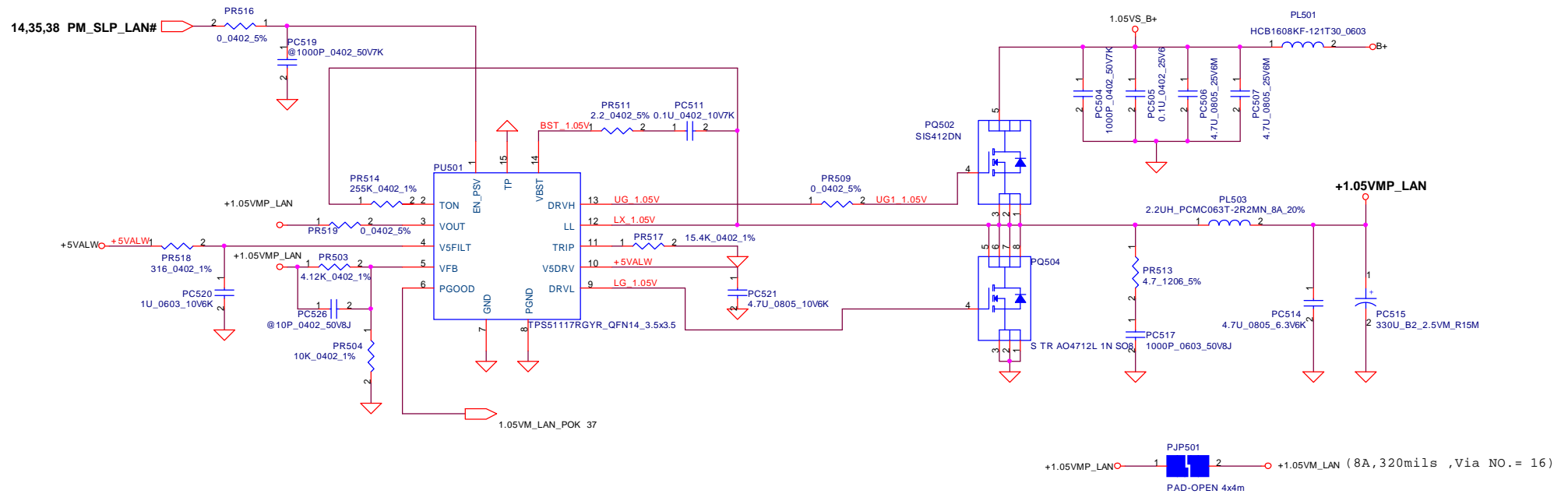
Security Classification		Compal Secret Data		Title	
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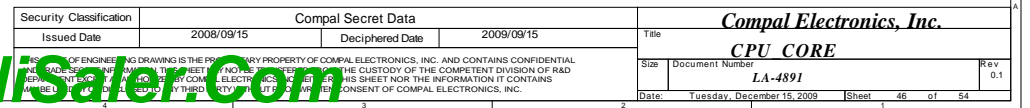
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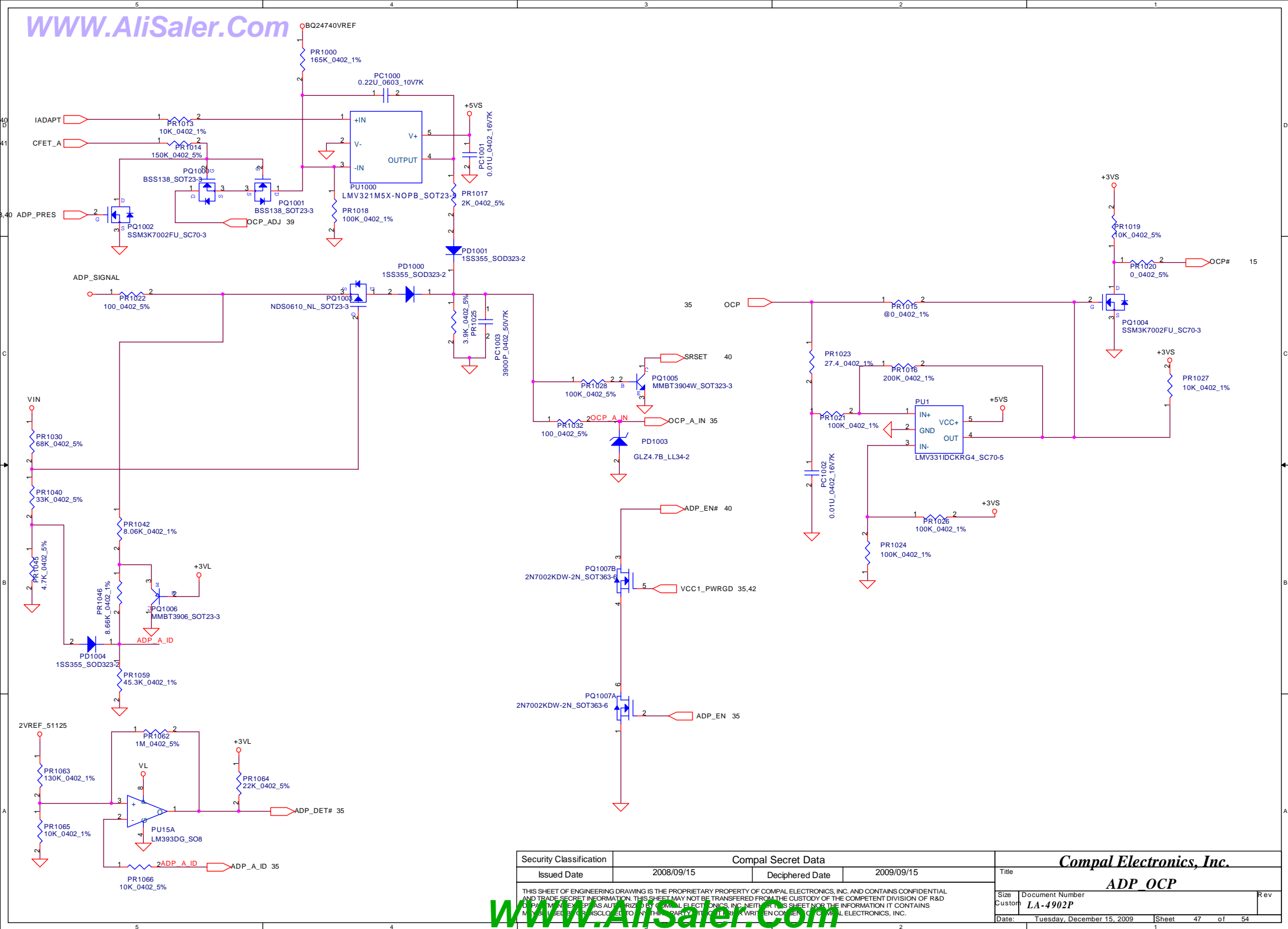


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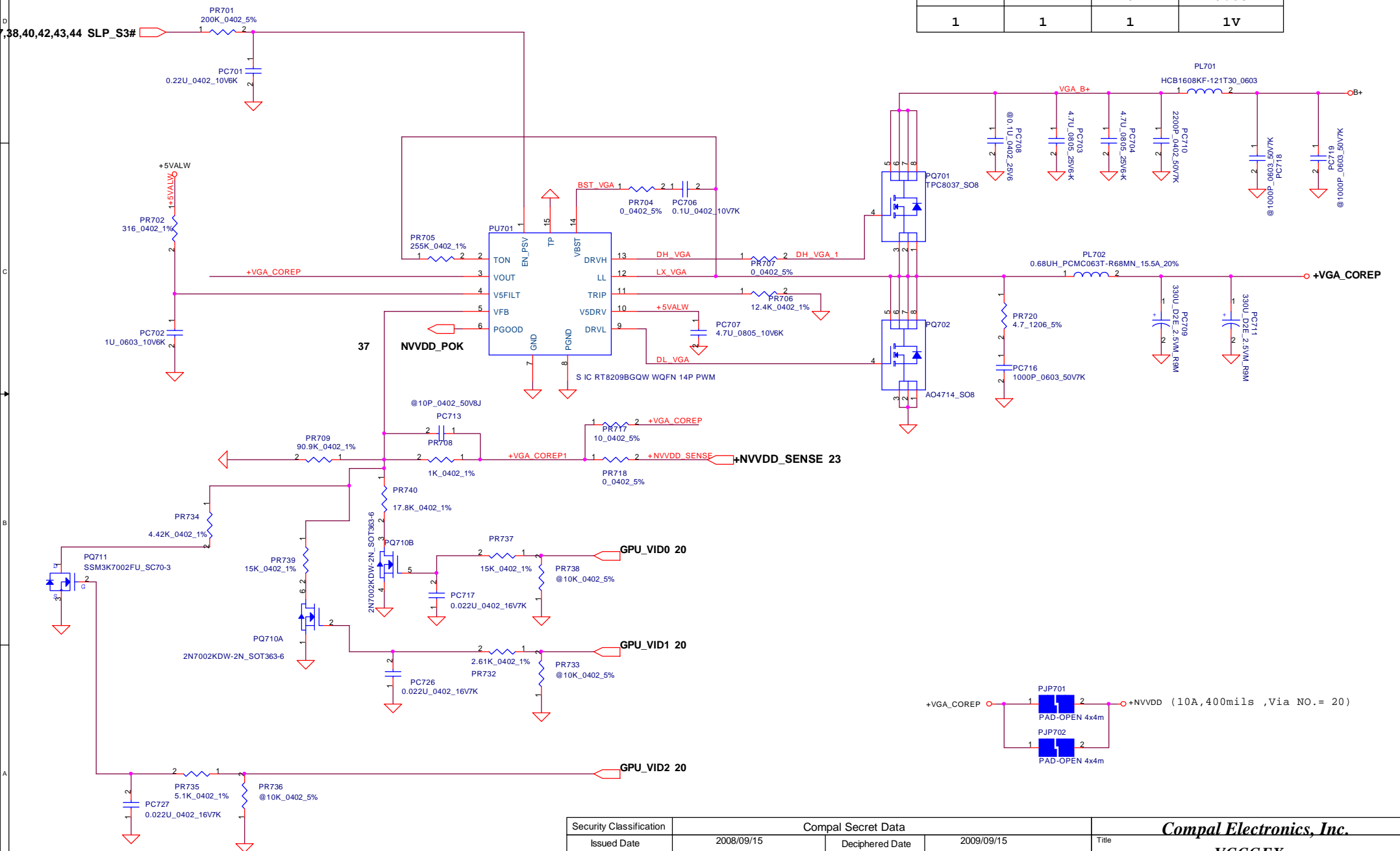
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Item	Page #	Date	Request Owner	Solution Description	Rev.
1	01	2009/02/06	HP	Del R4, R6, R8 for XDP.	0.2
2	01	2009/02/06	HP	Del R40 for Thermal sensor.	0.2
3	01	2009/02/06	HP	Del R34, R36, R46, R49, R37 for XDP.	0.2
4	01	2009/02/24	HP	Change R31 24.9K ohm to 1.5K ohm and R33 12.4K ohm to 750 ohm for intel WOW.	0.2
5	01	2009/02/25	HP	Debug port design guide changed XDP debug ports to CFG4 - pin 28, CFG5 - pin 30, CFG10 - pin 22, and CFG11 pin 24.	0.2
6	02	2009/02/19	HP	No install R60, R61.	0.2
7	12	2009/02/25	Compal	Install C193, C194 for WLAN nosie	0.2
8	12	2009/02/06	HP	Change R1026, R181, R185, R1027 from 15 ohm to 0 ohm.	0.2
9	12	2009/02/06	HP	Del C205, C207, C208, C209 for docking.	0.2
10	12	2009/02/06	HP	Change D1 pin 2 form +3VL to +VREG3_51125	0.2
11	12	2009/02/06	HP	Del JP15 pin 51, 53 trace (SMB_DATA_S3, SMB_CLK_S3) and add test point.	0.2
12	12	2009/02/11	HP	Add R1144 1K ohm	0.2
13	12	2009/02/11	HP	Add Test point for SATA bus.	0.2
14	12	2009/02/18	HP	Add NAND Flash dect pin (BRAID_DET)	0.2
15	13	2009/02/06	HP	Install R354	0.2
16	13	2009/02/06	HP	Add R1138 10K ohm	0.2
17	13	2009/02/20	HP	Change R1092, R1093 power sourece form +3VS to +3VS.	0.2
18	13	2009/02/20	HP	Del R208, R210	0.2
19	13	2009/02/22	HP	Add Q76A and Q76B for CBB	0.2
20	13	2009/02/22	HP	Reserve R1146 and R1147 for external VGA thermal	0.2
21	14	2009/01/22	HP	Del R238, because double pull down.	0.2
22	14	2009/02/19	HP	Del R288	0.2
23	14	2009/02/06	HP	Stuff R296	0.2
24	14	2009/02/06	HP	Add R1142 10K ohm	0.2
25	14	2009/02/25	Compal	Add C949 for RF	0.2
26	16	2009/01/22	HP	Del L1, C228, C229 and add test point.	0.2
27	16	2009/01/22	HP	Del L2, C241 and add test point.	0.2
28	16	2009/01/22	HP	Del C847 and add test point.	0.2
29	16	2009/01/22	HP	Del L4, C269 and add test point.	0.2
30	16	2009/01/22	HP	Del L3, C263, C262 and add test point.	0.2
31	16	2009/02/03	HP	Change R299, R300 form 10 ohm to 100ohm.	0.2
32	16	2009/02/03	HP	Change C278 form 0.1uF to 1uF.	0.2
33	16	2009/02/05	HP	Change C243, C244 form 10uF to 22uF.	0.2
34	18	2009/02/18	Compal	Add L48, L49, L50, C944, C943, C942 for EMI	0.2
35	18	2009/02/24	Nvidia	Change R321, R320, R319, R318 form 2.2K ohm to 4.7K ohm.	0.2
36	18	2009/02/18	Compal	Reserve D5, D6, D7, D8, D9 for ESD	0.2
37	18	2009/02/12	Nvidia	Del R1066	0.2
38	19	2009/01/22	HP	Add Q71, Q72, R1134 for Keyboard light	0.2

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Item	Page #	Date	Request Owner	Solution Description	Rev.
39	19	2009/02/06	Nvidia	Add C914, C915, C916, C917 for eDP.	0.2
40	19	2009/02/06	HP	Change R341 pin 1 power plan form +5VS to +3VALW.	0.2
41	20	2009/02/12	Nvidia	Reserve R1150	0.2
42	20	2009/02/24	HP	Change R355, R356 from 2K ohm to 10K ohm.	0.2
43	20	2009/02/25	HP	Add Q78, R1158, R1157 for thermal trip.	0.2
44	21	2009/01/20	HP	Del LVDS function	0.2
45	21	2009/02/26	Nvidia	Del C346, because no SPDIF.	0.2
46	22	2009/01/20	Nvidia	Add C924 ~ C931 for DP function	0.2
47	22	2009/02/06	Nvidia	Del RP28, 31, 34, 40, 49, 52, 55, 29, 43, 35, 39, 44, 48, 53, 56, 30, 33, 36, 38, 45, 46, 42 for VRAM	0.2
48	22	2009/02/06	Nvidia	Del C904, 899, 900, 901, 902, 903 for VRAM	0.2
49	23	2009/02/23	Nvidia	Add L47, C941 for SP_PLLVDD	0.2
50	26	2009/02/19	HP	Del C505, C509, C507, C508, C905, Q18, R440, R1062, R441	0.2
51	26	2009/02/06	HP	Add C934	0.2
52	27	2009/02/18	HP	ReserveD57 for ESD	0.2
53	28	2009/02/06	HP	Recover Q25, R503, R483	0.2
54	29	2009/02/26	HP	Add R1159 for NAND Flash detect	0.2
55	30	2009/02/24	Compal	Del R529, R528, R535, C618, U22	0.2
56	30	2009/02/24	Compal	Add Q79 ~ Q83, R1163, R1162, R1164, C945, C946, C947, C948	0.2
57	31	2009/02/22	Compal	Del D30, U28, C711, R608	0.2
58	32	2009/02/18	HP	Reserve D33, D34, D36, D68 for ESD	0.2
59	33	2009/02/18	Compal	Reserve D32 for ESD	0.2
60	34	2009/02/06	HP	Add JP32 of pin 179, 178, 11, 10 to +5VS	0.2
61	34	2009/01/22	HP	Del C746 ~ C761	0.2
62	34	2009/02/06	HP	Add Q75, R1145.	0.2
63	34	2009/02/06	HP	Del R647~ R650	0.2
64	35	2009/02/06	HP	Reserve R711	0.2
65	35	2009/02/06	HP	Add R1140	0.2
66	35	2009/02/16	HP	Change R692 from 10K ohm to 220 ohm	0.2
67	35	2009/01/22	HP	Del L4, C269 and add test point.	0.2
68	36	2009/02/18	Compal	change Super IO from ITE to SMSC	0.2
69	37	2009/02/25	HP	Del R758	0.2
70	37	2009/02/06	HP	Change R941 pin from PM_SLP_LAN# to PM_SLP_M#	0.2
71	37	2009/02/25	HP	change R1044 from 49.9K ohm to 78.7K ohm.	0.2
72	38	2009/02/18	Compal	Add Q77, R1170, C939, R1156, C935 ~ C938	0.2
73					0.2
74					0.2
75					0.2
76					0.2

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Item	Page #	Date	Request Owner	Solution Description	Rev.
01	15	2009/03/23	Compal	Add RP59	0.3
02	15	2009/03/23	Compal	Reserve C956 ~ C951 for RF	0.3
03	30	2009/03/08	Compal	Add R1173 for RF	0.3
04	36	2009/03/23	Compal	Add R1174 10K ohm	0.3
05	36	2009/03/23	Nvidia	Change R1059 from 40.2 ohm to 60.4 ohm	0.3
06	36	2009/03/23	Nvidia	Change R418 from 60.4 ohm to 40.2 ohm	0.3
07	36	2009/04/09	Compal	Install R706 for SMSC CBB	0.3
08	23	2009/04/09	Nvidia	Del L16, C429 ~ C432 for no use LVDS function	0.3
09	23	2009/04/09	Nvidia	Del L17, C433 ~ C435, C436, C437, C883 for no use LVDS function	0.3
10	23	2009/04/09	Nvidia	Add R1175 ~ R1177 for no use LVDS function	0.3
11	18	2009/04/09	HP	Add C957 for DP issue	0.3
12	01	2009/04/13	Compal	Del R12 for follow UMA	0.3
13	38	2009/04/13	Compal	Del Q70, R1094 for follow UMA	0.3
14	35	2009/04/13	Compal	Reserve R1140	0.3
15	31	2009/04/13	Compal	Add R1179 for follow UMA	0.3
16	16	2009/04/13	Compal	Del R290, R291 for follow UMA	0.3
17	15	2009/04/13	Compal	Change C956 ~ C951 from 47P to 22P for RF	0.3
18	04	2009/04/10	HP	Install R51	0.3
19	27	2009/04/10	HP	No install R1083	0.3
20	33	2009/04/10	HP	Reserve R1180	0.3
21	15	2009/04/10	HP	LAN_DIS# should pull up to +3VM_LAN instead of 3VALW	0.3
22	04	2009/04/10	HP	Change R14/R15 to 1.1K1%/3K1% per DGL52	0.3
23	20	2009/04/10	HP	Install R1085 and R1086 for SM communication to GPU	0.3
24	15	2009/04/10	HP	Reserve R1181	0.3
25	19	2009/04/10	HP	Reserve R331	0.3
26	19	2009/04/10	HP	Del R484	0.3
27	09	2009/04/10	HP	Del M2	0.3
28	10	2009/04/10	HP	Del M2	0.3
29	38	2009/04/13	Compal	Add R1178	0.3
30	13	2009/04/10	HP	XTAL_IN (ohm to GND if not used)For discrete:NO INSTALL: R215, Y4, C226, INSTALL 0ohm resistor in C227	0.3
31	35	2009/04/14	Compal	Install R1035, R1036, R1037; no install R1034.	0.3

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Item	Page #	Date	Request Owner	Solution Description	Rev.
01	16	2009/04/24	HP	Del R294	0.4
02	13	2009/04/24	HP	Del CLRP2	0.4
03	14	2009/04/24	HP	Del C147, C148 location	0.4
04	14	2009/04/24	HP	Add C961	0.4
05	14	2009/04/24	HP	Add R1188, R1187 for intel spec	0.4
06	27	2009/04/24	HP	Add C962 ~ C965	0.4
07	36	2009/04/24	HP	Add R1190, R1189	0.4
08	29	2009/04/24	HP	Del C574~C577 and C579~C582.	0.4
09	33	2009/04/24	HP	Add R1191, R1192	0.4
10	26	2009/05/02	HP	Add R1198, R1199	0.4
11	04	2009/05/02	HP	Install R1060	0.4
12	35	2009/05/02	HP	Add R1197, C967, C966, C968, R1195, R1196	0.4
13	38	2009/05/02	HP	Add C978, C979	0.4

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Item	Page #	Date	Request Owner	Solution Description	Rev.
01	04	2009/07/21	HP	Add C997, R1219, Q87, R1218 for CPU S3 power savings issue	0.5
02	04	2009/07/02	HP	Add R1226 for CPU S3 power savings issue	0.5
03	05	2009/07/21	HP	Add Q88, Q89 and reserve R1220, R1222 for CPU S3 power savings issue	0.5
04	07	2009/07/02	HP	Add Q91, R1224, C994, C995, C991, C992, C993, C996, Q92, R1223 for CPU S3 power savings issue	0.5
05	19	2009/06/30	HP	Add R1213, D72 for Lid SW issue	0.5
06	37	2009/07/09	HP	Change C864 from 0.047uF to 0.068uF.	0.5

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