

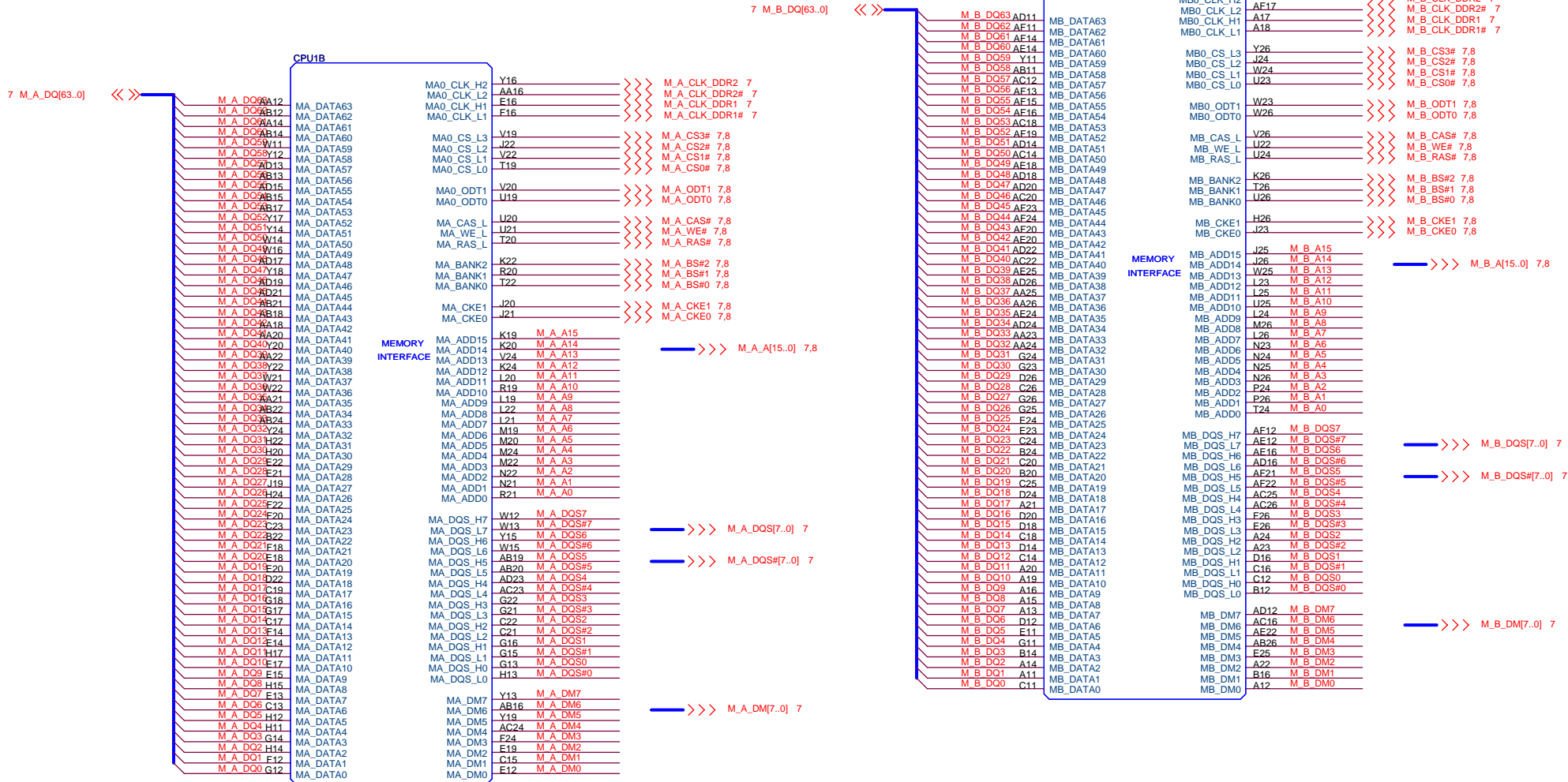
test

緯創資通		Wistron Corporation	
		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
Size	Document Number	Rev	
A3	TIBET	SA	
Date:	Tuesday, August 14, 2007	Sheet 2 of	44



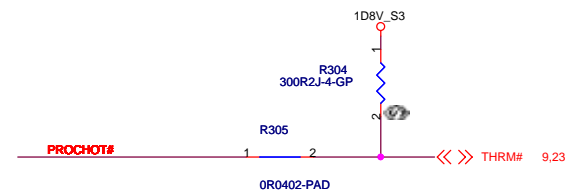
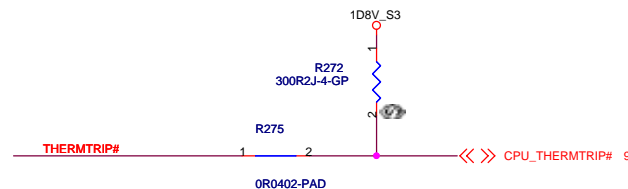
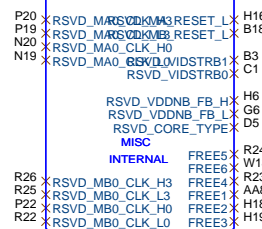
test

緯創資通		Wistron Corporation	
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.			
Title			
CPU(1/4) HT			
Size	Document Number	Rev	
A3	TIBET	SA	
Date:	Tuesday, August 14, 2007	Sheet	3 of 44

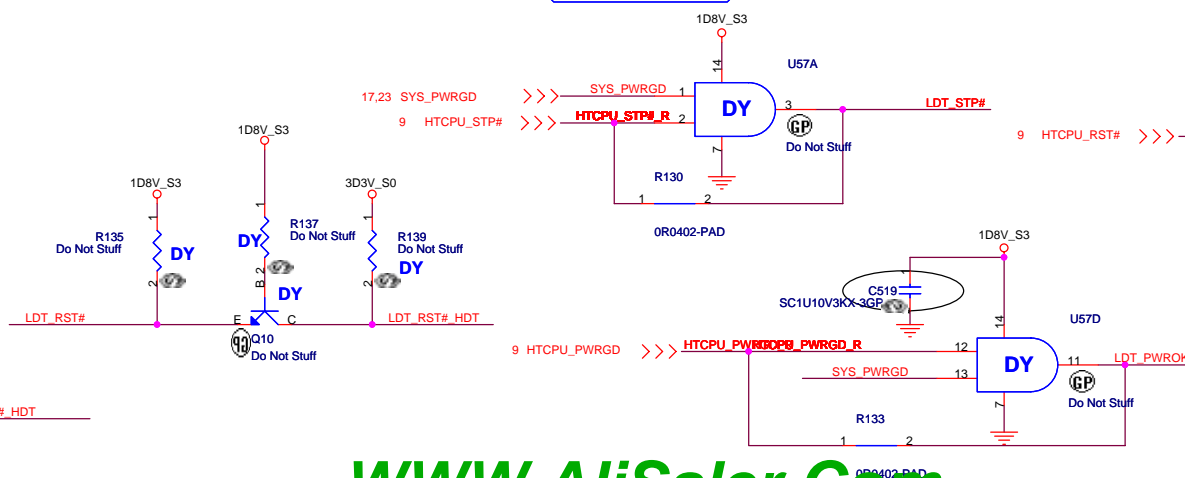
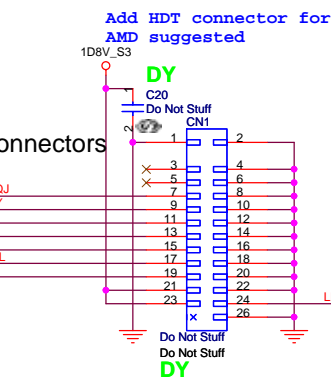
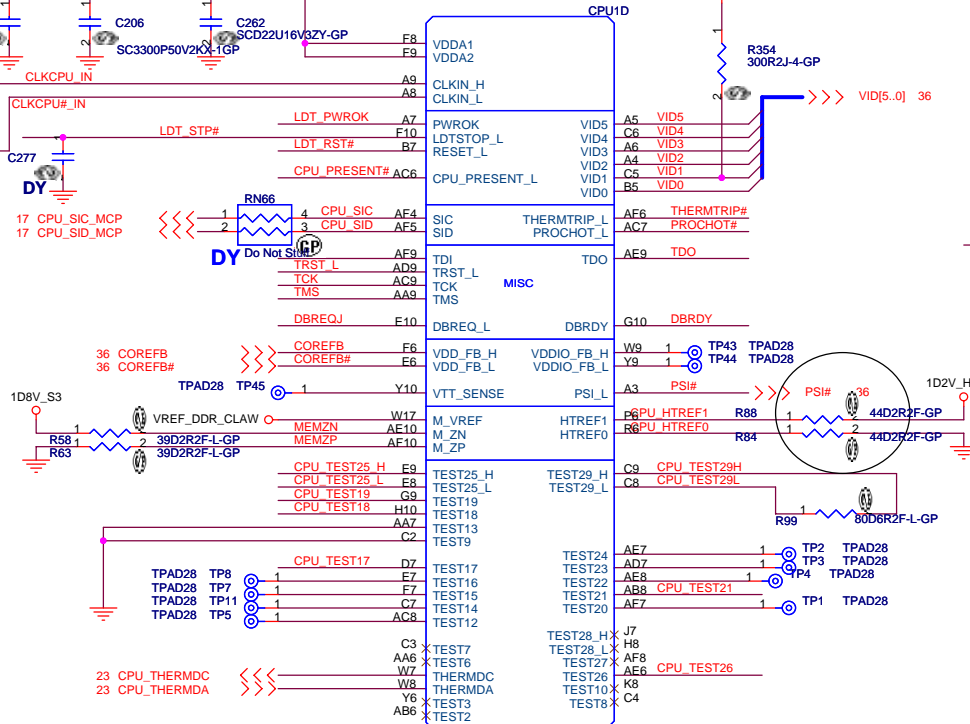
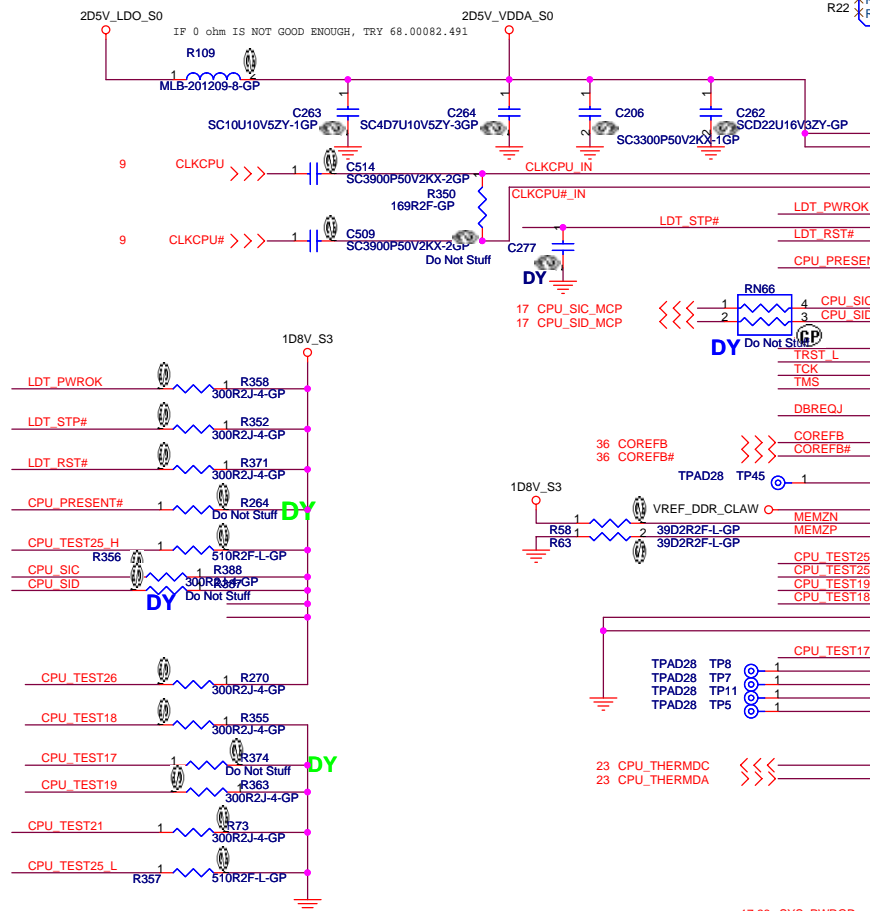
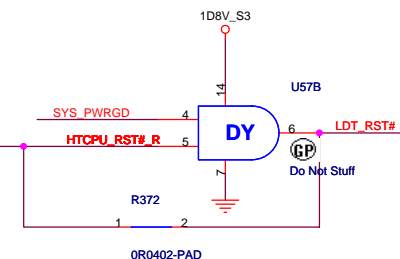
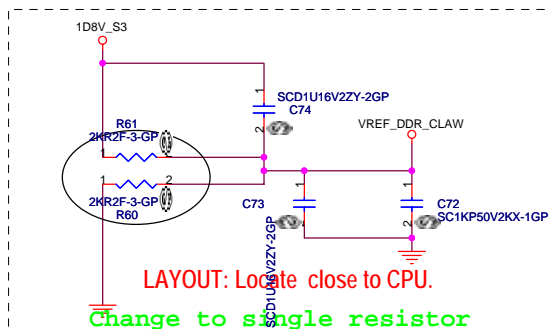


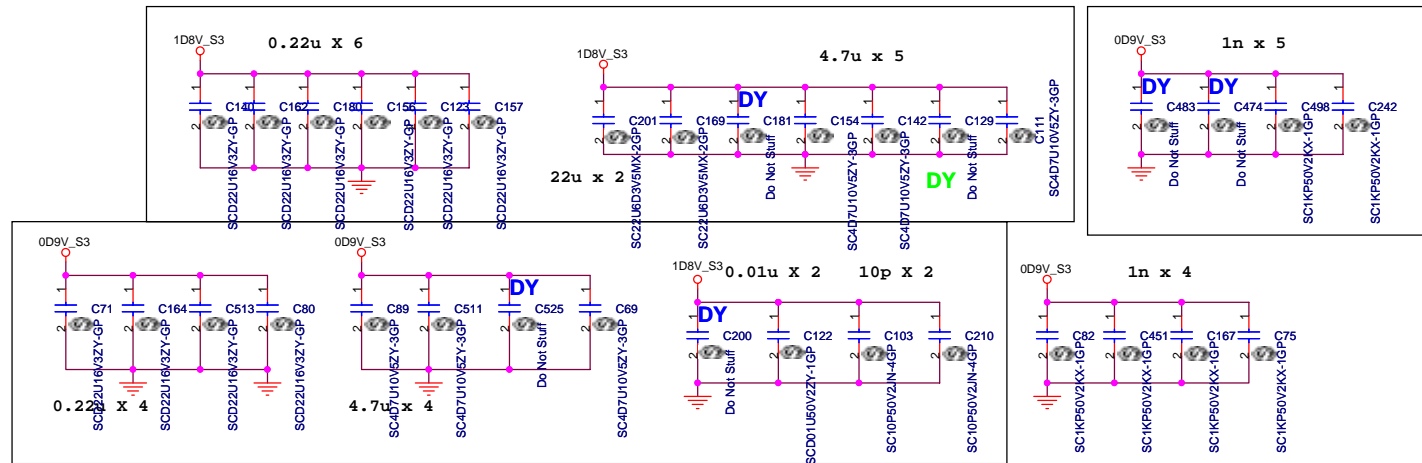
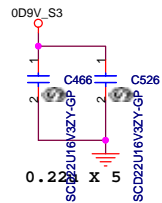
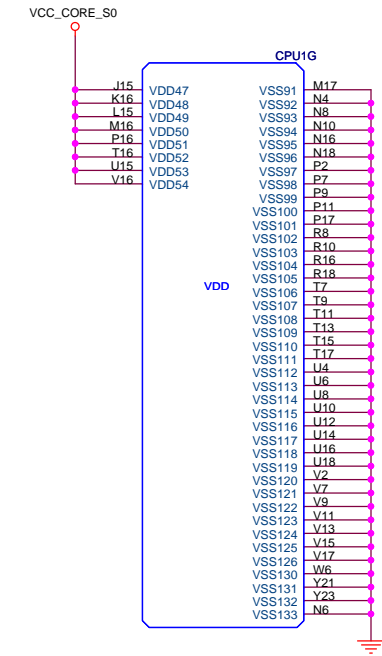
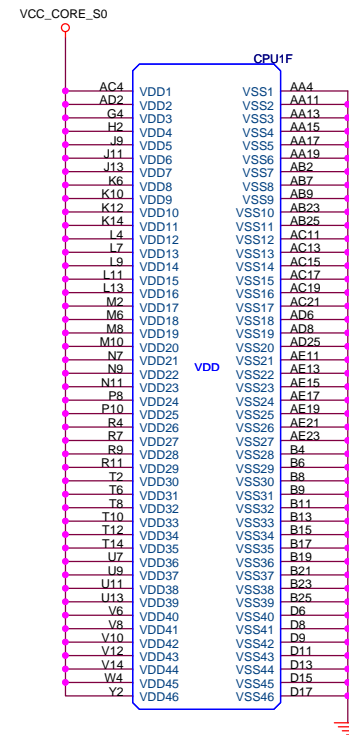
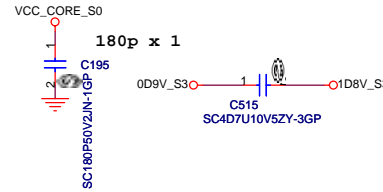
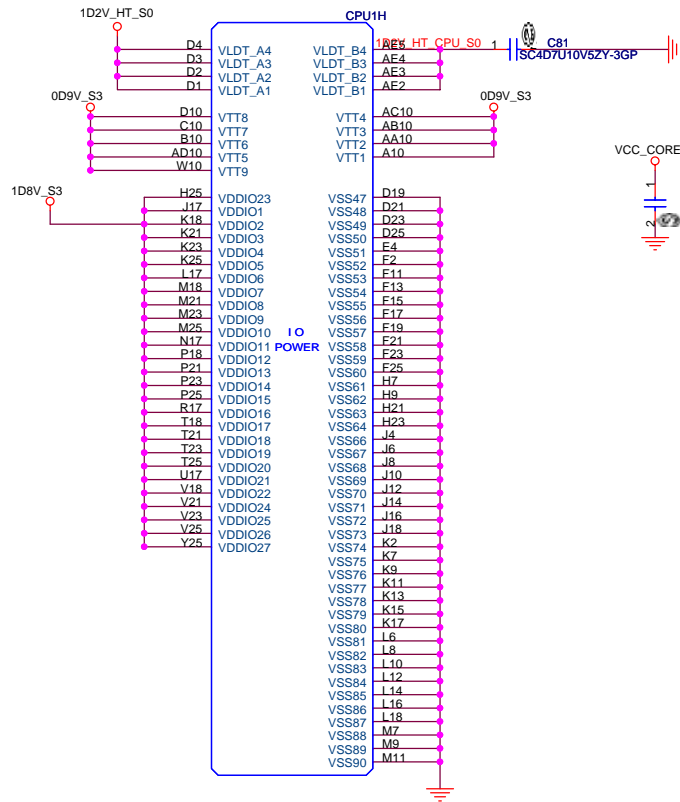
test

緯創資通		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
CPU(2/4) DDR II			
Size A3	Document Number TIBET	Rev SA	
Date: Tuesday, August 14, 2007		Sheet 4	of 44

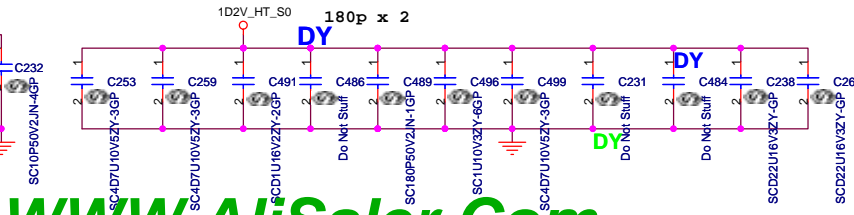
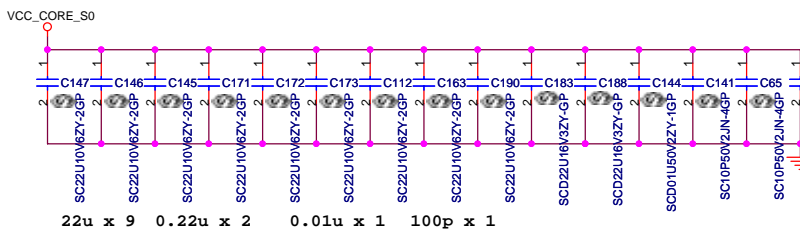


VREF DDR CLAW





LAYOUT: Place on backside of processor.



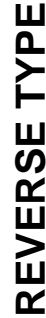
test

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.

Title: **CPU(4/4) POWER**

Size: A3 Document Number: **TIBET** Rev: **SA**

Date: Tuesday, August 14, 2007 Sheet: 6 of 44



The image shows two circuit diagrams. The top diagram shows a capacitor labeled C255 connected between the M_B_CLK_DDR1 signal line and the M_B_CLK_DDR1# signal line. The bottom diagram shows a capacitor labeled C76 connected between the M_B_CLK_DDR2 signal line and the M_B_CLK_DDR2# signal line. Both capacitors are identified as SC1D5P50V2CN-1GF.

High 9.2mm

[illegible]

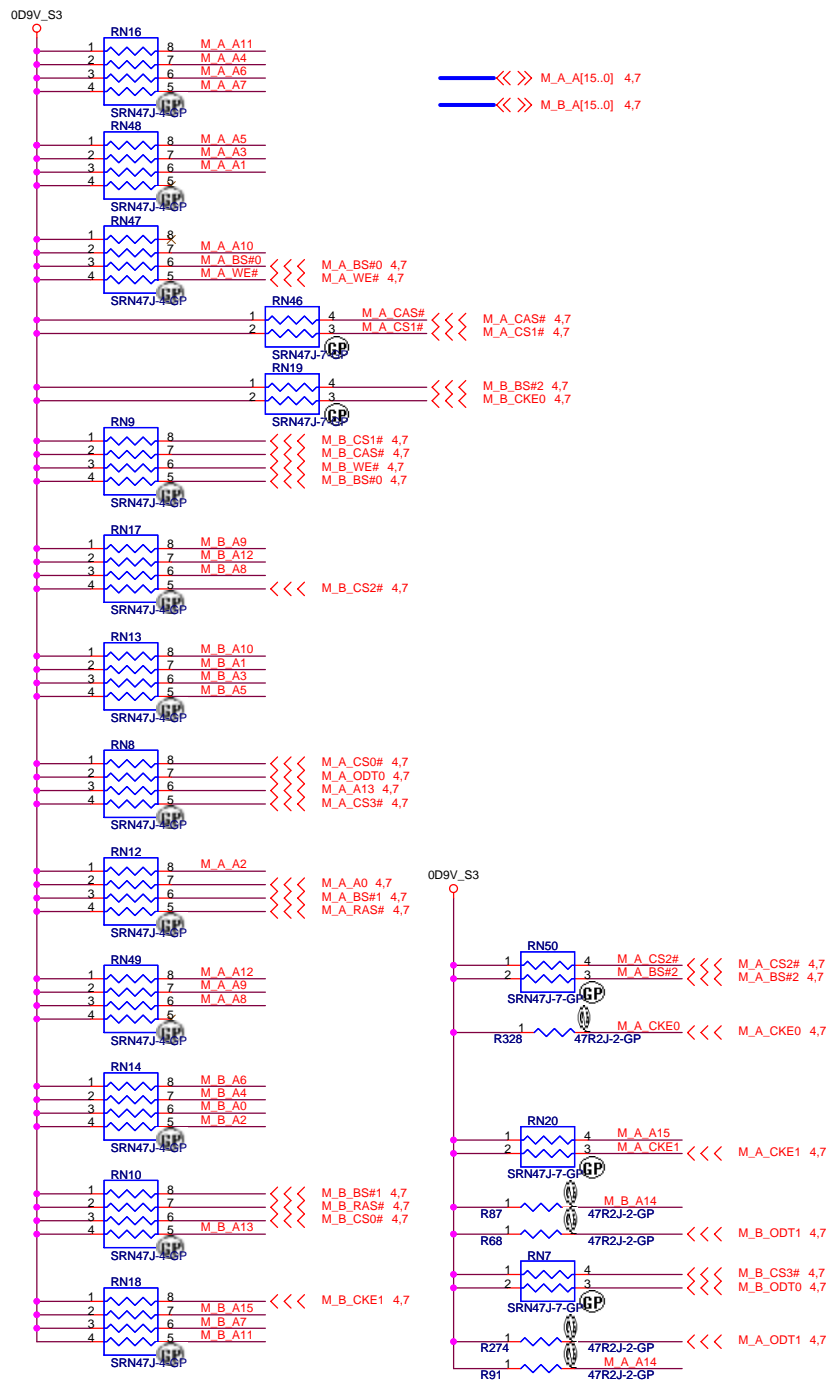
LAYOUT: Locate close to DIMM

1 R342 

Do Not Stuff

PARALLEL TERMINATION

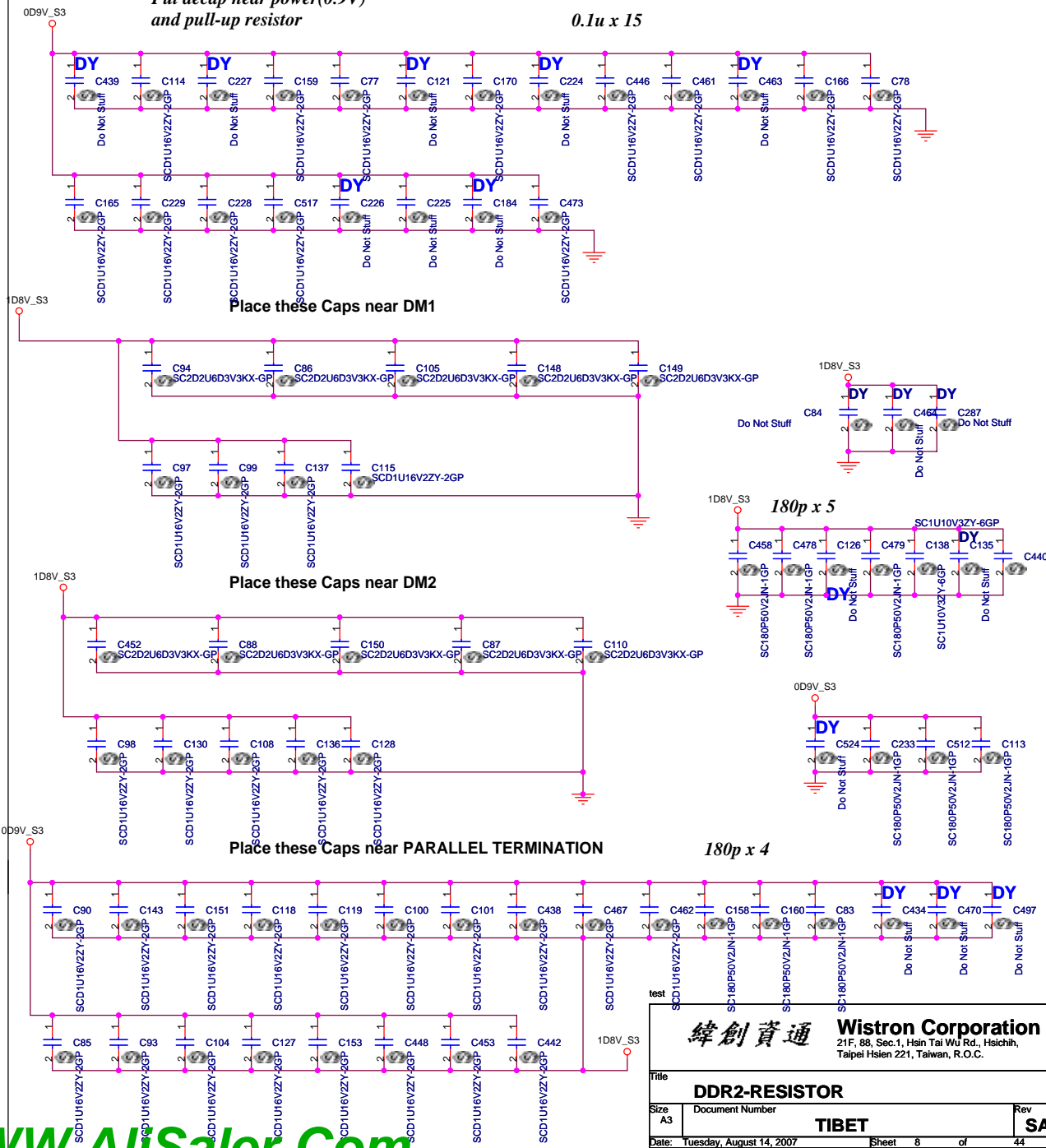
Put decap near power(0.9V) and pull-up resistor



Decoupling Capacitor

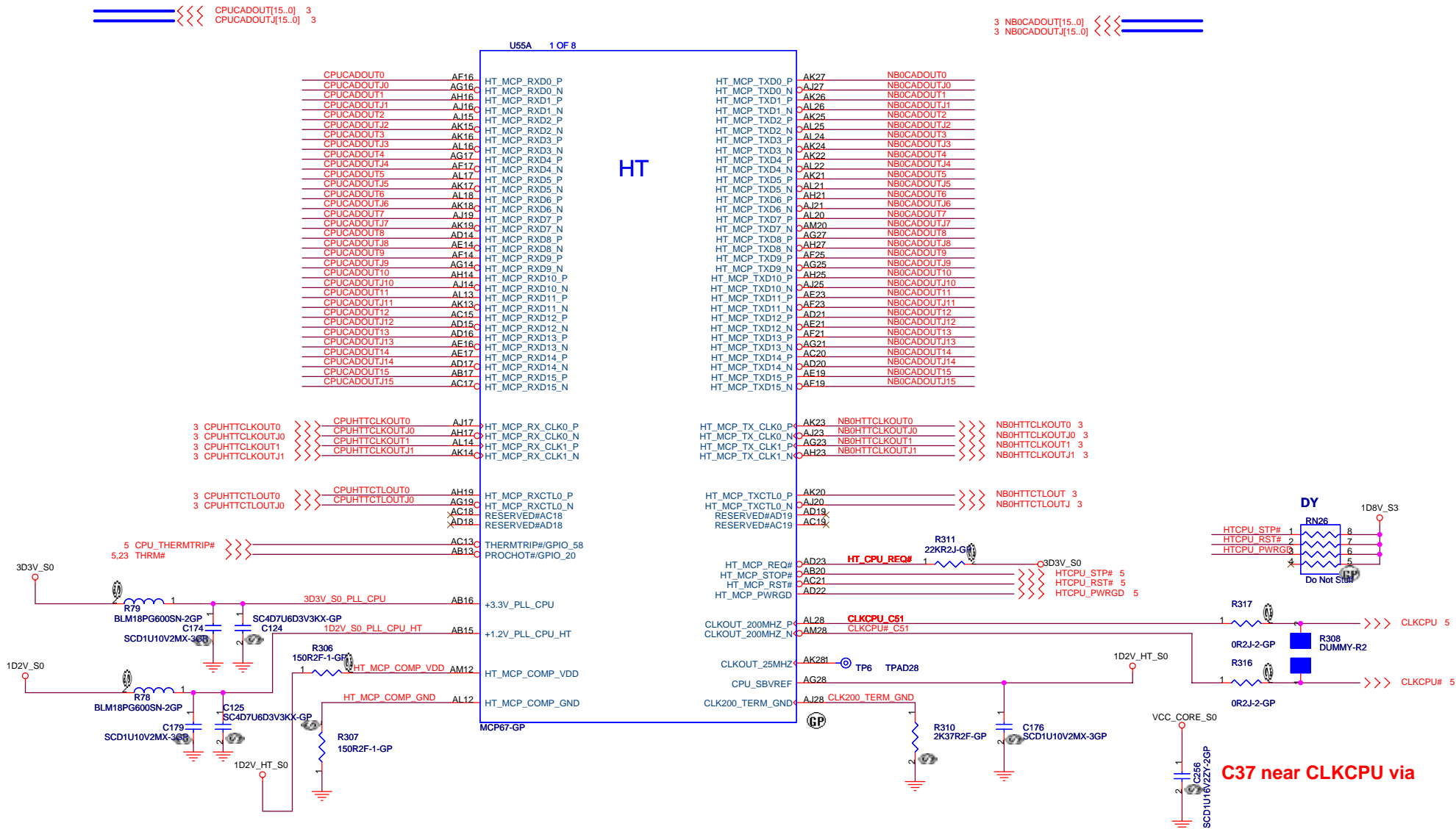
*Put decap near power(0.9V)
and pull-up resistor*

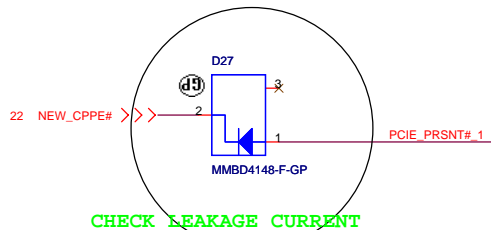
0.1u x 15



緯創資通 **Wistron Corporation**
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Tainai Hsien 221 Taiwan, R.O.C.

Title			
DDR2-RESISTOR			
Size A3	Document Number		Rev
	TIBET		S
Date:	Tuesday, August 14, 2007	Sheet 8 of	44

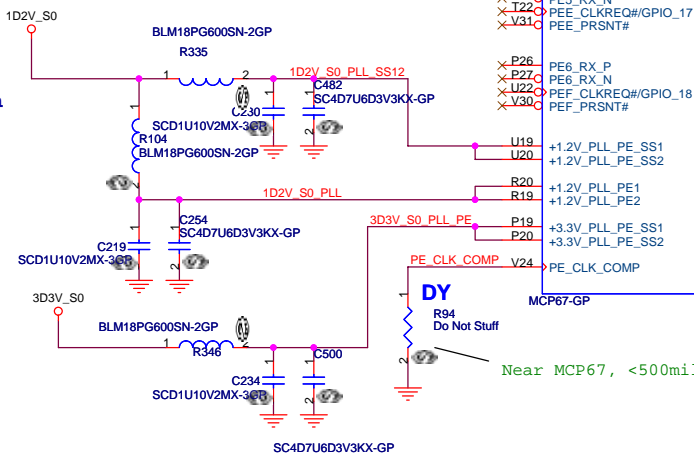
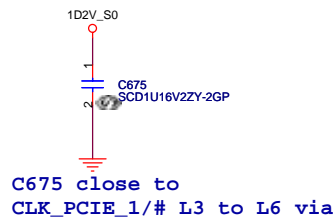




NEW CARD

MINI CARD1

MINI CARD2



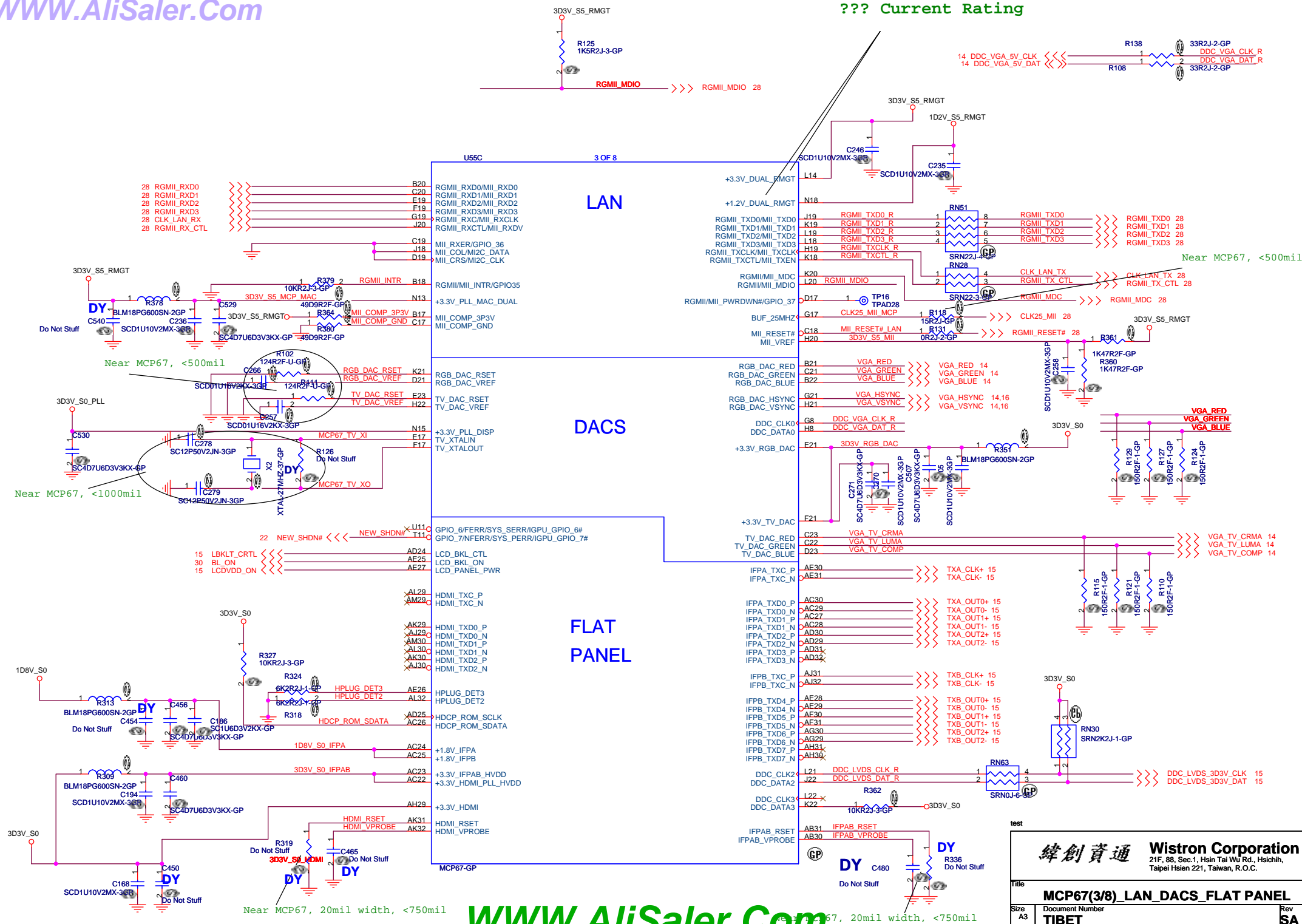
PCIE

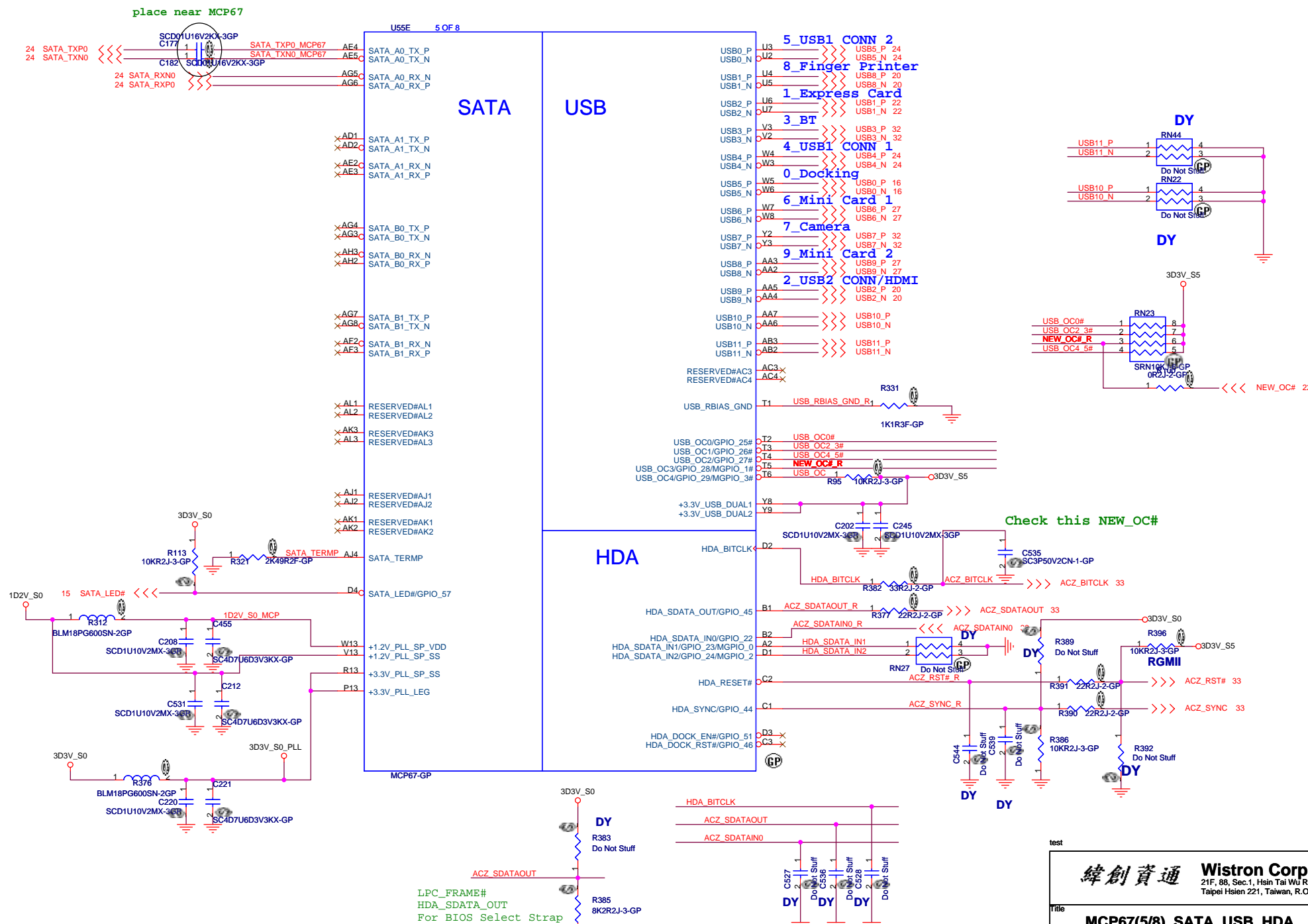
Near Connector, <500mil

For New_Card

test

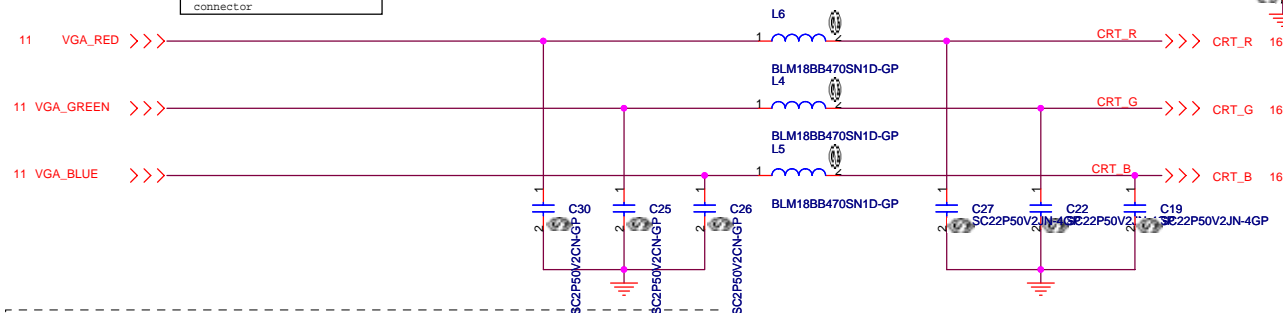
緯創資通 Wistron Corporation	
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title MCP67(2/8)_PCIE	
Size A3	Document Number TIBET
Date: Tuesday, August 14, 2007	Sheet 10 of 44





CRT I/F & CONNECTOR

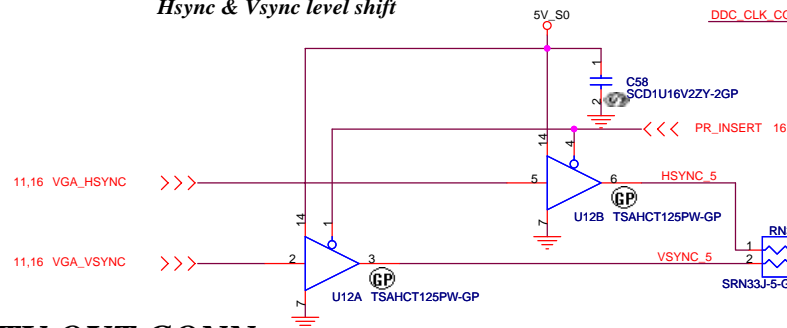
Layout Note:
Place these resistors
close to the CRT-out
connector



Layout Note:

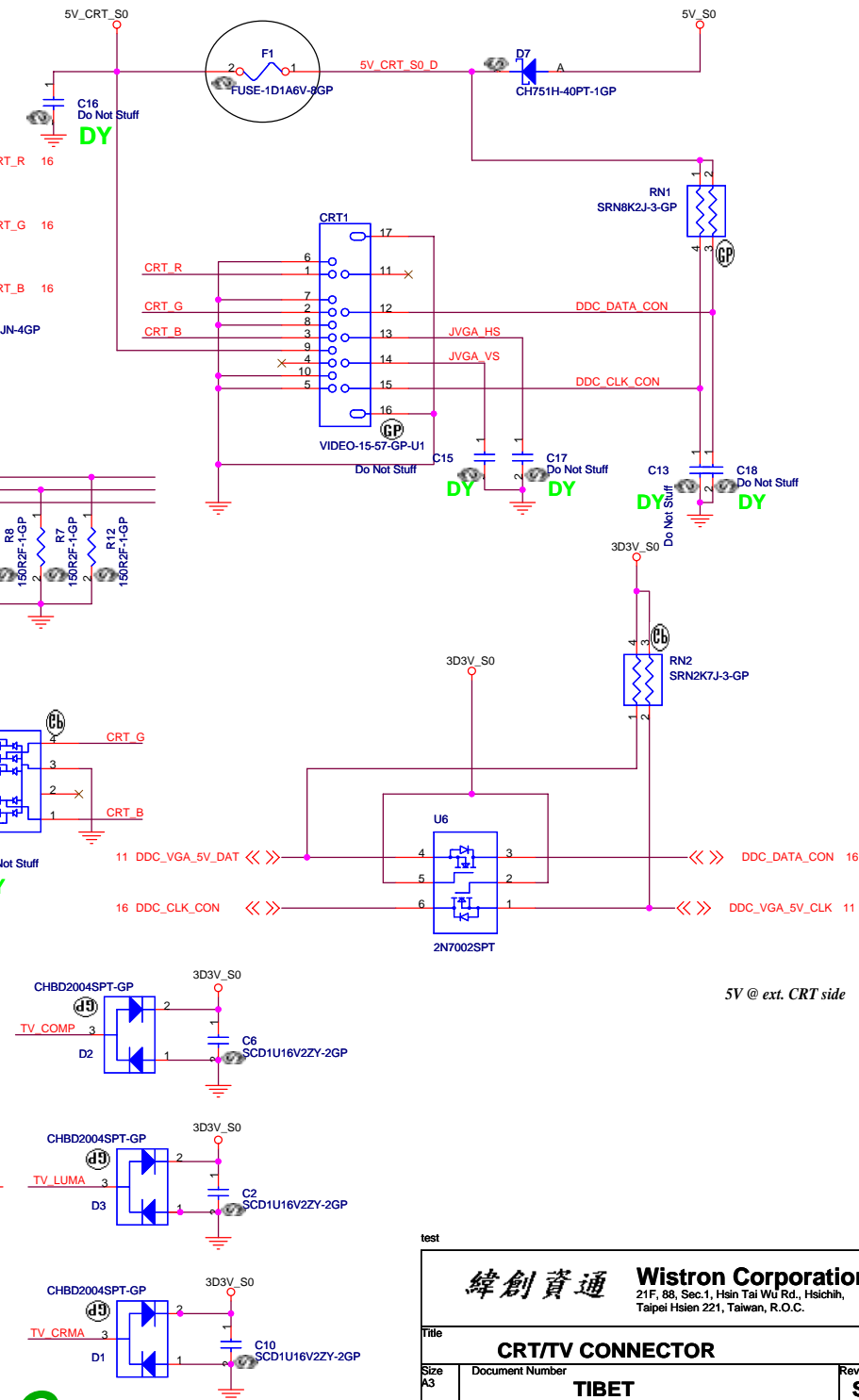
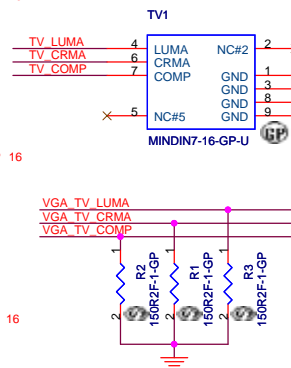
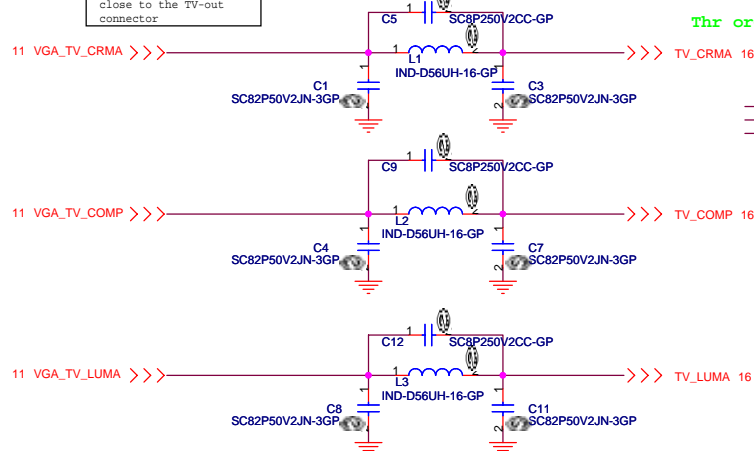
* Must be a ground return path between this ground and the ground on the VGA connector.
Pi-filter & 150 Ohm pull-down resistors should be as close as to CRT CONN. RGB will hit 75 Ohm first, pi-filter, then CRT CONN.

Hsync & Vsync level shift



TV OUT CONN

Layout Note:
Place this 2 resistors
close to the TV-out
connector

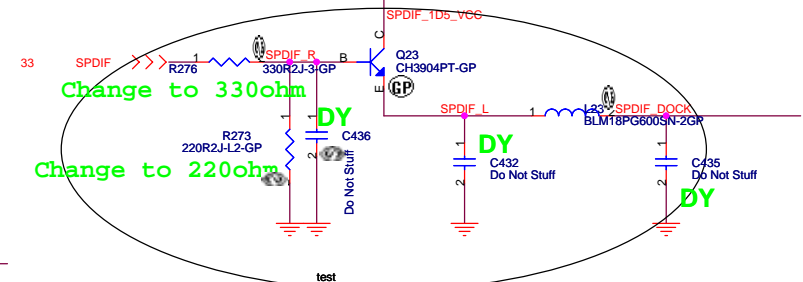
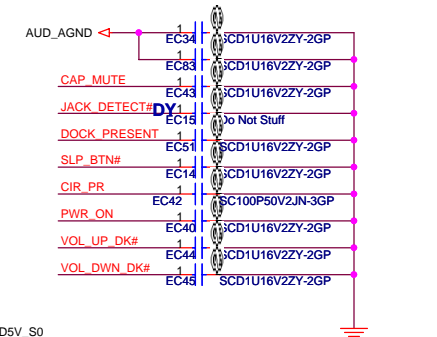
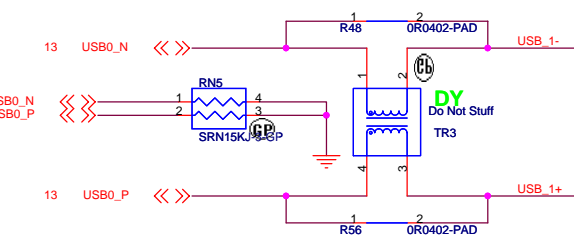
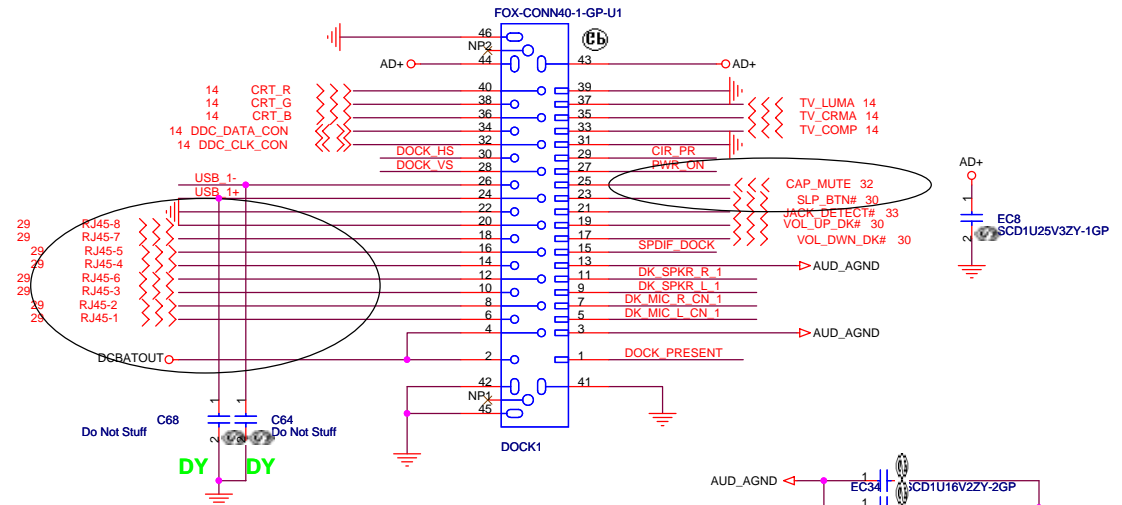
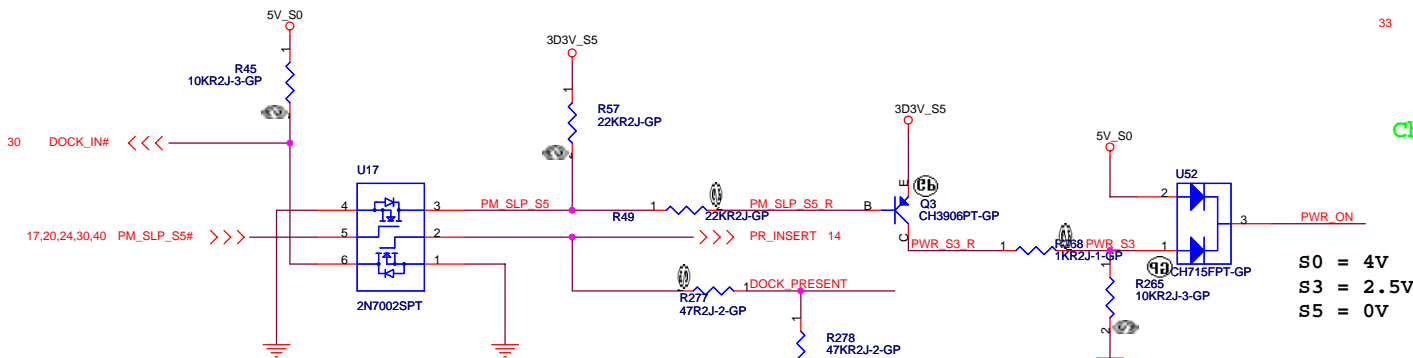
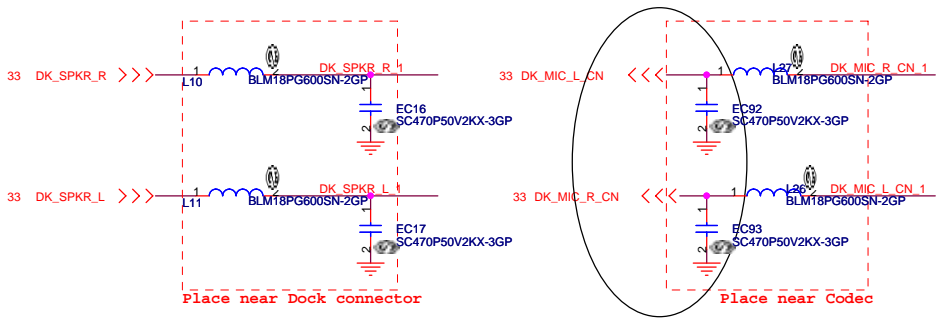
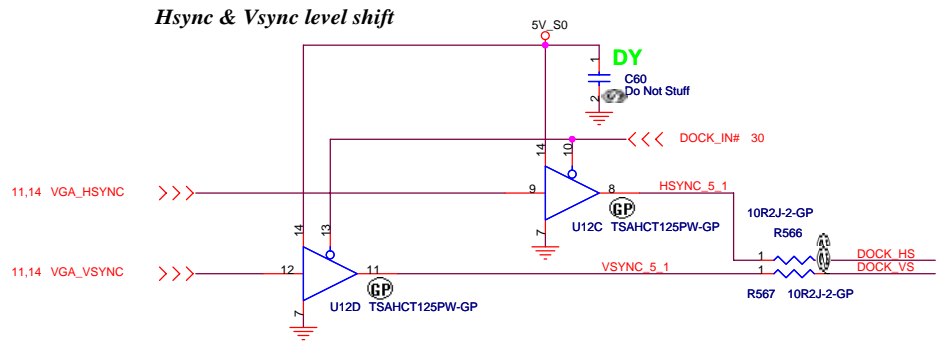


5V @ ext. CRT side

<p>緯創資通 Wistron Corporation</p> <p>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</p>	
<p>Title CRT/TV CONNECTOR</p>	
Size A3	Document Number TIBET
Date: Tuesday, August 14, 2007	Sheet 14 of 44

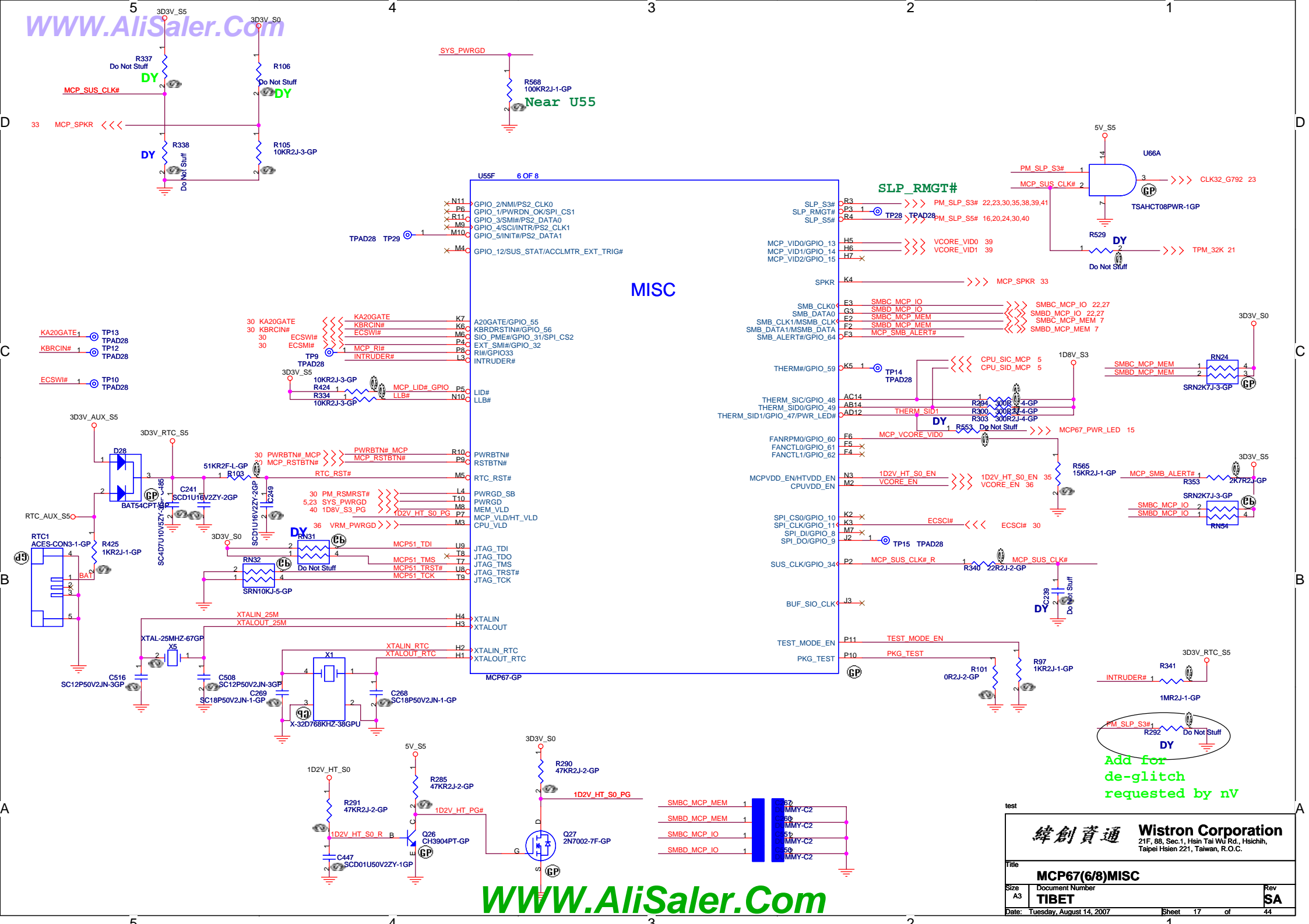


Docking Connector

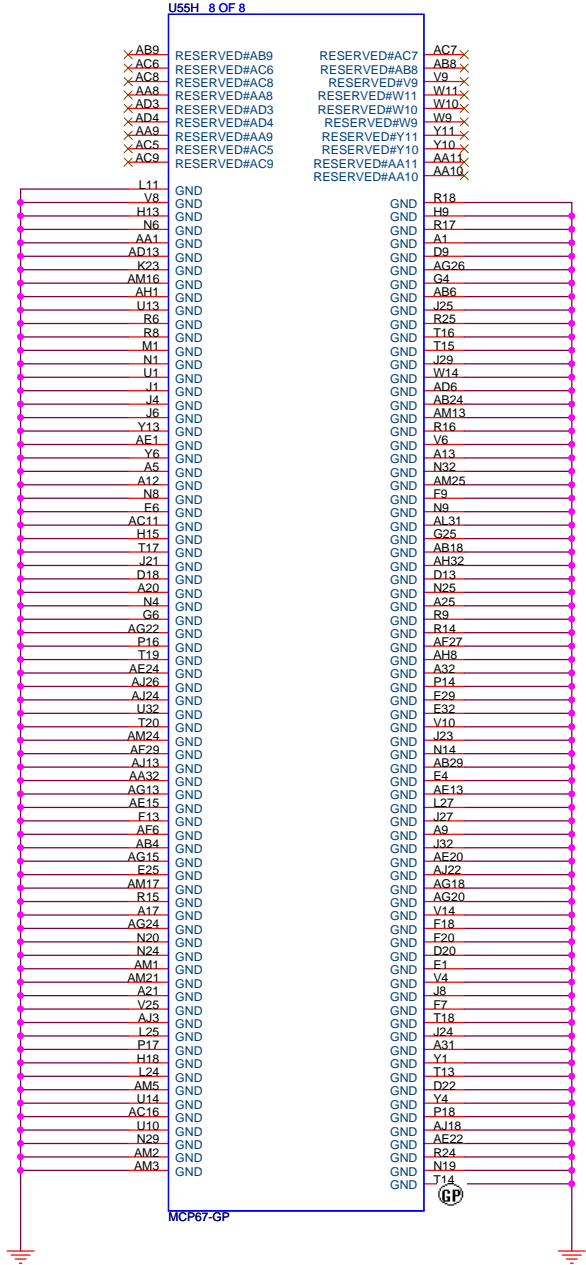


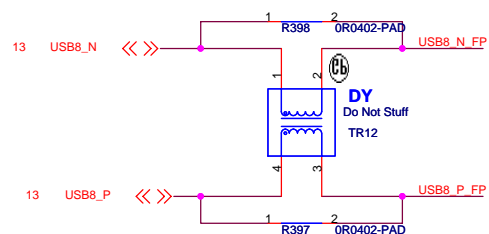
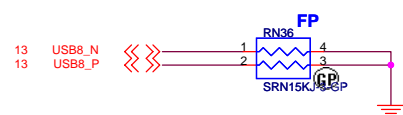
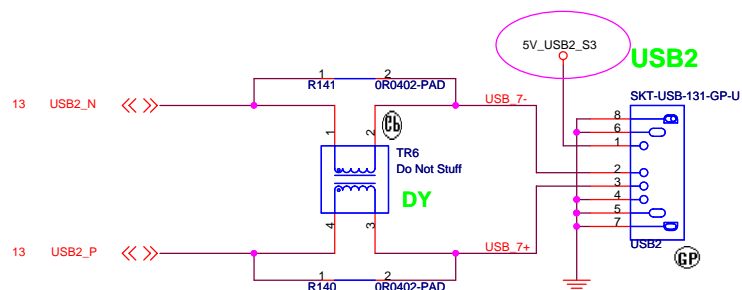
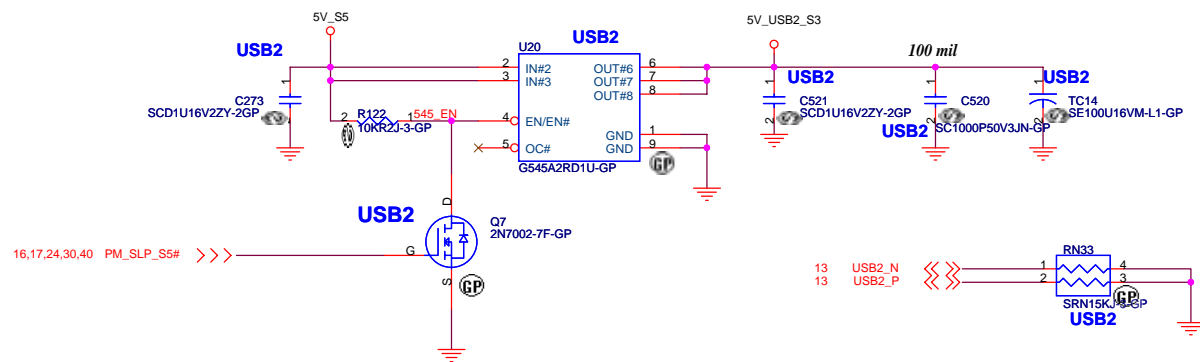
緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Board to board conn/ Docking			
Size	Document Number	Rev	
A3	TIBET	SA	
Date: Tuesday, August 14, 2007	Sheet 16	of 44	

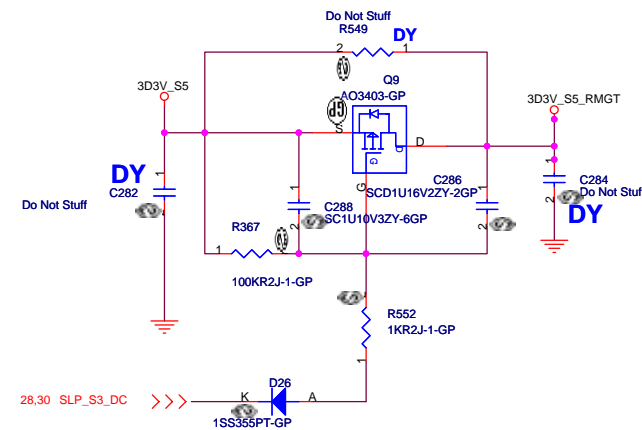
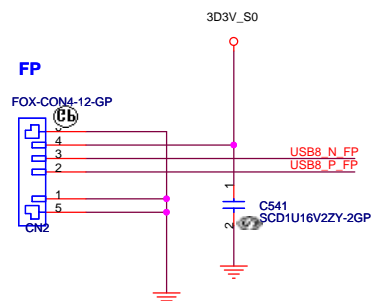




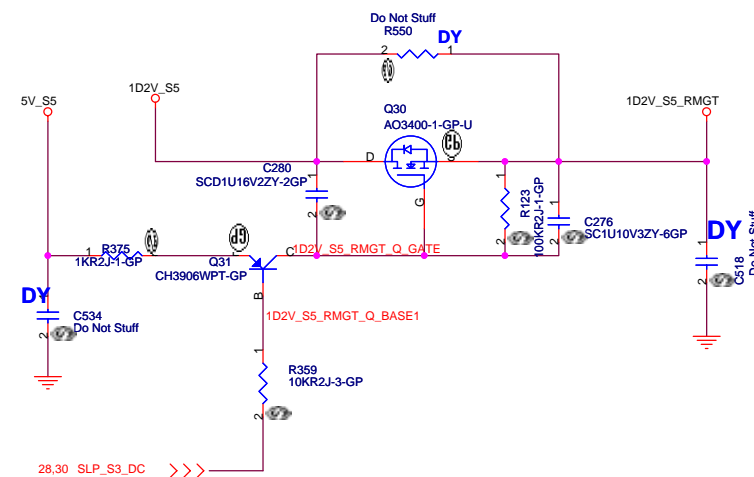




Finger Printer



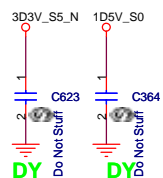
??? Current Rating



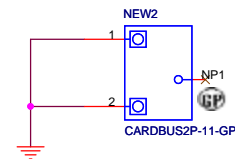
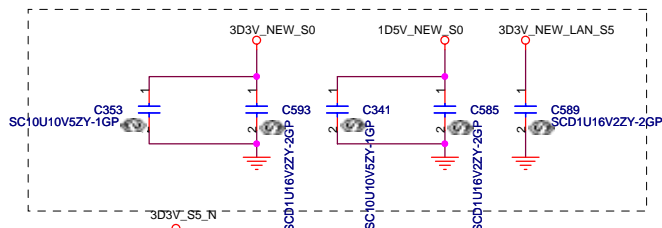


NEWCARD Connector

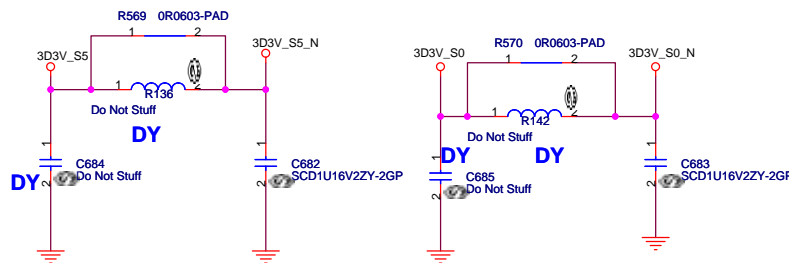
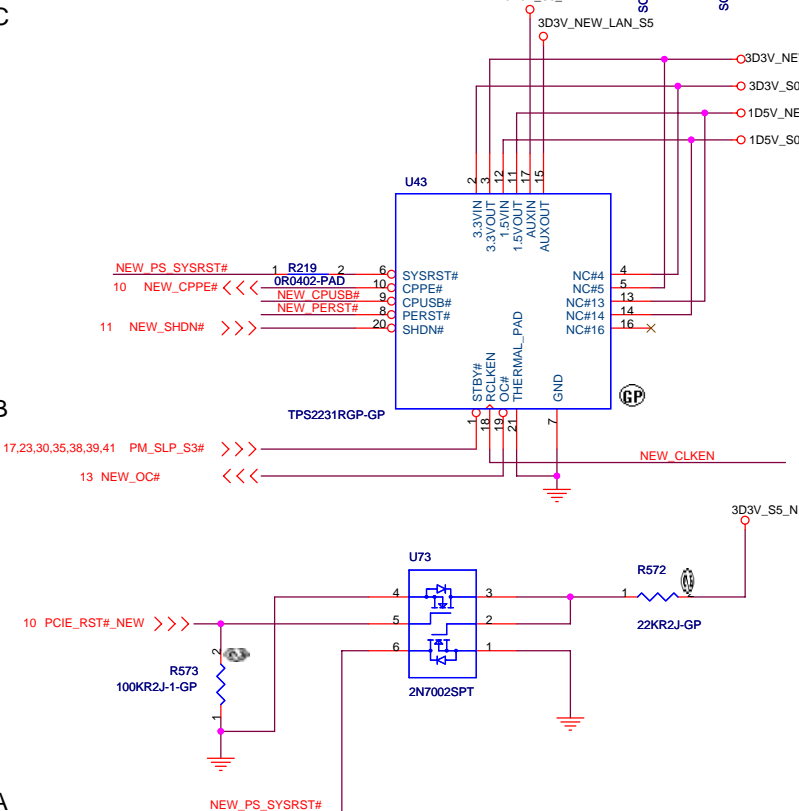
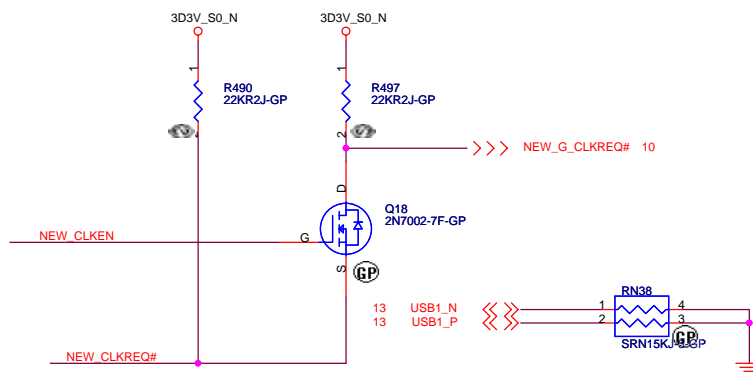
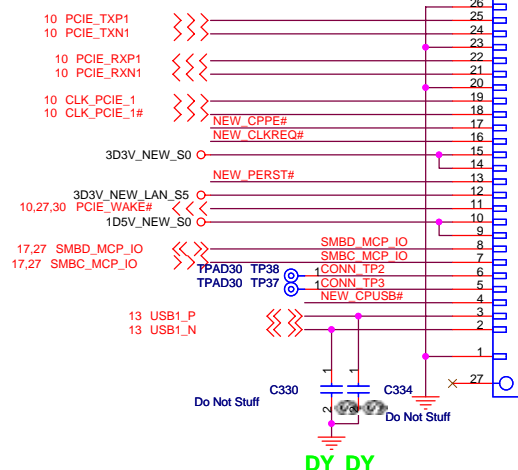
Place them Near to Chip



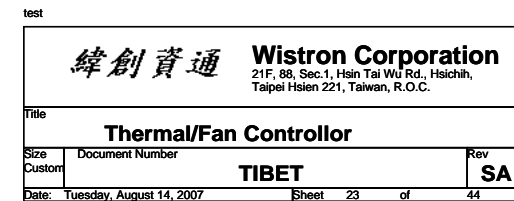
Place them Near to Connector



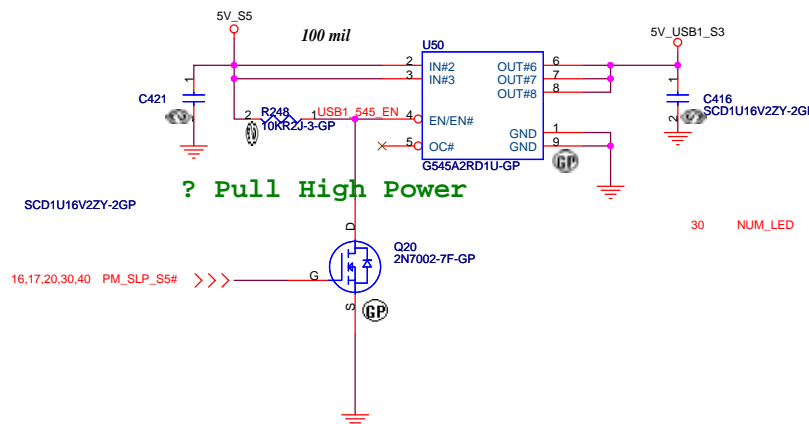
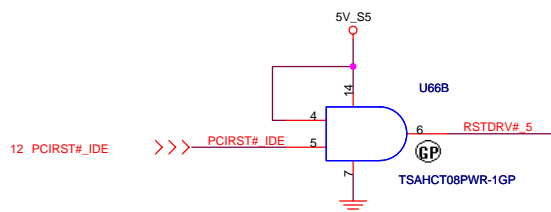
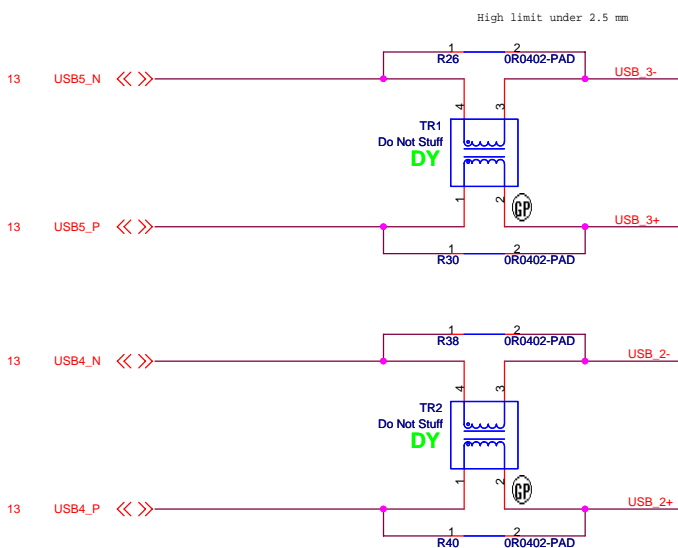
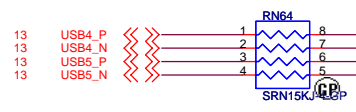
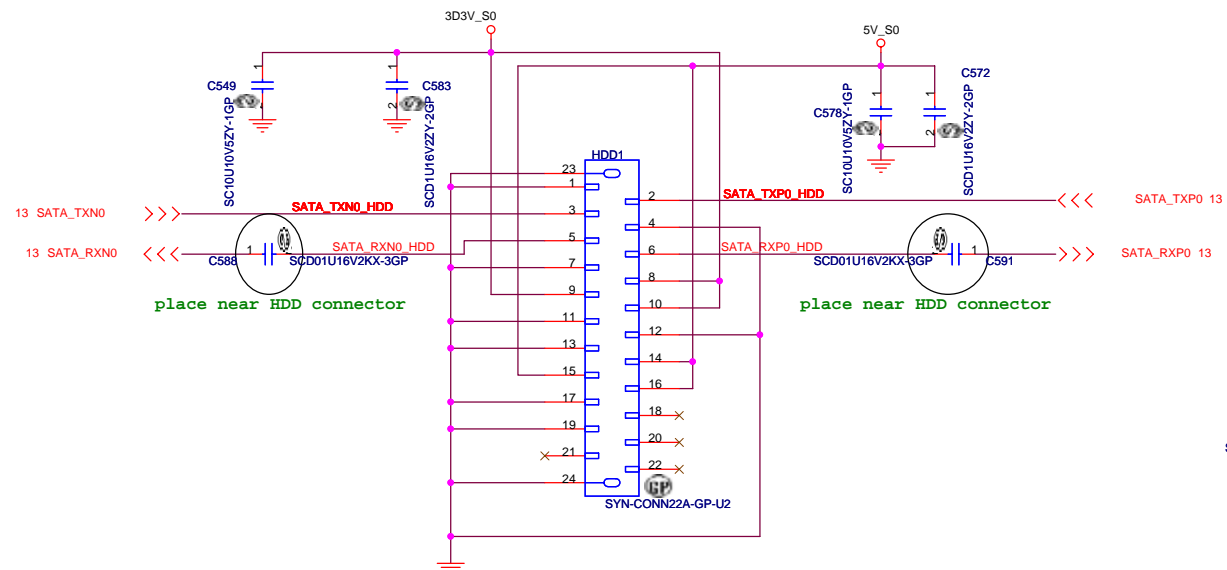
For Newcard socket



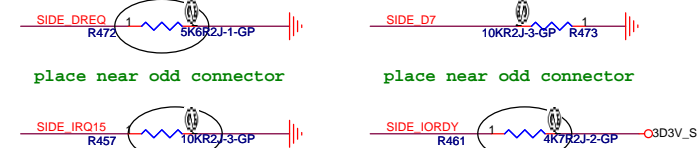
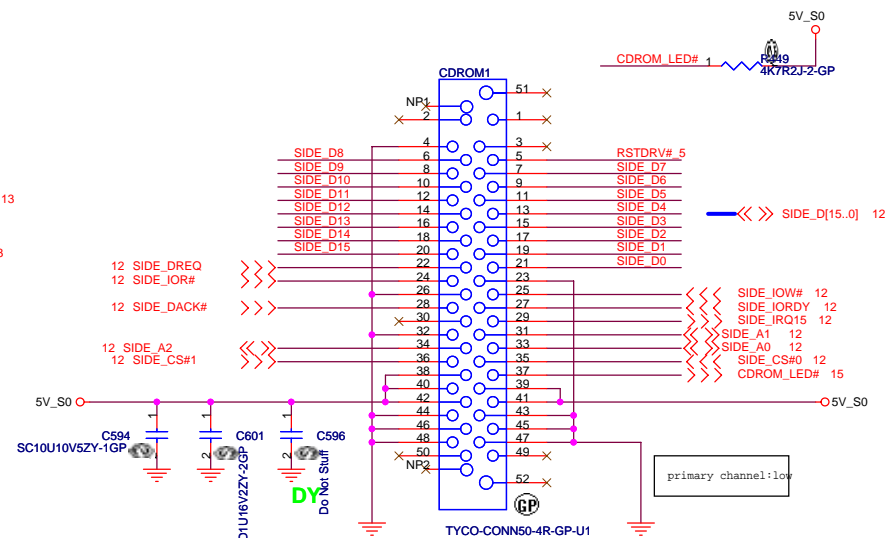
test	
緯創資通 Wistron Corporation	
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title	
NEW CARD	
Size	Document Number
A3	TIBET
Date:	Tuesday, August 14, 2007
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Rev	SA



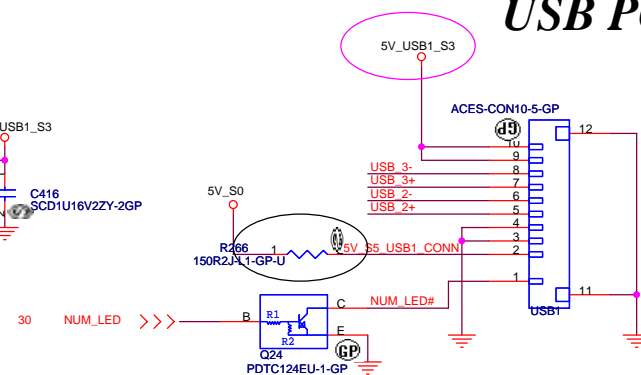
SATA HD Connector

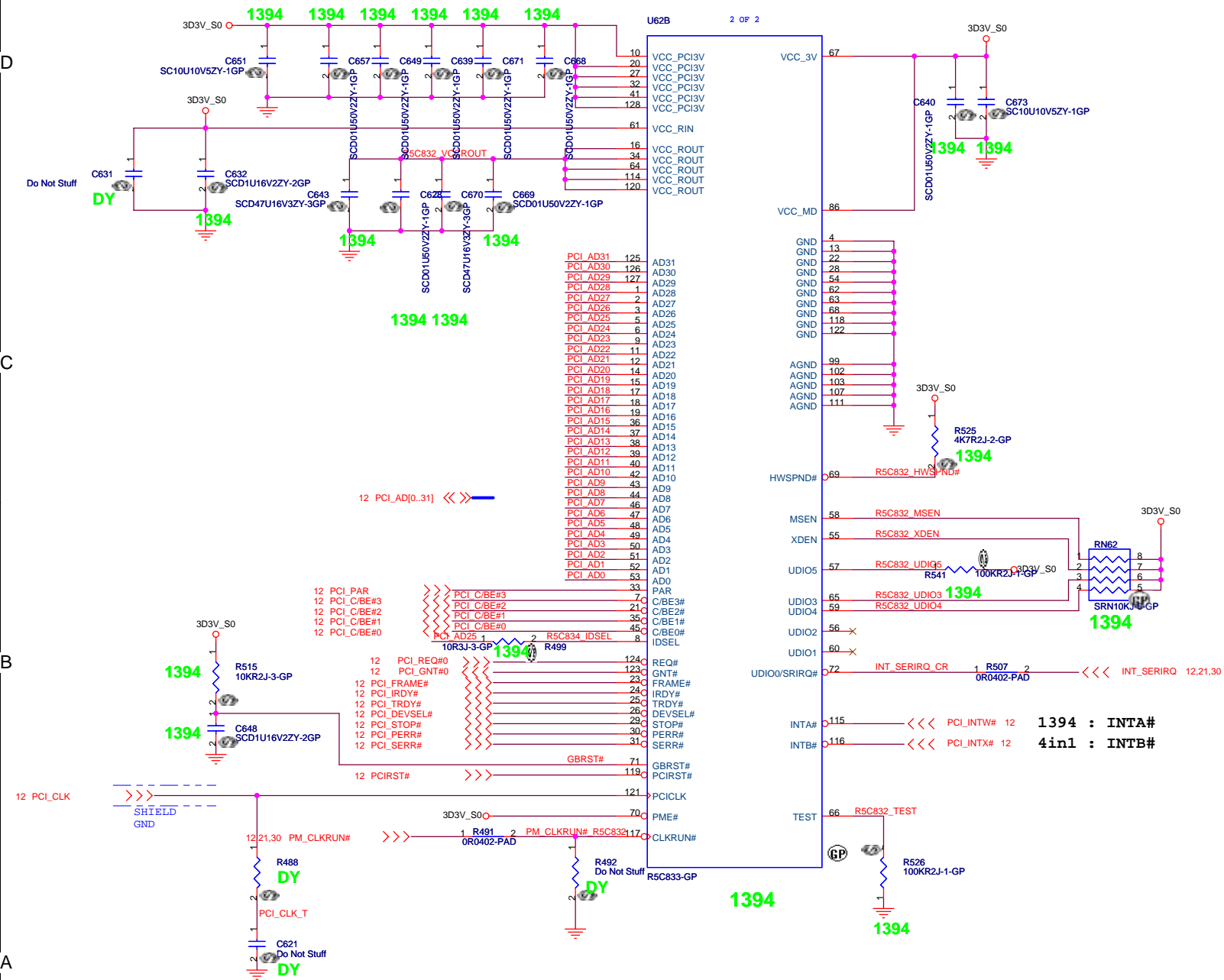


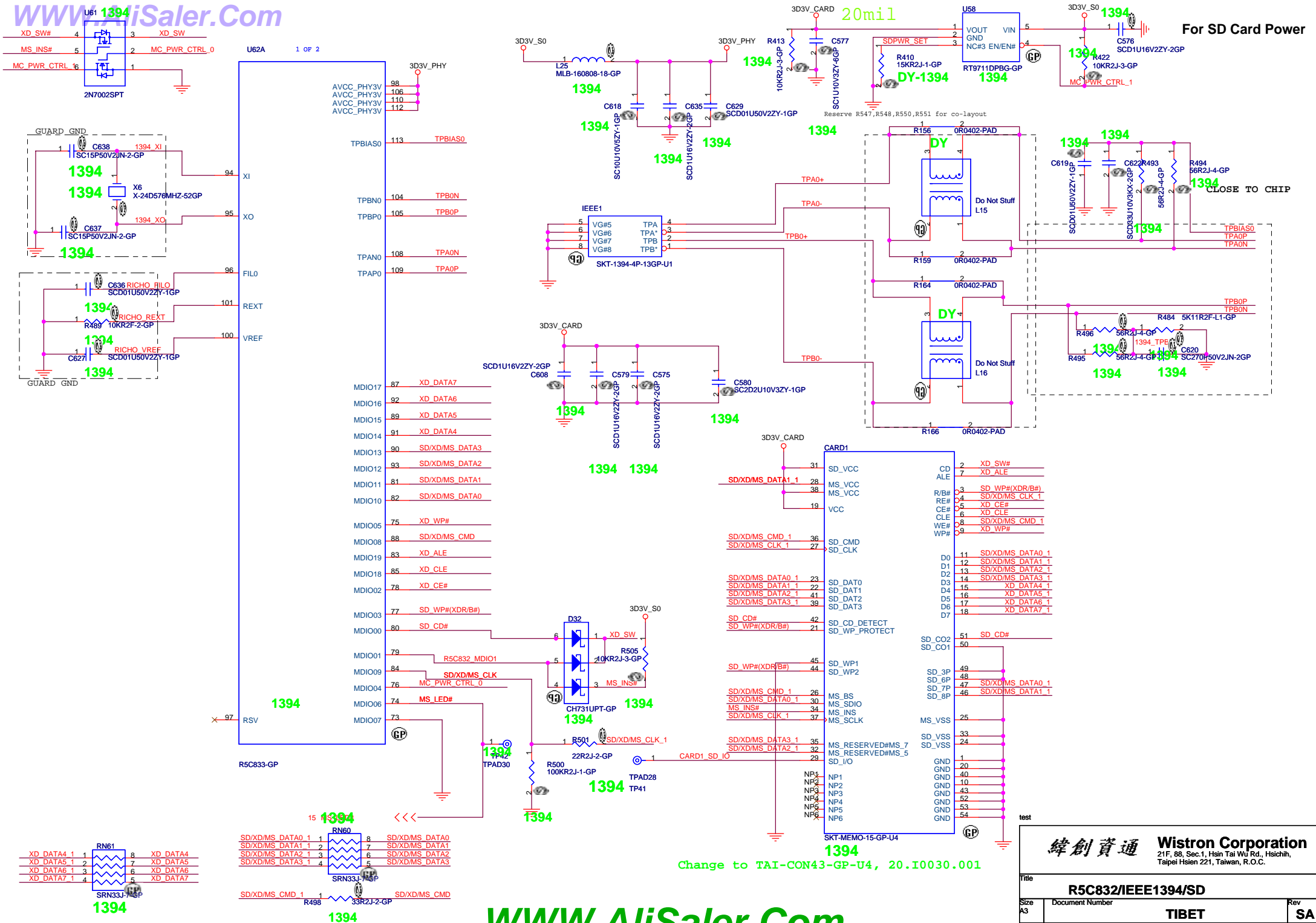
CD-ROM CONNECTOR



USB PORT

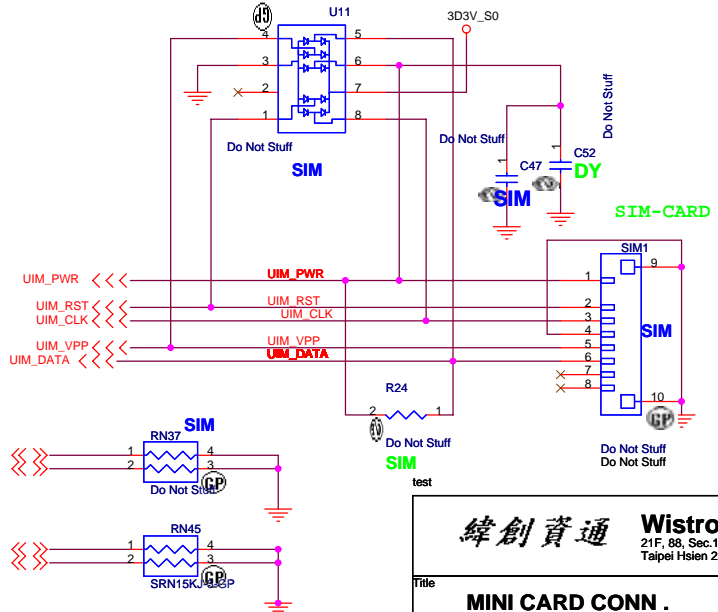






Mini Card Connector 1

WWW.AliSaler.Com

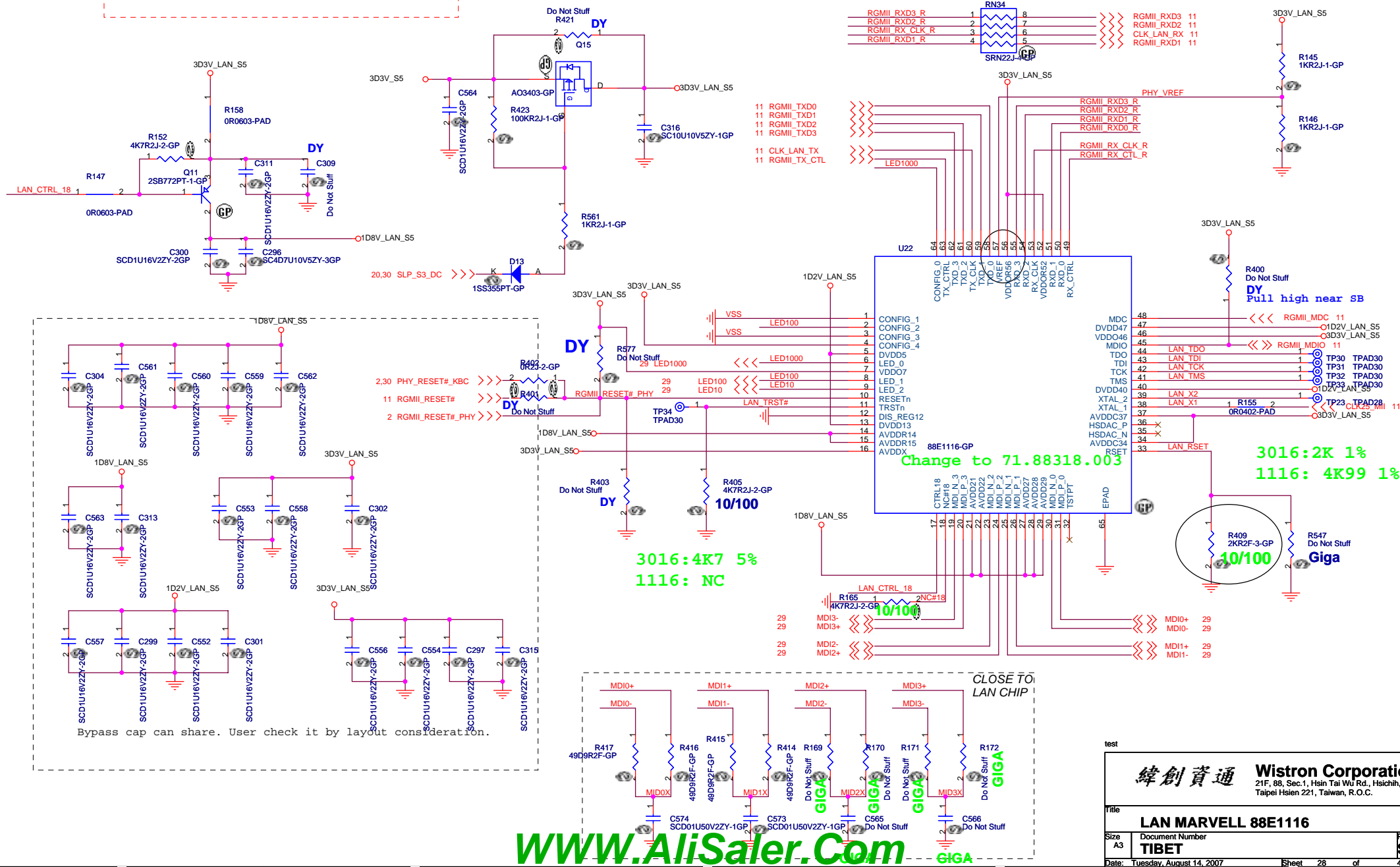


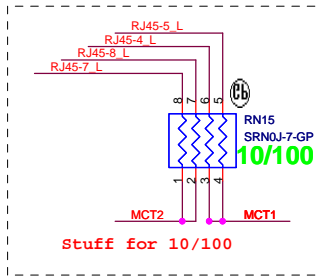
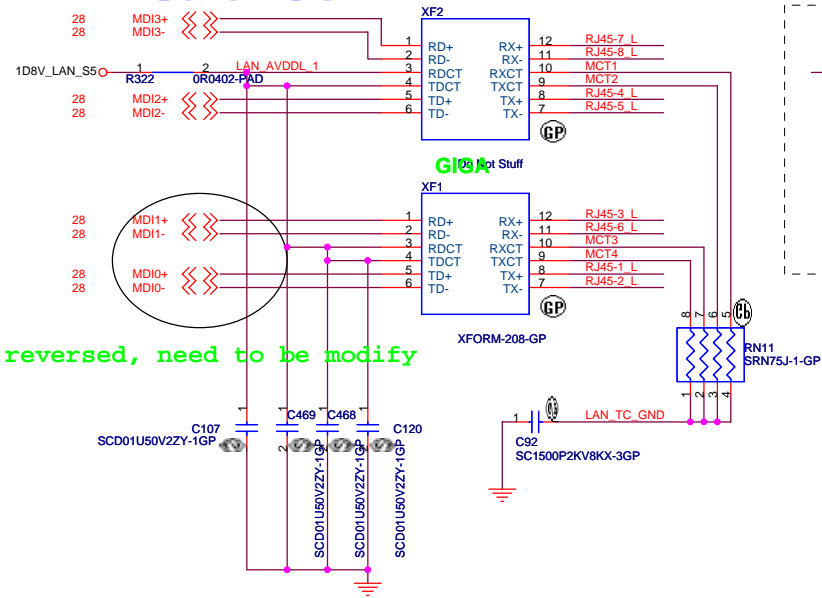
- ```

1. PHY address:00001
2. ENA_XC:Enable Auto-Crossover
3. RGMII_TX:Transmit clock not internally delayed
4. RGMII_RX:Receive clock transition when data transitions
5. RGMRD:Read data from RGMII RX CTL R

```

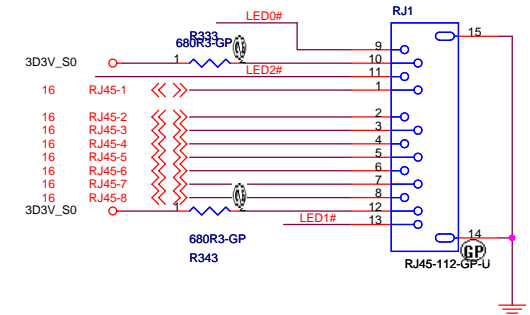
E1116 use external 2.5V single power supply.  
1.8V create by PNP and 1.2V use internal reg.



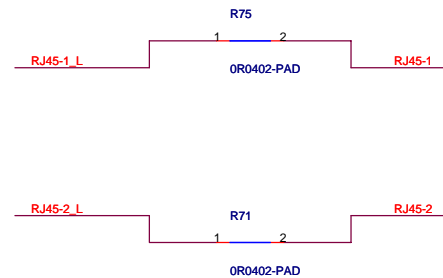
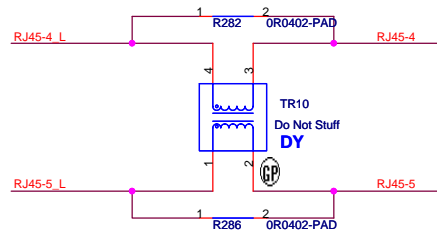
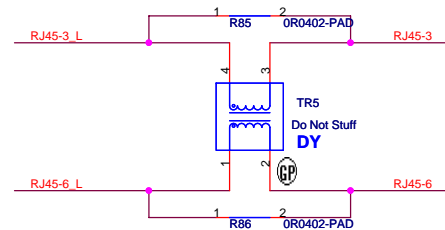
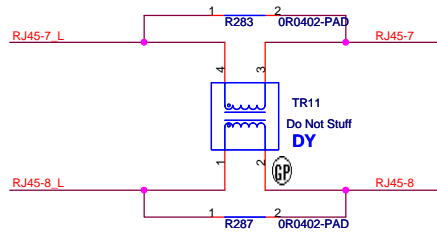


- 1.route on bottom as differential pairs.
- 2.Tx+/Tx- are pairs. Rx+/Rx- are pairs.
- 3.No vias, No 90 degree bends.
- 4.pairs must be equal lengths.
- 5.6mil trace width, 12mil separation.
- 6.36mil between pairs and any other trace.
- 7.Must not cross ground moat, except RJ-45 moat.

GREEN: LINK 10/100 Mbps  
YELLOW: TX/RX ACTIVITY  
ORANGE: LINK 1000Mbps



PIN09 : GREEN  
PIN11 : ORANGE  
PIN13 : YELLOW



test

|                                                                                                                            |                                 |
|----------------------------------------------------------------------------------------------------------------------------|---------------------------------|
| <b>緯創資通</b><br><b>Wistron Corporation</b><br>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,<br>Taipei Hsien 221, Taiwan, R.O.C. |                                 |
| Title<br><b>LAN CONN</b>                                                                                                   |                                 |
| Size<br>A3                                                                                                                 | Document Number<br><b>TIBET</b> |
| Date: Tuesday, August 14, 2007                                                                                             | Sheet 29 of 44                  |
| Rev<br><b>SA</b>                                                                                                           |                                 |



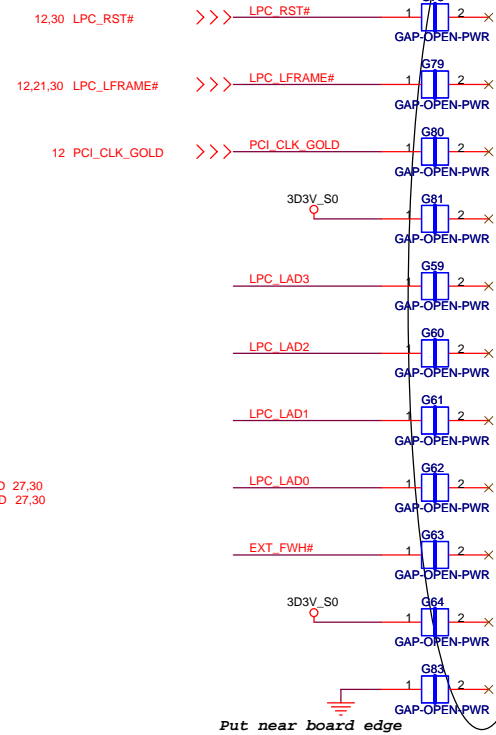
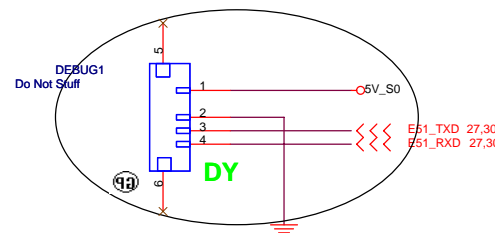
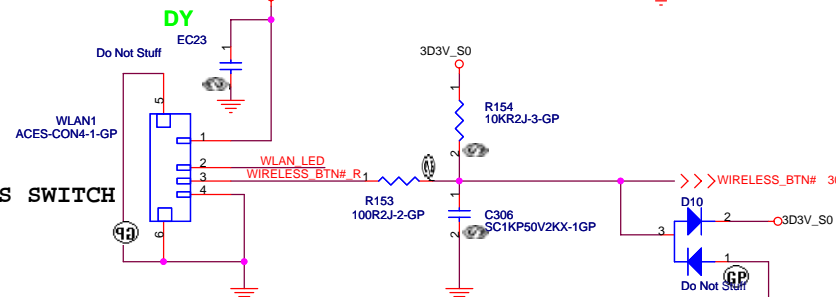
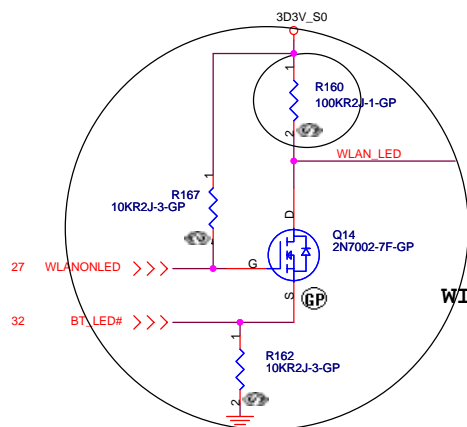
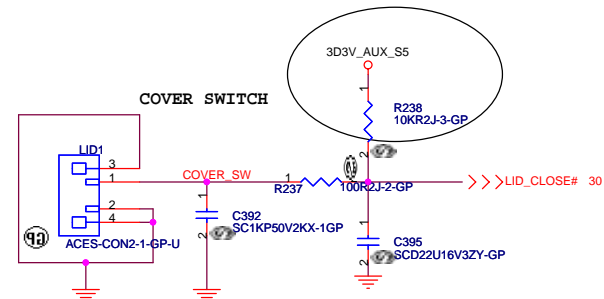
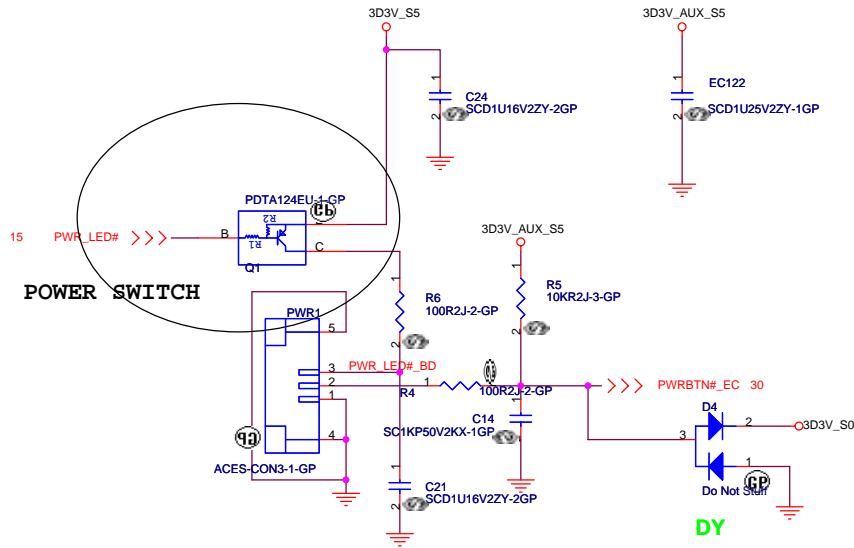


# GOLDEN FINGER FOR DEBUG BOARD

TOP VIEW

A15 (B1)  
A14 (B2)  
:  
:  
A2 (B14)  
A1 (B15)

(BOTTOM VIEW)

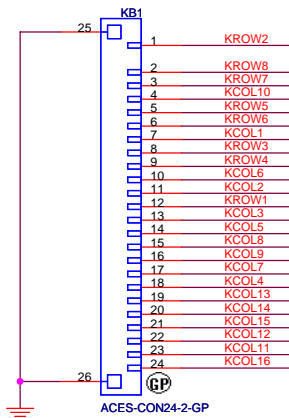


# Internal KeyBoard Connector

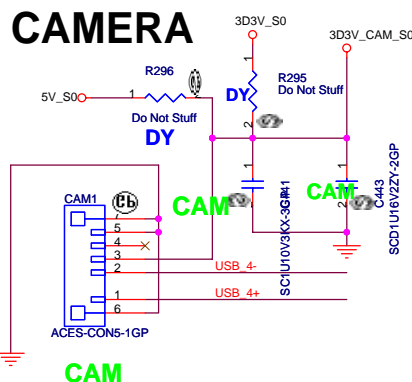
30 KROW[1..8] <<< <<<  
30 KCOL[1..16] <<< <<<

Keyboard matrix ( from vendor )

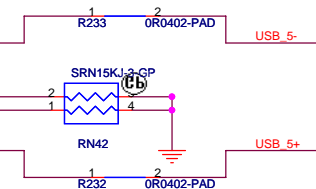
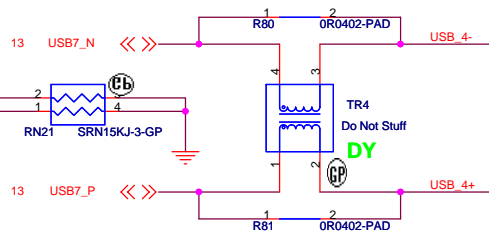
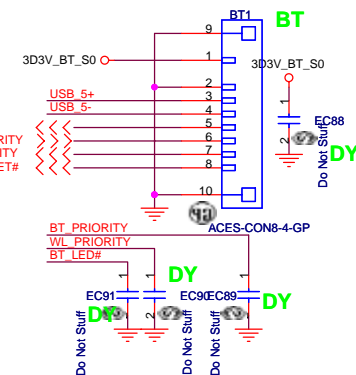
|            | US | Eur | Jap |
|------------|----|-----|-----|
| MATRIXID1# | 0  | 1   | 0   |
| MATRIXID2# | 0  | 0   | 1   |



## CAMERA



## BT



R349, R253 place near G792

23,30 KBC\_SCL1 <<< <<< 23,30 KBC\_SDA1 <<< <<<

5V\_S0 <<< <<< 3D3V\_S0 <<< <<<

3D3V\_AUX\_S5 <<< <<< 3D3V\_AUX\_S5 <<< <<<

INSTANT\_ON\_BTN# <<< <<< CAP\_DAT <<< <<<

30,33,34 EAPD <<< <<< CAP\_MUTE <<< <<<

3D3V\_S0 <<< <<< 3D3V\_S0 <<< <<<

3D3V\_AUX\_S5 <<< <<< 3D3V\_AUX\_S5 <<< <<<

INSTANT\_ON\_BTN# <<< <<< CAP\_DAT <<< <<<

30,33,34 EAPD <<< <<< CAP\_MUTE <<< <<<

3D3V\_S0 <<< <<< 3D3V\_S0 <<< <<<

3D3V\_AUX\_S5 <<< <<< 3D3V\_AUX\_S5 <<< <<<

INSTANT\_ON\_BTN# <<< <<< CAP\_DAT <<< <<<

30,33,34 EAPD <<< <<< CAP\_MUTE <<< <<<

3D3V\_S0 <<< <<< 3D3V\_S0 <<< <<<

3D3V\_AUX\_S5 <<< <<< 3D3V\_AUX\_S5 <<< <<<

INSTANT\_ON\_BTN# <<< <<< CAP\_DAT <<< <<<

30,33,34 EAPD <<< <<< CAP\_MUTE <<< <<<

3D3V\_S0 <<< <<< 3D3V\_S0 <<< <<<

3D3V\_AUX\_S5 <<< <<< 3D3V\_AUX\_S5 <<< <<<

INSTANT\_ON\_BTN# <<< <<< CAP\_DAT <<< <<<

30,33,34 EAPD <<< <<< CAP\_MUTE <<< <<<

3D3V\_S0 <<< <<< 3D3V\_S0 <<< <<<

3D3V\_AUX\_S5 <<< <<< 3D3V\_AUX\_S5 <<< <<<

INSTANT\_ON\_BTN# <<< <<< CAP\_DAT <<< <<<

30,33,34 EAPD <<< <<< CAP\_MUTE <<< <<<

3D3V\_S0 <<< <<< 3D3V\_S0 <<< <<<

3D3V\_AUX\_S5 <<< <<< 3D3V\_AUX\_S5 <<< <<<

INSTANT\_ON\_BTN# <<< <<< CAP\_DAT <<< <<<

30,33,34 EAPD <<< <<< CAP\_MUTE <<< <<<

## TouchPad Connector

TPAD1

TPAD1

TPAD1

TPAD1

TPAD1

TPAD1

TPAD1

TPAD1

TPAD1

TPAD1

TPAD1

TPAD1

TPAD1

TPAD1

TPAD1

TPAD1

TPAD1

TPAD1

TPAD1

## TOUCH-PAD SWITCH

TOUCH-PAD SW

TOUCH-PAD SW

TOUCH-PAD SW

TOUCH-PAD SW

TOUCH-PAD SW

TOUCH-PAD SW

TOUCH-PAD SW

TOUCH-PAD SW

TOUCH-PAD SW

TOUCH-PAD SW

PLACE NEAR CAP1

PLACE NEAR CAP1

PLACE NEAR CAP1

PLACE NEAR CAP1

PLACE NEAR CAP1

PLACE NEAR CAP1

PLACE NEAR CAP1

PLACE NEAR CAP1

PLACE NEAR CAP1

PLACE NEAR CAP1

PLACE NEAR CAP1

PLACE NEAR CAP1

PLACE NEAR CAP1

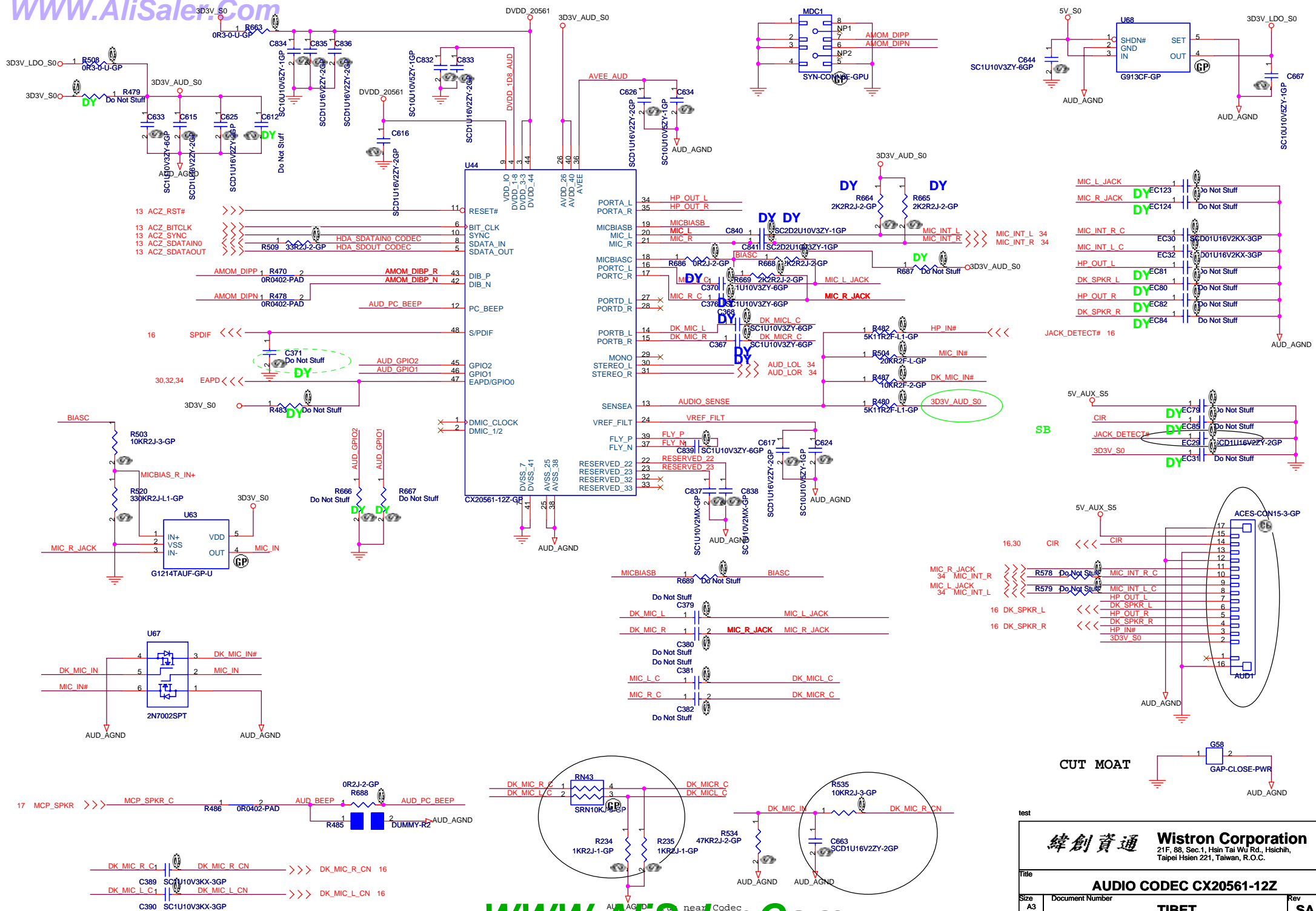
PLACE NEAR CAP1

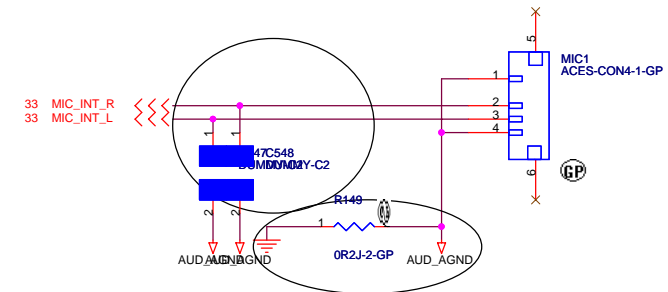
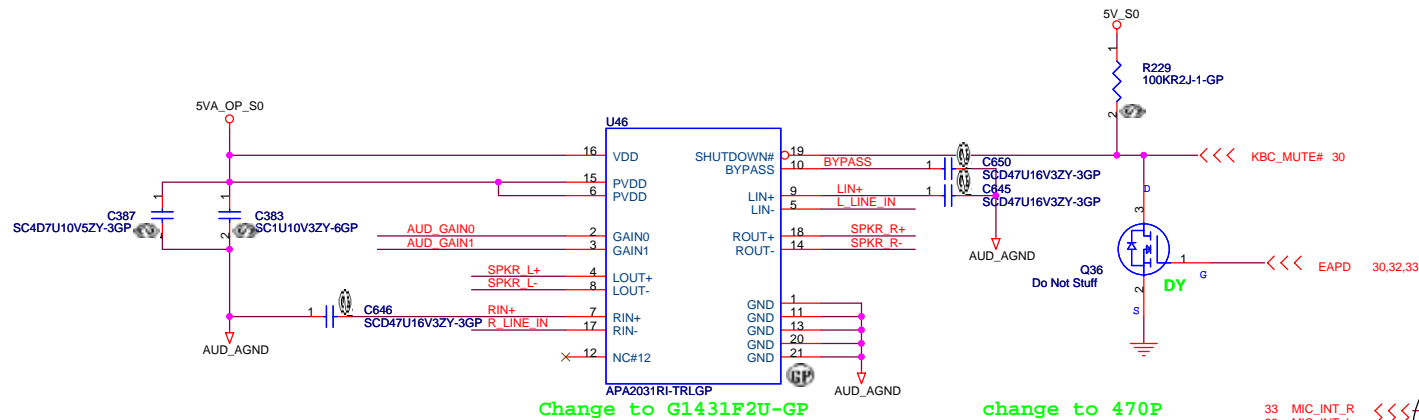
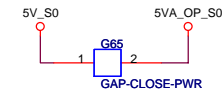
PLACE NEAR CAP1

PLACE NEAR CAP1

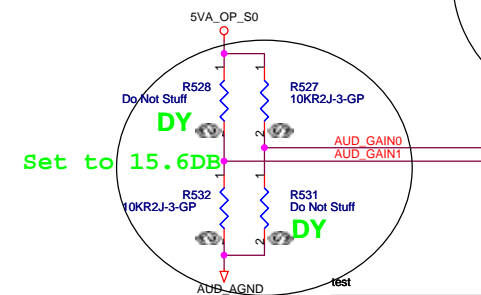
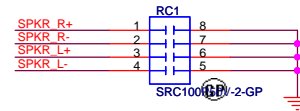
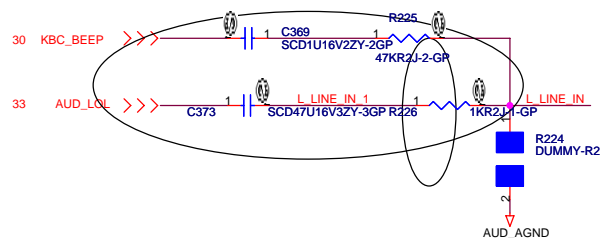
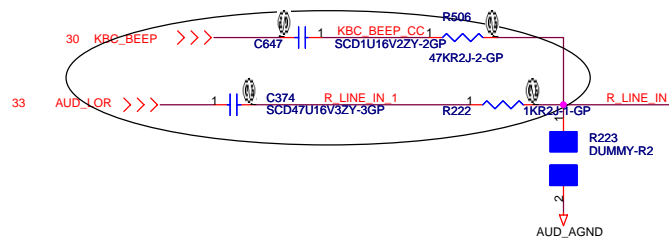
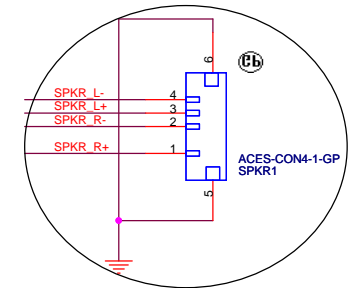
PLACE NEAR CAP1

|                                                                                                                   |                       |
|-------------------------------------------------------------------------------------------------------------------|-----------------------|
| <p>緯創資通 Wistron Corporation</p> <p>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</p> |                       |
| <p>Title</p> <p>KeyBoard-CONN</p>                                                                                 |                       |
| <p>Size A3</p> <p>Document Number</p>                                                                             | <p>Rev SA</p>         |
| <p>Date: Tuesday, August 14, 2007</p>                                                                             | <p>Sheet 32 of 44</p> |





## Speaker



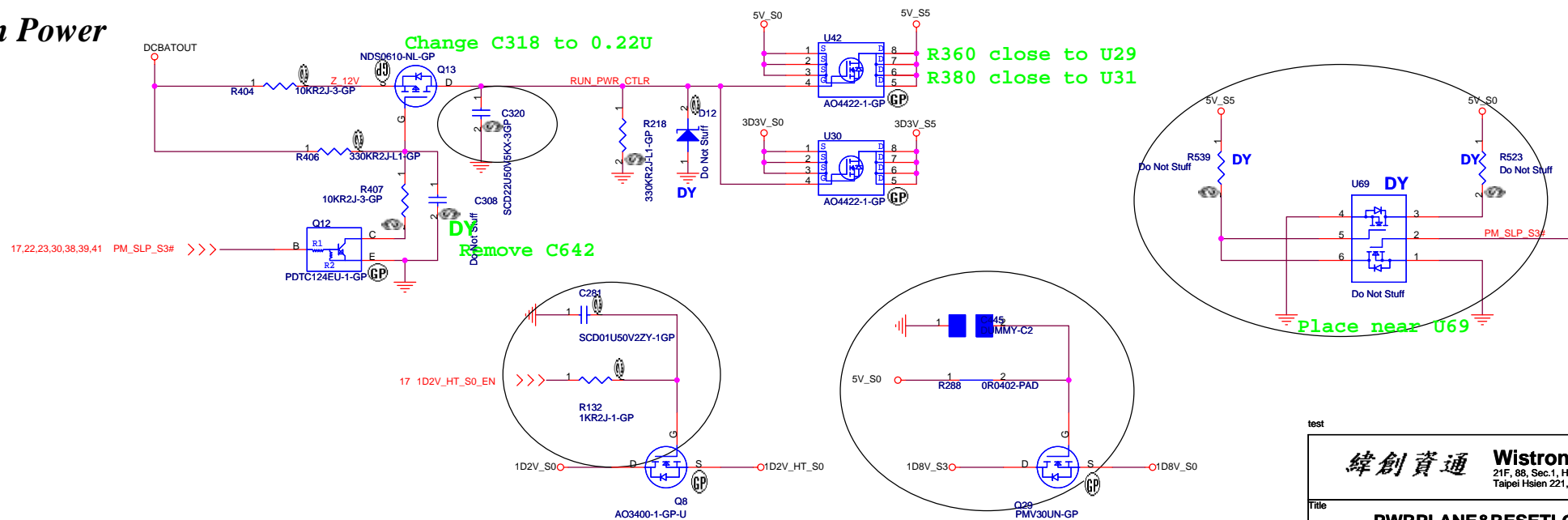
緯創資通 Wistron Corporation  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

|       |                          |       |                   |       |
|-------|--------------------------|-------|-------------------|-------|
| Title |                          |       | AUDIO AMP APA3031 |       |
| Size  | Document Number          | Rev   |                   |       |
| A3    |                          | TIBET |                   | SA    |
| Date: | Tuesday, August 14, 2007 | Sheet | 34                | of 44 |

## *Suspend Power*

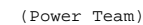
## Remove Dummy PAD


### *Run Power*

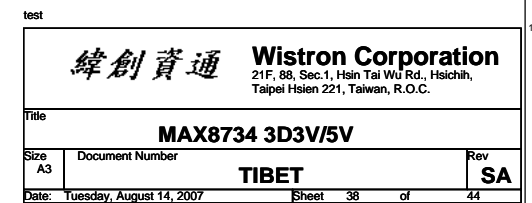


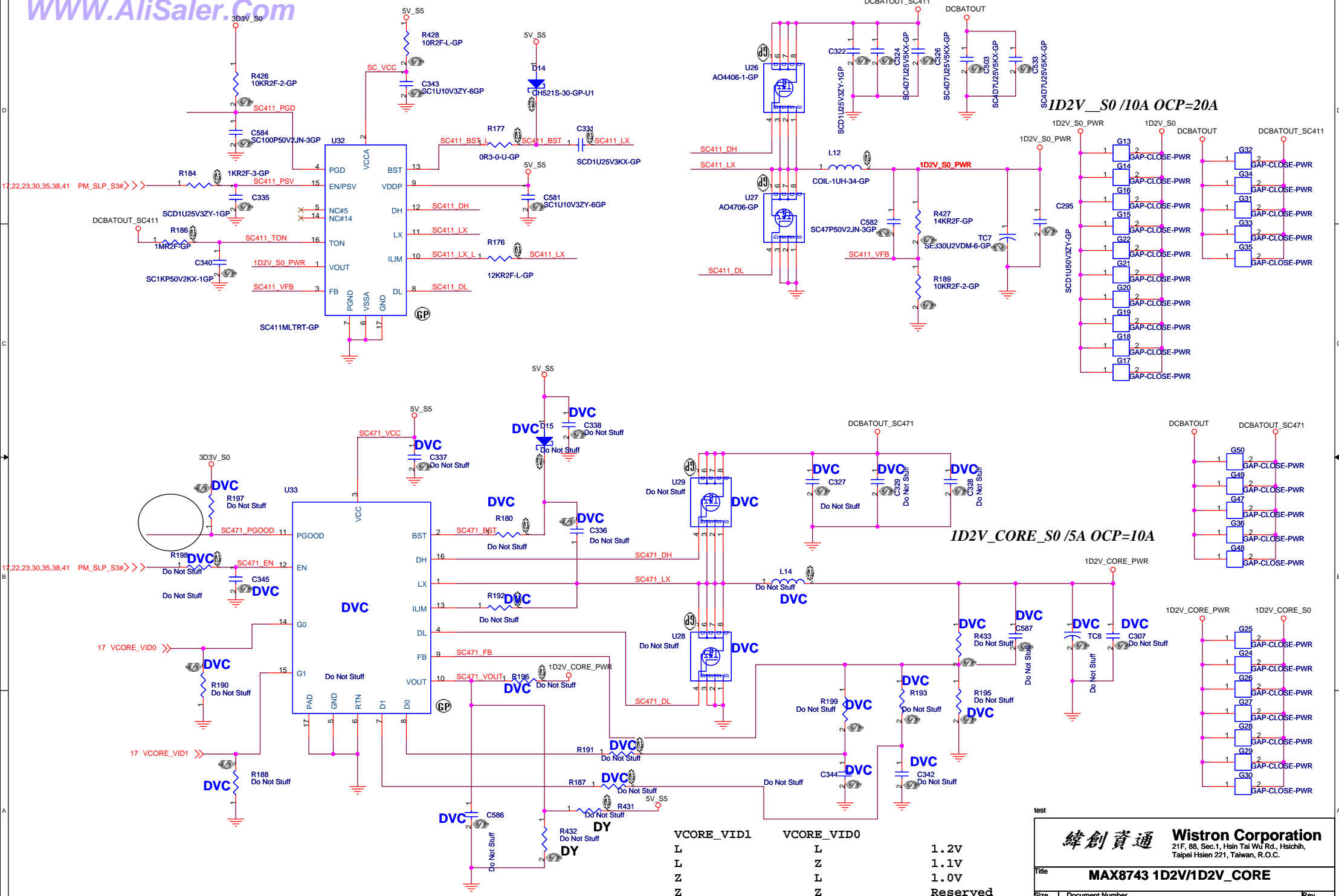




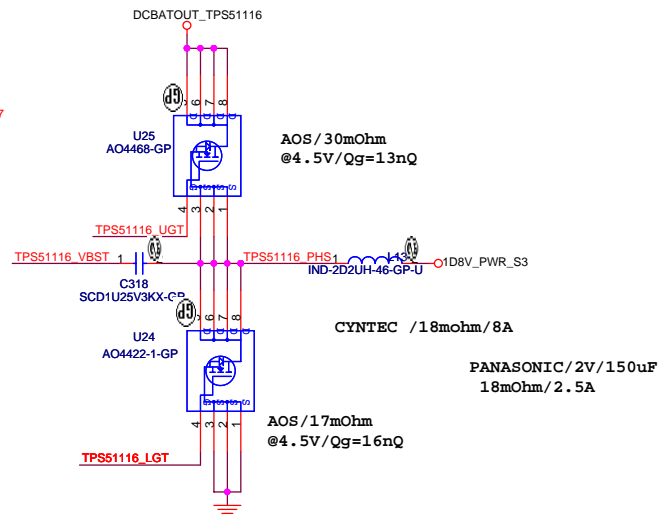
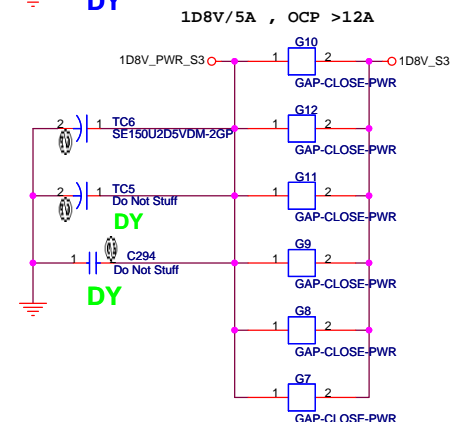
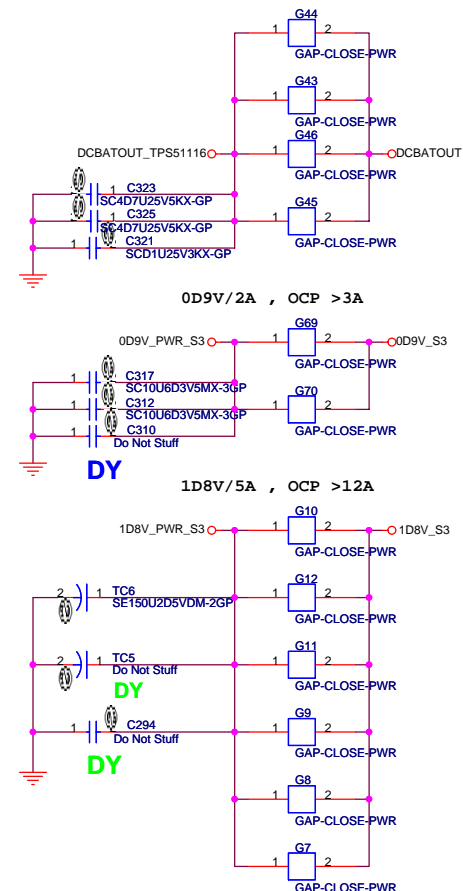
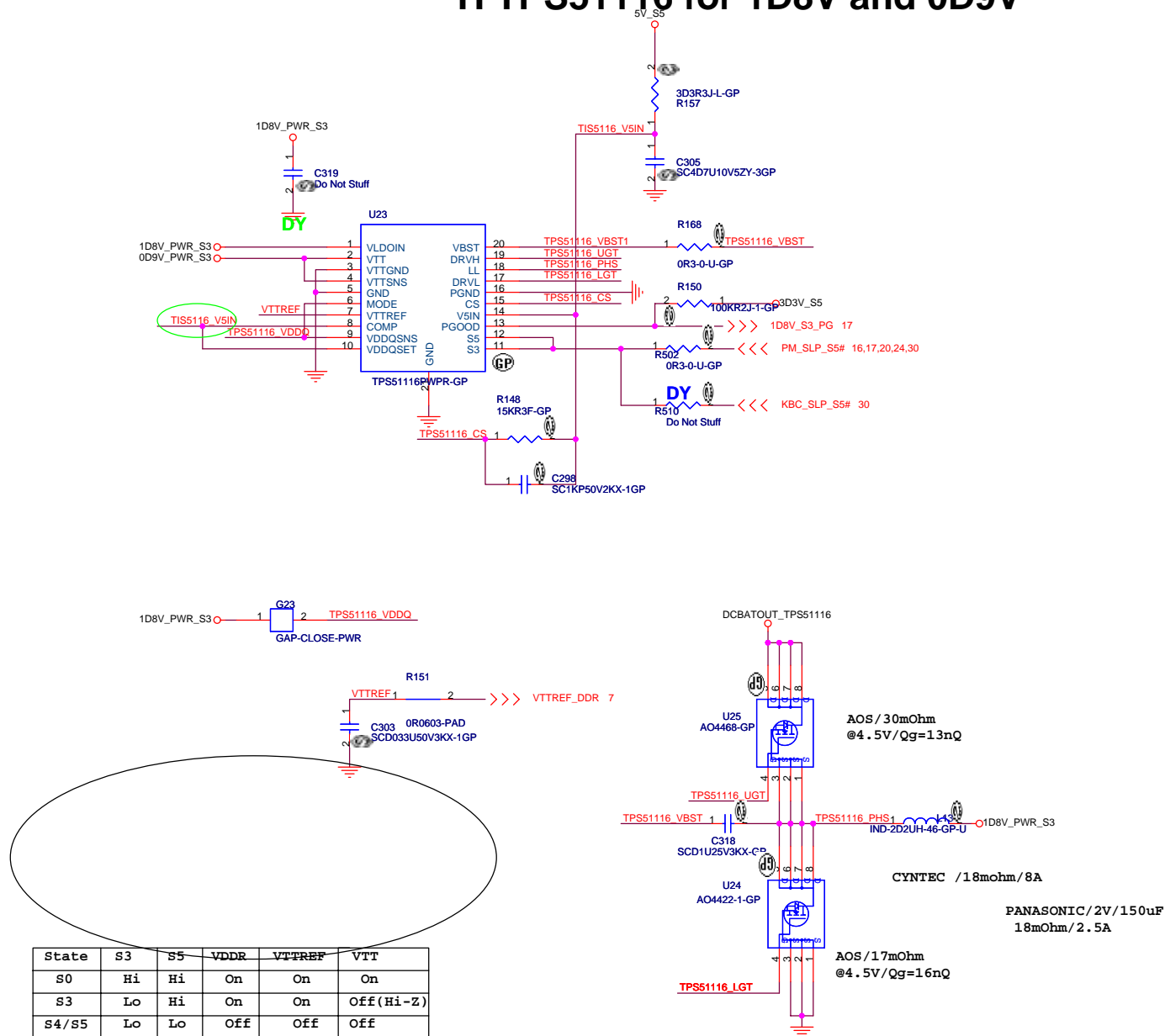


|                                                                                                   |              |                                                                                                             |                  |
|---------------------------------------------------------------------------------------------------|--------------|-------------------------------------------------------------------------------------------------------------|------------------|
|  <b>緯創資通</b> |              | <b>Wistron Corporation</b><br>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,<br>Taipei Hsien 221, Taiwan, R.O.C. |                  |
| <b>CPU CORE MAX8760(2/2)</b>                                                                      |              |                                                                                                             |                  |
| Document Number                                                                                   | <b>TIBET</b> |                                                                                                             | Rev<br><b>SA</b> |
| Tuesday, August 14, 2007                                                                          | Sheet        | 37 of 44                                                                                                    |                  |



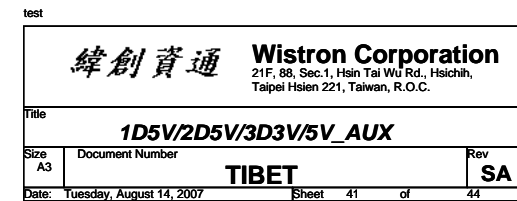


# TI TPS51116 for 1D8V and 0D9V

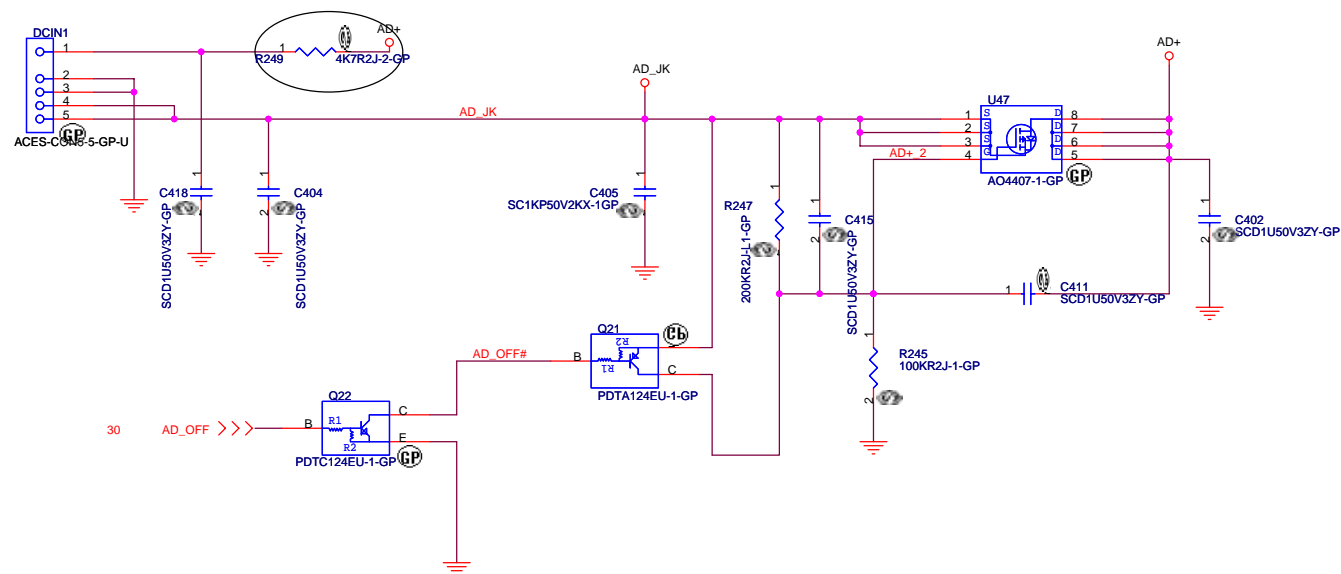


test

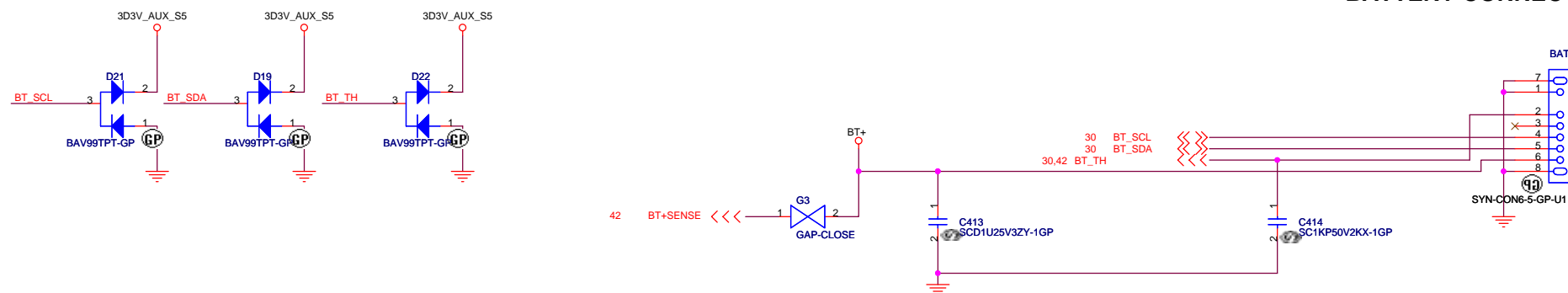
|                                                                                                                   |                 |
|-------------------------------------------------------------------------------------------------------------------|-----------------|
| <p>緯創資通 Wistron Corporation</p> <p>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</p> |                 |
| <p>Title: TPS51116 1D8V/0D9V</p>                                                                                  |                 |
| Size A3                                                                                                           | Document Number |
| Date: Tuesday, August 14, 2007                                                                                    | Sheet 40 of 44  |
| <p>TIBET</p>                                                                                                      |                 |
| <p>SA</p>                                                                                                         |                 |



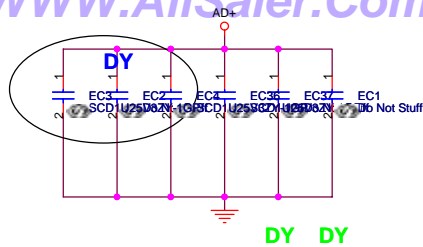




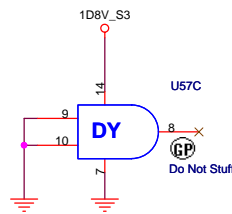
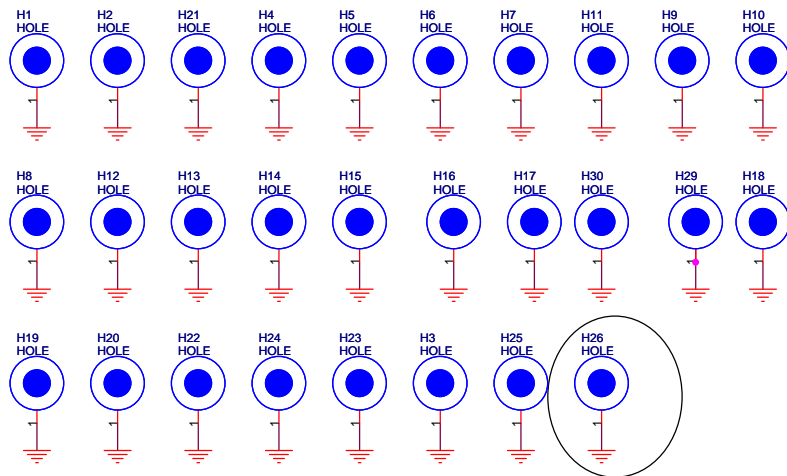
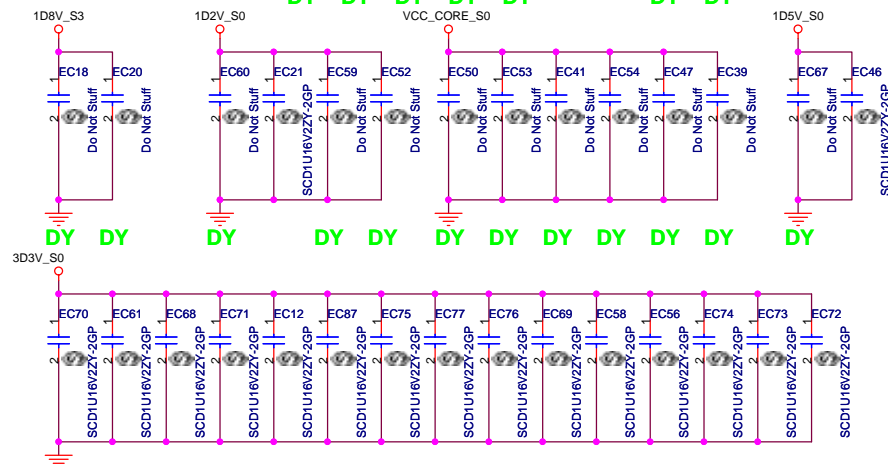
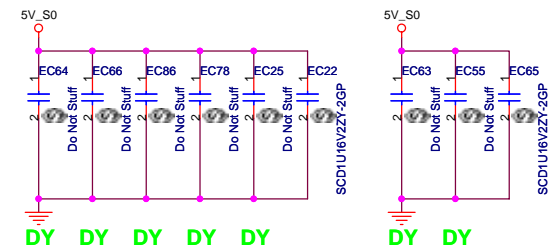
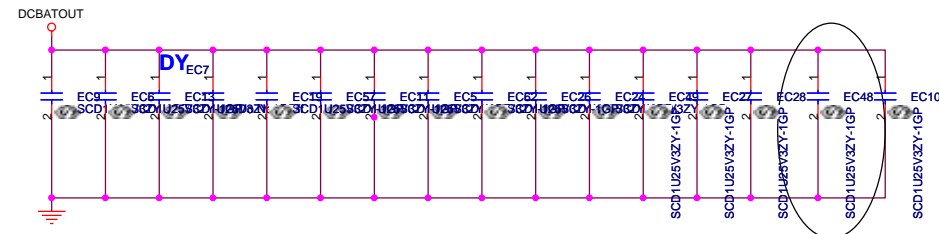
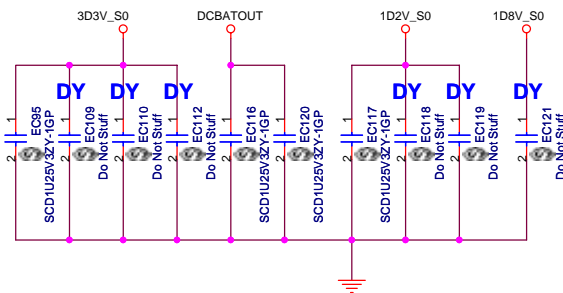
## BATTERY CONNECTOR



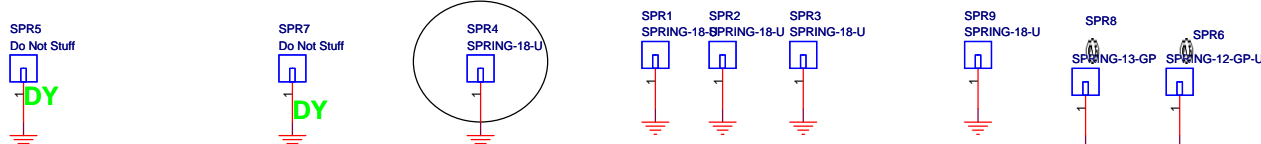




|             | FF     | DF     |
|-------------|--------|--------|
| CAMERA      | ○      | ×      |
| LAN         | 10/100 | 10/100 |
| 1394/5 IN 1 | ○      | ×      |
| EXTRA USB   | ○      | ×      |
| MIC         | ○      | ×      |
| CR LED      | ○      | ×      |

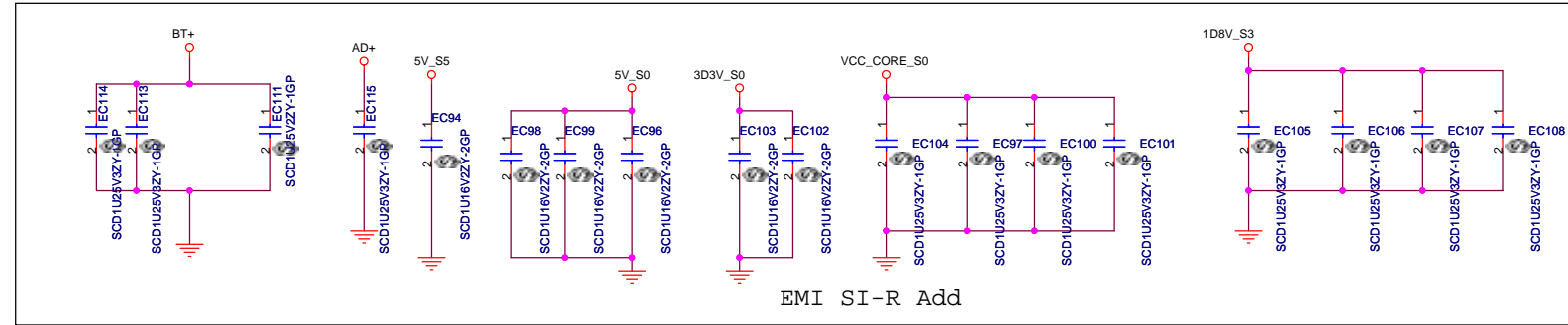


## Modify SPR7 P/N manually




THE FOOTPRINT OF SPR5, SPR6, AND SPR7 CANNOT BE CHIP

WWW.AllSaler.Com



EMI SI-R Add

|                                                                                       |                            |                                                                                                             |           |
|---------------------------------------------------------------------------------------|----------------------------|-------------------------------------------------------------------------------------------------------------|-----------|
| test                                                                                  |                            |                                                                                                             |           |
|  |                            | <b>Wistron Corporation</b><br>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,<br>Taipei Hsien 221, Taiwan, R.O.C. |           |
| Title                                                                                 |                            |                                                                                                             |           |
| <div>MISC</div>                                                                       |                            |                                                                                                             |           |
| Size<br>A3                                                                            | Document Number            |                                                                                                             | Rev<br>SA |
| Date:                                                                                 | Wednesday, August 15, 2007 | Sheet 44 of 44                                                                                              |           |