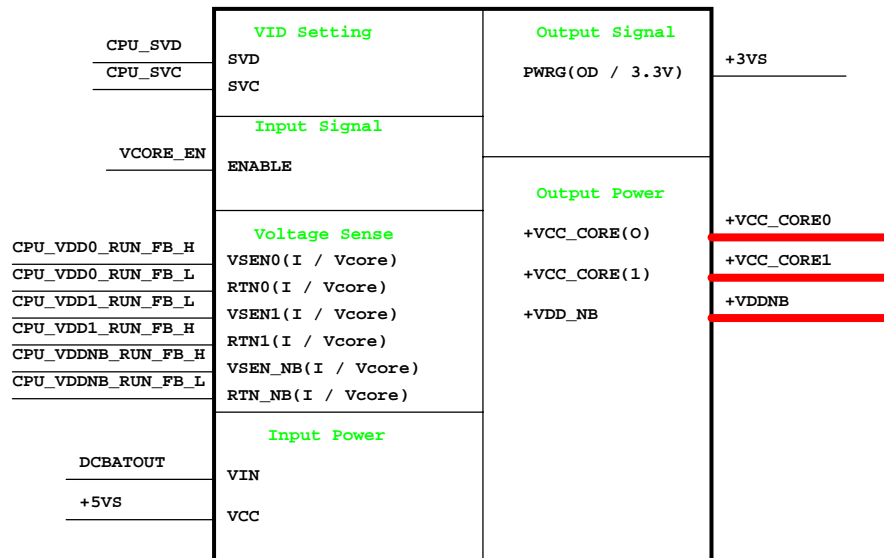
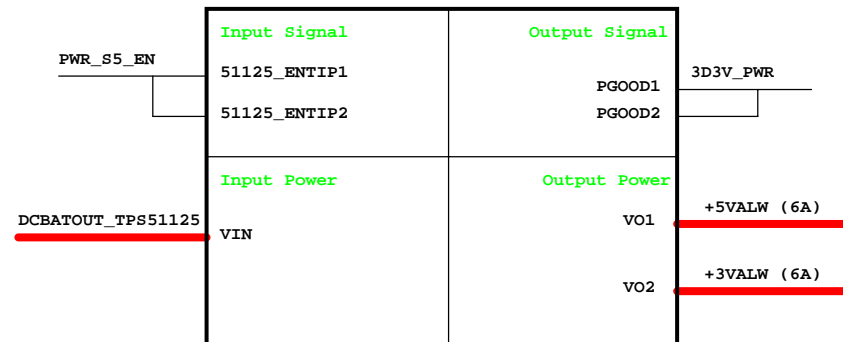


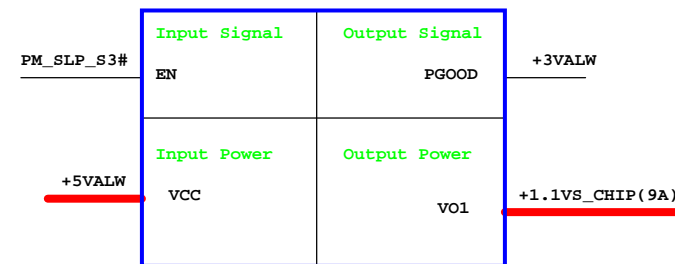
CPU_CORE
ISL6265



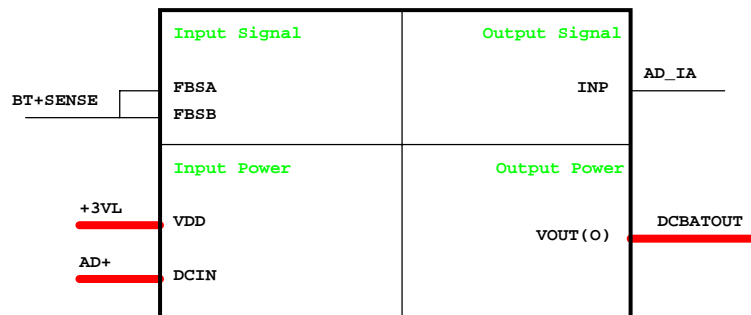
5V/3.3V
TI TPS51125



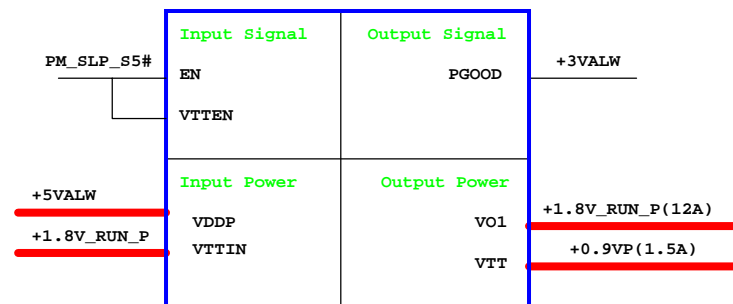
+1.1VS_CHIP
SC412A



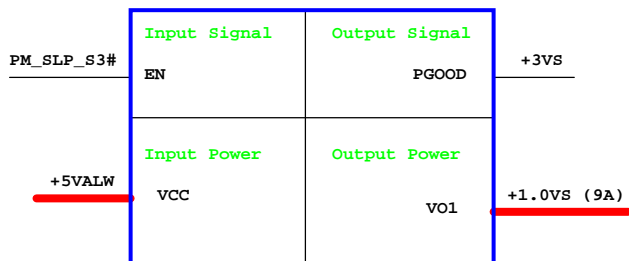
Charger
MAX8731A



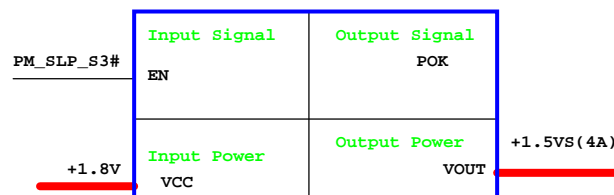
+1.8V/ +0.9VP
TPS51116



+1.0VS SC471A



+1.5V_VS
APL5913



<Variant Name>

緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Power Diagram	
Title Size A3 Date: Monday, June 09, 2008	Document Number Astrosphere Sheet 2 of 44
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D

D

C

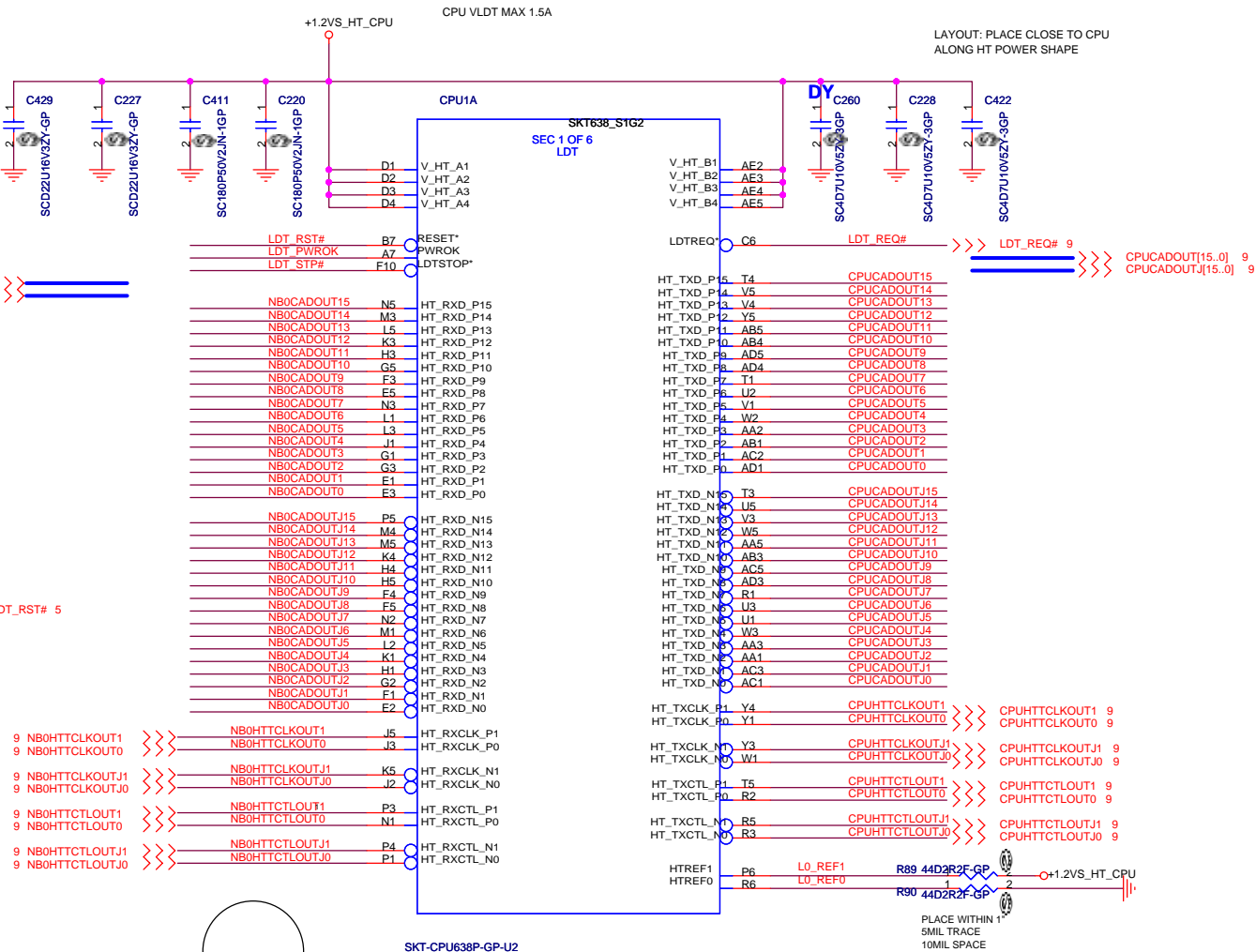
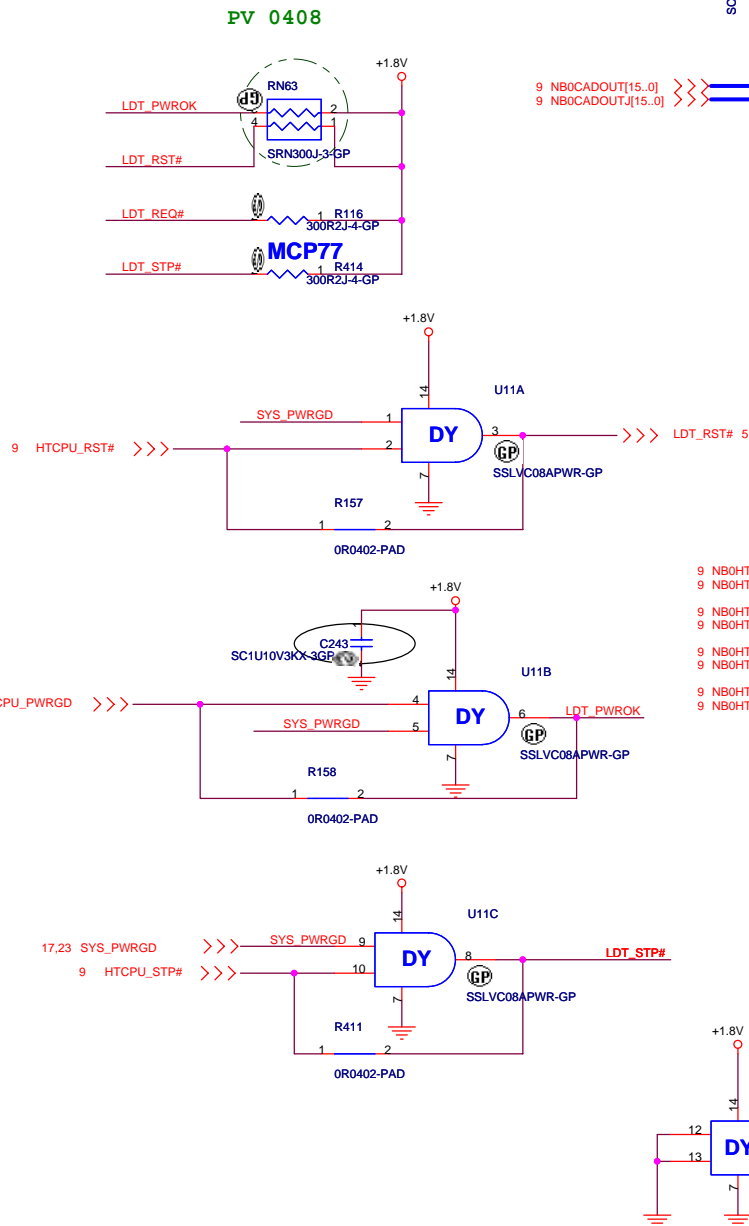
C

B

B

A

A



<Variant Name>

緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
CPU(1/4) HT	
Title Size A3 Date: Monday, June 09, 2008	Document Number Astrosphere Sheet 3 of 44
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M_A_DQ63	AA12	MA_DATA63
M_A_DQ62	AB12	MA_DATA62
M_A_DQ61	AA14	MA_DATA61
M_A_DQ60	AB14	MA_DATA60
M_A_DQ59	W11	MA_DATA59
M_A_DQ58	Y12	MA_DATA58
M_A_DQ57	AD13	MA_DATA57
M_A_DQ56	AB13	MA_DATA56
M_A_DQ55	AD15	MA_DATA55
M_A_DQ54	AB15	MA_DATA54
M_A_DQ53	AB17	MA_DATA53
M_A_DQ52	Y17	MA_DATA52
M_A_DQ51	Y14	MA_DATA51
M_A_DQ50	W14	MA_DATA50
M_A_DQ49	W16	MA_DATA49
M_A_DQ48	AD17	MA_DATA48
M_A_DQ47	Y18	MA_DATA47
M_A_DQ46	AD19	MA_DATA46
M_A_DQ45	AD21	MA_DATA45
M_A_DQ44	AB21	MA_DATA44
M_A_DQ43	AB18	MA_DATA43
M_A_DQ42	AA18	MA_DATA42
M_A_DQ41	AA20	MA_DATA41
M_A_DQ40	Y20	MA_DATA40
M_A_DQ39	AA22	MA_DATA39
M_A_DQ38	Y22	MA_DATA38
M_A_DQ37	W21	MA_DATA37
M_A_DQ36	W22	MA_DATA36
M_A_DQ35	AA21	MA_DATA35
M_A_DQ34	AB22	MA_DATA34
M_A_DQ33	AB24	MA_DATA33
M_A_DQ32	Y24	MA_DATA32
M_A_DQ31	H22	MA_DATA31
M_A_DQ30	H20	MA_DATA30
M_A_DQ29	E22	MA_DATA29
M_A_DQ28	E21	MA_DATA28
M_A_DQ27	J19	MA_DATA27
M_A_DQ26	H24	MA_DATA26
M_A_DQ25	F22	MA_DATA25
M_A_DQ24	F20	MA_DATA24
M_A_DQ23	C23	MA_DATA23
M_A_DQ22	B22	MA_DATA22
M_A_DQ21	F18	MA_DATA21
M_A_DQ20	E18	MA_DATA20
M_A_DQ19	E20	MA_DATA19
M_A_DQ18	D22	MA_DATA18
M_A_DQ17	C19	MA_DATA17
M_A_DQ16	G18	MA_DATA16
M_A_DQ15	G17	MA_DATA15
M_A_DQ14	C17	MA_DATA14
M_A_DQ13	F14	MA_DATA13
M_A_DQ12	E14	MA_DATA12
M_A_DQ11	H17	MA_DATA11
M_A_DQ10	E17	MA_DATA10
M_A_DQ9	F15	MA_DATA9
M_A_DQ8	H15	MA_DATA8
M_A_DQ7	E13	MA_DATA7
M_A_DQ6	C13	MA_DATA6
M_A_DQ5	H12	MA_DATA5
M_A_DQ4	H11	MA_DATA4
M_A_DQ3	G14	MA_DATA3
M_A_DQ2	H14	MA_DATA2
M_A_DQ1	F12	MA_DATA1
M_A_DQ0	G12	MA_DATA0
M_A_A15	K19	MA_ADD15
M_A_A14	K24	MA_ADD14
M_A_A13	V24	MA_ADD13
M_A_A12	K22	MA_ADD12
M_A_A11	L22	MA_ADD11
M_A_A10	R21	MA_ADD10
M_A_A9	K22	MA_ADD9
M_A_A8	L19	MA_ADD8
M_A_A7	L21	MA_ADD7
M_A_A6	M24	MA_ADD6
M_A_A5	L20	MA_ADD5
M_A_A4	M22	MA_ADD4
M_A_A3	M19	MA_ADD3
M_A_A2	N22	MA_ADD2
M_A_A1	M20	MA_ADD1
M_A_A0	N21	MA_ADD0

MA_DM7	Y13	M_A_DM7
MA_DM6	AB16	M_A_DM6
MA_DM5	Y19	M_A_DM5
MA_DM4	AC24	M_A_DM4
MA_DM3	F24	M_A_DM3
MA_DM2	E19	M_A_DM2
MA_DM1	C15	M_A_DM1
MA_DM0	E12	M_A_DM0

MA_DQS_F7	W12	M_A_DQS7
MA_DQS_F6	Y15	M_A_DQS6
MA_DQS_F5	AB19	M_A_DQS5
MA_DQS_F4	AD23	M_A_DQS4
MA_DQS_F3	C22	M_A_DQS3
MA_DQS_F2	C22	M_A_DQS2
MA_DQS_F1	G16	M_A_DQS1
MA_DQS_F0	G13	M_A_DQS0

MA_DQS_N7	W13	M_A_DQS#7
MA_DQS_N6	W15	M_A_DQS#6
MA_DQS_N5	AB20	M_A_DQS#5
MA_DQS_N4	AC23	M_A_DQS#4
MA_DQS_N3	G21	M_A_DQS#3
MA_DQS_N2	C21	M_A_DQS#2
MA_DQS_N1	G15	M_A_DQS#1
MA_DQS_N0	H13	M_A_DQS#0

MA_CLK5_P	N19	X
MA_CLK5_N	N20	X
MA_CLK1_P	E16	M_A_CLK_DDR1
MA_CLK1_N	F16	M_A_CLK_DDR1#
MA_CLK7_P	Y16	M_A_CLK_DDR2
MA_CLK7_N	AA16	M_A_CLK_DDR2#
MA_CLK4_P	P19	X
MA_CLK4_N	P20	X

MA_BANK2	J21	M_A_BS#2
MA_BANK1	R23	M_A_BS#1
MA_BANK0	R20	M_A_BS#0

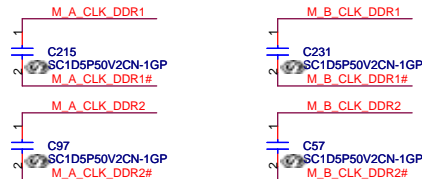
MA_RAS#	R19	M_A_RAS#
MA_CAS#	T22	M_A_CAS#
MA_WE#	T24	M_A_WE#

MA1_CS1#	V20	X
MA1_CS0	U20	X
MA0_CS1#	U19	M_A_CS1#
MA0_CS0	T20	M_A_CS0#

MA_CKE1	J20	M_A_CKE1
MA_CKE0	J22	M_A_CKE0

MA1_ODT1	V19	X
MA1_ODT0	U21	X
MA0_ODT1	V22	X
MA0_ODT0	T19	M_A_ODT1

Place near CPU



MAX NECKDOWN TO & FROM CAPS IS 500MILS
TRACE FROM CAP TO CPU MUST BE LESS THAN 1200MILS

M_B_DQ63	AD11	MB_DATA63
M_B_DQ62	AF11	MB_DATA62
M_B_DQ61	AF14	MB_DATA61
M_B_DQ60	AE14	MB_DATA60
M_B_DQ59	Y11	MB_DATA59
M_B_DQ58	AB11	MB_DATA58
M_B_DQ57	AC12	MB_DATA57
M_B_DQ56	AF13	MB_DATA56
M_B_DQ55	AF16	MB_DATA55
M_B_DQ54	AF16	MB_DATA54
M_B_DQ53	AC18	MB_DATA53
M_B_DQ52	AF19	MB_DATA52
M_B_DQ51	AD14	MB_DATA51
M_B_DQ50	AC14	MB_DATA50
M_B_DQ49	AD18	MB_DATA49
M_B_DQ48	AD18	MB_DATA48
M_B_DQ47	AD20	MB_DATA47
M_B_DQ46	AD20	MB_DATA46
M_B_DQ45	AF23	MB_DATA45
M_B_DQ44	AF24	MB_DATA44
M_B_DQ43	AF20	MB_DATA43
M_B_DQ42	AF20	MB_DATA42
M_B_DQ41	AD23	MB_DATA41
M_B_DQ40	AC22	MB_DATA40
M_B_DQ39	AE25	MB_DATA39
M_B_DQ38	AD26	MB_DATA38
M_B_DQ37	AA25	MB_DATA37
M_B_DQ36	AA26	MB_DATA36
M_B_DQ35	AE24	MB_DATA35
M_B_DQ34	AD24	MB_DATA34
M_B_DQ33	AA23	MB_DATA33
M_B_DQ32	AA24	MB_DATA32
M_B_DQ31	G24	MB_DATA31
M_B_DQ30	G23	MB_DATA30
M_B_DQ29	D26	MB_DATA29
M_B_DQ28	C26	MB_DATA28
M_B_DQ27	G26	MB_DATA27
M_B_DQ26	G25	MB_DATA26
M_B_DQ25	E24	MB_DATA25
M_B_DQ24	E23	MB_DATA24
M_B_DQ23	C24	MB_DATA23
M_B_DQ22	B24	MB_DATA22
M_B_DQ21	C20	MB_DATA21
M_B_DQ20	B20	MB_DATA20
M_B_DQ19	C25	MB_DATA19
M_B_DQ18	D24	MB_DATA18
M_B_DQ17	A21	MB_DATA17
M_B_DQ16	D20	MB_DATA16
M_B_DQ15	D18	MB_DATA15
M_B_DQ14	C18	MB_DATA14
M_B_DQ13	D14	MB_DATA13
M_B_DQ12	C14	MB_DATA12
M_B_DQ11	A20	MB_DATA11
M_B_DQ10	A19	MB_DATA10
M_B_DQ9	A16	MB_DATA9
M_B_DQ8	A15	MB_DATA8
M_B_DQ7	A13	MB_DATA7
M_B_DQ6	D12	MB_DATA6
M_B_DQ5	E11	MB_DATA5
M_B_DQ4	G11	MB_DATA4
M_B_DQ3	B14	MB_DATA3
M_B_DQ2	A14	MB_DATA2
M_B_DQ1	A11	MB_DATA1
M_B_DQ0	C11	MB_DATA0
M_B_A15	J24	MB_ADD15
M_B_A14	W23	MB_ADD14
M_B_A13	W24	MB_ADD13
M_B_A12	L25	MB_ADD12
M_B_A11	L26	MB_ADD11
M_B_A10	T26	MB_ADD10
M_B_A9	K26	MB_ADD9
M_B_A8	M26	MB_ADD8
M_B_A7	L24	MB_ADD7
M_B_A6	N25	MB_ADD6
M_B_A5	L23	MB_ADD5
M_B_A4	N26	MB_ADD4
M_B_A3	N23	MB_ADD3
M_B_A2	P26	MB_ADD2
M_B_A1	N24	MB_ADD1
M_B_A0	P24	MB_ADD0

MB_DM7	AD12	M_B_DM7
MB_DM6	AC16	M_B_DM6
MB_DM5	AE22	M_B_DM5
MB_DM4	AB26	M_B_DM4
MB_DM3	E25	M_B_DM3
MB_DM2	A22	M_B_DM2
MB_DM1	B16	M_B_DM1
MB_DM0	A12	M_B_DM0

MB_DQS_F7	AF12	M_B_DQS7
MB_DQS_F6	AF16	M_B_DQS6
MB_DQS_F5	AF21	M_B_DQS5
MB_DQS_F4	AC25	M_B_DQS4
MB_DQS_F3	F26	M_B_DQS3
MB_DQS_F2	A24	M_B_DQS2
MB_DQS_F1	D16	M_B_DQS1
MB_DQS_F0	C12	M_B_DQS0

MB_DQS_N7	AF12	M_B_DQS#7
MB_DQS_N6	AD16	M_B_DQS#6
MB_DQS_N5	AF22	M_B_DQS#5
MB_DQS_N4	AC26	M_B_DQS#4
MB_DQS_N3	E26	M_B_DQS#3
MB_DQS_N2	A23	M_B_DQS#2
MB_DQS_N1	C16	M_B_DQS#1
MB_DQS_N0	B12	M_B_DQS#0

MB_CLK5_P	P22	X
MB_CLK5_N	R22	X
MB_CLK1_P	A17	M_B_CLK_DDR1
MB_CLK1_N	A18	M_B_CLK_DDR1#
MB_CLK7_P	AF18	M_B_CLK_DDR2
MB_CLK7_N	AF17	M_B_CLK_DDR2#
MB_CLK4_P	R26	X
MB_CLK4_N	R25	X

MB_BANK2	J26	M_B_BS#2
MB_BANK1	U26	M_B_BS#1
MB_BANK0	R24	M_B_BS#0

MB_RAS#	U25	M_B_RAS#
MB_CAS#	U24	M_B_CAS#
MB_WE#	U23	M_B_WE#

MB1_CS0	U22	X
MB0_CS0	W25	M_B_CS1#
MB0_CS0	V26	M_B_CS0#

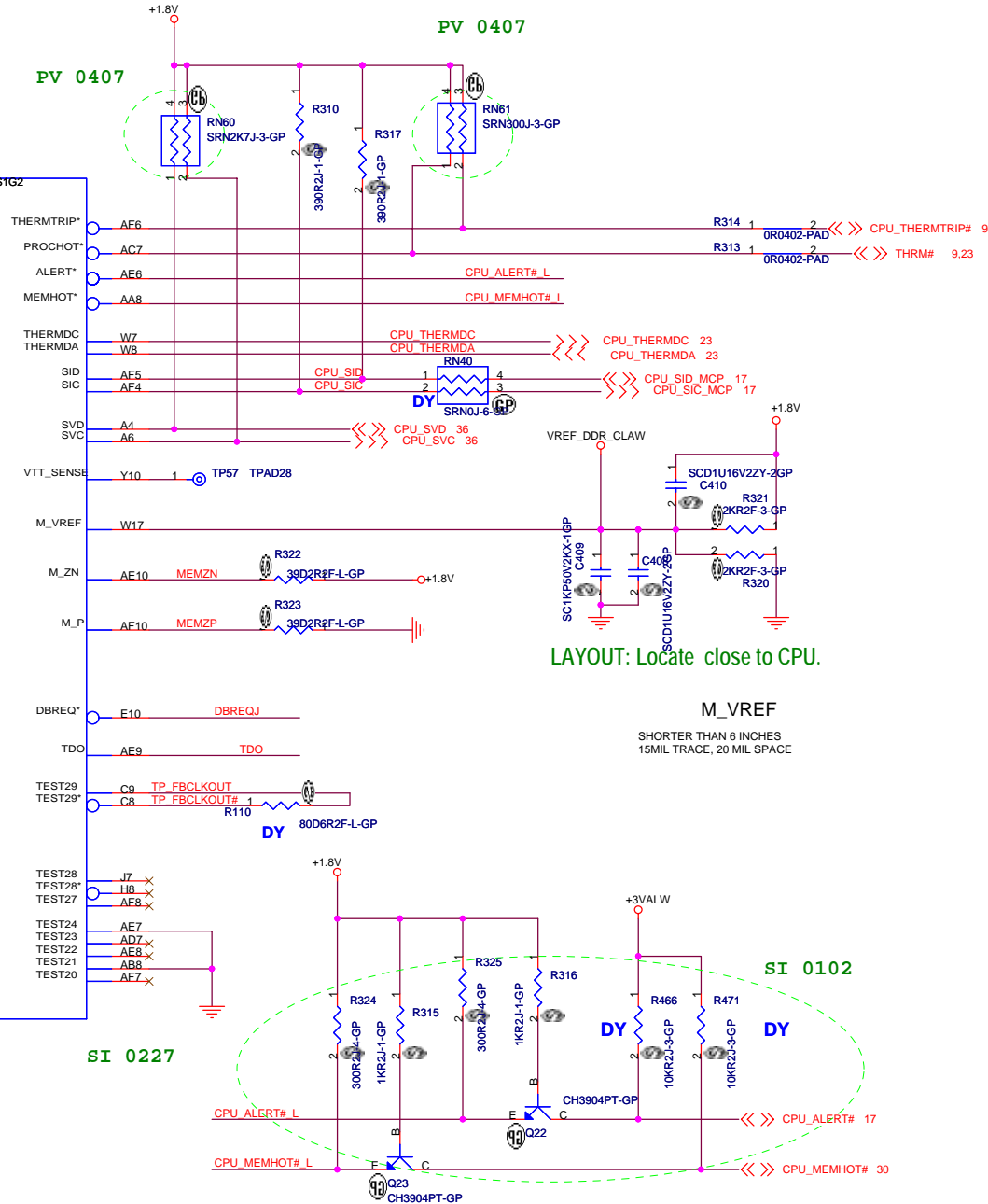
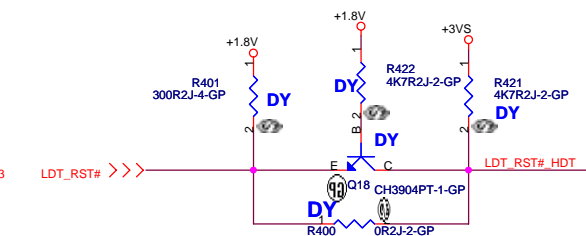
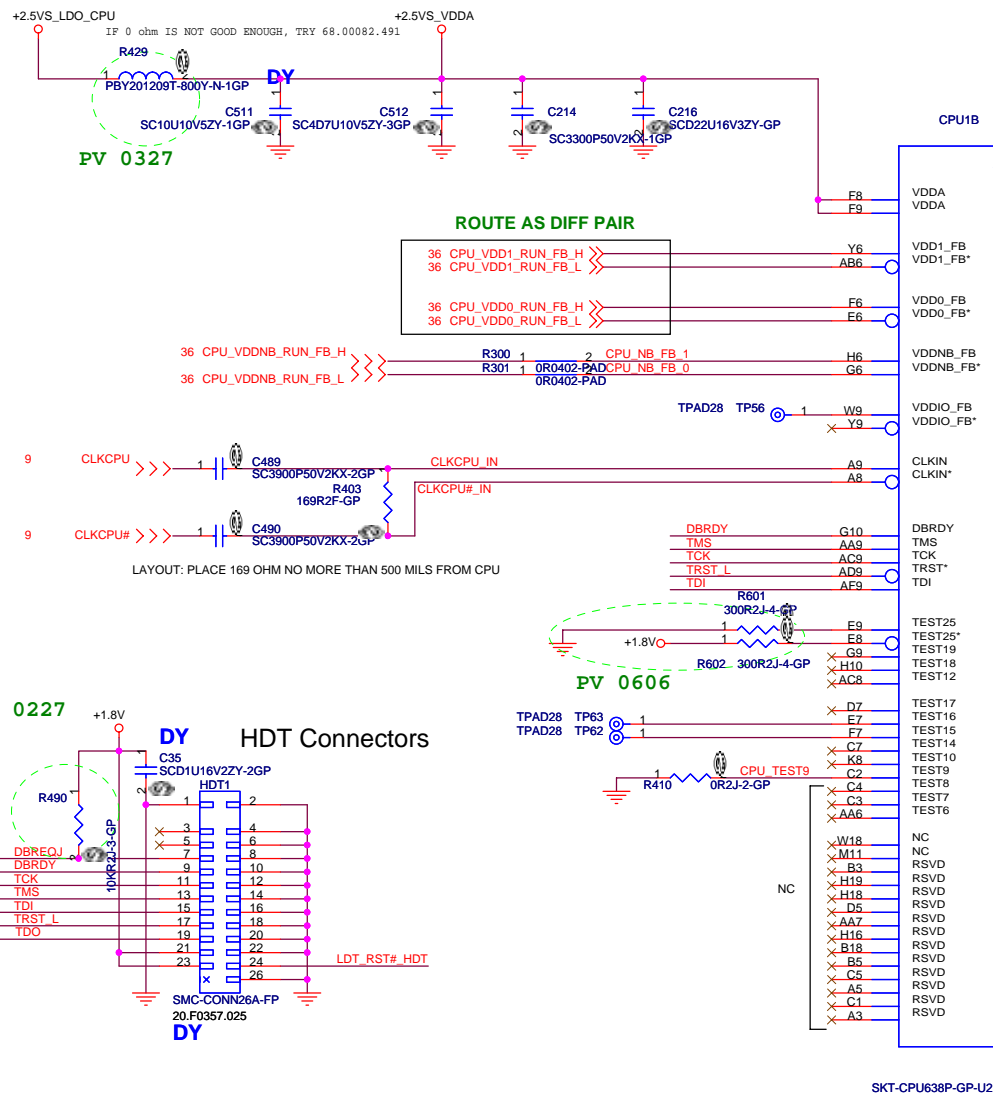
MB_CKE1	H26	M_B_CKE1
MB_CKE0	J25	M_B_CKE0

MB1_ODT0	Y26	X
MB0_ODT1	W23	M_B_ODT1
MB0_ODT0	W26	M_B_ODT0

<Variant Name>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichin,
Taipei Hsien 221, Taiwan, R.O.C.

Title			CPU(2/4) DDR II
Size	Document Number	Rev	
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<Variant Name>

緯創資通

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title	Author	Year	Journal	Volume	Page
1. The Effect of the 1994 North American Free Trade Agreement on the U.S. Trade Balance	Davidson, P. H.	1995	Journal of International Trade and Economics	1	1-15
2. The Effect of the 1994 North American Free Trade Agreement on the U.S. Trade Balance	Davidson, P. H.	1995	Journal of International Trade and Economics	1	1-15
3. The Effect of the 1994 North American Free Trade Agreement on the U.S. Trade Balance	Davidson, P. H.	1995	Journal of International Trade and Economics	1	1-15
4. The Effect of the 1994 North American Free Trade Agreement on the U.S. Trade Balance	Davidson, P. H.	1995	Journal of International Trade and Economics	1	1-15
5. The Effect of the 1994 North American Free Trade Agreement on the U.S. Trade Balance	Davidson, P. H.	1995	Journal of International Trade and Economics	1	1-15
6. The Effect of the 1994 North American Free Trade Agreement on the U.S. Trade Balance	Davidson, P. H.	1995	Journal of International Trade and Economics	1	1-15
7. The Effect of the 1994 North American Free Trade Agreement on the U.S. Trade Balance	Davidson, P. H.	1995	Journal of International Trade and Economics	1	1-15
8. The Effect of the 1994 North American Free Trade Agreement on the U.S. Trade Balance	Davidson, P. H.	1995	Journal of International Trade and Economics	1	1-15
9. The Effect of the 1994 North American Free Trade Agreement on the U.S. Trade Balance	Davidson, P. H.	1995	Journal of International Trade and Economics	1	1-15
10. The Effect of the 1994 North American Free Trade Agreement on the U.S. Trade Balance	Davidson, P. H.	1995	Journal of International Trade and Economics	1	1-15

CPU(3/4) CONTROL

Size

Document Number

A3

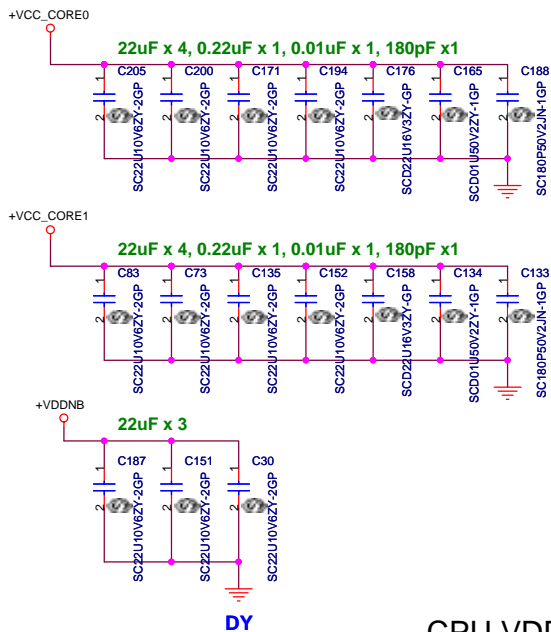
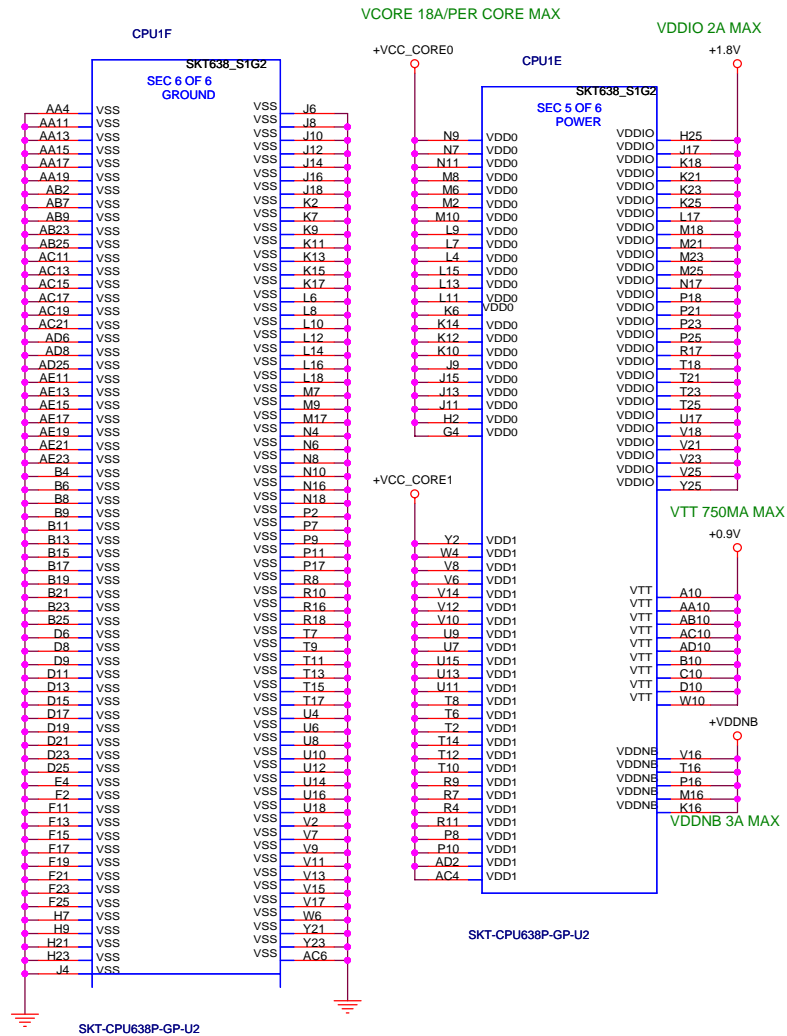
Astrosphere

	Astrosipne
	200 mg/100 mg/100 mg

[illegible]

Rev

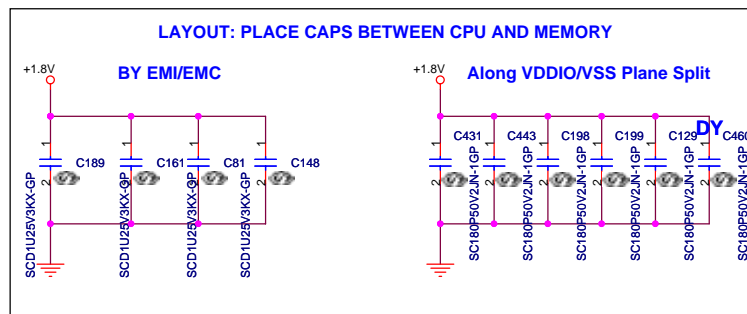
SA



CPU VDDIO

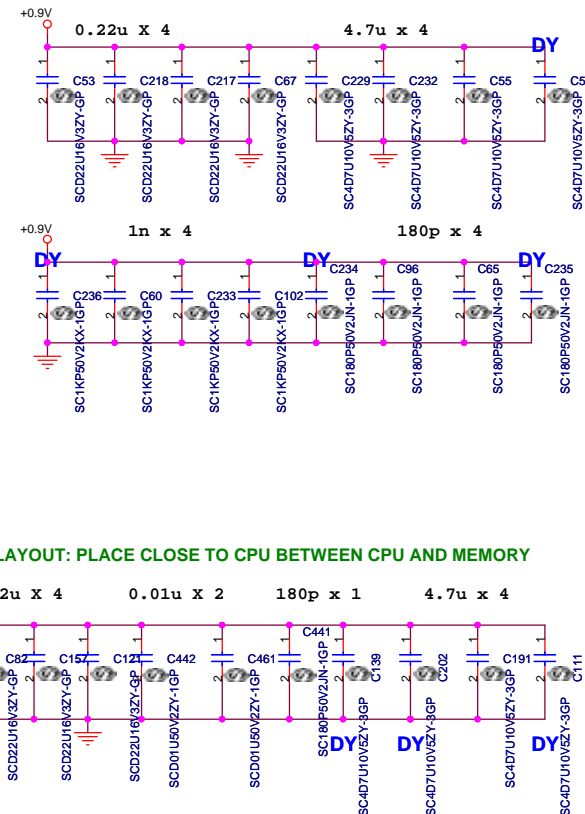
LAYOUT: PLACE UNDER CPU ON BACK

LAYOUT: PLACE CLOSE TO CPU BETWEEN CPU AND MEMORY



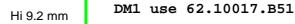
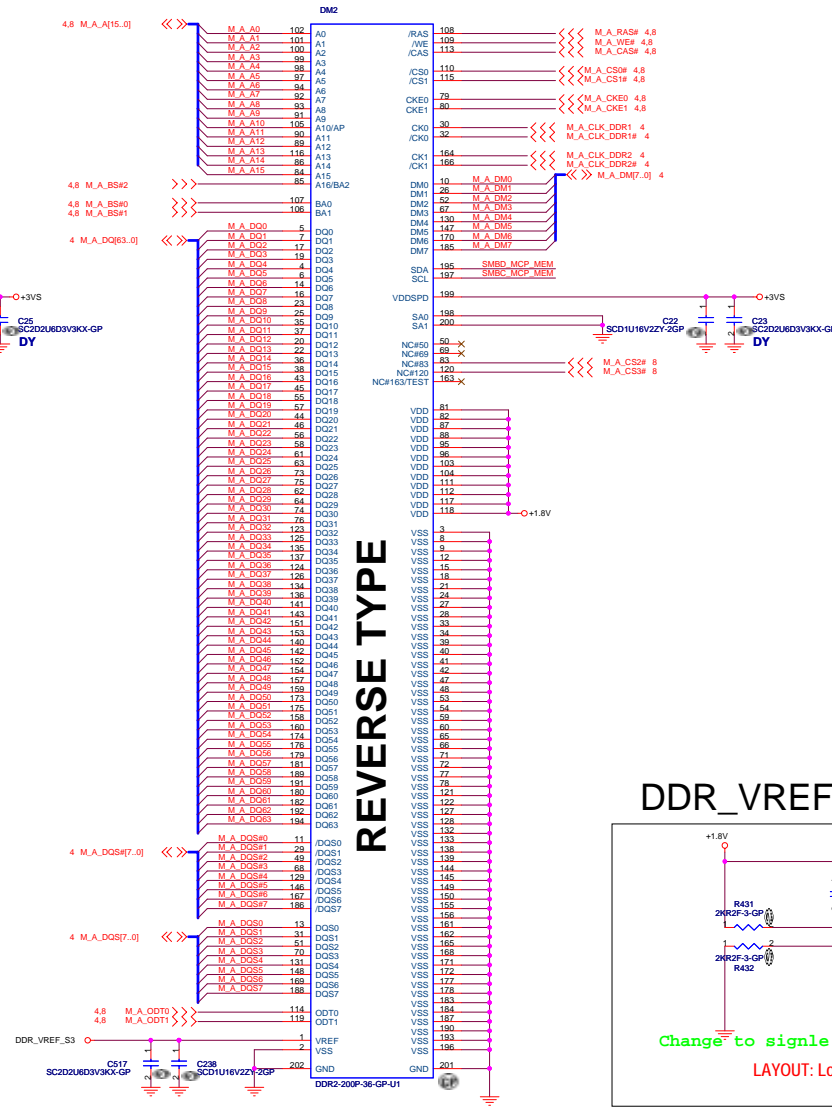
CPU MEMORY VTT

LAYOUT: PLACE CLOSE TO CPU BETWEEN CPU AND MEMORY



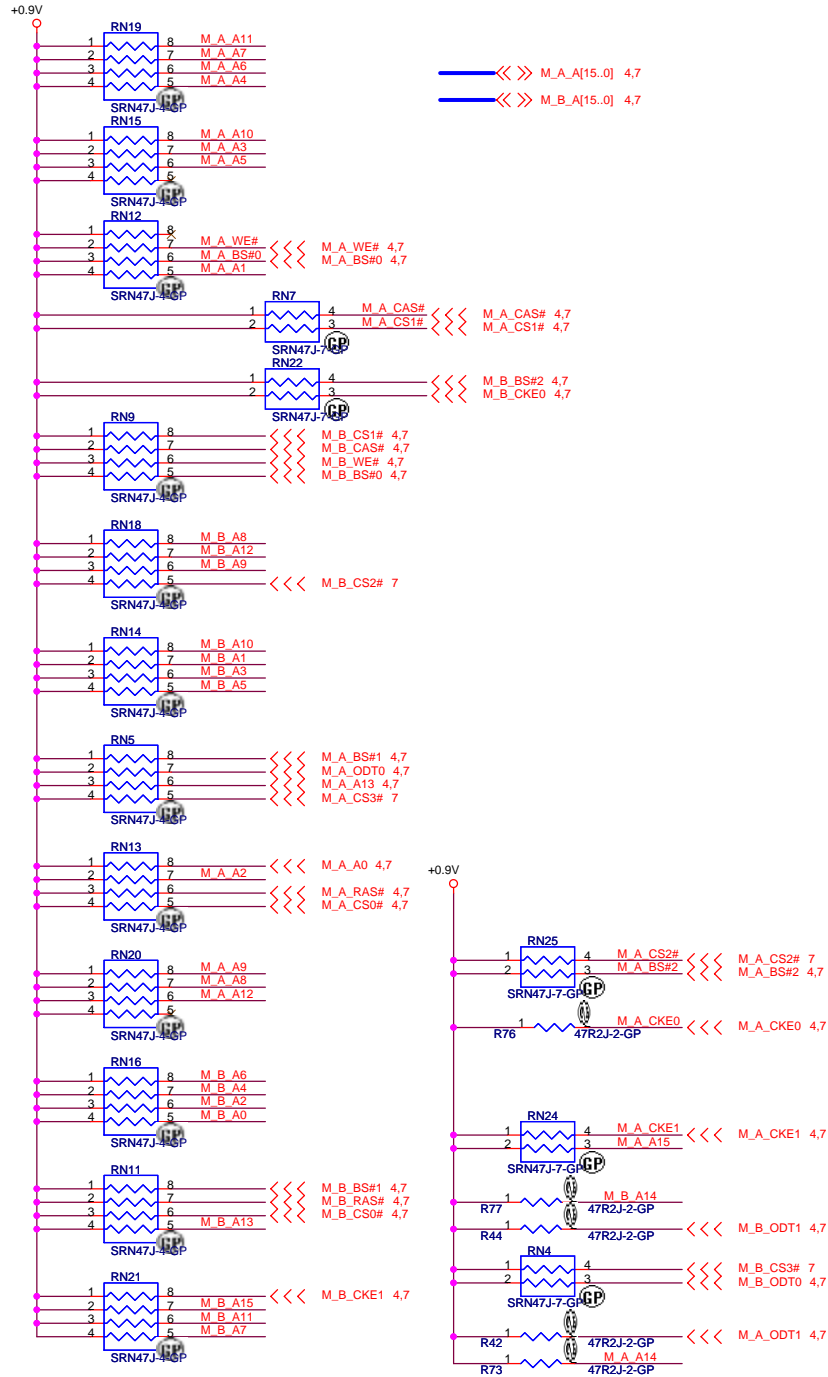
<Variant Name>

緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
CPU(4/4) POWER	
Title Size A3 Date: Monday, June 09, 2008	Document Number Astrosphere Sheet 6 of 44
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DM2 use 62.10017.E11

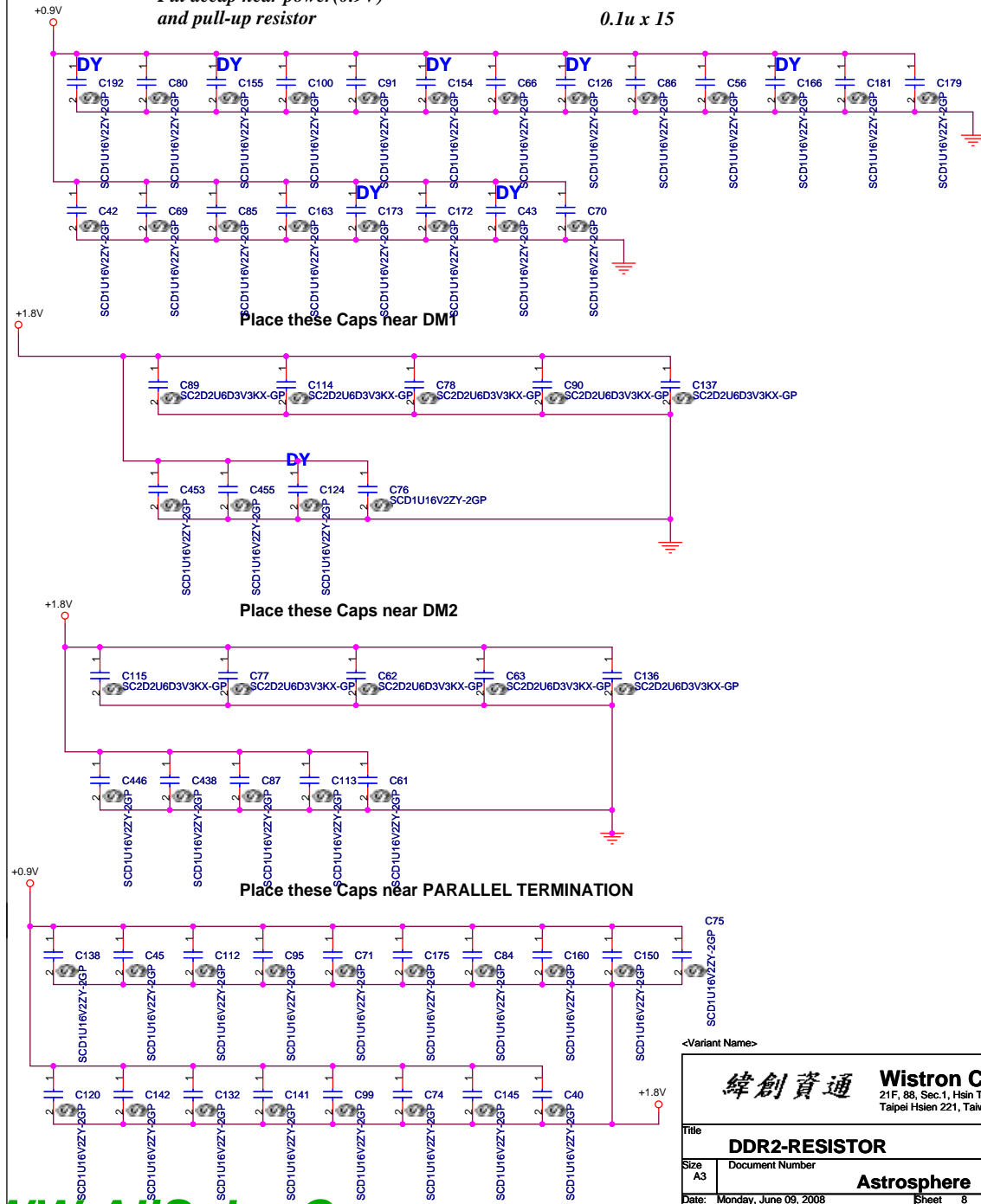
Put decap near power(0.9V) and pull-up resistor



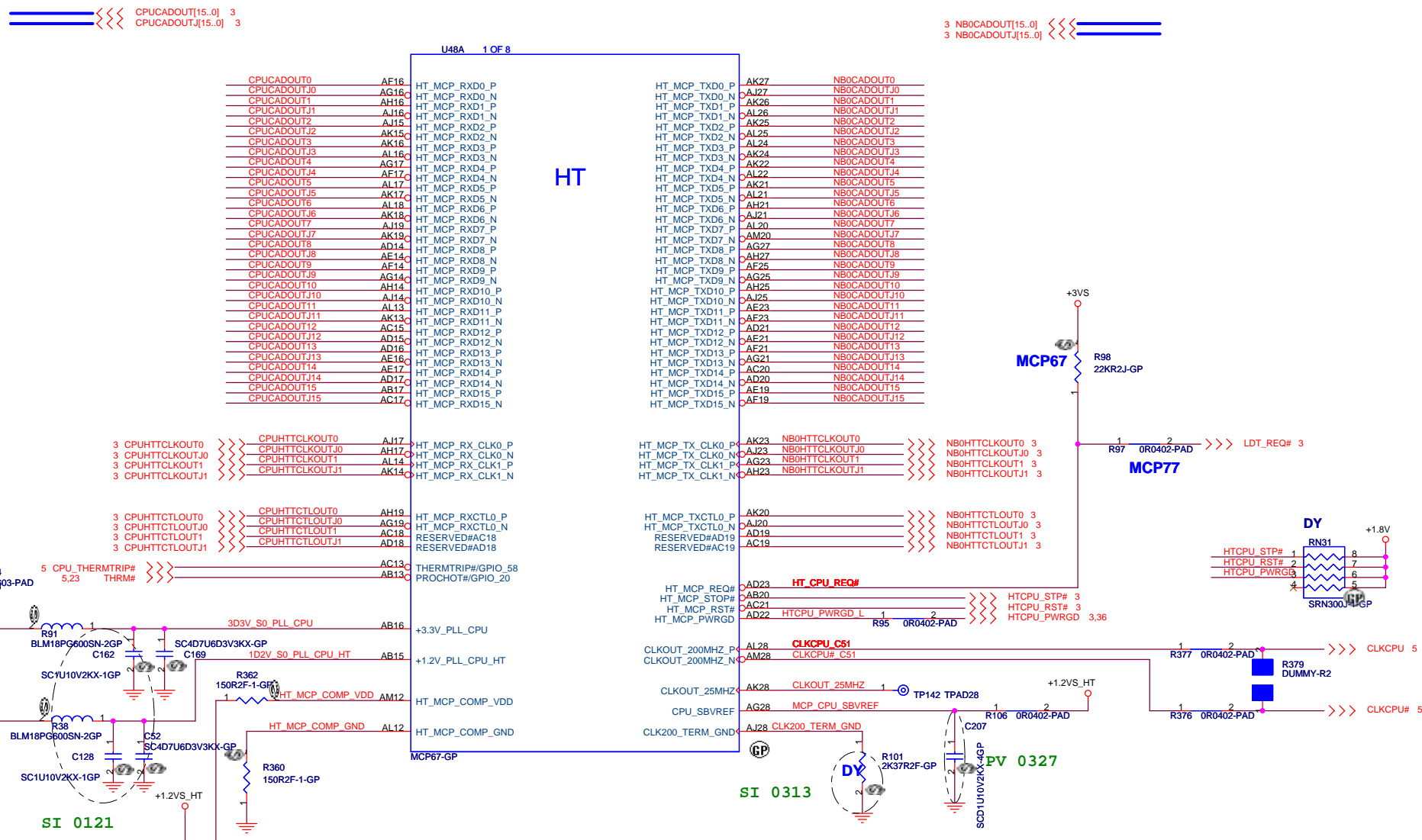
Decoupling Capacitor

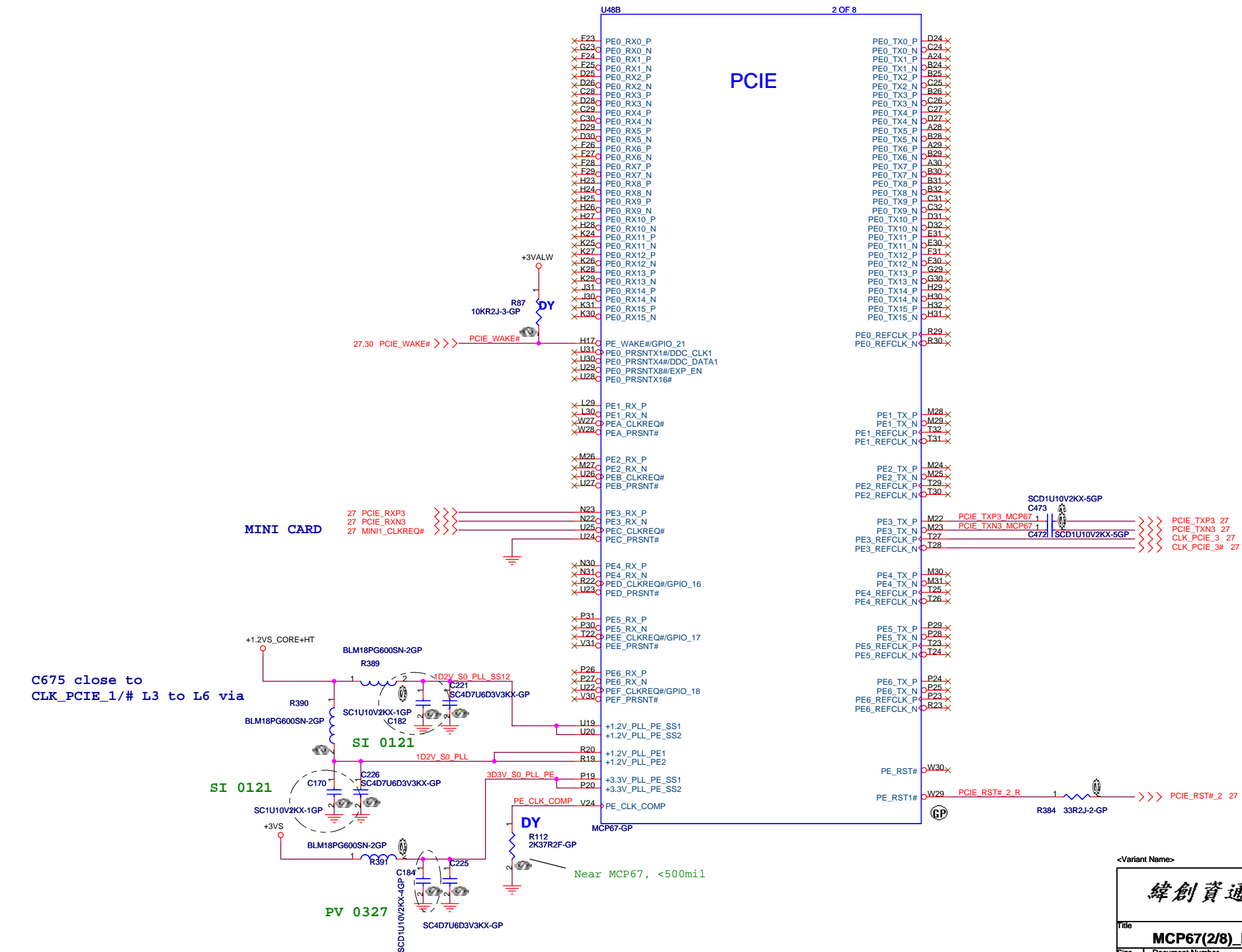
Put decap near power(0.9V) and pull-up resistor

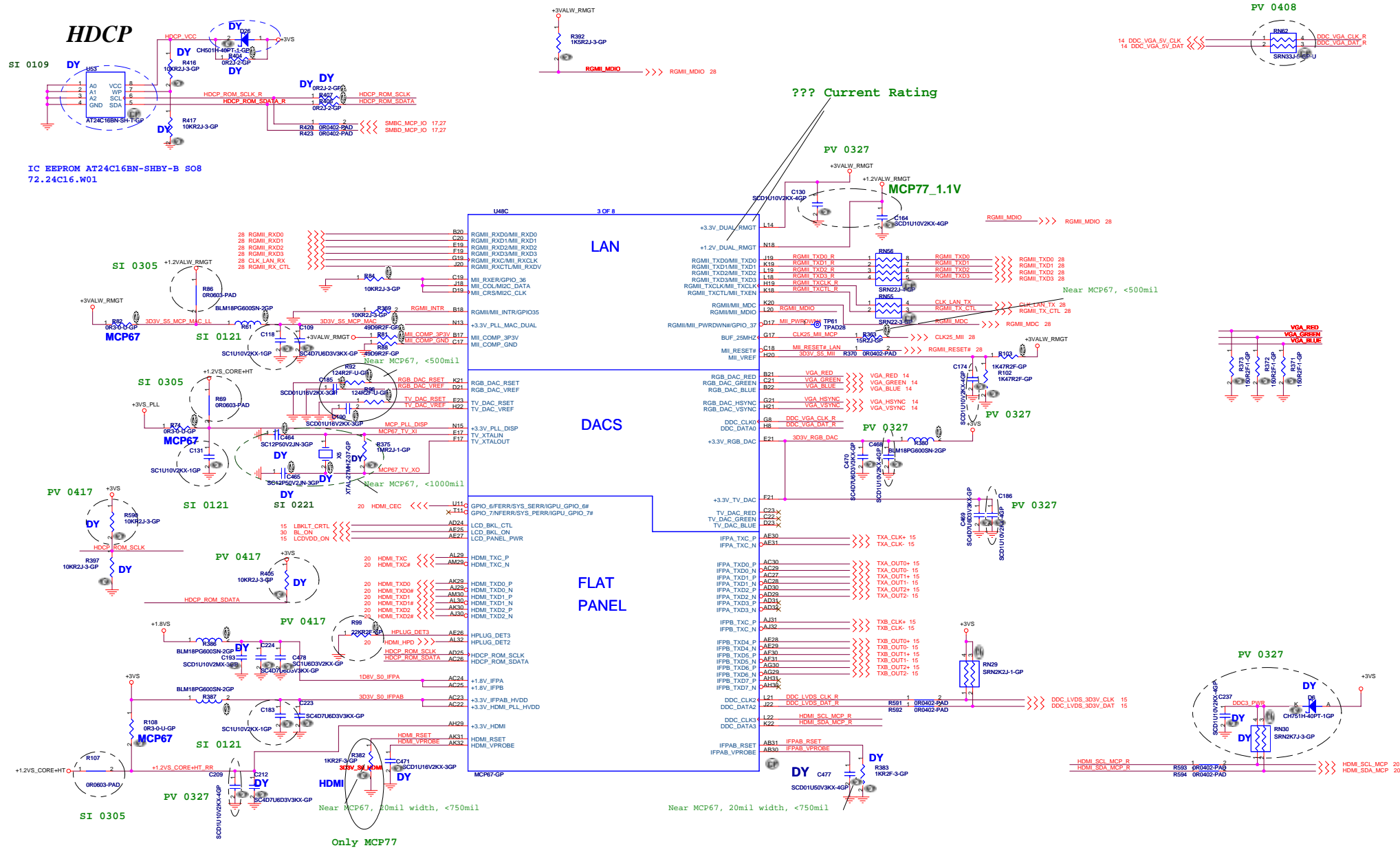
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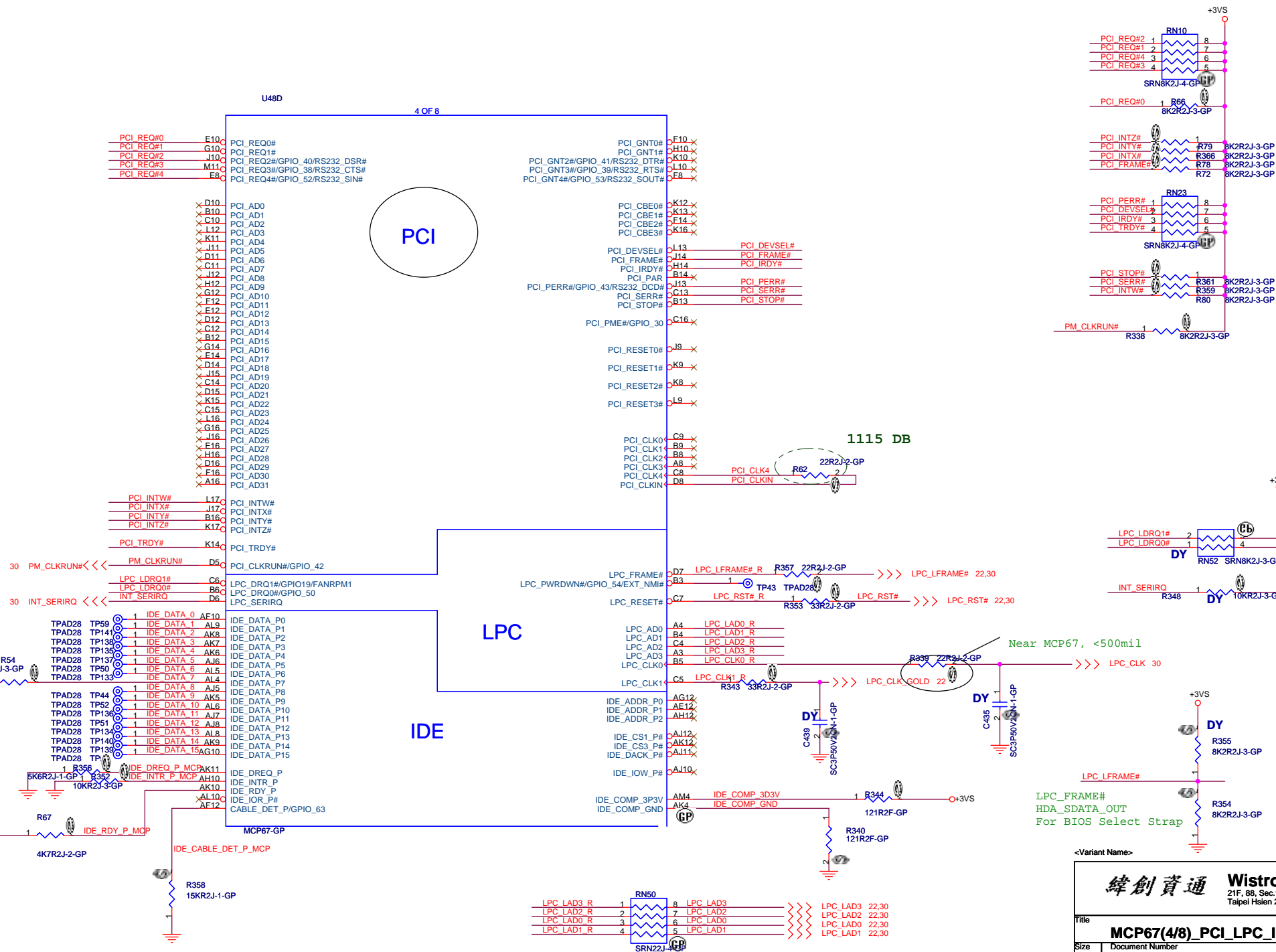


緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
DDR2-RESISTOR	
Title Size A3	Document Number Astrosphere
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If no 1394
Port 0 for NVidia Win-debug

place near MCP67

SATA

USB

Single USB

CARD READER

Bluetooth

Camera

MINI CARD

USB2

USB1

HDA

Only MCP77
1.1K on MCP67

PV 0327

Check this NEW_OC#

SI 1217

SI 0227

SI 0303

LPC_FRAME#
HDA_SDATA_OUT
For BIOS Select Strap

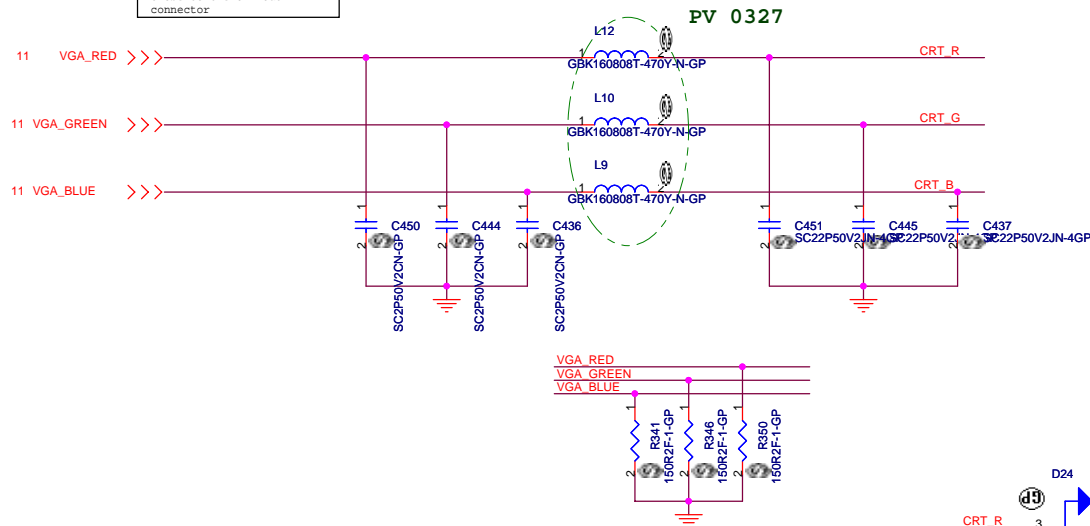
<Variant Name>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

MCP67(5/8)_SATA_USB_HDA			
Title			
Size	Document Number	Rev	
A3	Astrosphere	SA	
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CRT I/F & CONNECTOR

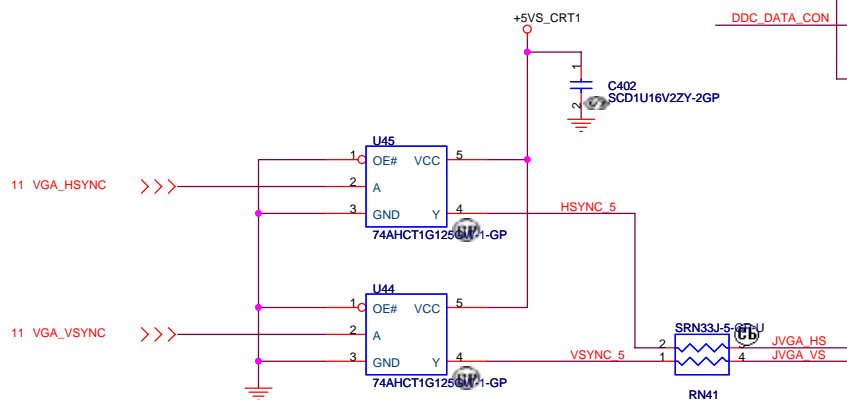
Layout Note:
Place these resistors
close to the CRT-out
connector



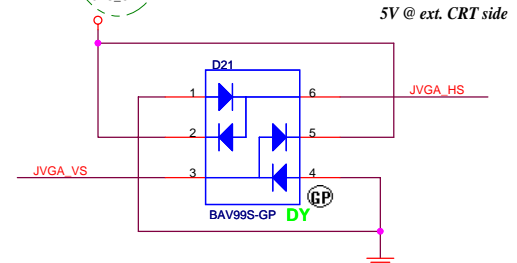
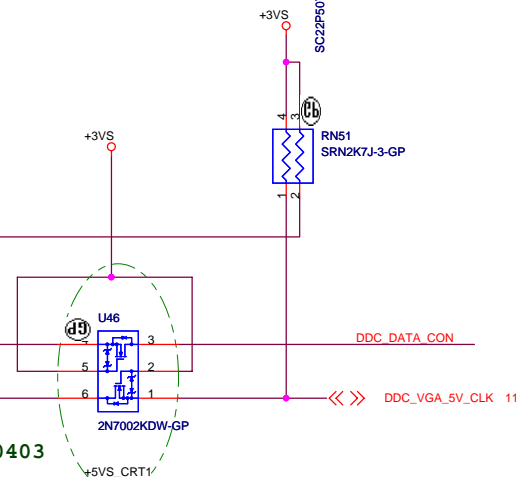
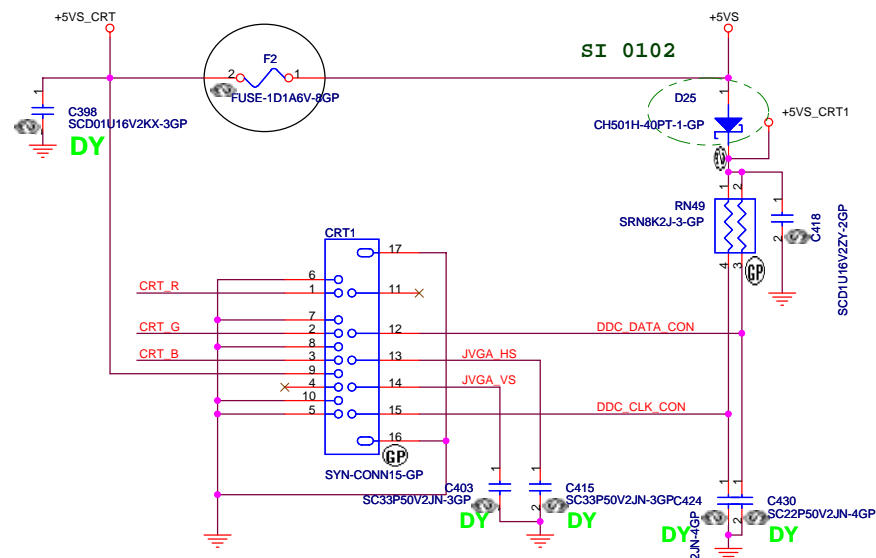
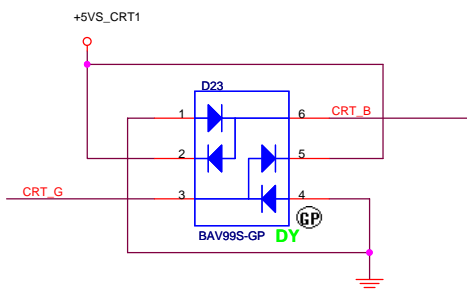
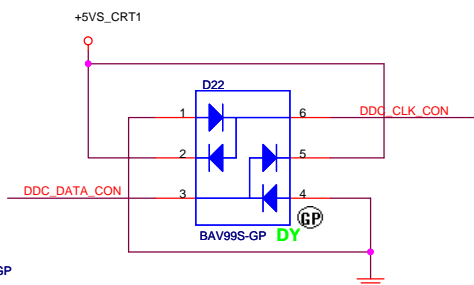
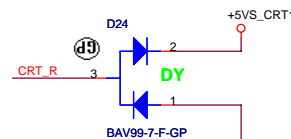
Layout Note:

* Must be a ground return path between this ground and the ground on the VGA connector.

Pi-filter & 150 Ohm pull-down resistors should be as close as to CRT CONN. RGB will hit 75 Ohm first, pi-filter, then CRT CONN.



Hsync & Vsync level shift



<Variant Name>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

CRT/TV CONNECTOR		
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<Variant Name>

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

Board to board conn/ Docking

Size
A3

Document Number
Astrosphere

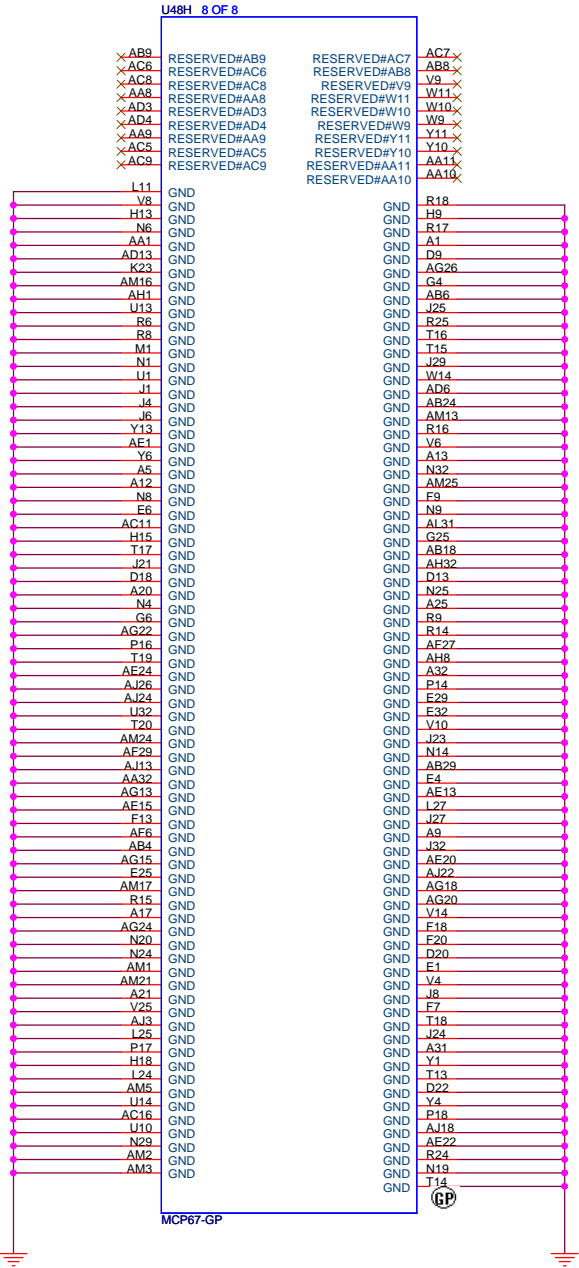
Rev
SA

Date: Monday, June 09, 2008

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<Variant Name>

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

MCP67(8/8)_GND

Size

A3

Document Number

Astrosphere

Rev

SA

Date:

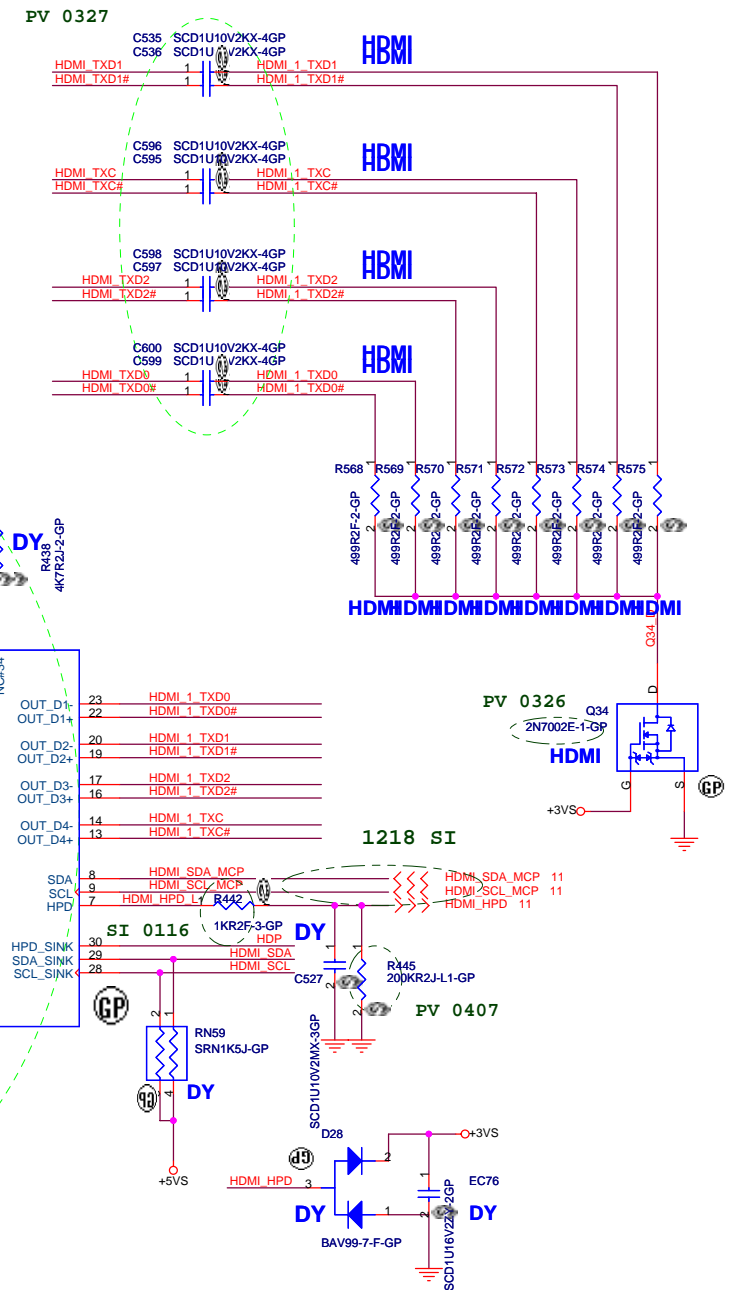
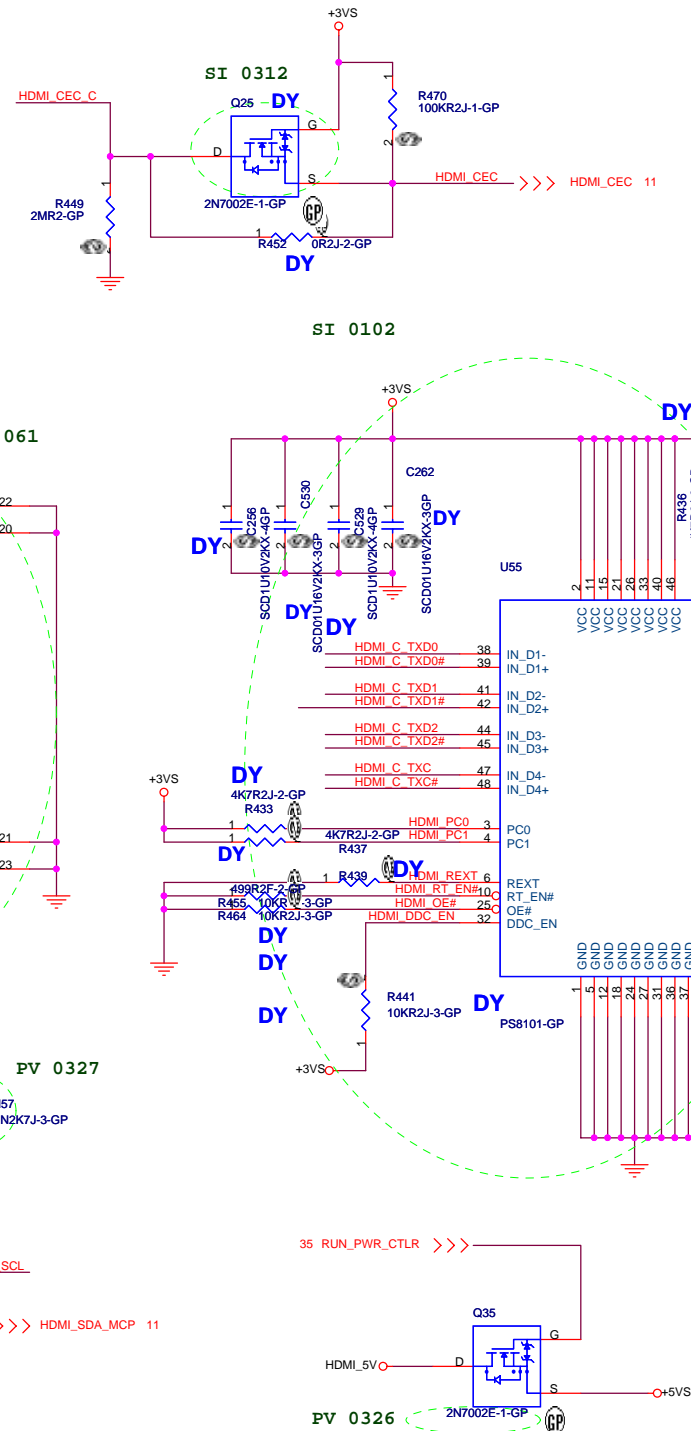
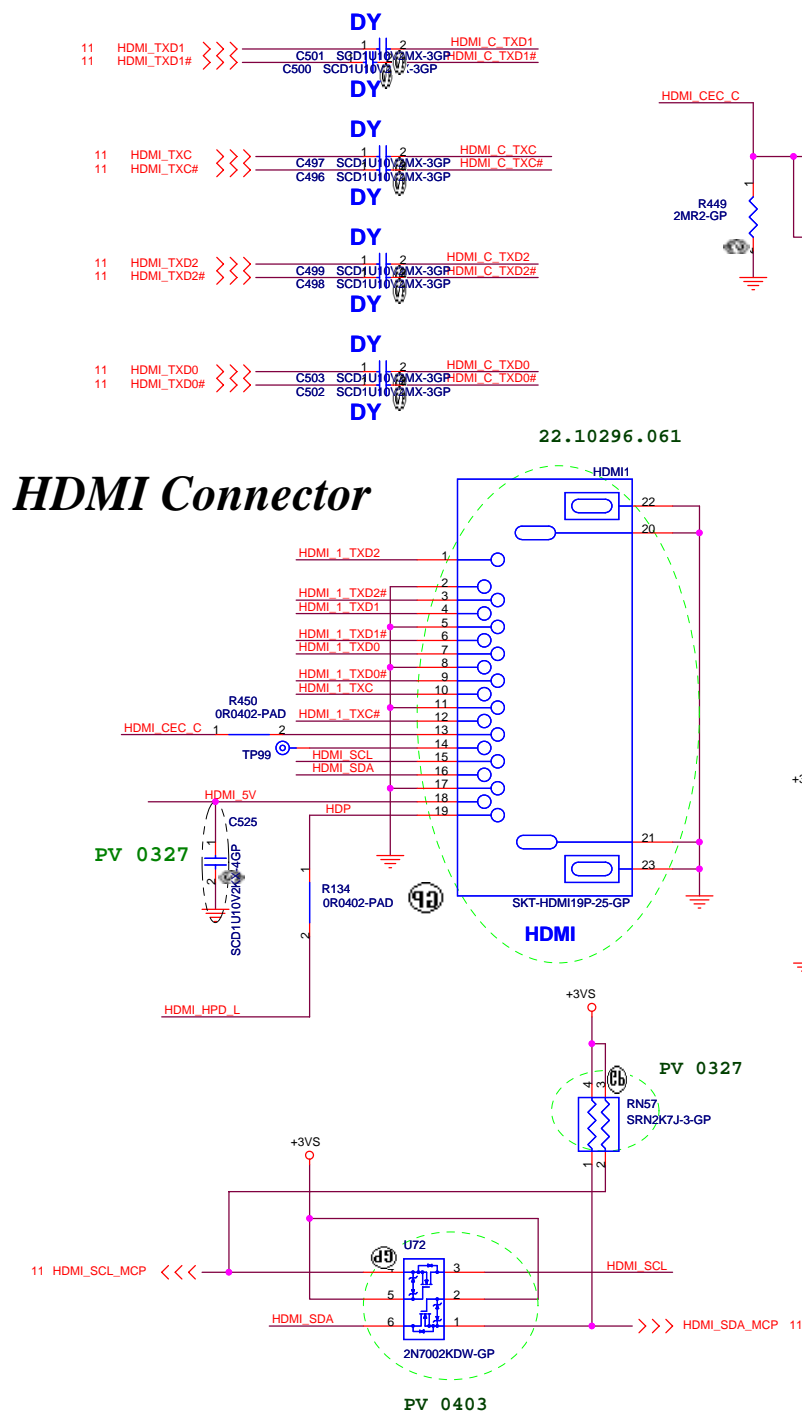
Monday, June 09, 2008

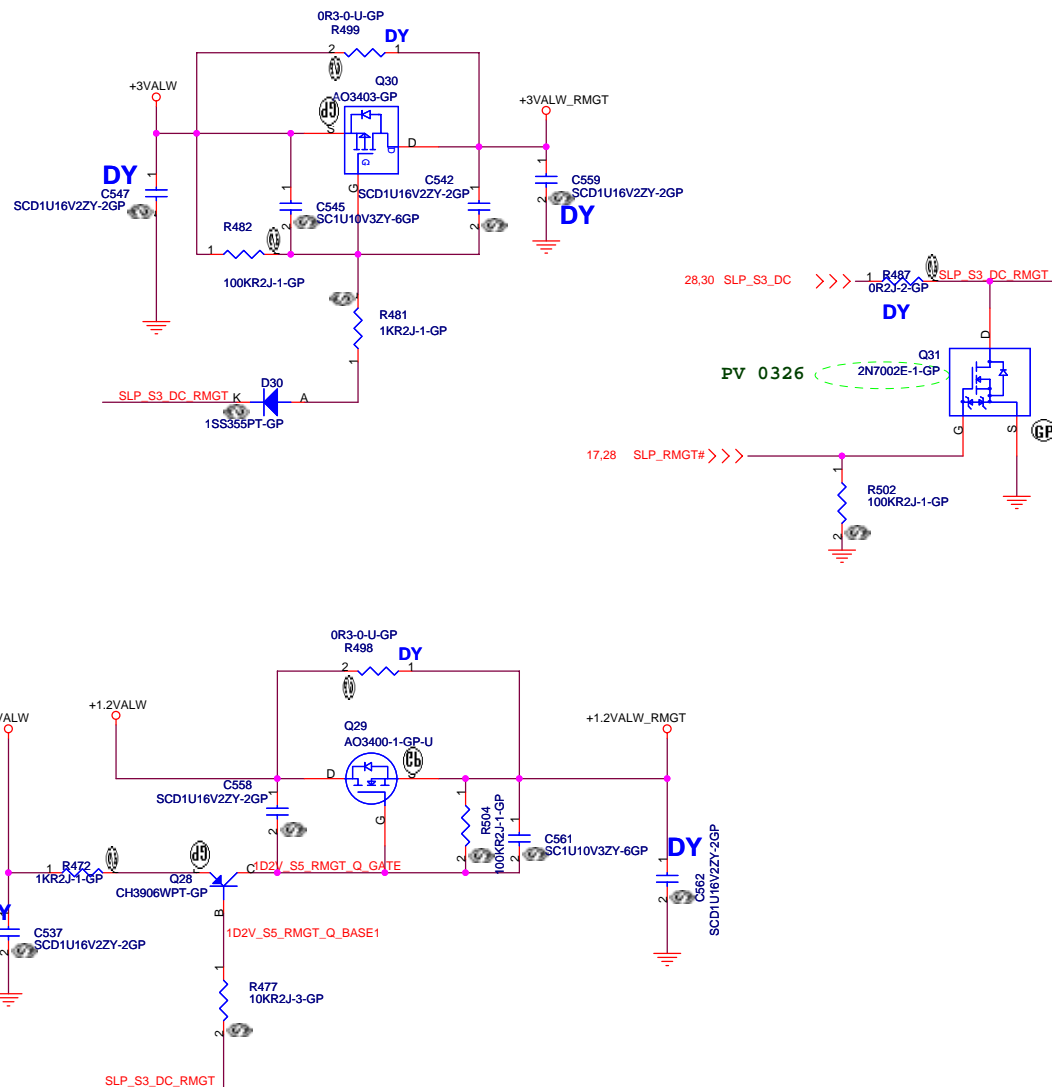
Sheet

19

of

44

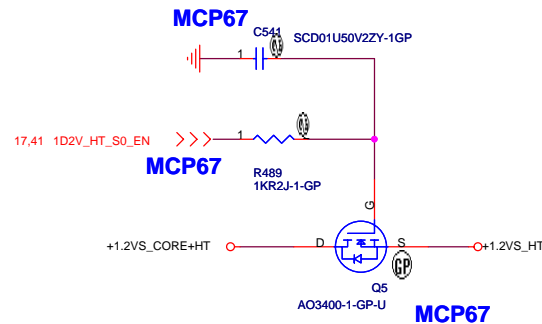




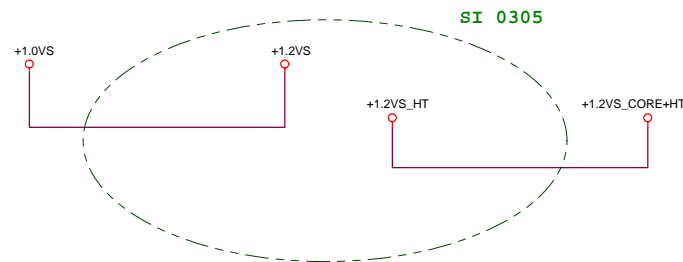
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緯創資通		Wistron Corporation	
		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
Size A3	Document Number Astrosphere	Rev SA	
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MCP 67



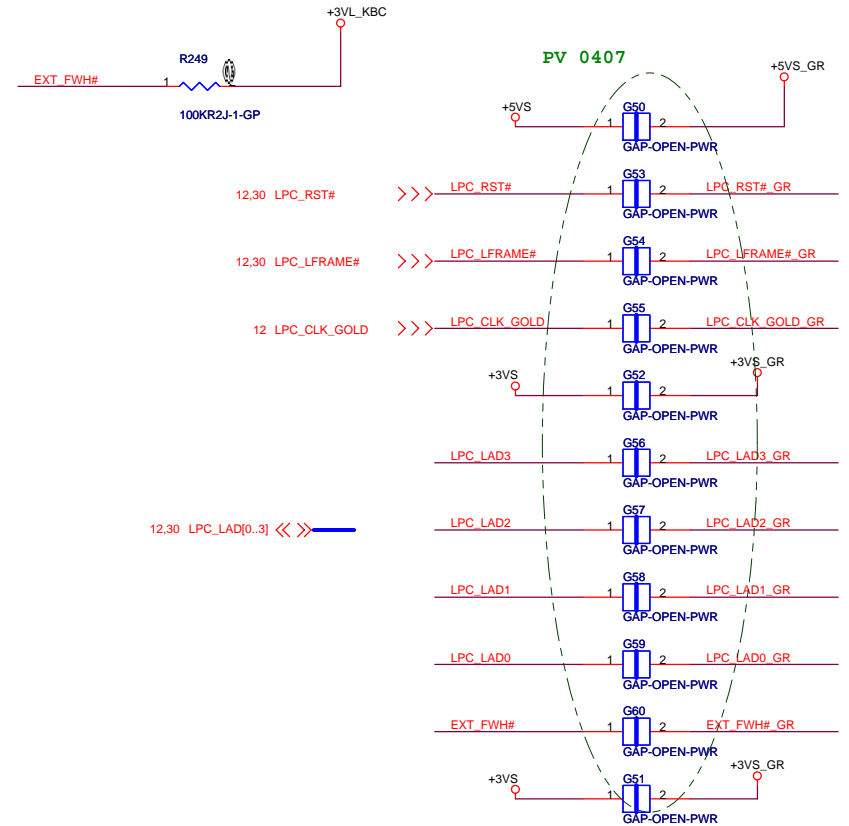
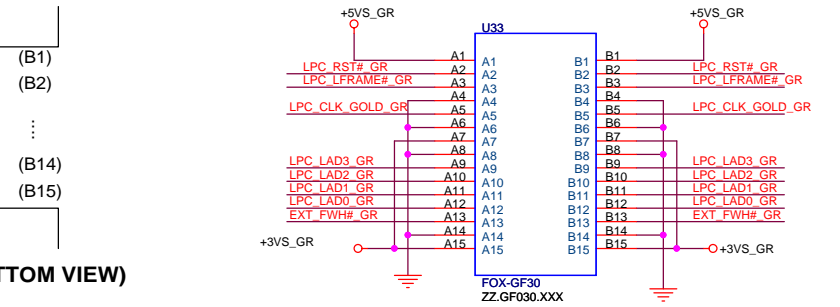
MCP77



TOP VIEW

(BOTTOM VIEW)

GOLDEN FINGER FOR DEBUG BOARD



Put near board edge

<Variant Name>

緯創資通

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

POWER PATH

Size
A3

Document Number

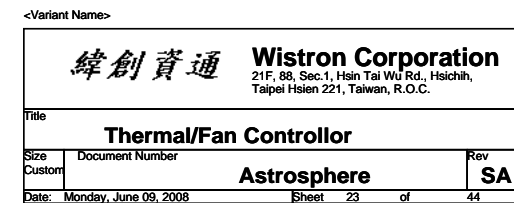
Astrosphere

SA

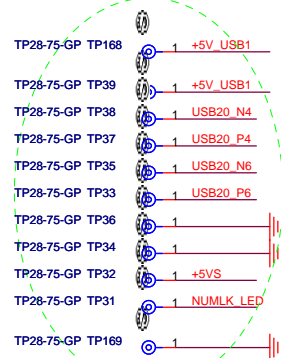
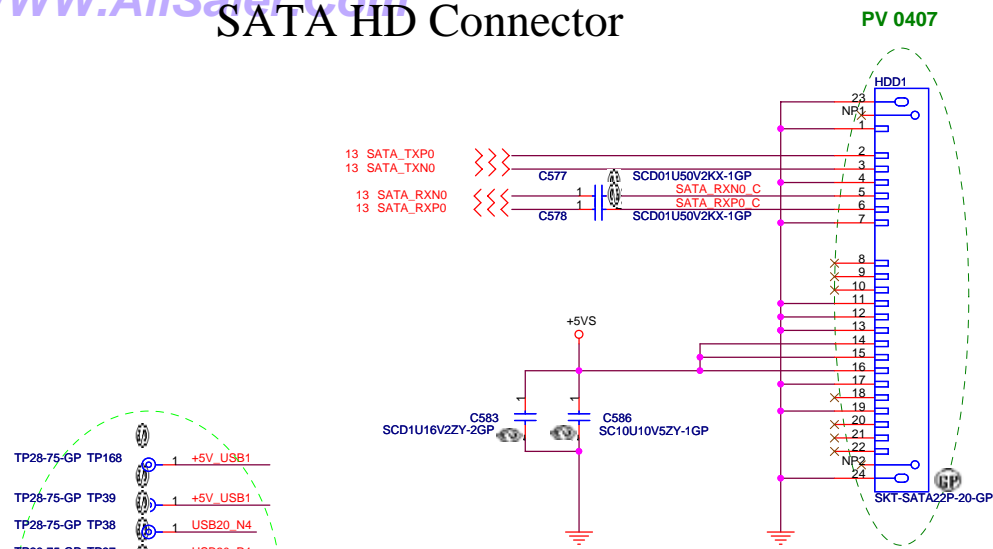
Date: Monday, June 09, 2008

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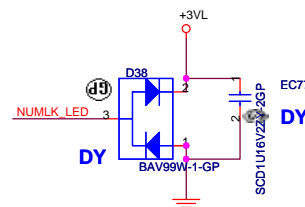
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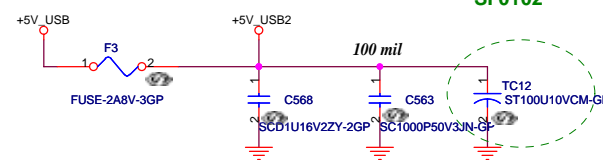
SATA HD Connector



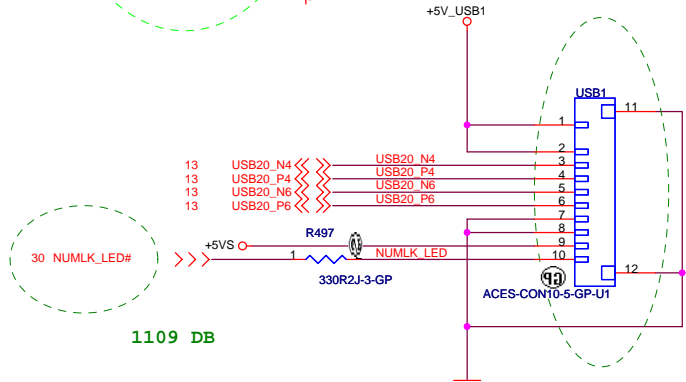
1115 DB
Close to USB1



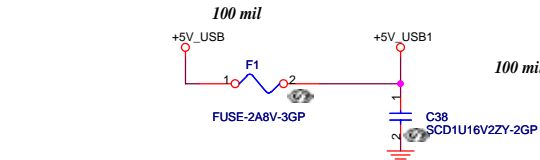
USB PORT



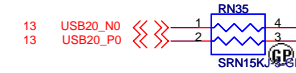
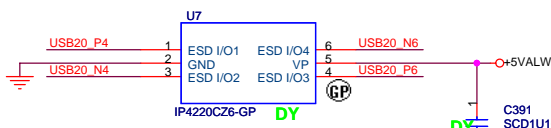
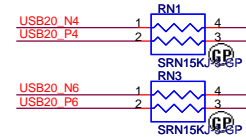
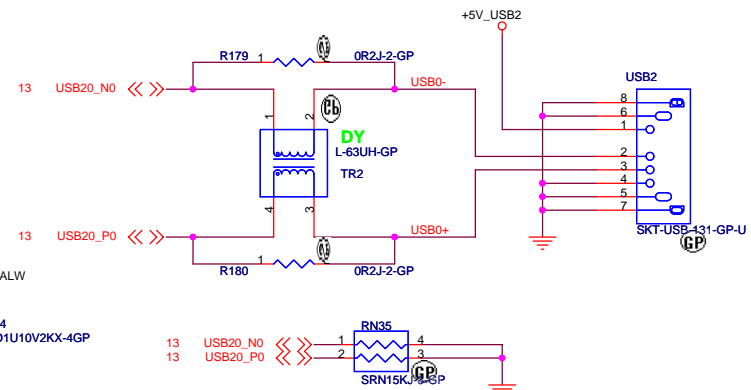
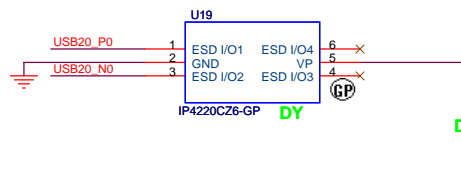
SI 0102



1109 DB

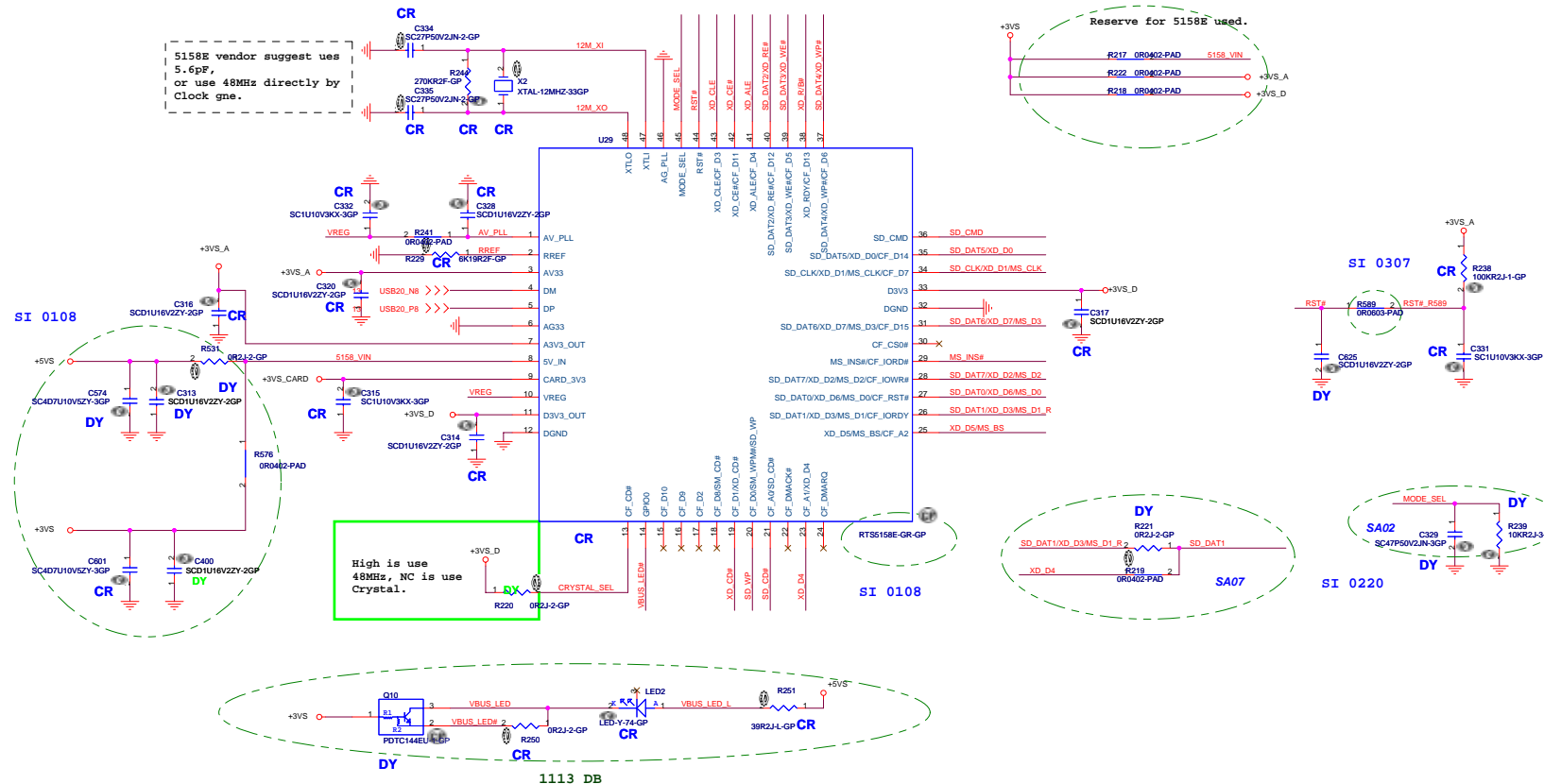


1112 DB

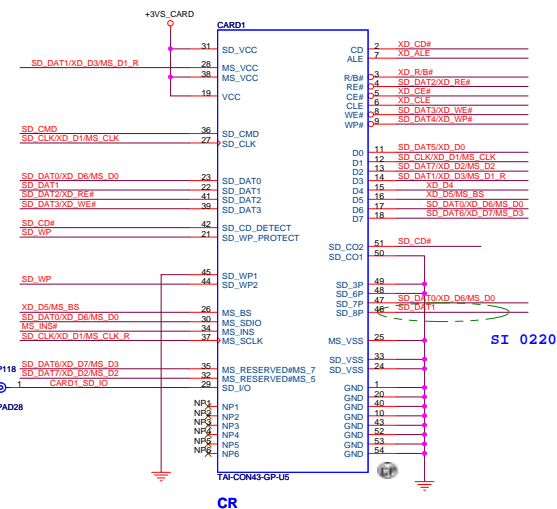


<Variant Name>

緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title	HD/CDROM
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4 IN1 CARD-READER (SD/SD IO/MMC/MMC4.0/MS/MS PRO/XD)



<Variant Name>

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.

Title

R5C832/IEEE1394/SD

Size

Document Number

Rev

A3

Astrosphere

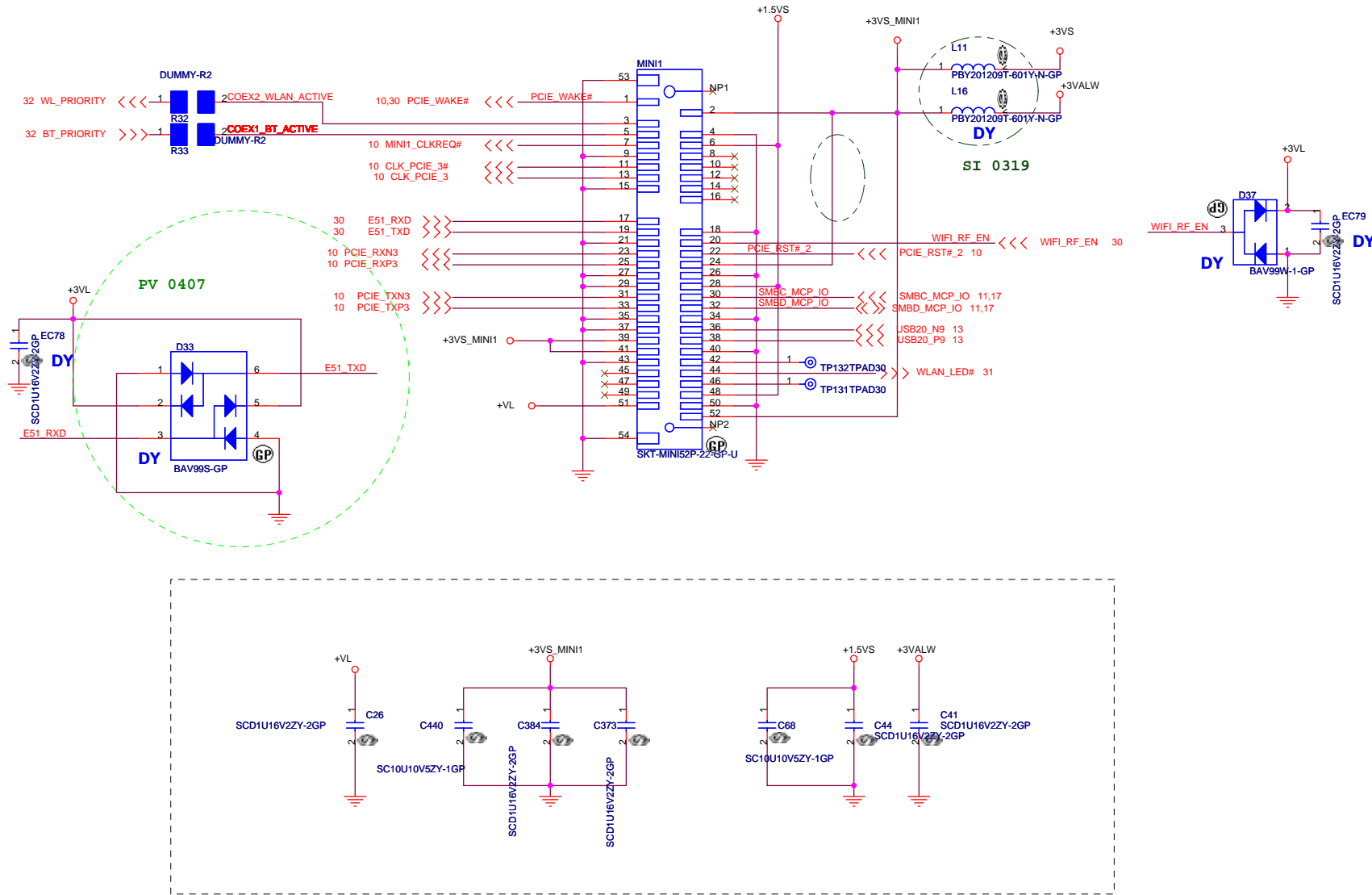
SA

Date: Monday, June 09, 2008

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Mini Card Connector 1

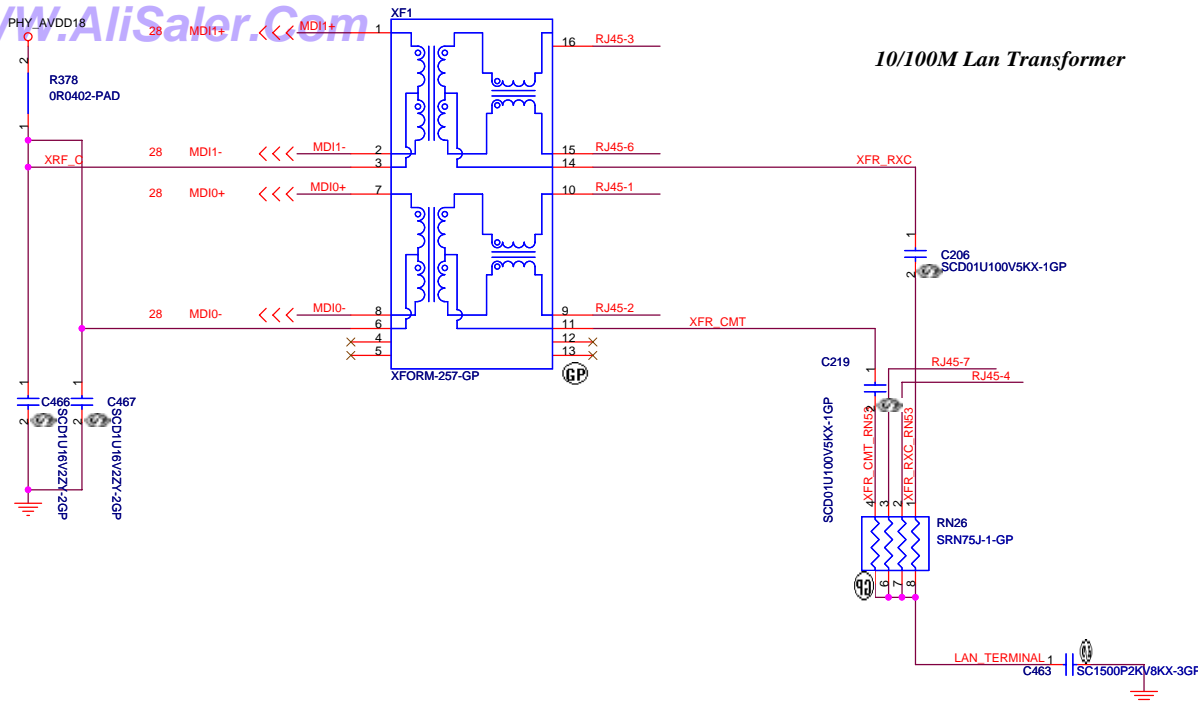
Wireless Card



<Variant Name>

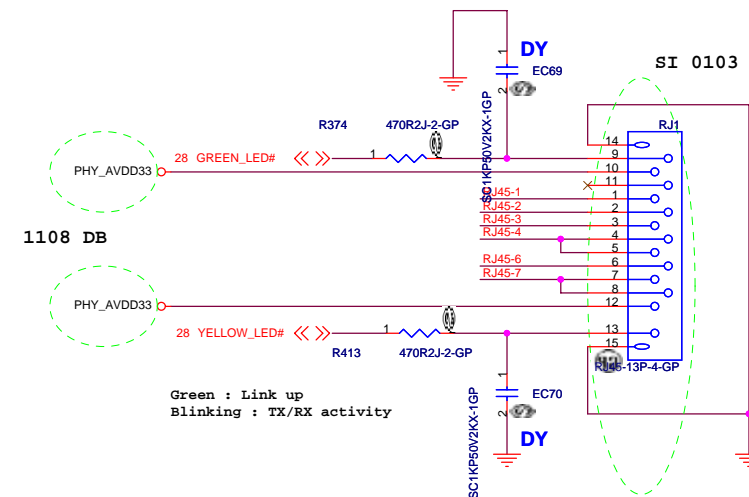
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Title MINI CARD CONN .			
Size A3	Document Number Astrosphere		Rev SA
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- 1.route on bottom as differential pairs.
- 2.Tx+/Tx- are pairs. Rx+/Rx- are pairs.
- 3.No vias, No 90 degree bends.
- 4.pairs must be equal lengths.
- 5.6mil trace width, 12mil separation.
- 6.36mil between pairs and any other trace.
- 7.Must not cross ground moat,except RJ-45 moat.

PIN A1 : GREEN
PIN A3 : ORANGE
PIN B2 : YELLOW



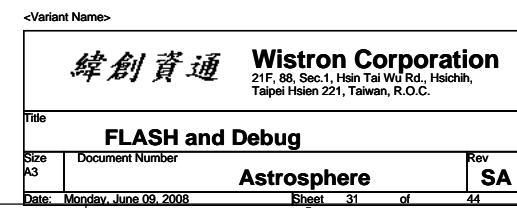
Remark:

Add trace width to 20mils
for RJ1 pin4, 5 and pin 7, 8.

<Variant Name>

緯創資通		Wistron Corporation	
		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
LAN CONN			
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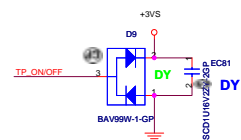


Keyboard matrix (from vendor)

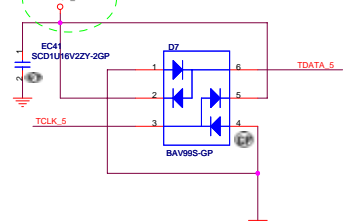
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SI 0310

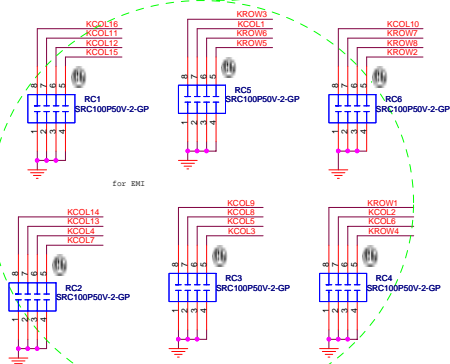
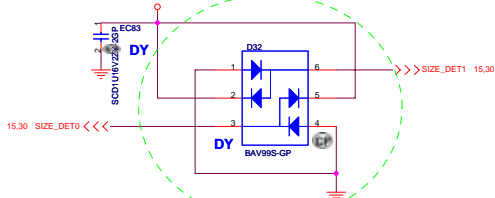
1114 DB
Close to KB1



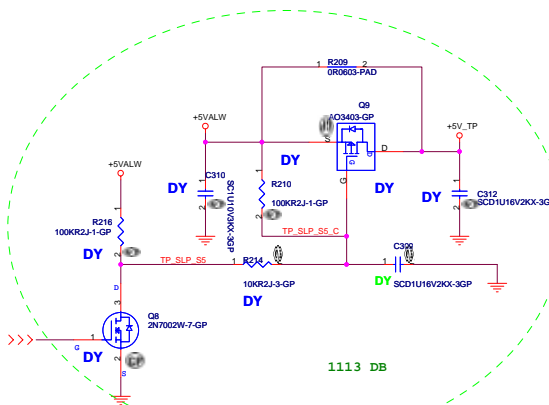
1108 DB



PV 0407

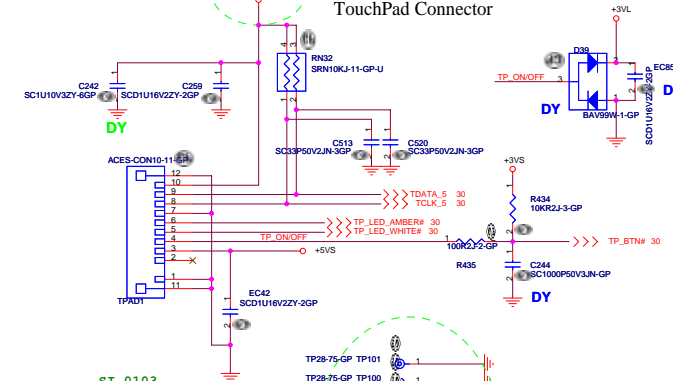


for EMI

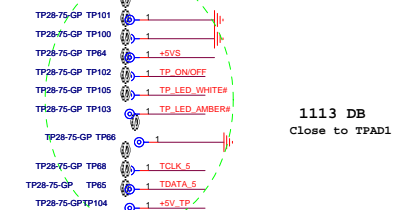


1108 D

TouchPad Connector

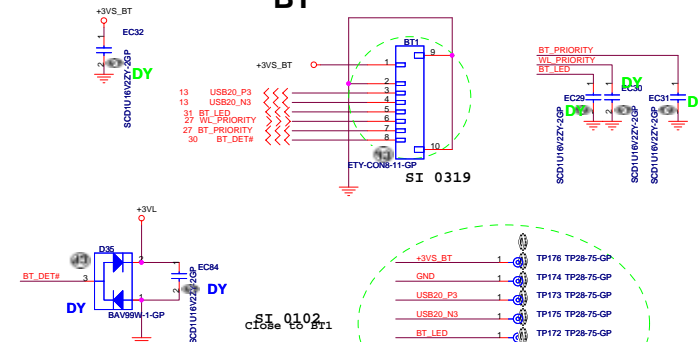


SI 0103



1113 DB
Close to TPAD1

BT



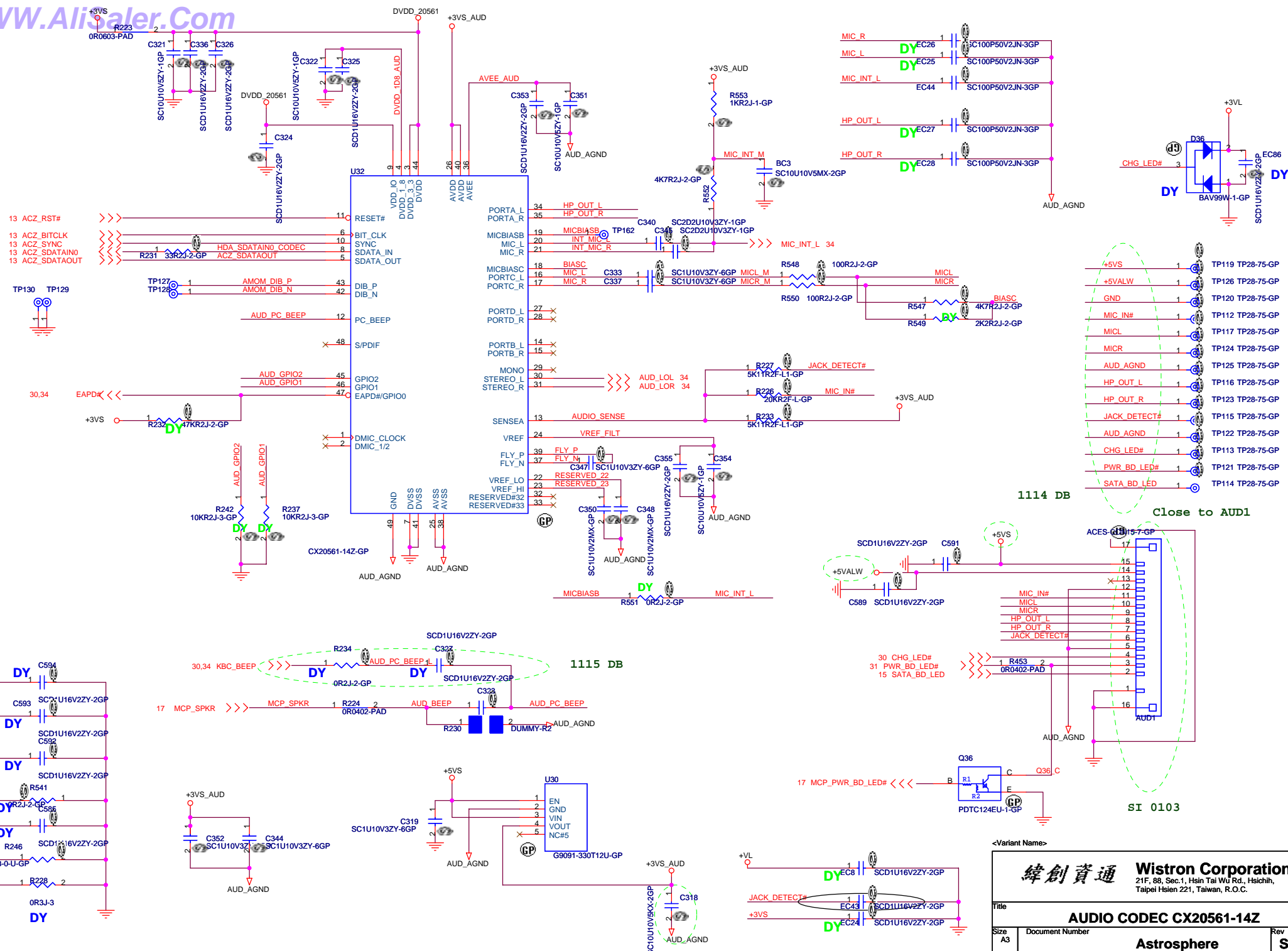
SI 0102
Close to BTI

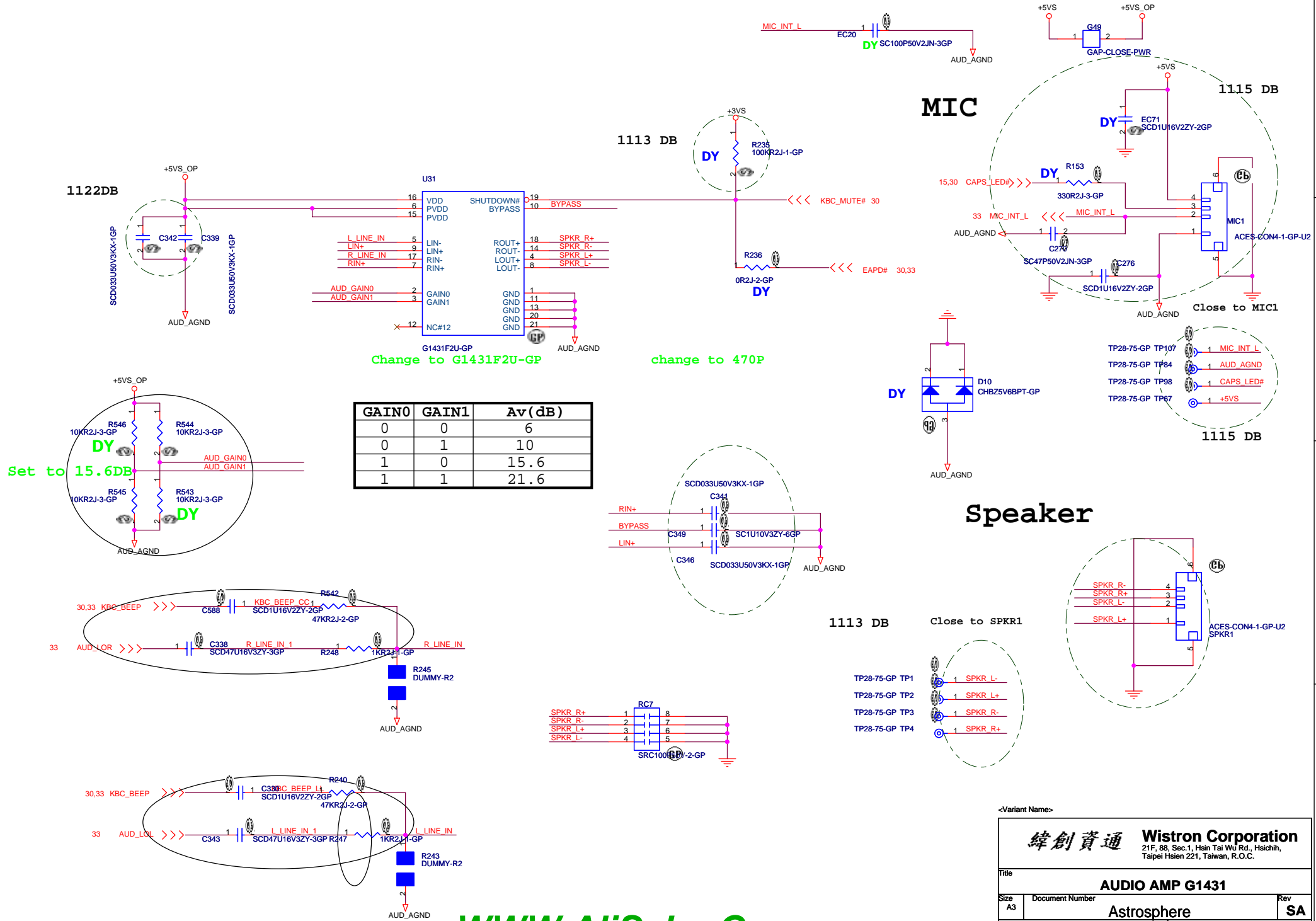
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緯創資通

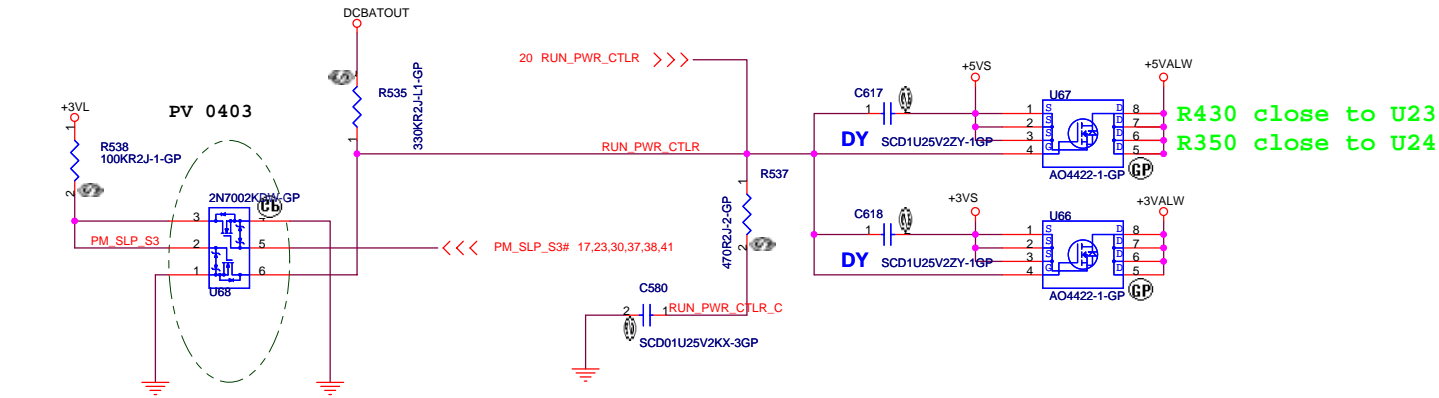
Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title			
KeyBoard-CONN			
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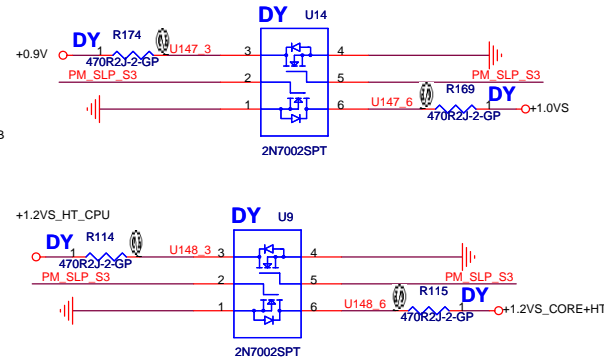
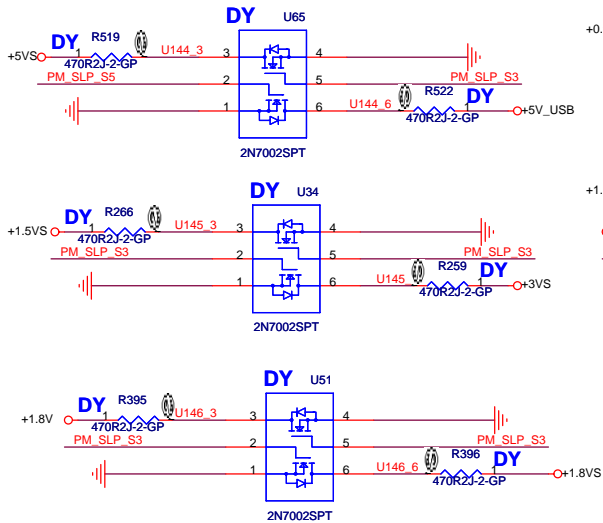
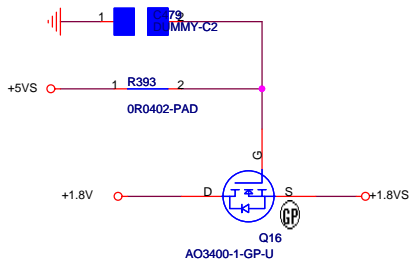
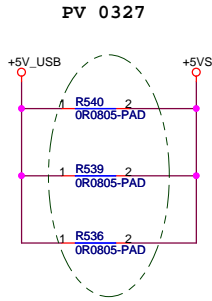
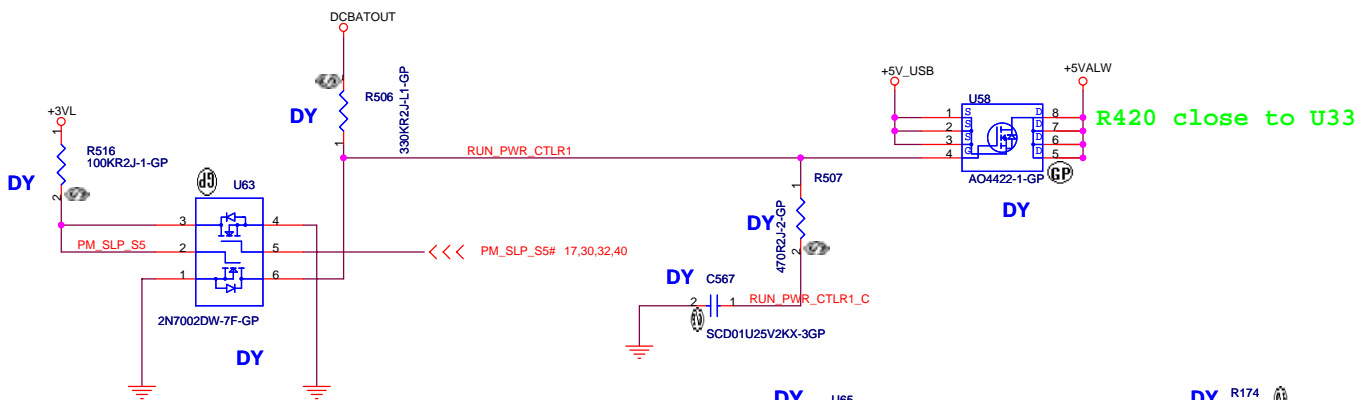


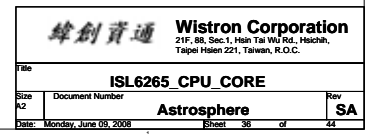


Run Power



Run Power





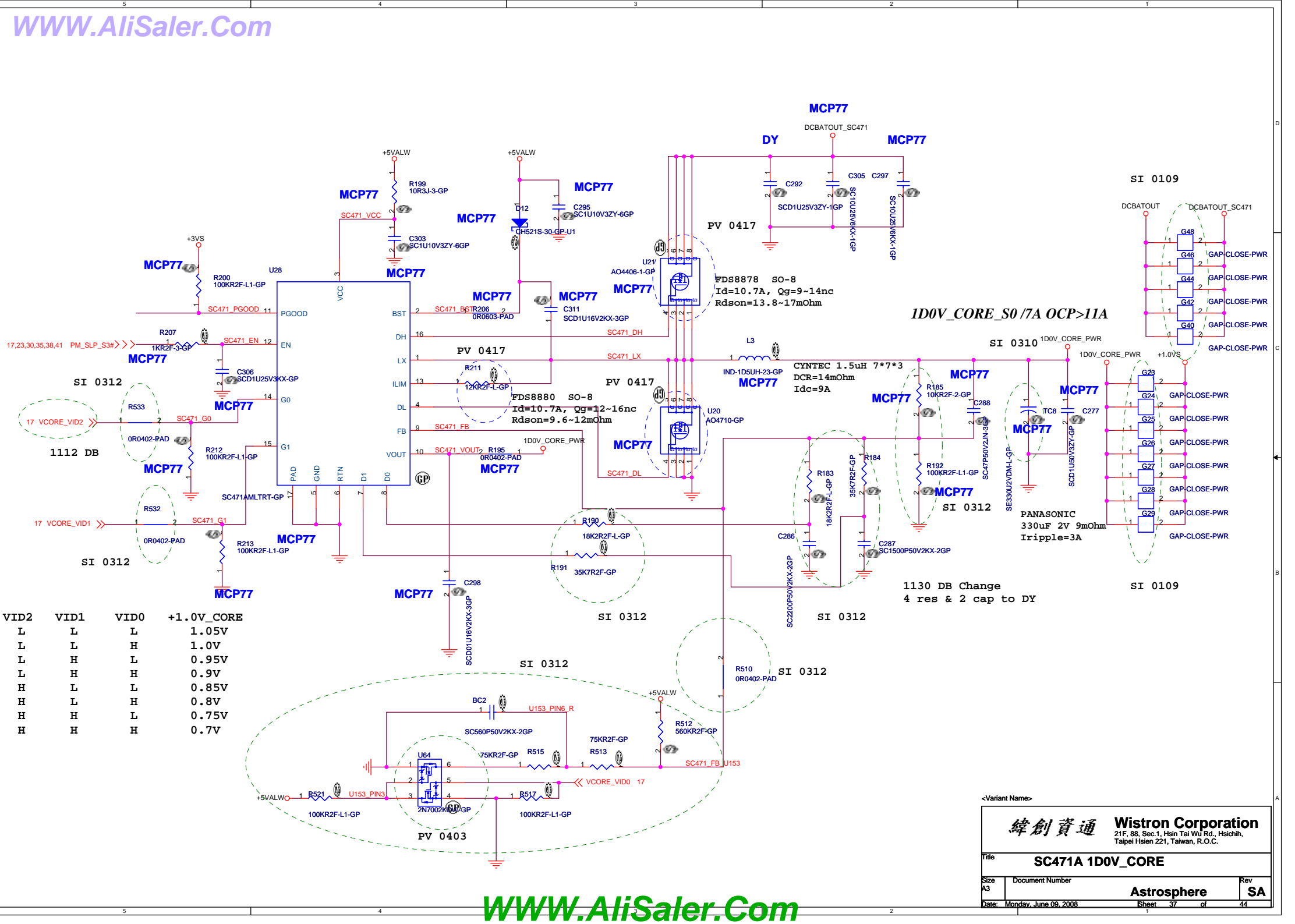
WWW.AliSaler.Com

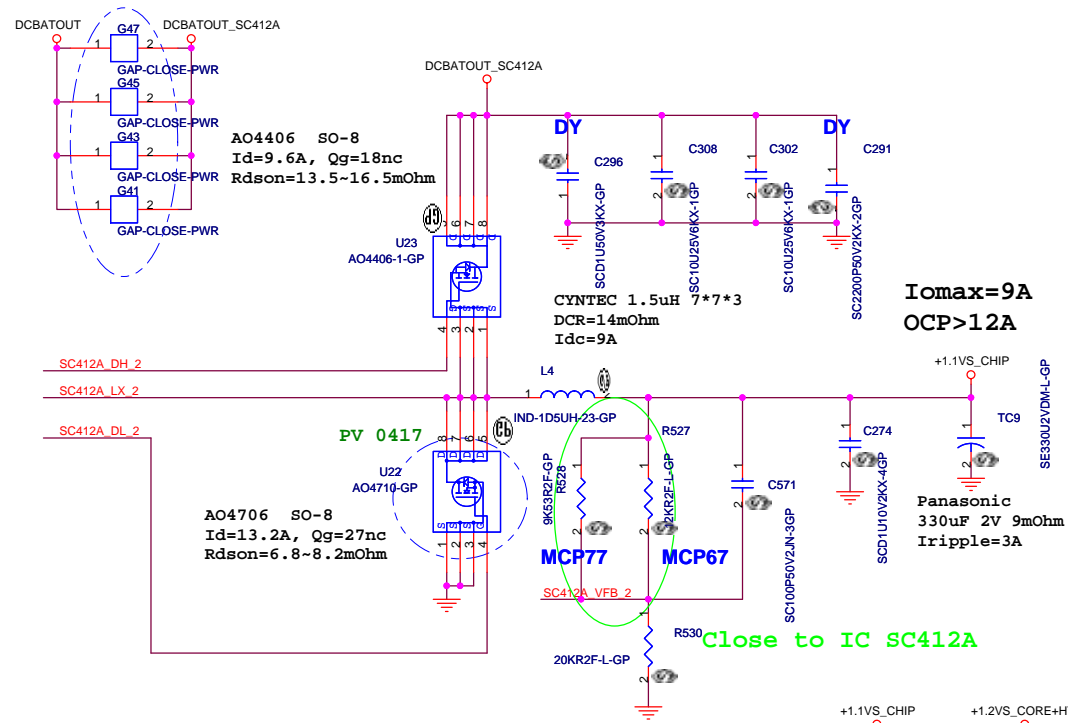
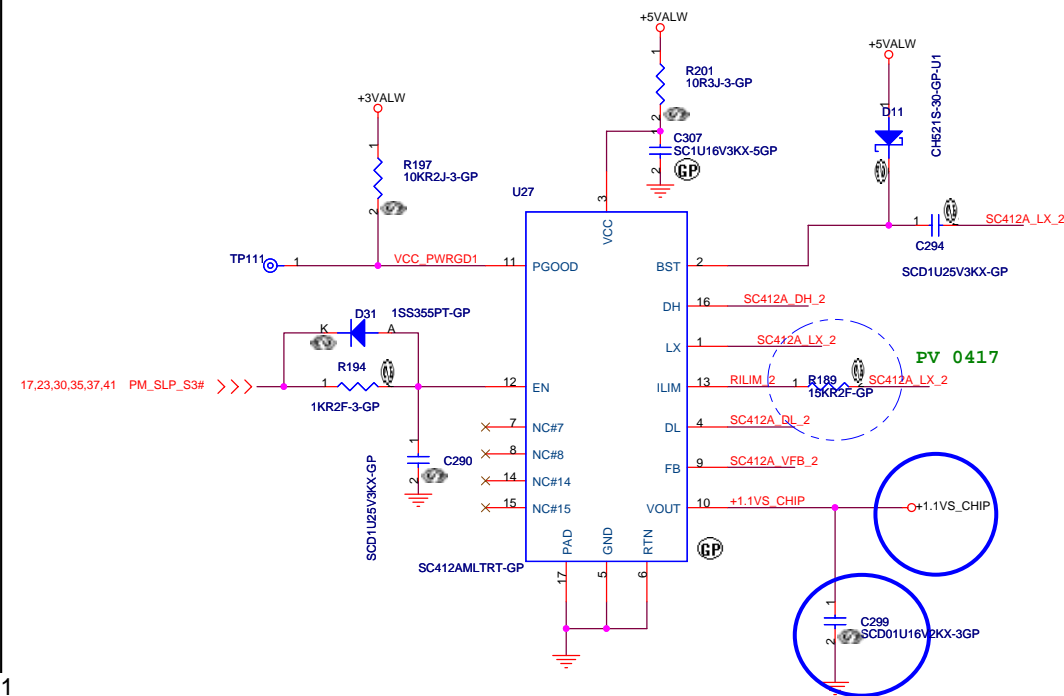
VID2	VID1	VID0	+1.0V_CORE
L	L	L	1.05V
L	L	H	1.0V
L	H	L	0.95V
L	H	H	0.9V
H	L	L	0.85V
H	L	H	0.8V
H	H	L	0.75V
H	H	H	0.7V

<Variant Name>

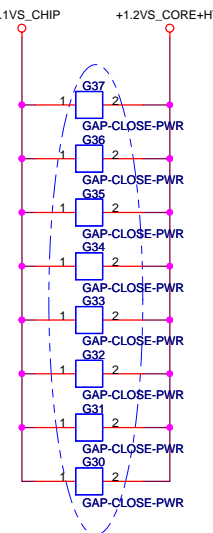
緯創資通 Wistron Corporation		
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		
Title	SC471A 1D0V_CORE	
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WWW.AliSaler.Com





MCP67: 1.2V (R425/R426) = (12K/20K)
MCP77: 1.1V (R425/R426) = (9.53K/20K)

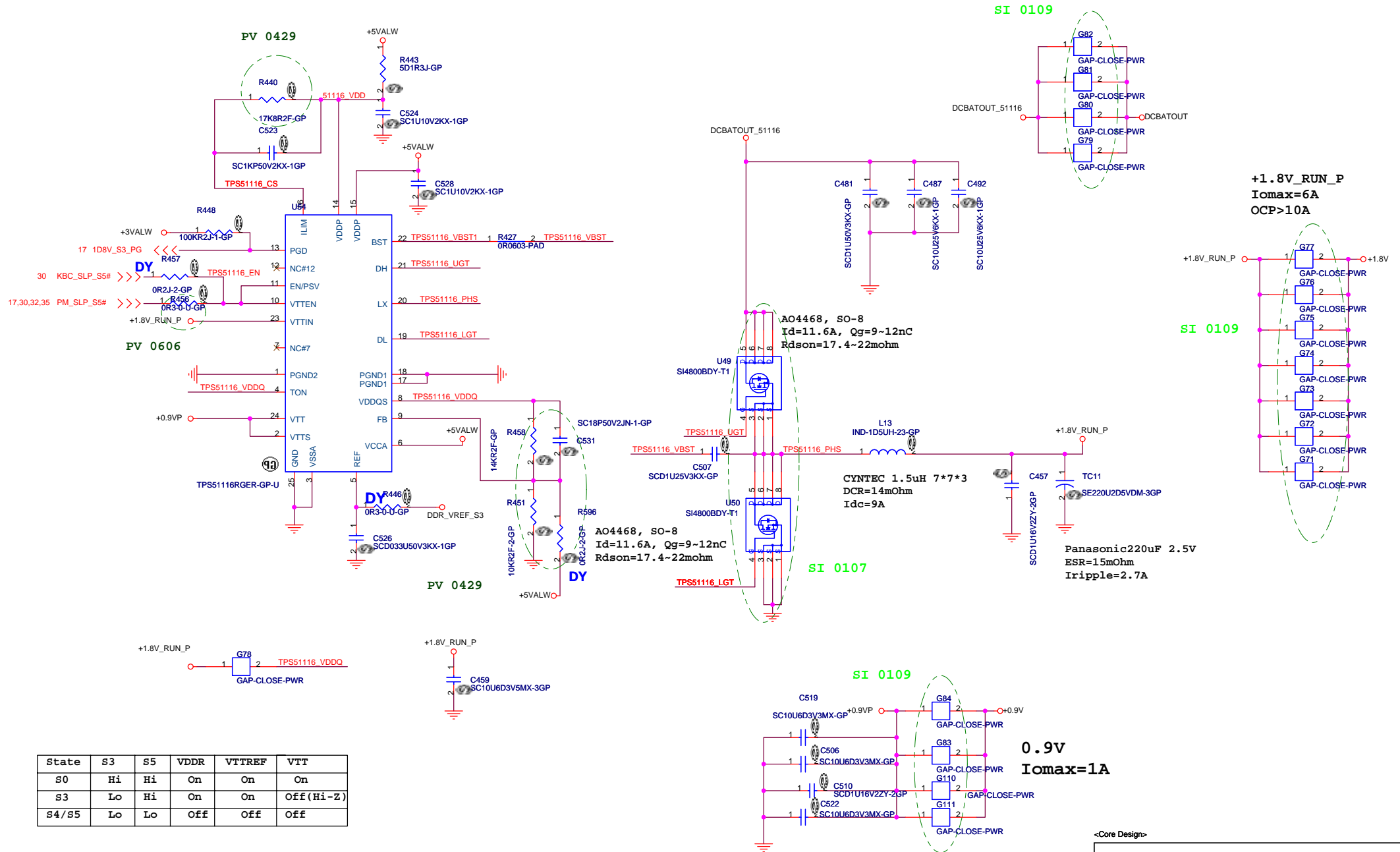


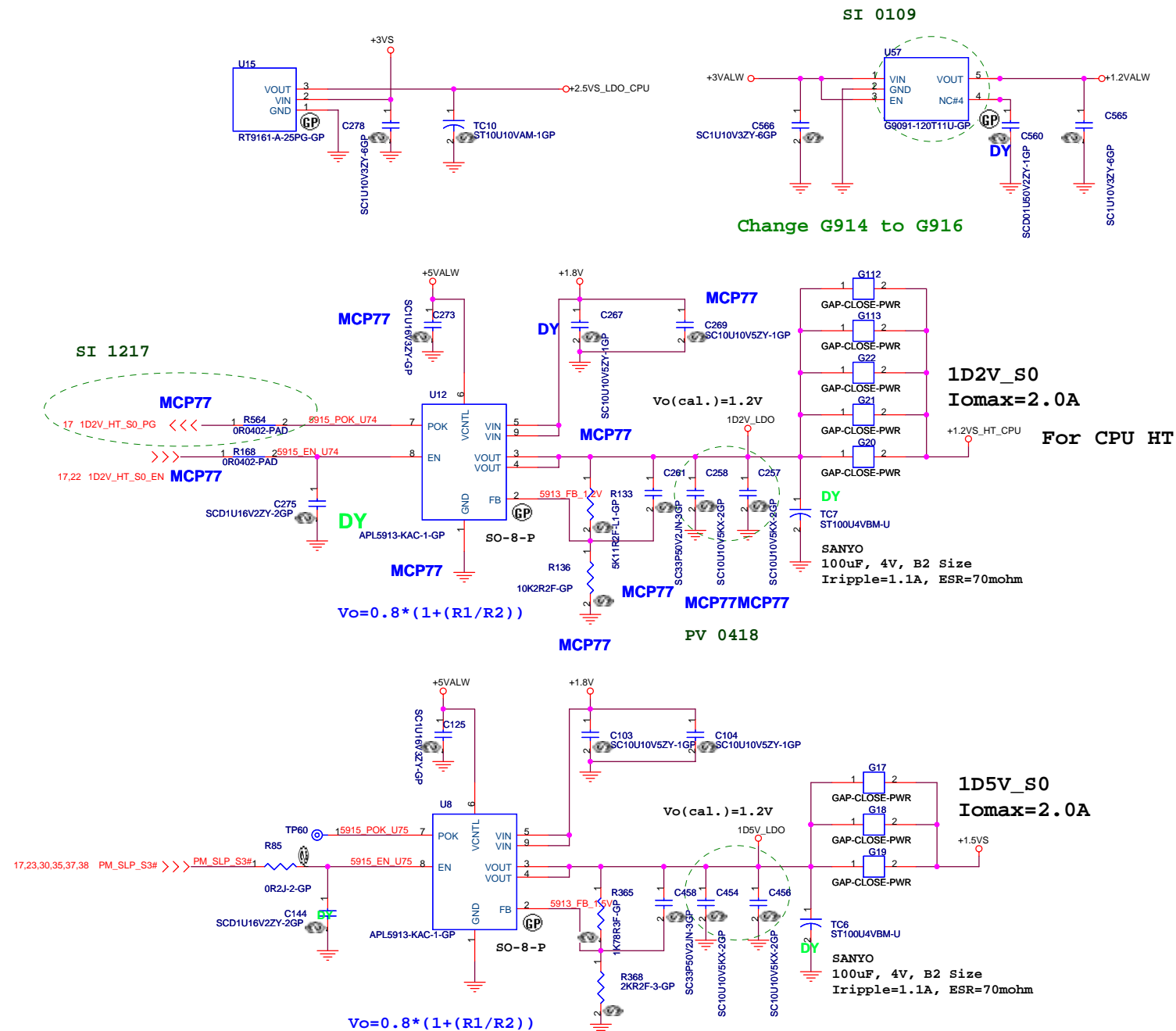
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緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title SC412A 1D1V_CORE	
Size A3	Document Number
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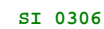
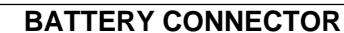
TI TPS51116 for 1D8V and 0D9V





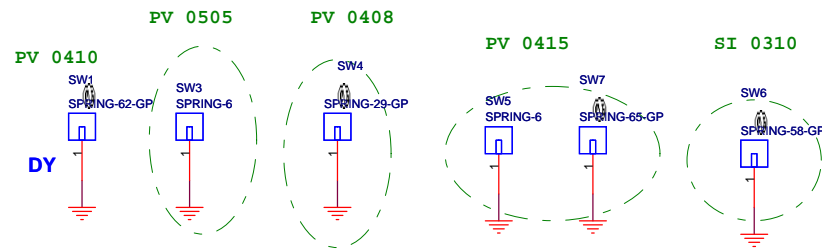
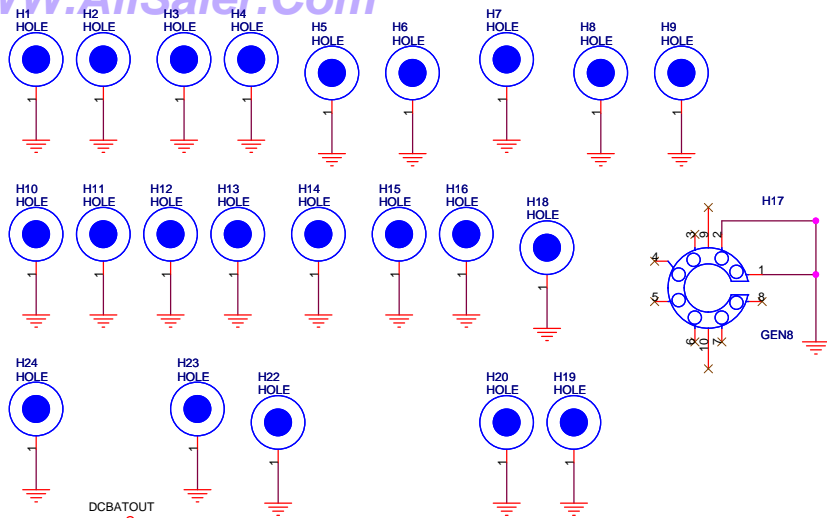
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1D5V/2D5V/3D3V/5V_AUX	
Size	Document Number
A3	
Astrosphere	
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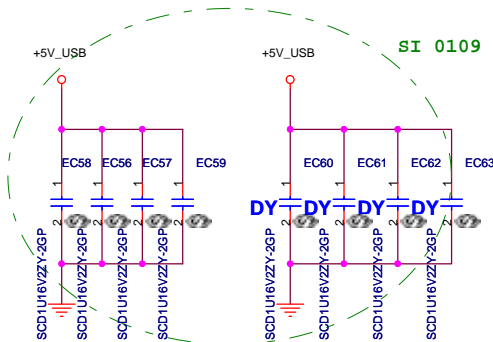
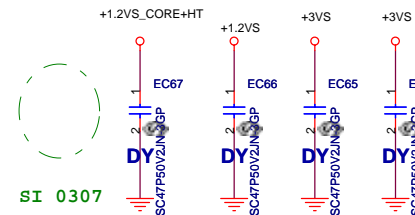
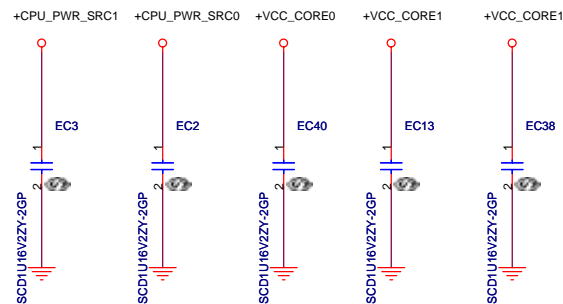
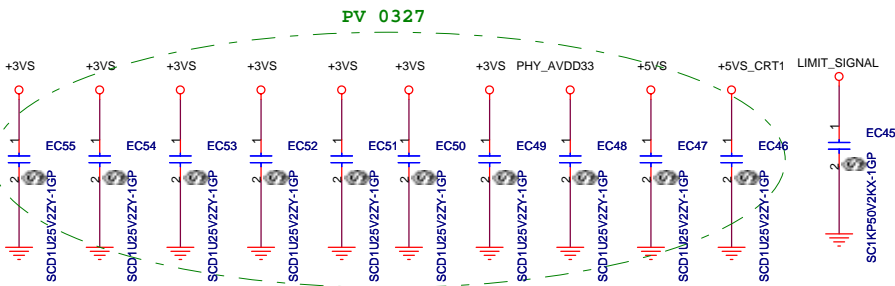
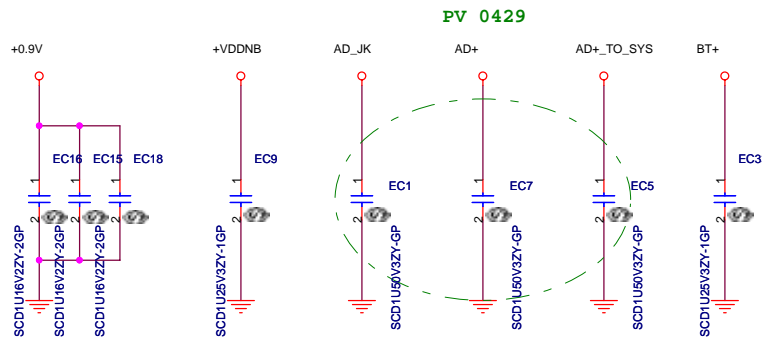
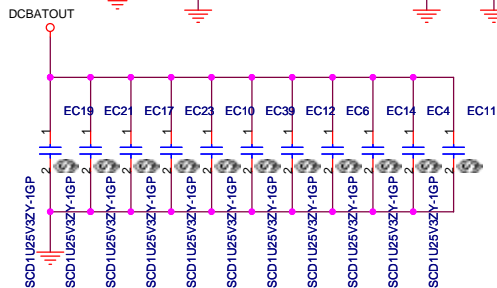


緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title			
AD/BATT CONN			
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For Touch Pad



<Variant Name>			
緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.			
Title	MISC		
Size A3	Document Number	Astrosphere	
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