

ZA1 SYSTEM BLOCK DIAGRAM

DOTHAN
(Micro-FCPGA 478)
Page : 02 , 03

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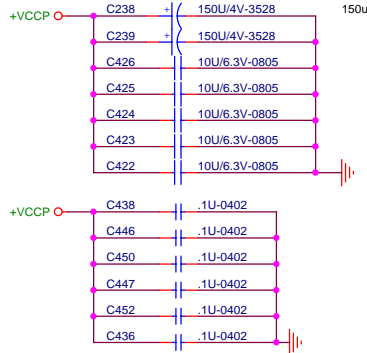
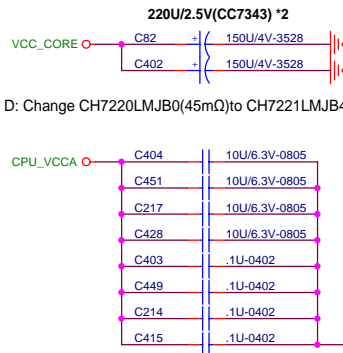
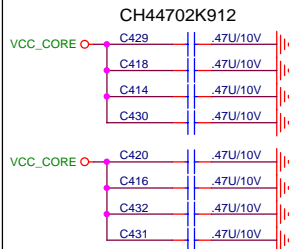
PCLK_I_CH (SB,SB-PCI DEVICE
PCLK_SIO (SIO,SIO-SB)
DEEFCCLK
PCLK_551 (EC,EC-SB)
PCLK_MINI (MINI,MINI-SB)
PCLK_LAN (LAN,LAN-SB)
PCLK_1394 (1394.1394-SB)
CLK48_USB(SB,SB-USB)

```

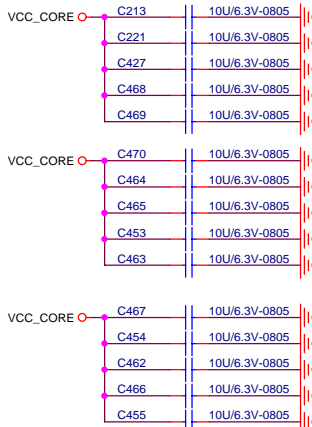
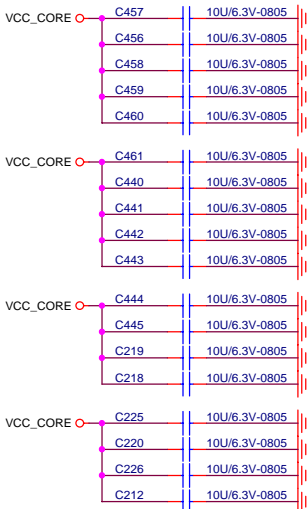
Page : 08

| DOTTED LINE USE INTERNAL VGA(UMA)

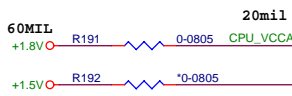
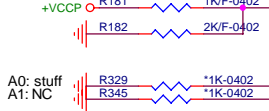
The diagram illustrates the layout of a DAUGHTER BOARD. At the top, there are two rectangular blocks: 'AD20' on the left and 'AD18' on the right. Below 'AD20' is a block labeled 'Page :15'. Below 'AD18' is a block labeled 'RJ45' with 'PAGE :15' underneath it. A line connects the 'Page :15' block to the 'RJ45' block. To the left of the 'Page :15' block is the text '10/100 LAN'. Below these are two large rectangular blocks: 'USB * 2' with 'Page :22' underneath, and 'USB 2.0 Conn. x1'. To the left of the 'USB * 2' block is a large block labeled 'USB ICSI 1213' with '3 IN 1' and '+5V' below it. To the right of the 'USB 2.0 Conn. x1' block is another large block labeled 'USB ICSI 1213' with '3 IN 1' and '+5V' below it. The entire diagram is labeled 'DAUGHTER BOARD' at the bottom.



10U/6.3V/X5R(CC0805) *38



4/10 change P/N
to CS02743F907

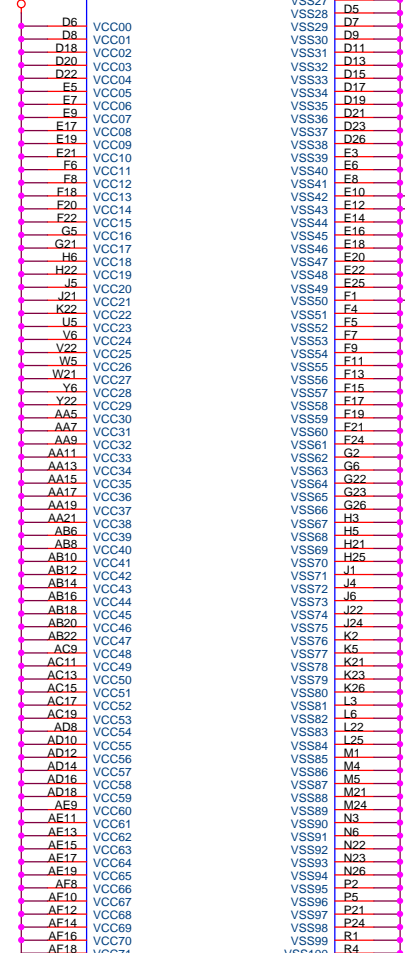


U15B

DOTHAN

2 OF 3

POWER,
GROUND,
RESERVED
SIGNALS



DOTHAN PROCESSOR

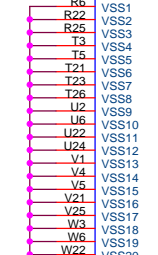
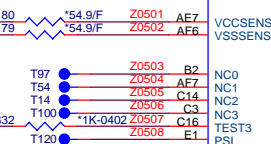
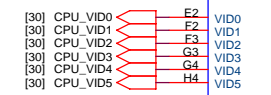
U15C

DOTHAN

3 OF 3

POWER, GROUND AND NC

VID




DOTHAN PROCESSOR

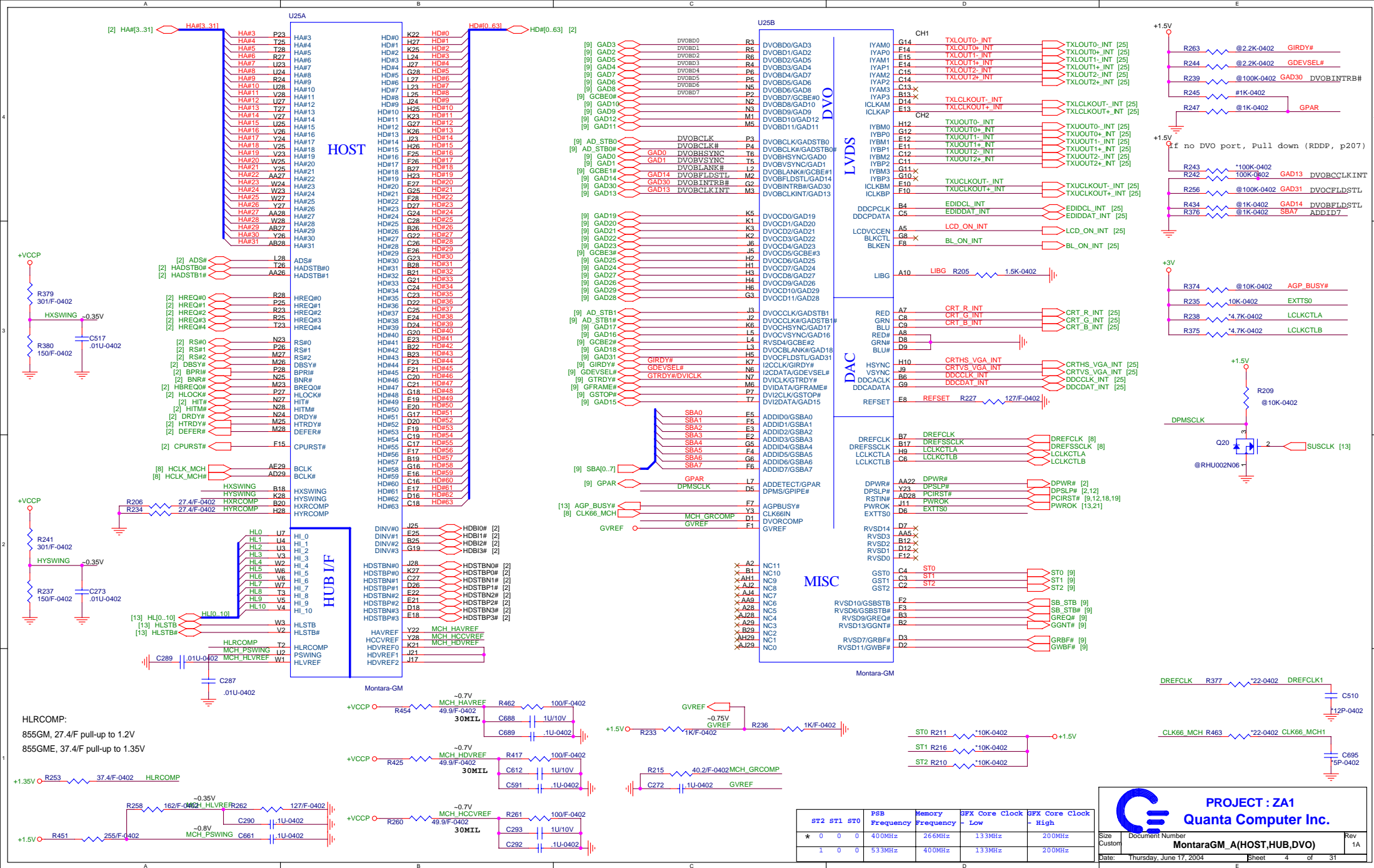
VCC_CORE: 1.356V~0.844V
,0.748V(Deeper sleep)

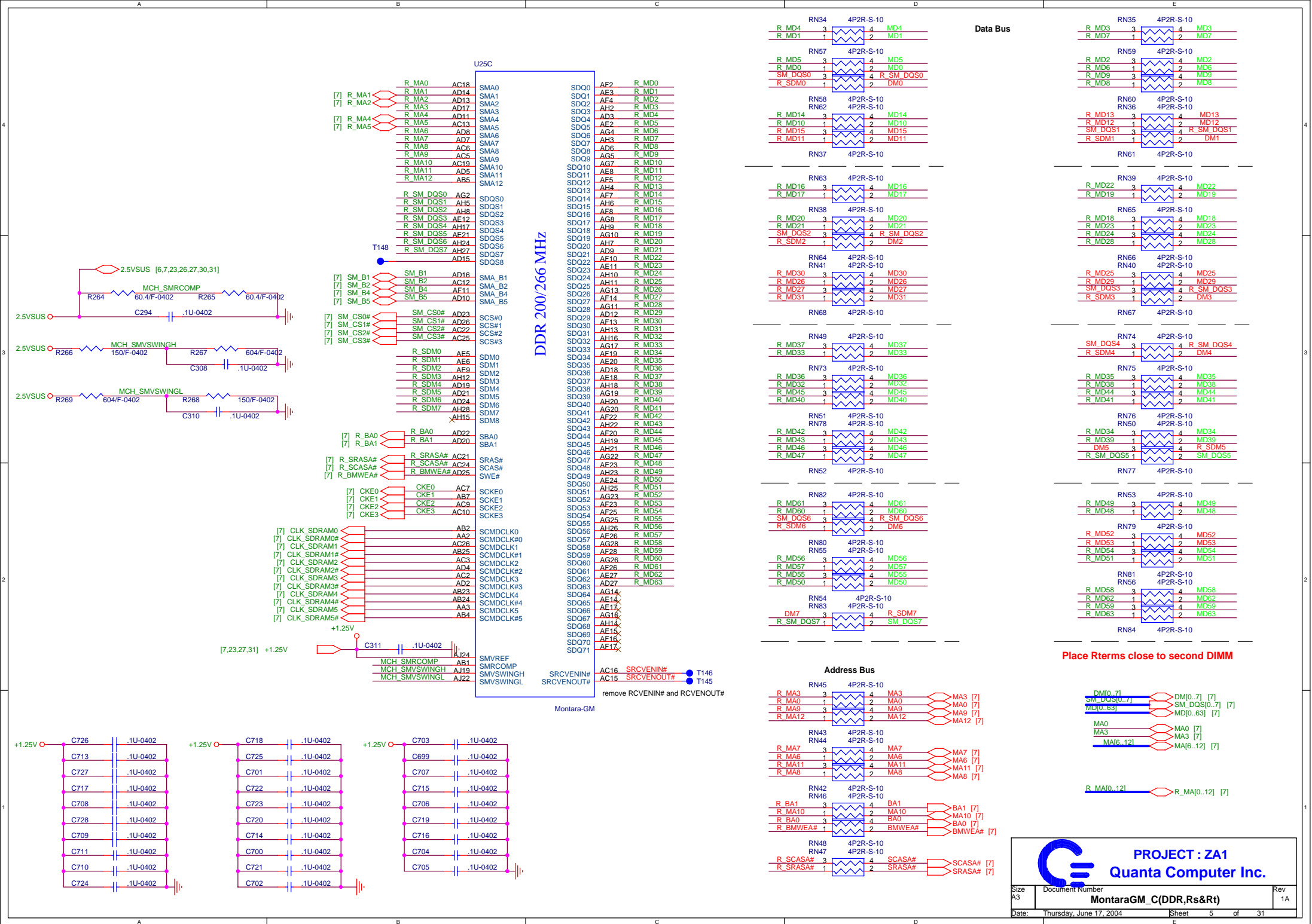
VCCP : 1.05V

VCCA : 1.8V

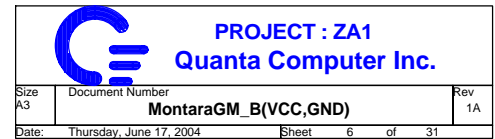
 **PROJECT : ZA1**
Quanta Computer Inc.

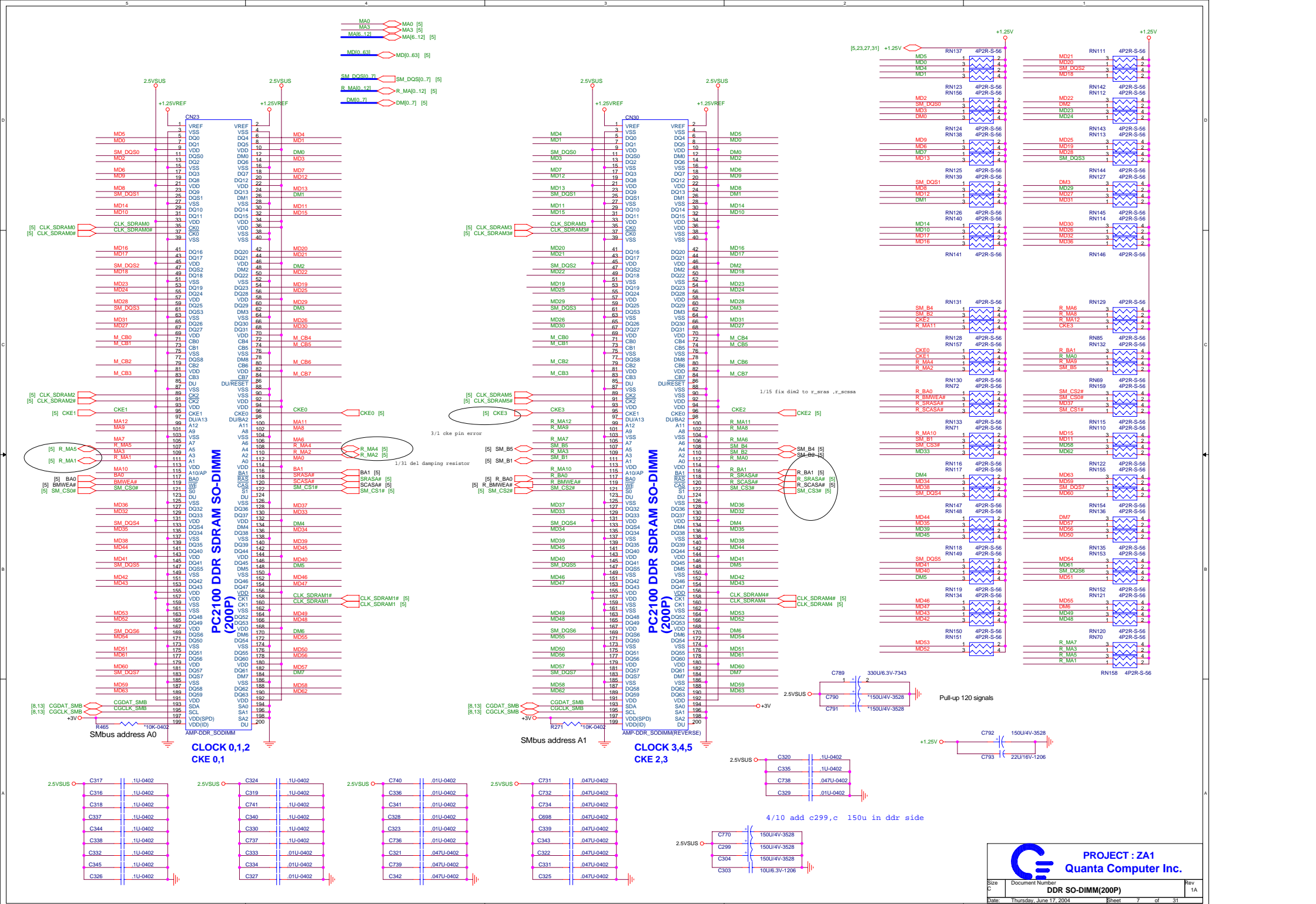
Size B	Doc Number	Rev
	CPU DOTHAN (POWER/NC)-B	1A
Date:	Tuesday, July 06, 2004	Sheet 3 of 31



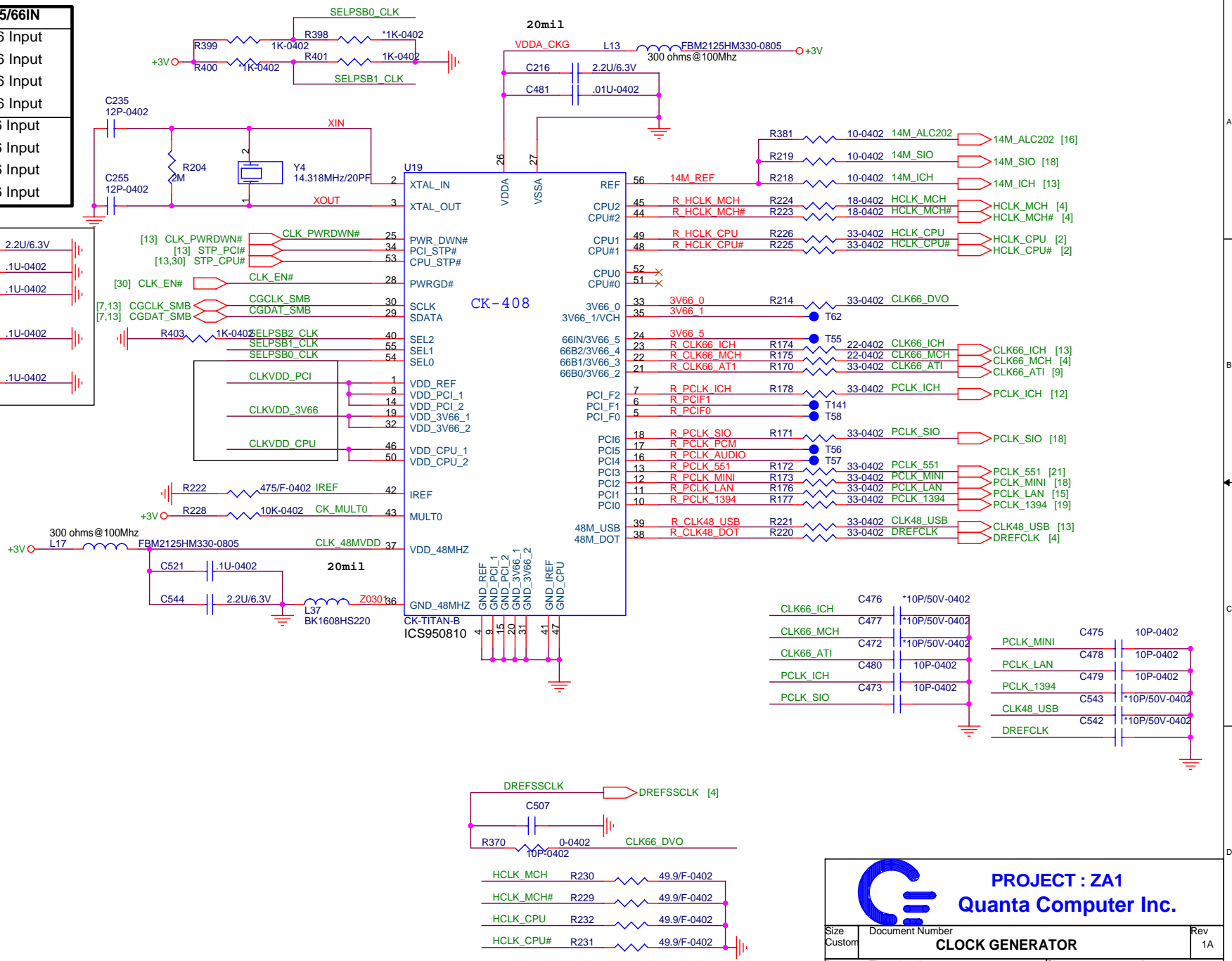
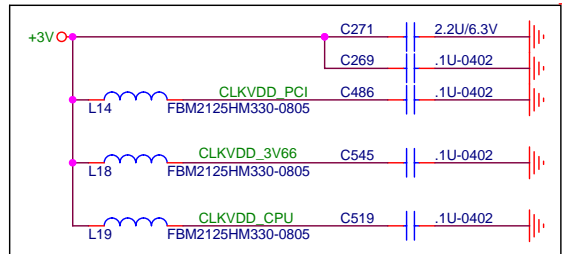



NB: +1.35V 60MIL





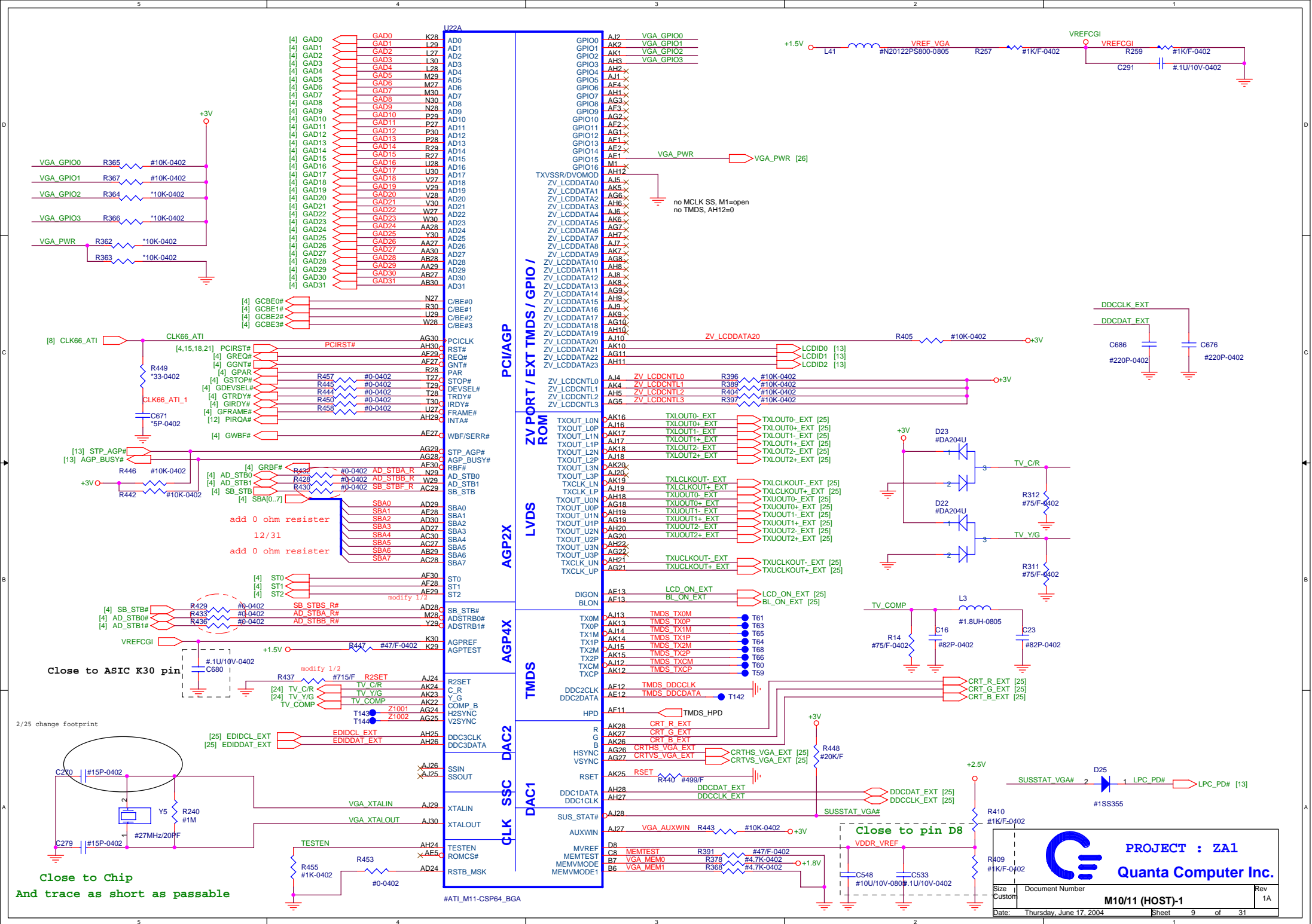
S2	S1	S0	CPU	3V66[0..4]	3V66_5/66IN
1	0	0	66	66IN	66 Input
1	0	1	100	66IN	66 Input
1	1	0	200	66IN	66 Input
1	1	1	133	66IN	66 Input
0	0	0	66	66	66 Input
0	0	1	100	66	66 Input
0	1	0	200	66	66 Input
0	1	1	133	66	66 Input

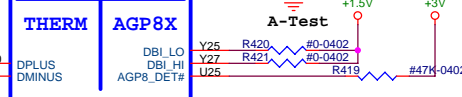
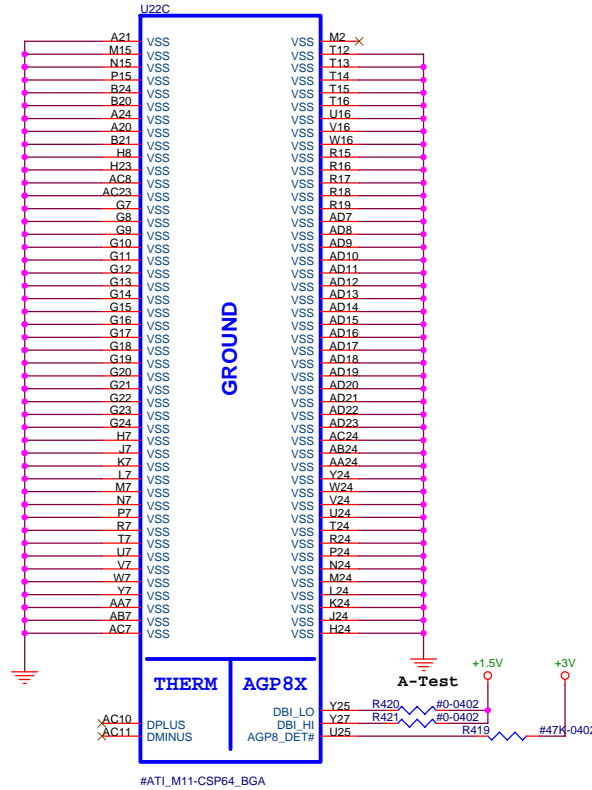
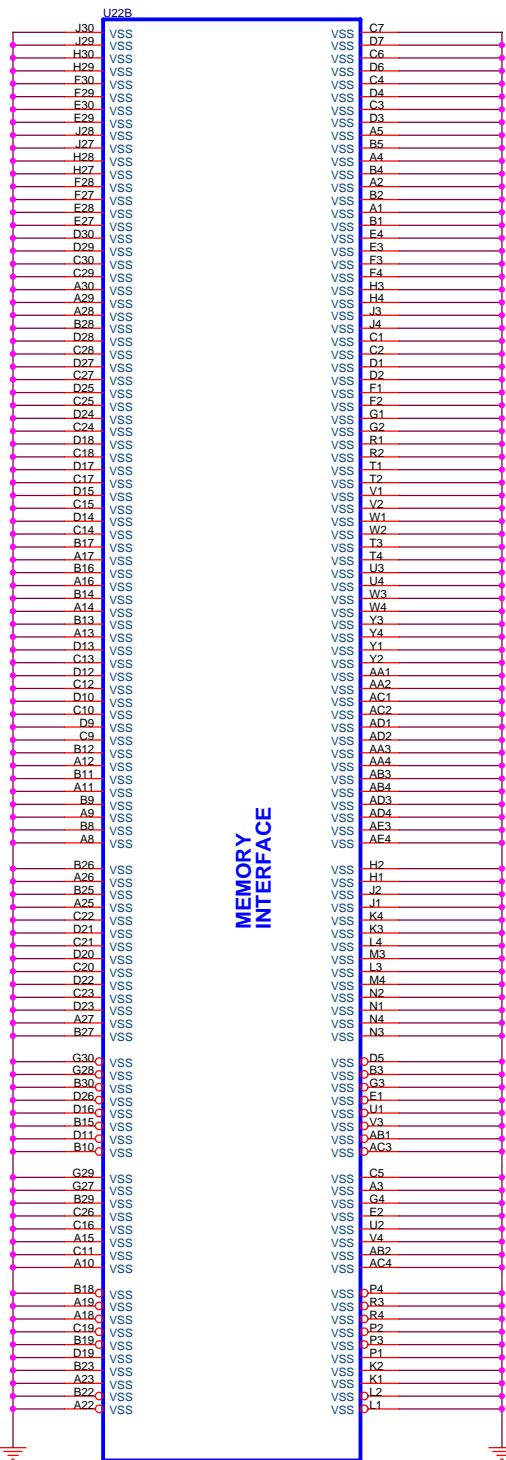




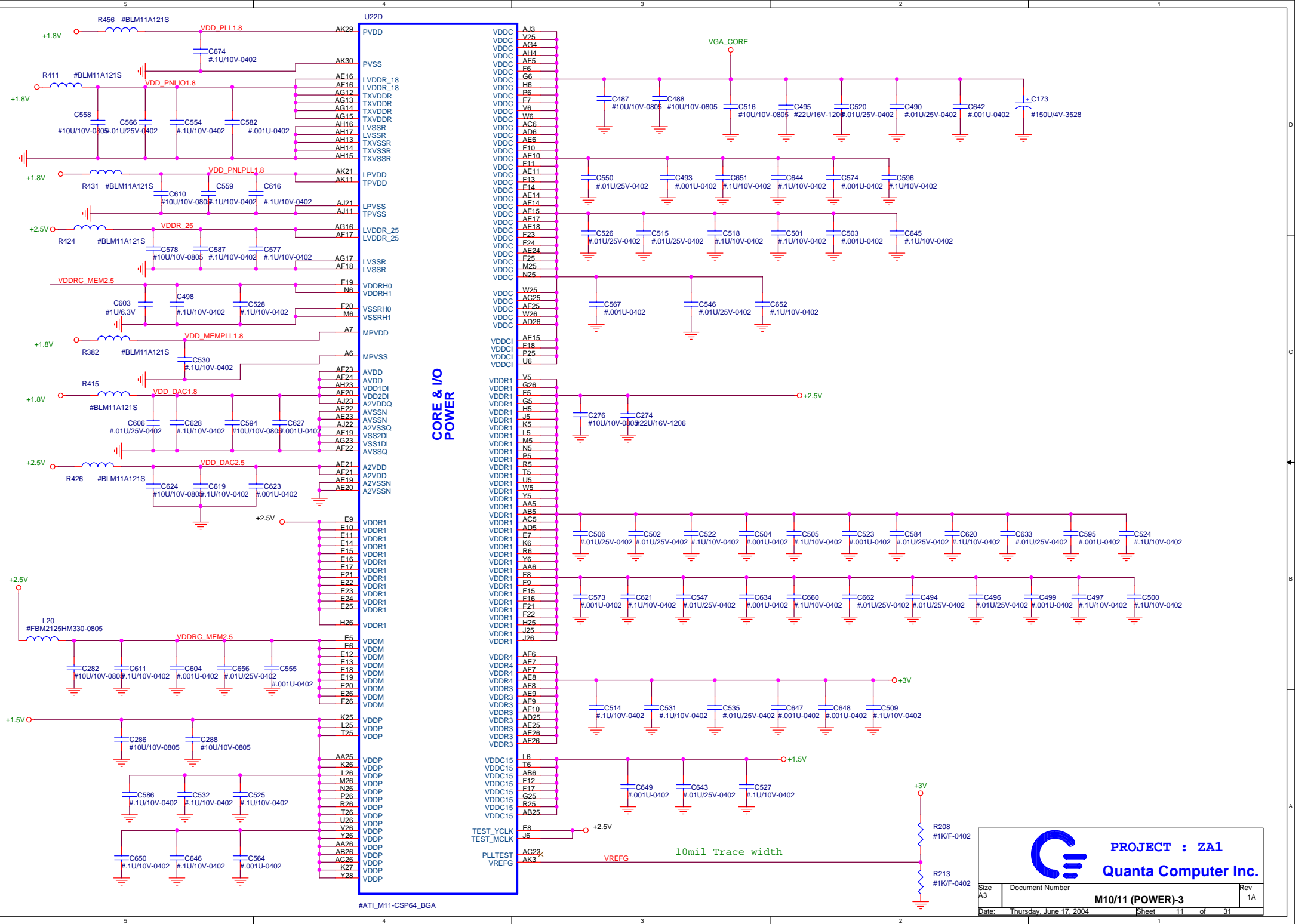
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CLOCK GENERATOR		
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#ATI_M11-CSP64_BGA

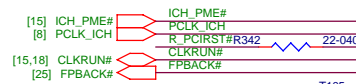


CORE & I/O
POWER

VGA_CORE

10mil Trace width

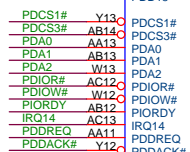
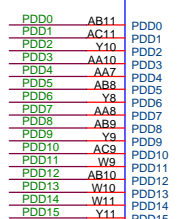
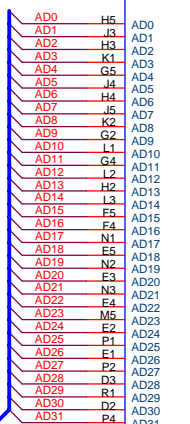
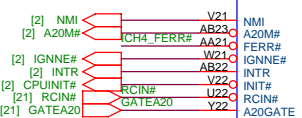
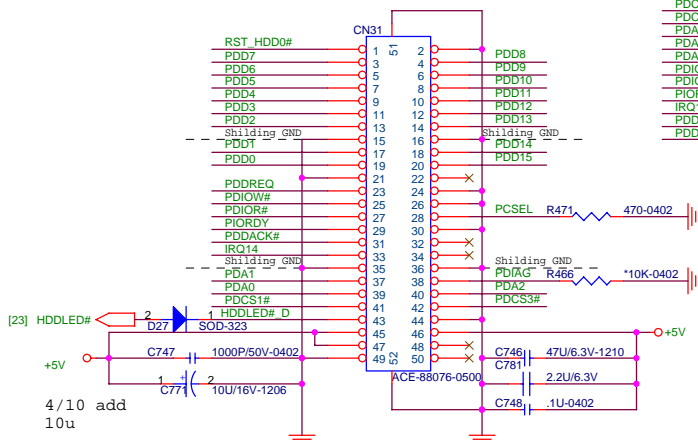
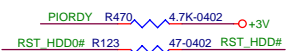
VREFG



HDD CONNECTOR

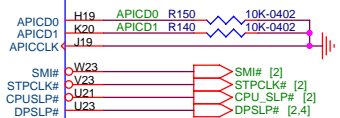
150MIL

4/10 del L50

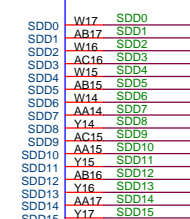
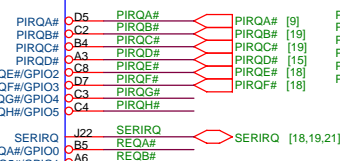
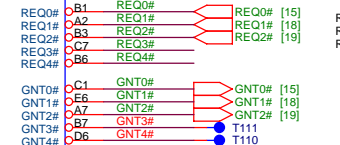
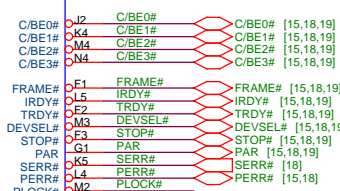


ICH HUB

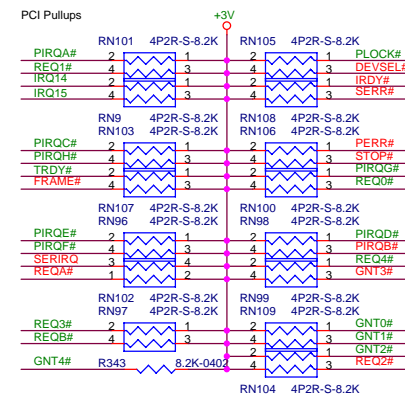
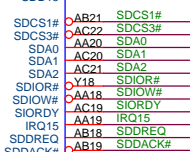
CPU



PCI

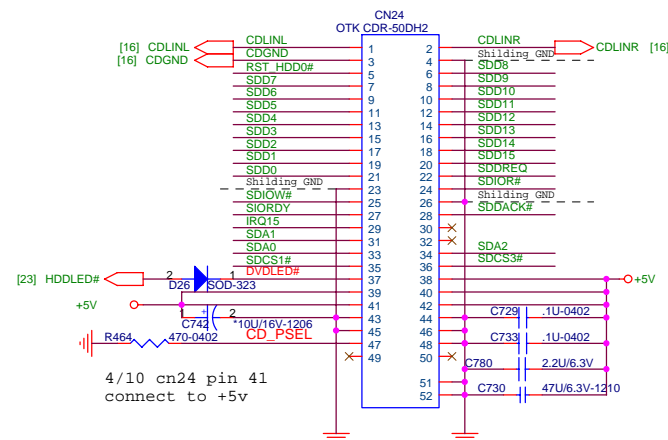
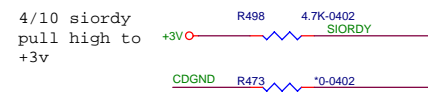


IDE



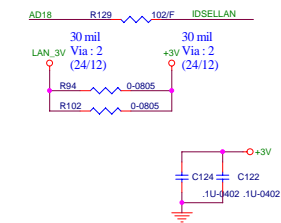
CD-ROM CONNECTOR

4/10 del L49

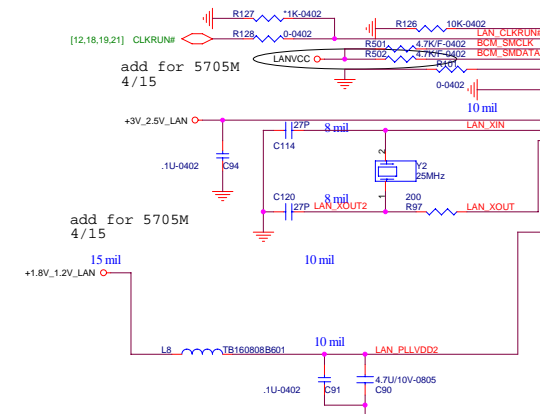


LAN (Boardcom BCM5705M)

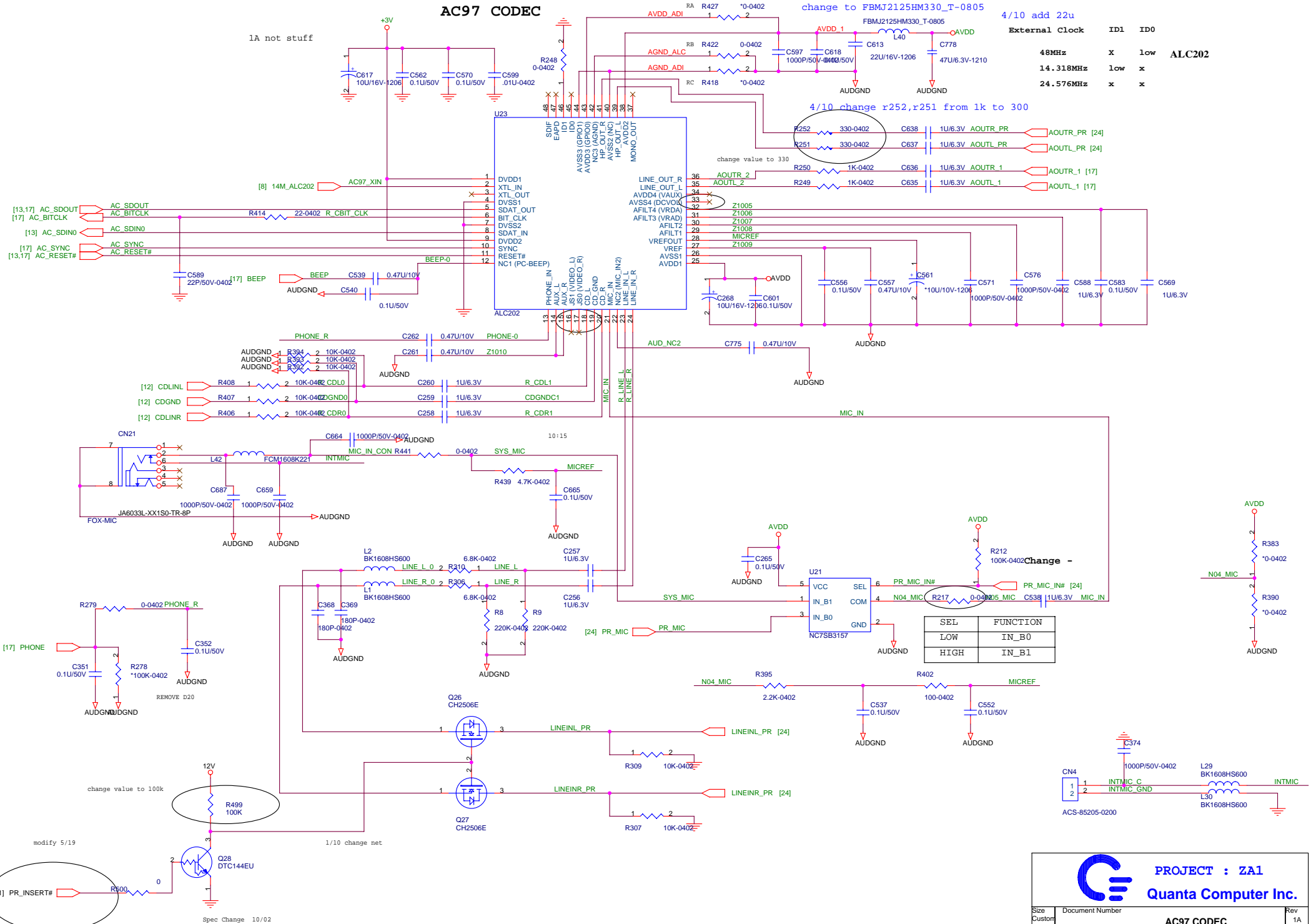
ID Select : AD18
Interrupt Pin : PIRQB#
Request indicates : REQ0#
Grant indicates : GNT0#



4/10: If use CLKRUN# mount R128
If not use CLKRUN# mount R127

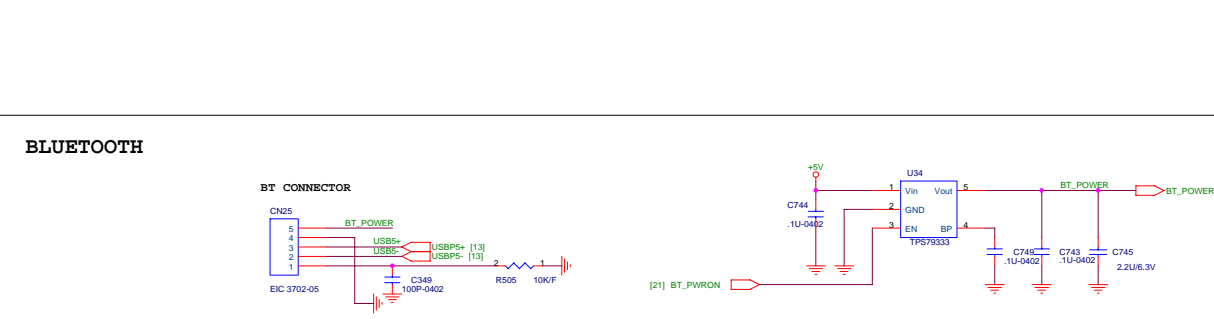
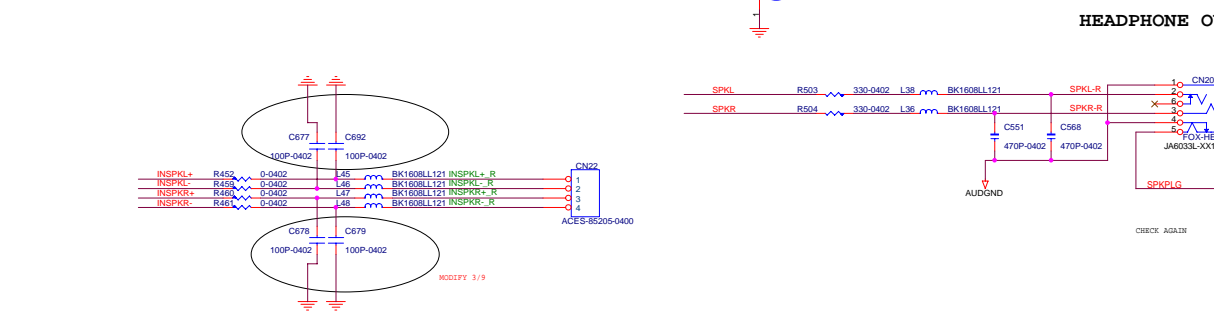
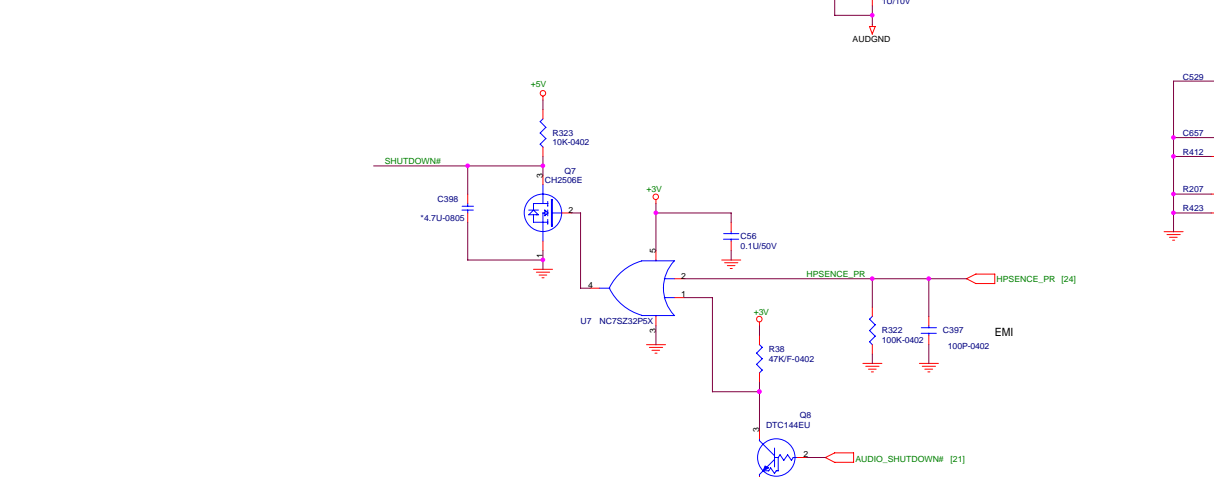
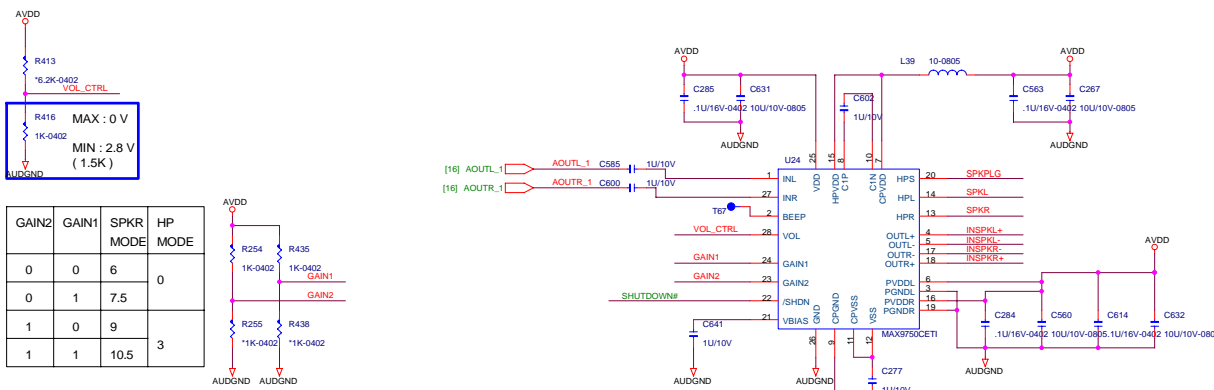


AC97 CODEC

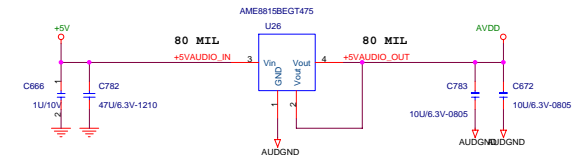


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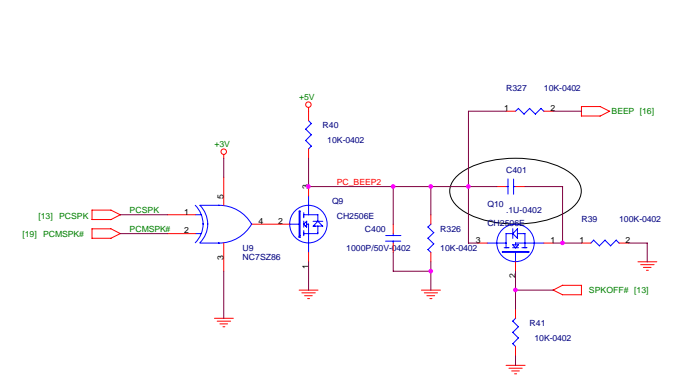
Size	Document Number	Rev
Custom	AC97 CODEC	1A
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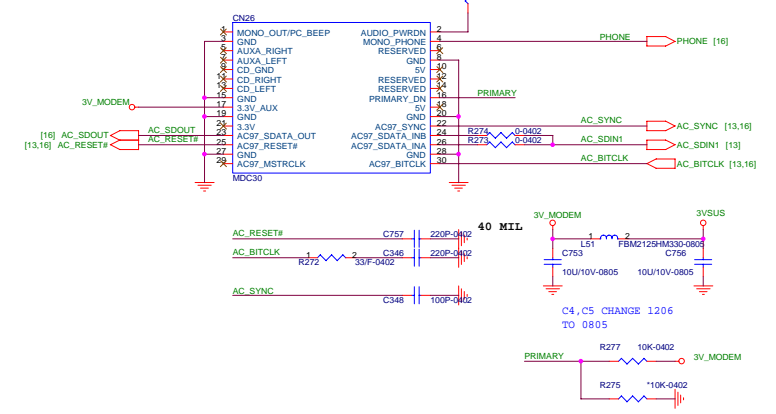
AUDIO POWER



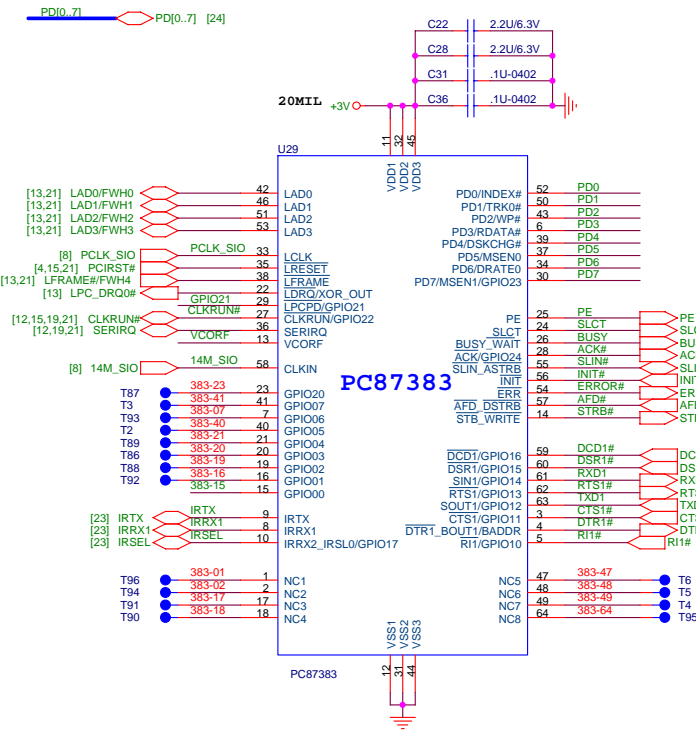
AUDIO BEEP



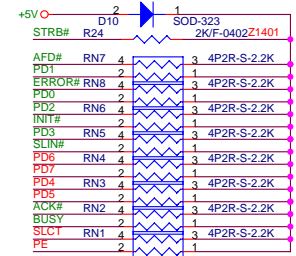
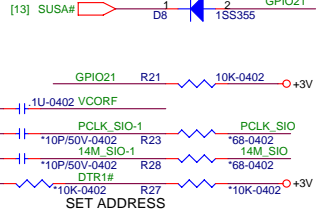
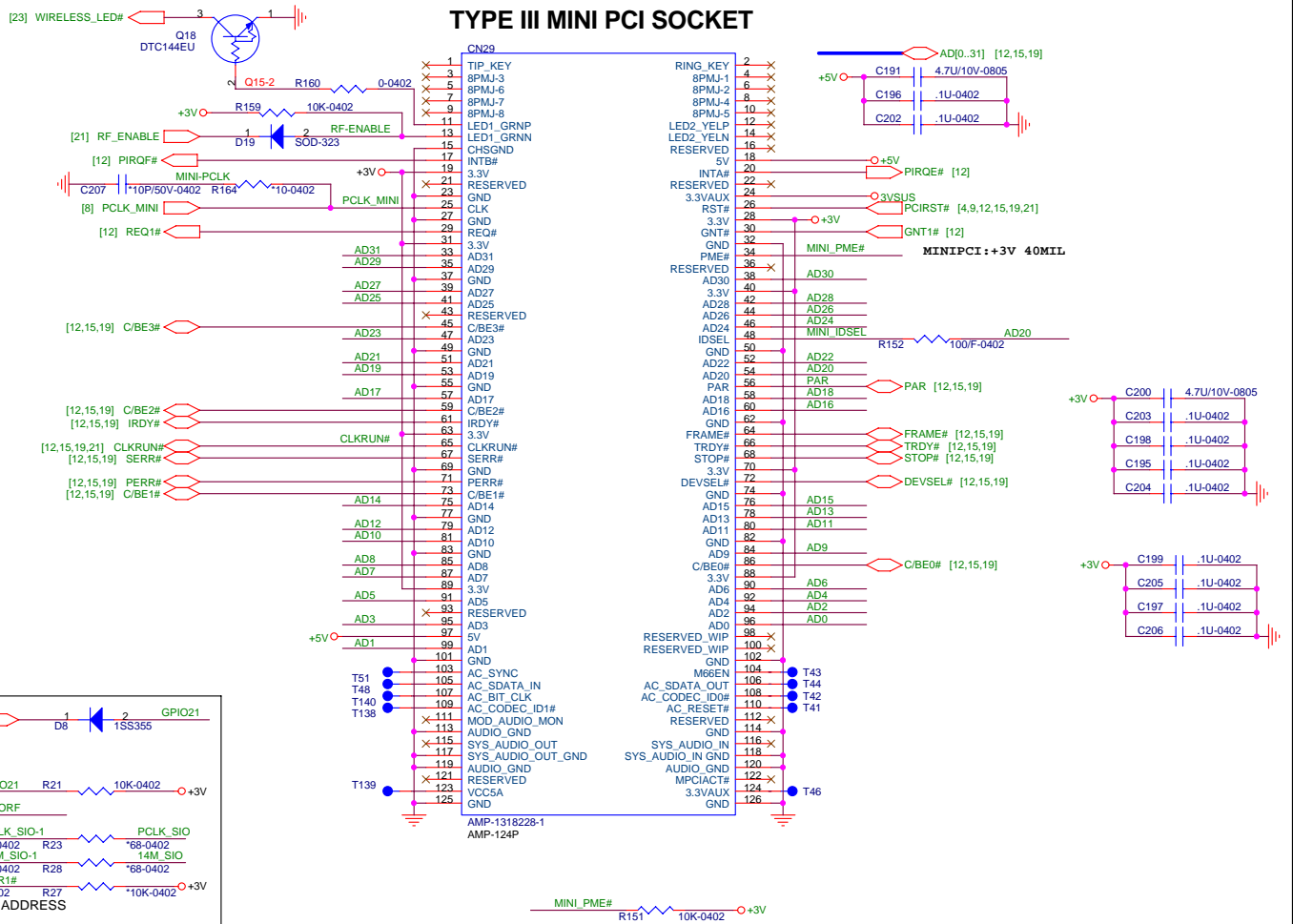
MDC



Super I/O

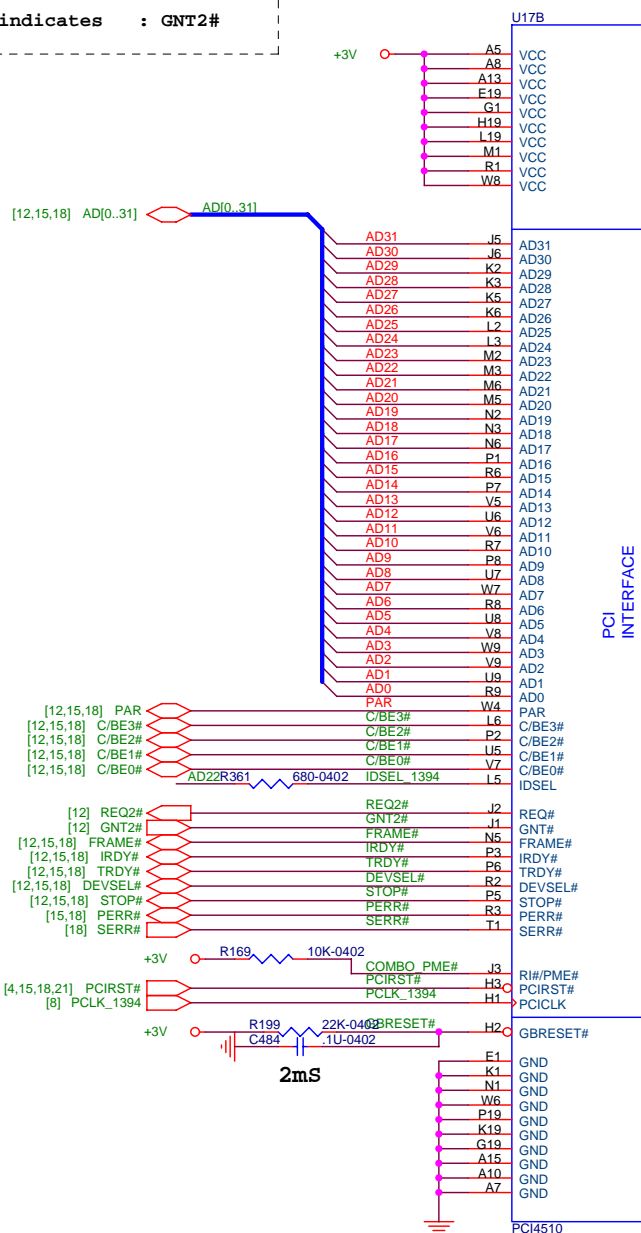


TYPE III MINI PCI SOCKET

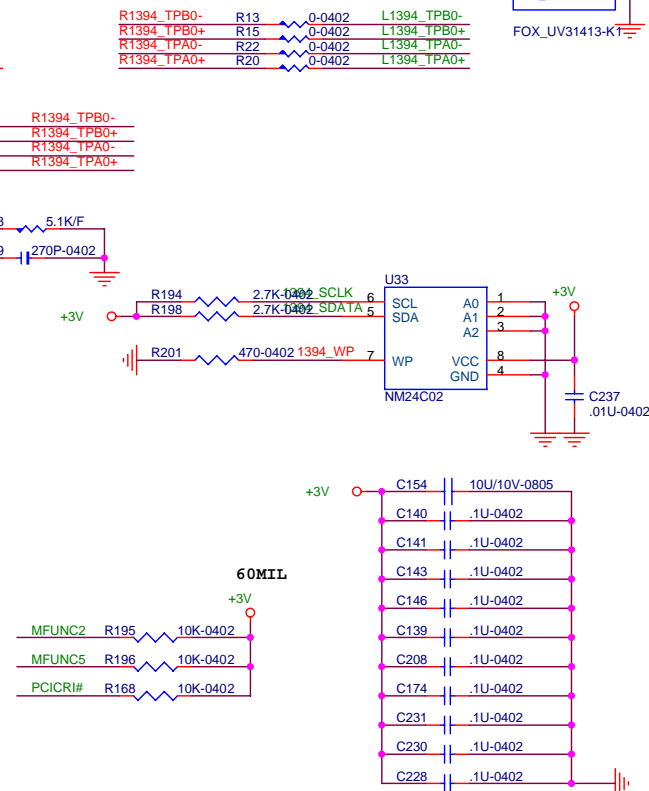
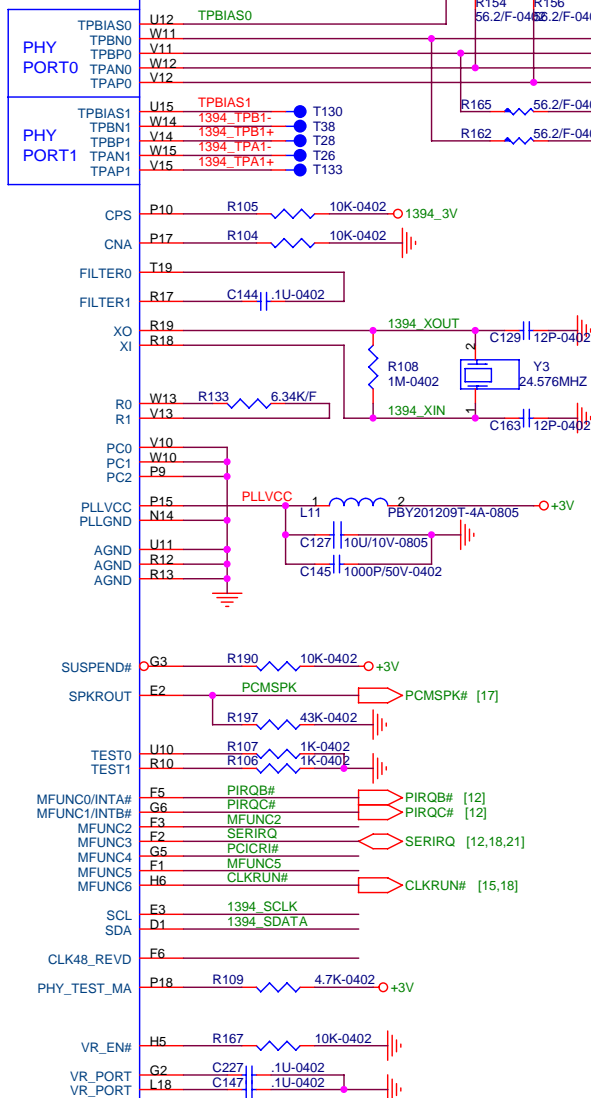



```
ID Select      : AD22
Interrupt Pin  : PIRQ 1
Request indicates : REQ2#
Grant indicates  : GNT2#
```

CARDBUS / 1394

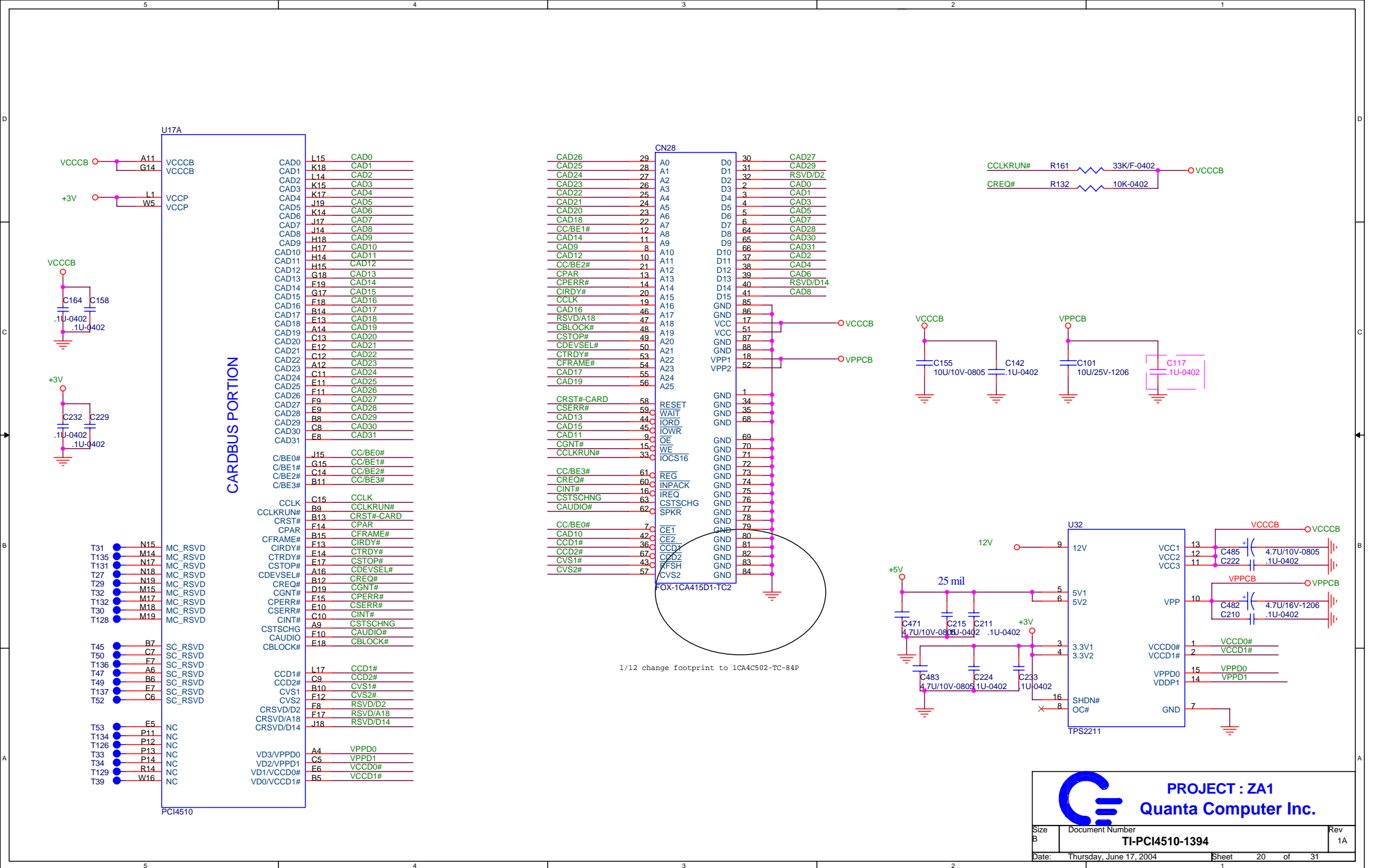


PCI/1394_OHCI PORTION



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FAN CONTROL

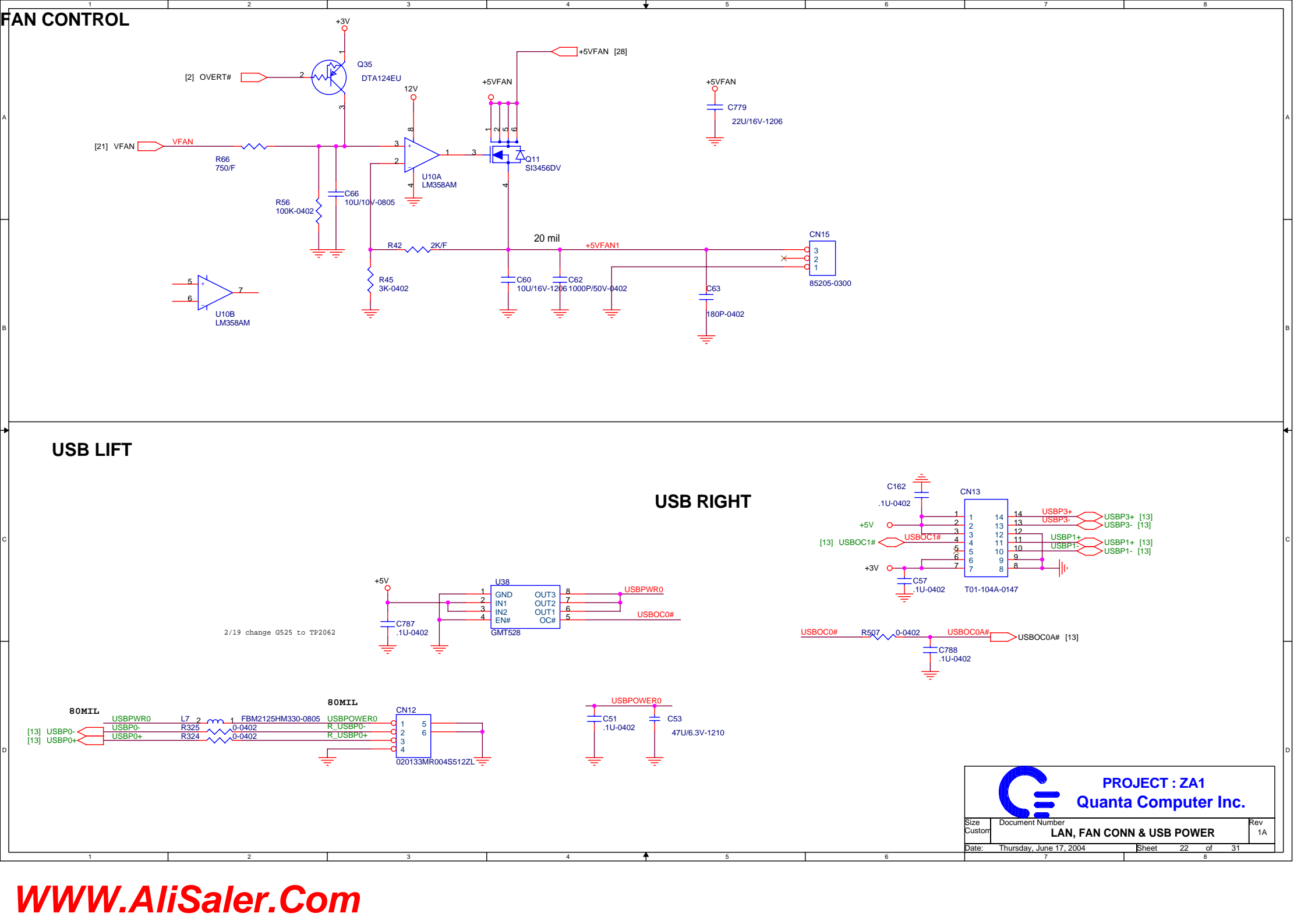
USB LIFT

USB RIGHT

USB LEFT

PROJECT : ZA1
Quanta Computer Inc.

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FAN CONTROL

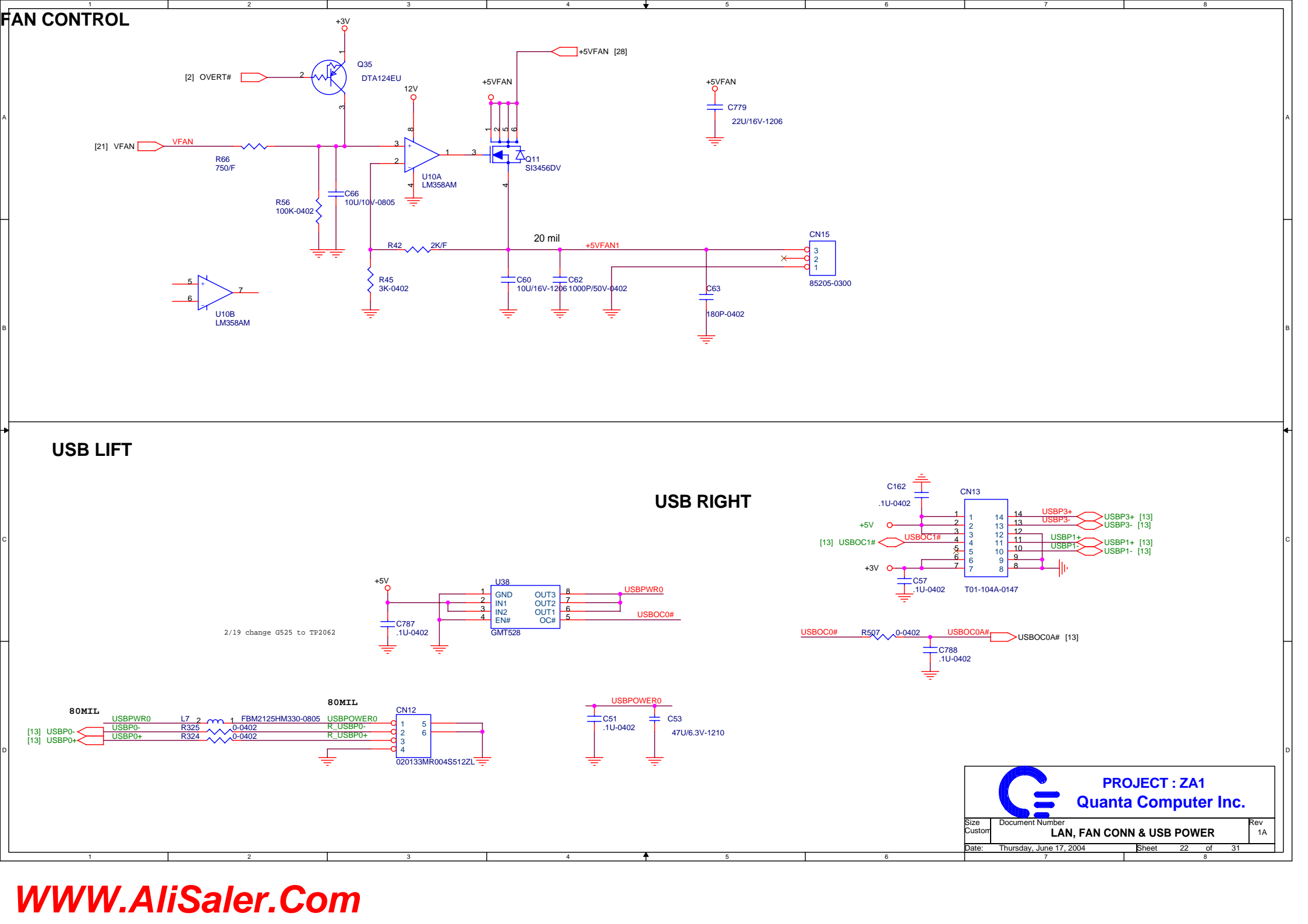
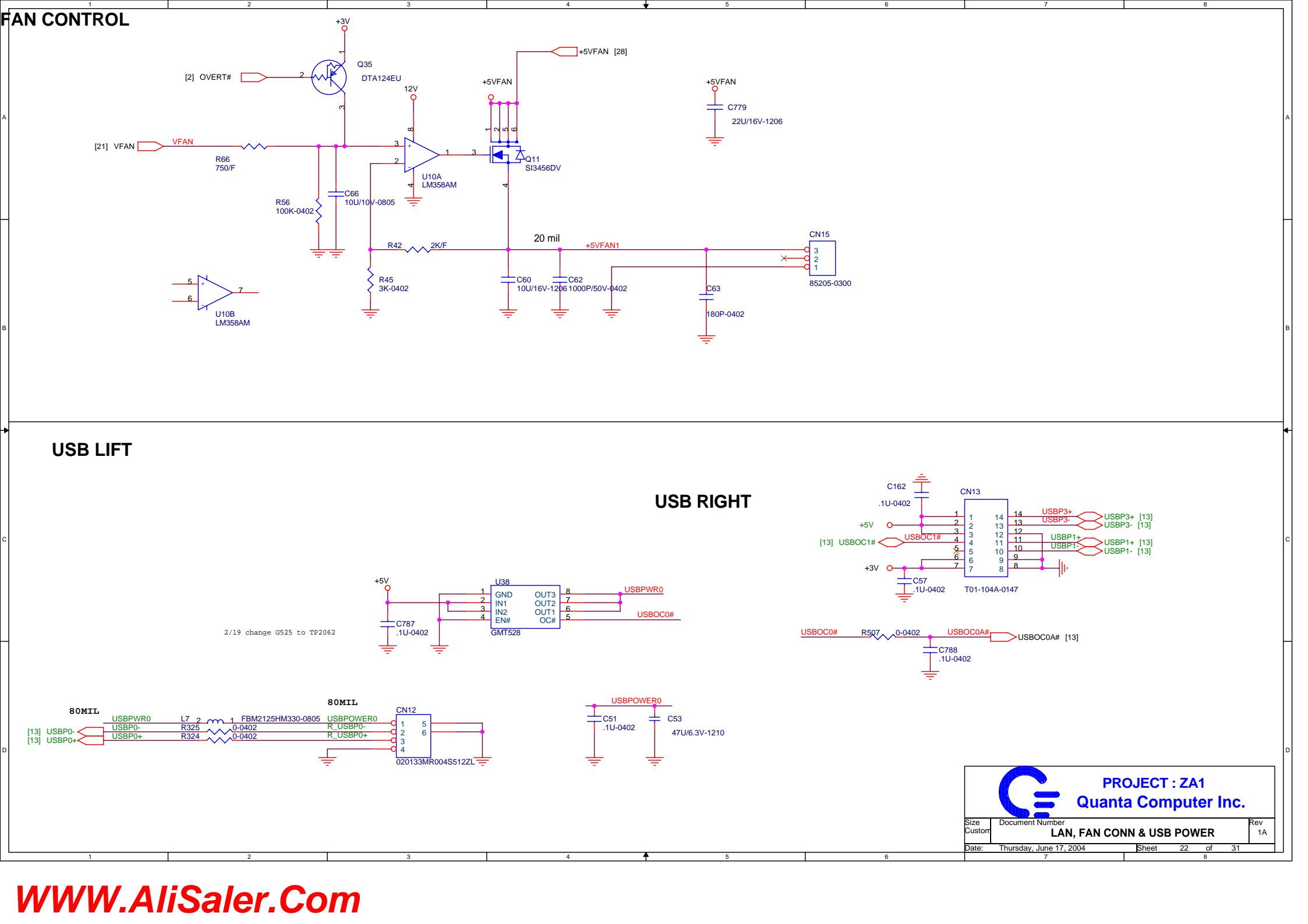
USB LIFT

USB RIGHT

USB LEFT

PROJECT : ZA1
Quanta Computer Inc.

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FAN CONTROL

USB LIFT

USB RIGHT

USB LEFT

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