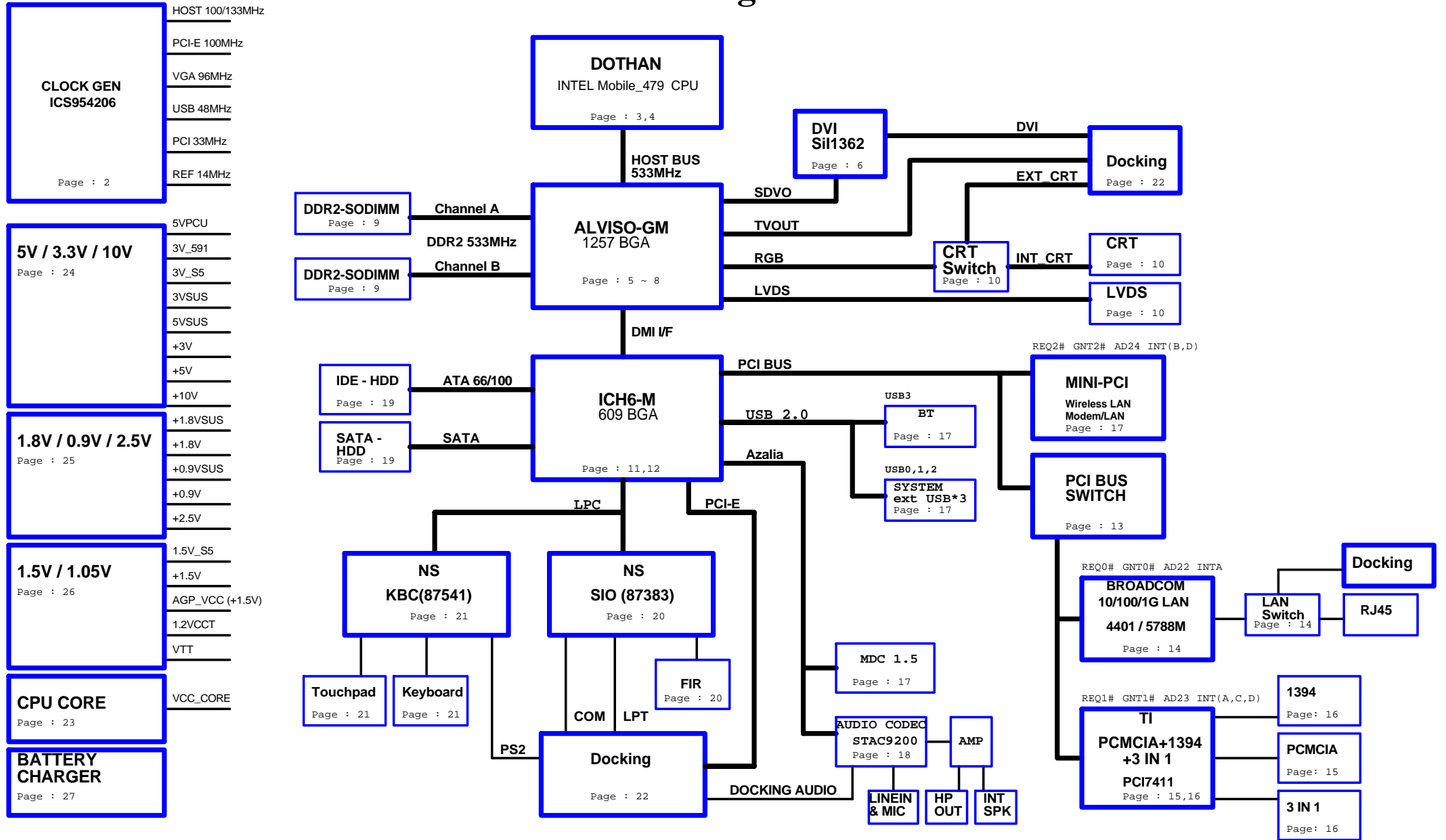
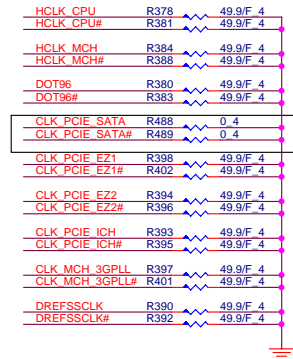
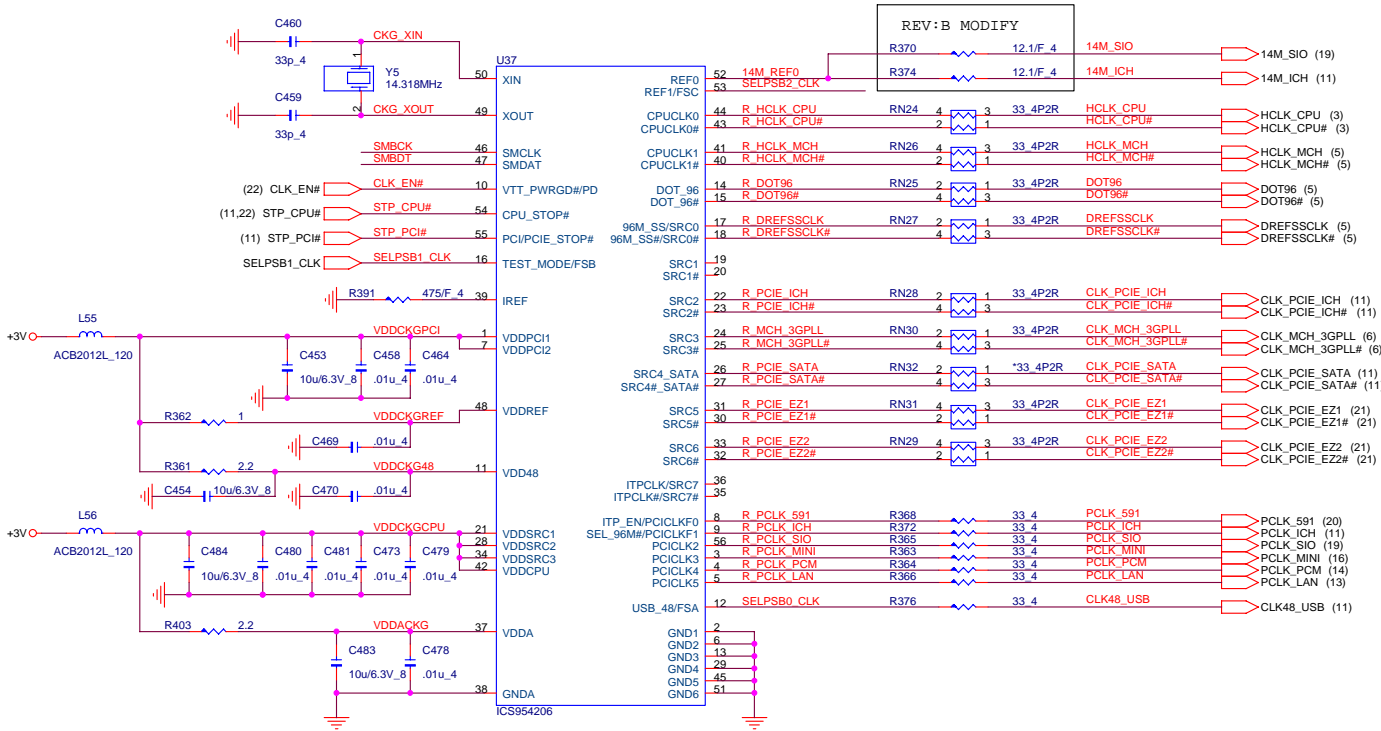


[illegible]

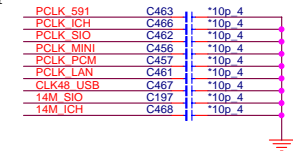
ZH1 Block Diagram



PCI ROUTING TABLE	IDSEL	INTERUPT	DEVICE
REQ0# / GNT0#	AD22	INTA#	BROADCOM LAN
REQ2# / GNT2#	AD24	INTB# , INTD#	MINI-PCI
REQ1# / GNT1#	AD23	INTC#,INTD#,INTA#	TI 7411



REV:B MODIFY
W/O SATA : 0 OHM
W SATA : 49.9 1% OHM



96MSS/SRC0 SELECT

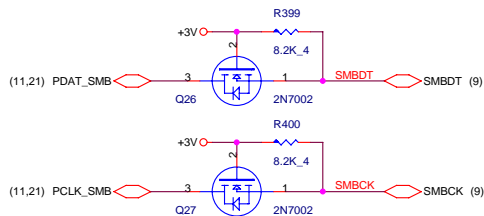
0: 96MSS 1: SRC0

R_PCLK_ICH R375 1K 4

R_PCLK_591 R371 1K 4

ITP/SRC7 SELECT

0: SRC7 1: ITP



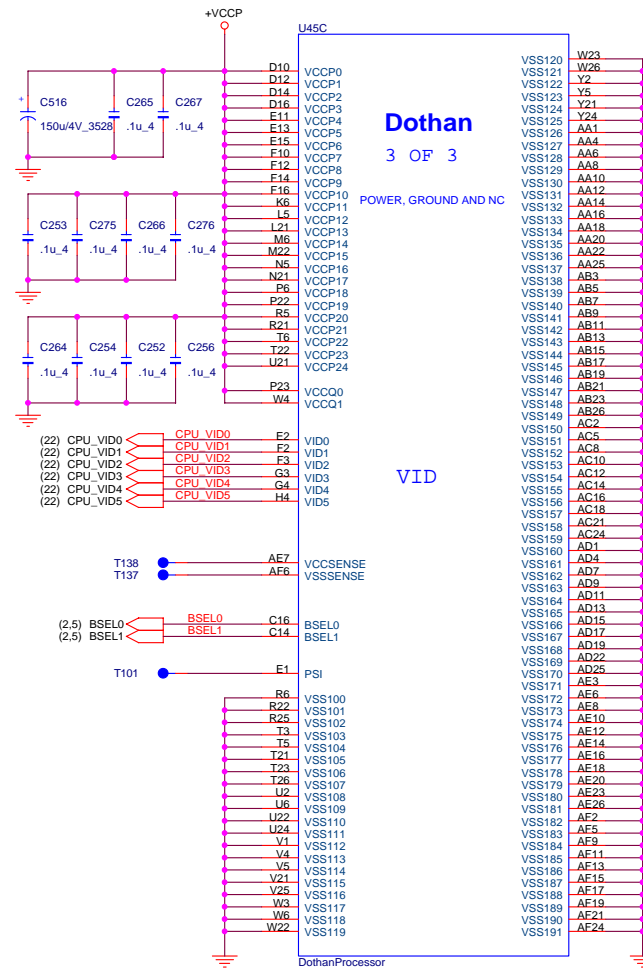
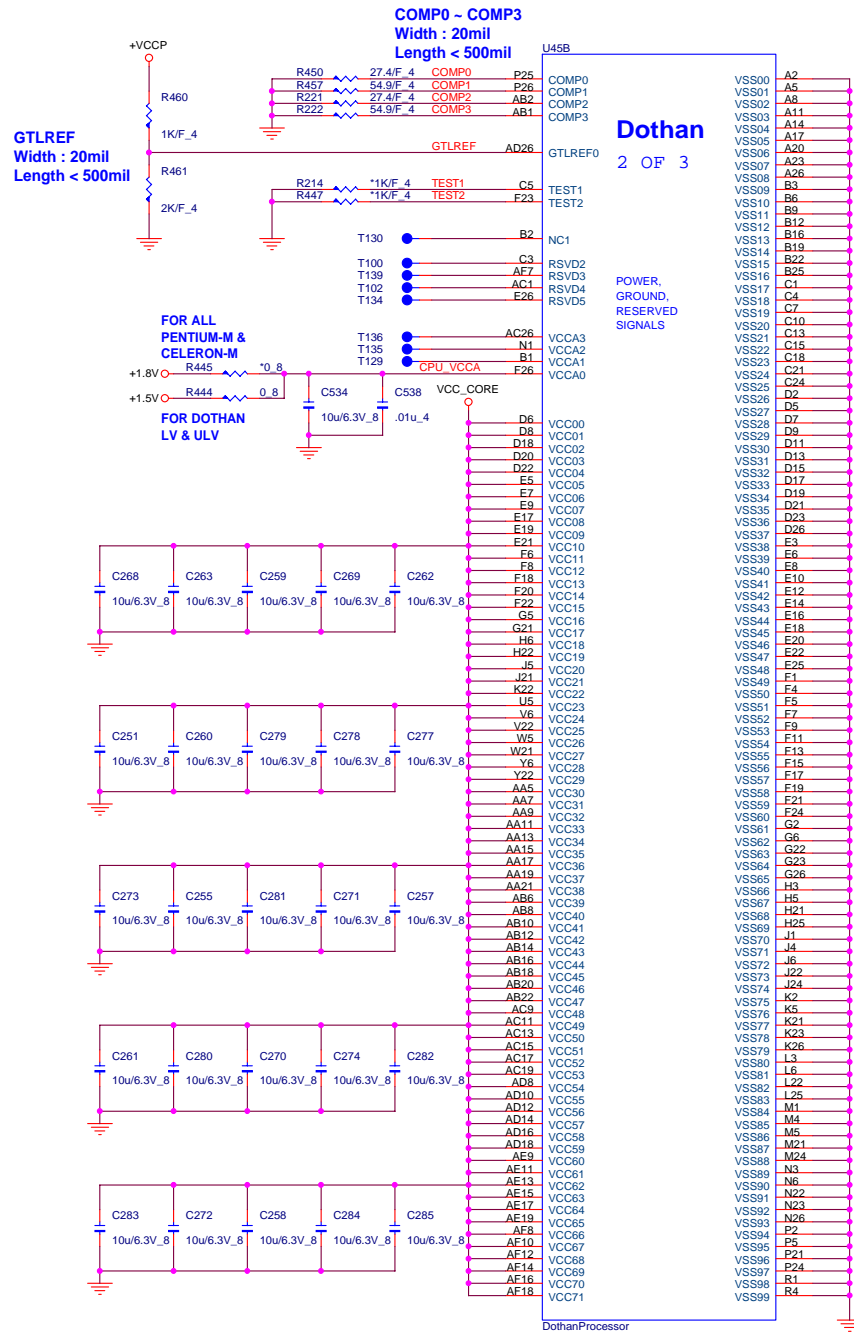
REV:B MODIFY

FSB SETTING

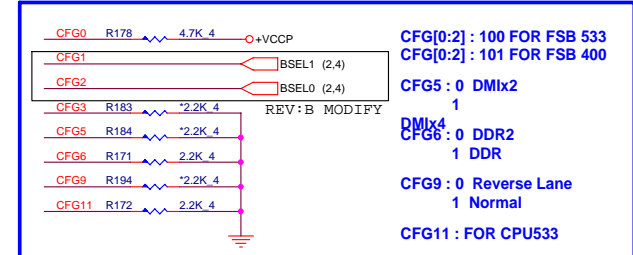
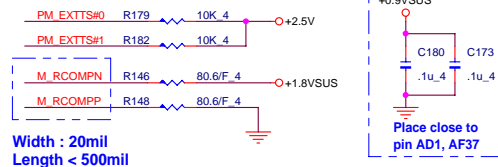
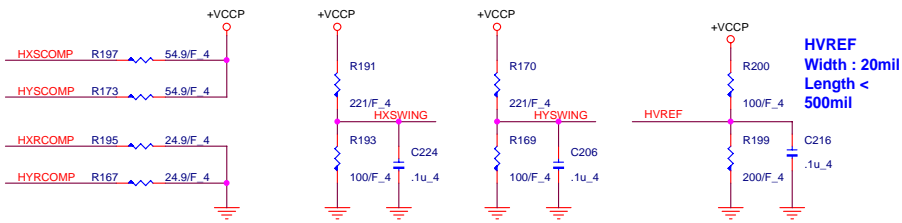
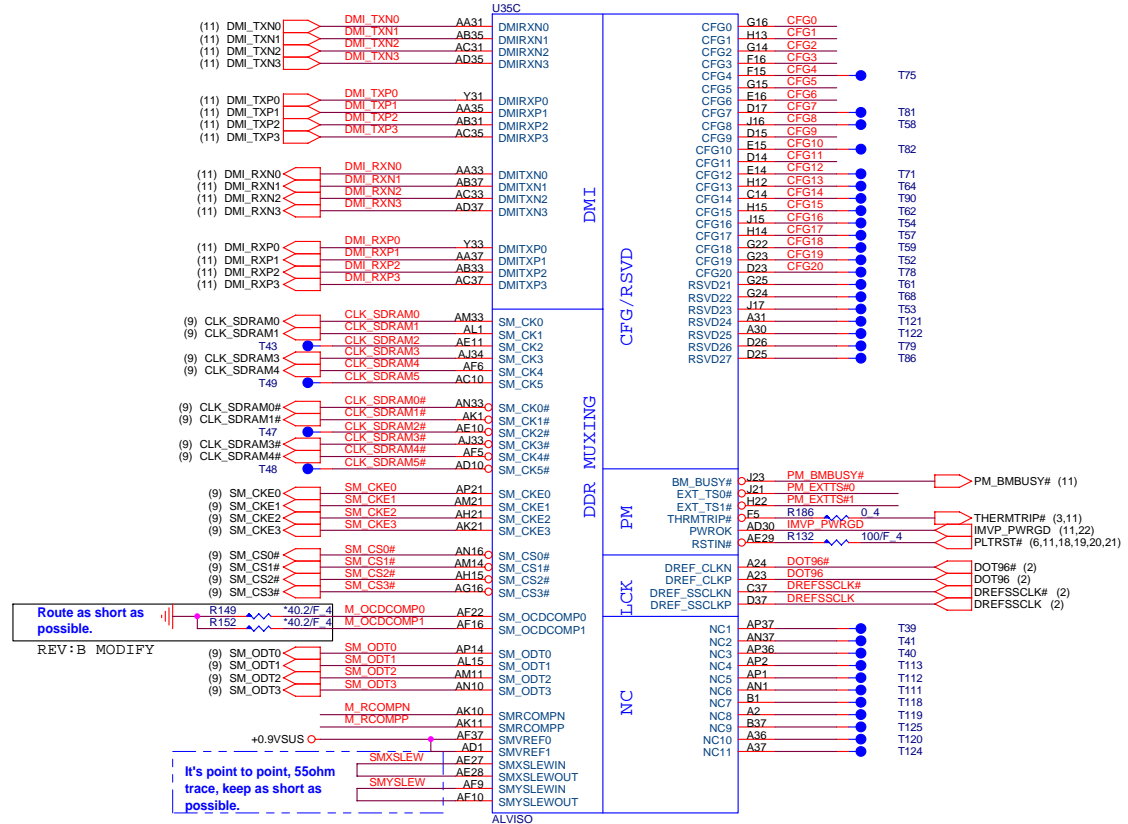
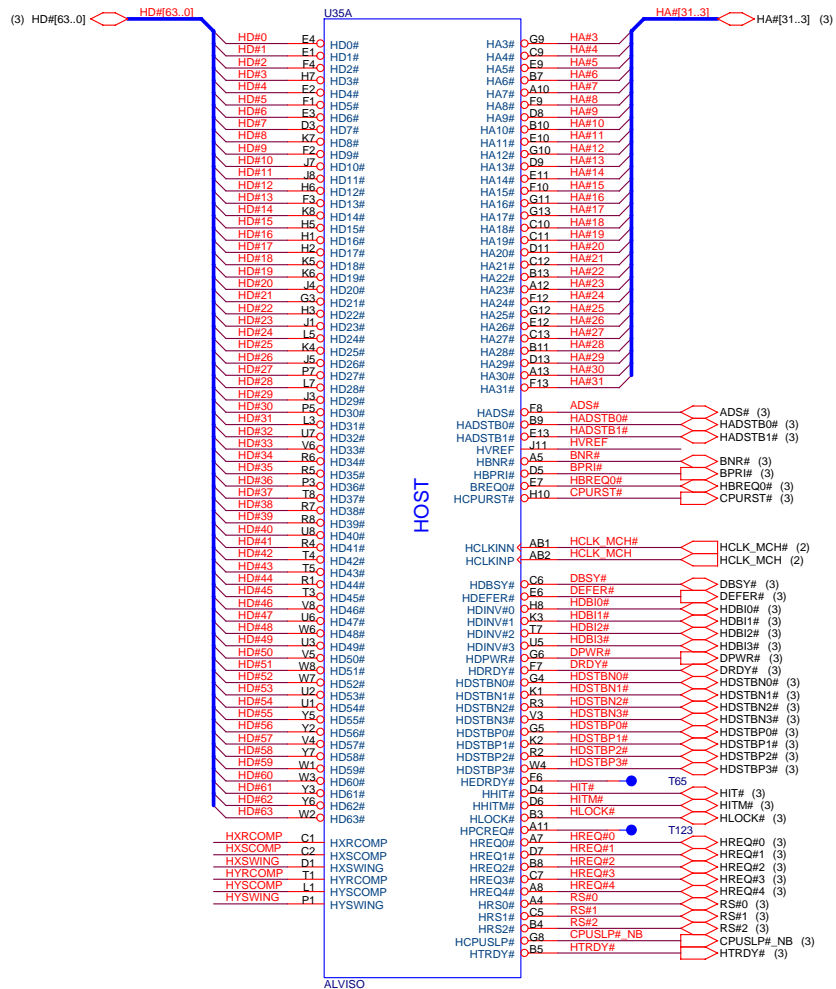
FSC	FSB	FSA	CPU	SRC	PCI
1	0	1	100	100	33
0	0	1	133	100	33
0	1	1	166	100	33
0	1	0	200	100	33
0	0	0	266	100	33
1	0	0	333	100	33
1	1	0	400	100	33
1	1	1	RSVD	100	33



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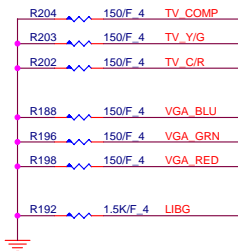


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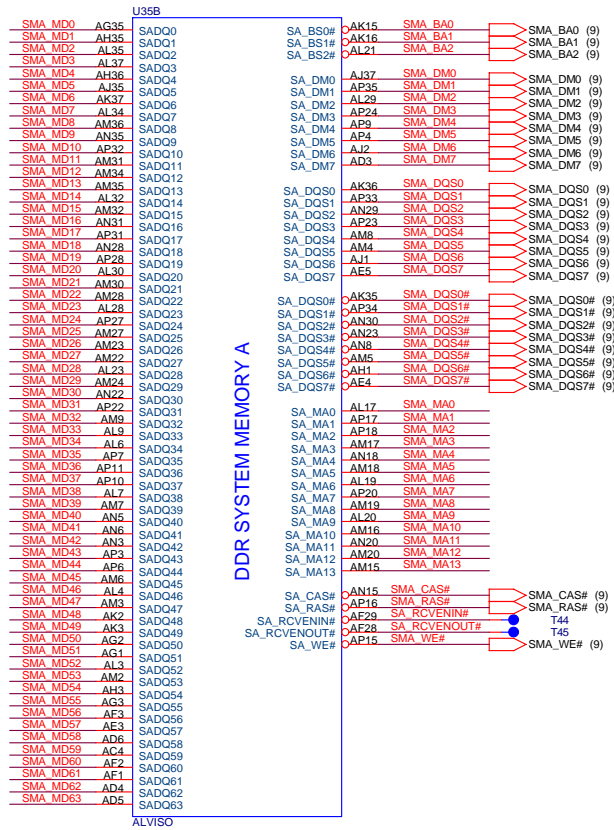
Size	Document Number	Rev
	GMCH (HOST/DMI)	2B
Date:	Wednesday, January 19, 2005	Sheet 5 of 27



Size	Document Number GMCH (VGA)	Rev 3A
Date:	Wednesday, January 19, 2005	Sheet 6 of 27

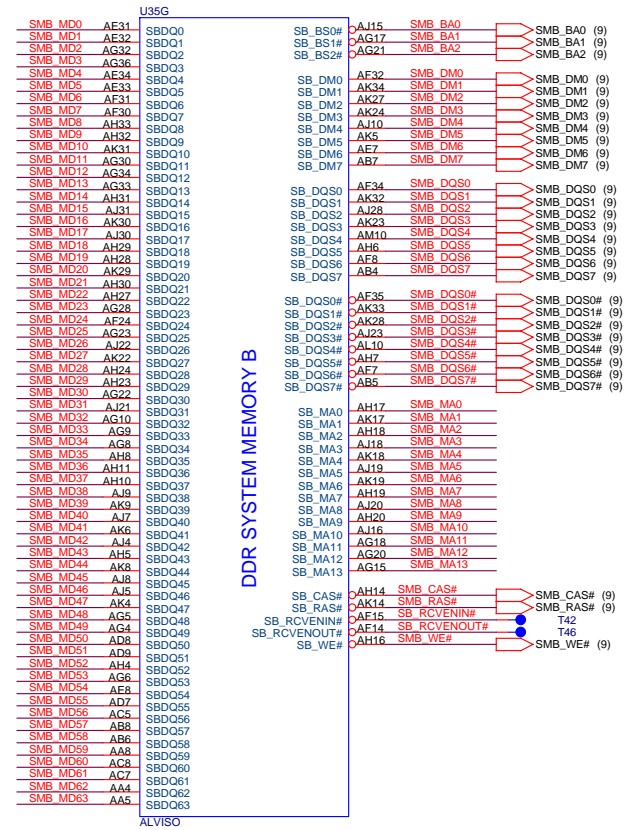
SMA_MD[63..0] (9)

SMA_MA[13..0] (9)



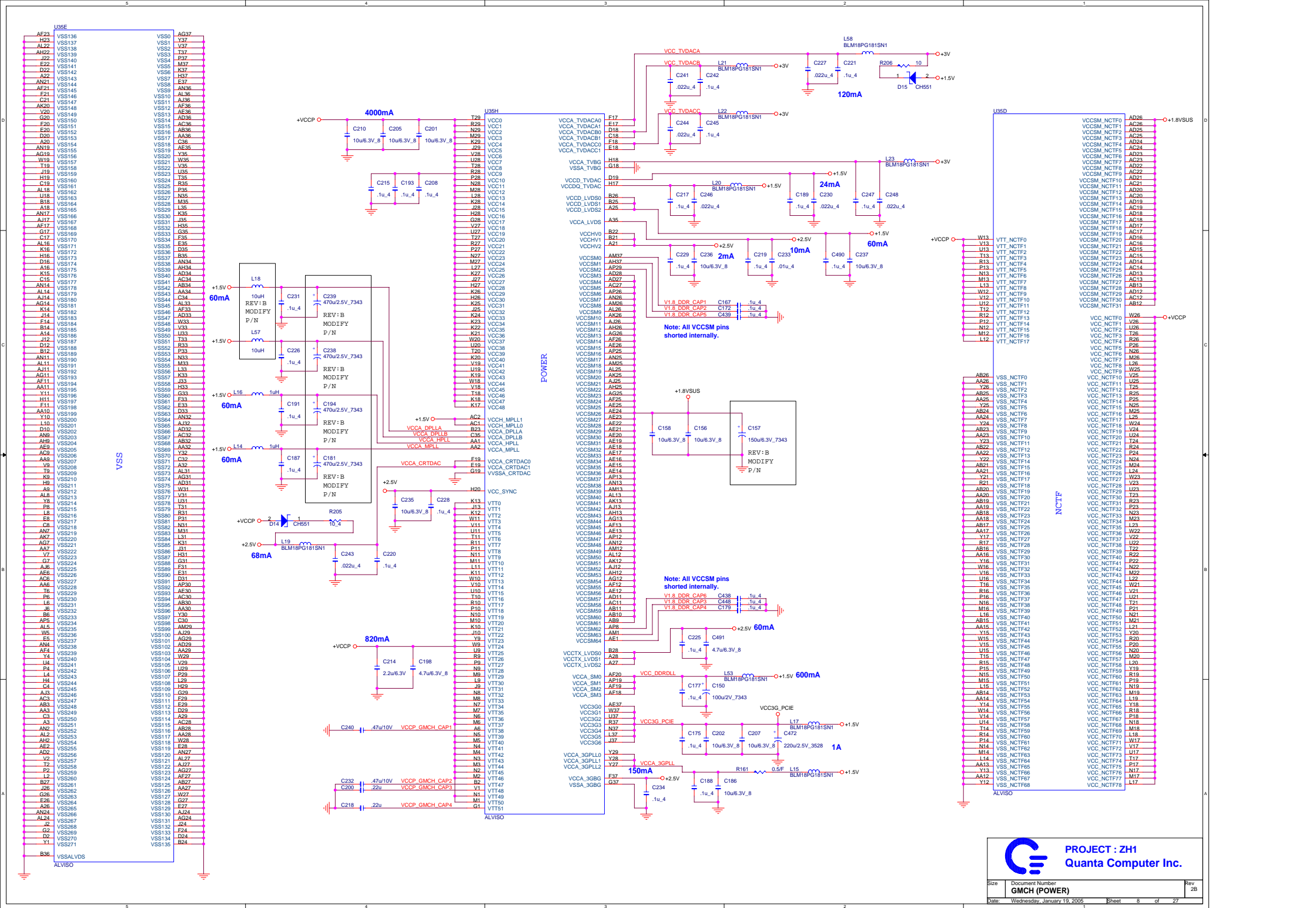
SMB_MD[63..0] (9)

SMB_MA[13..0] (9)

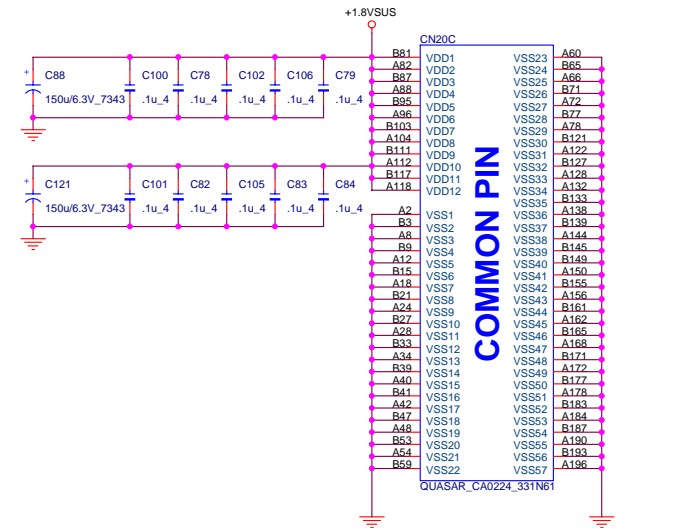
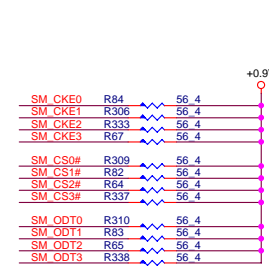


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	GMCH (MEMORY)	2B
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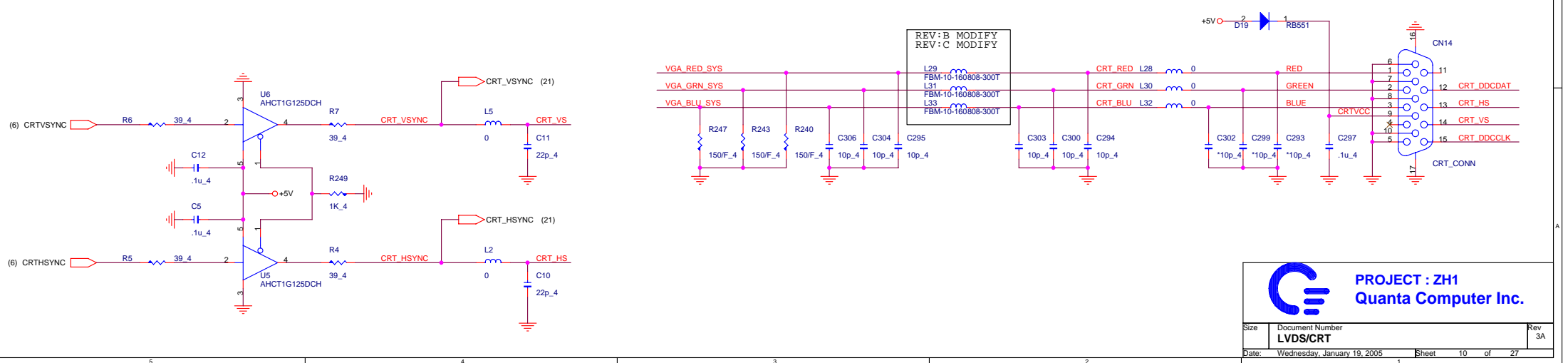
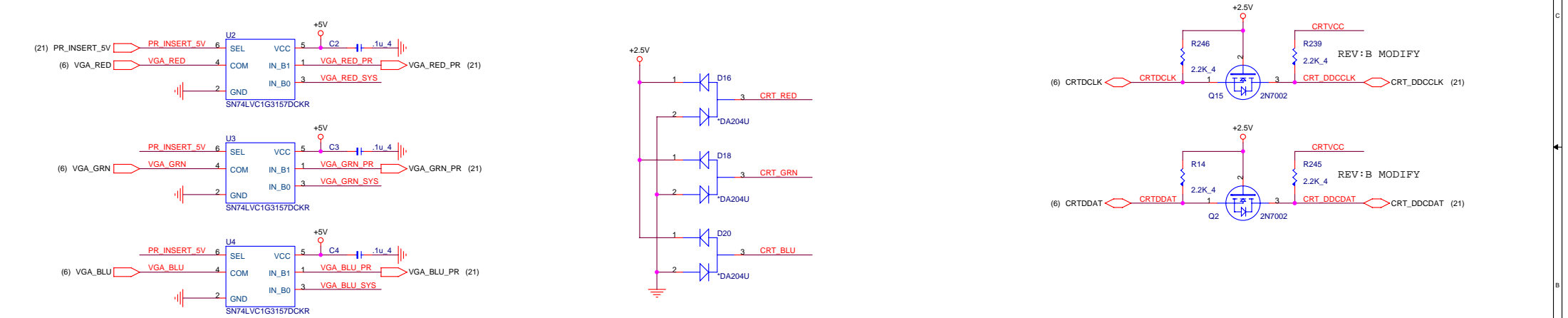
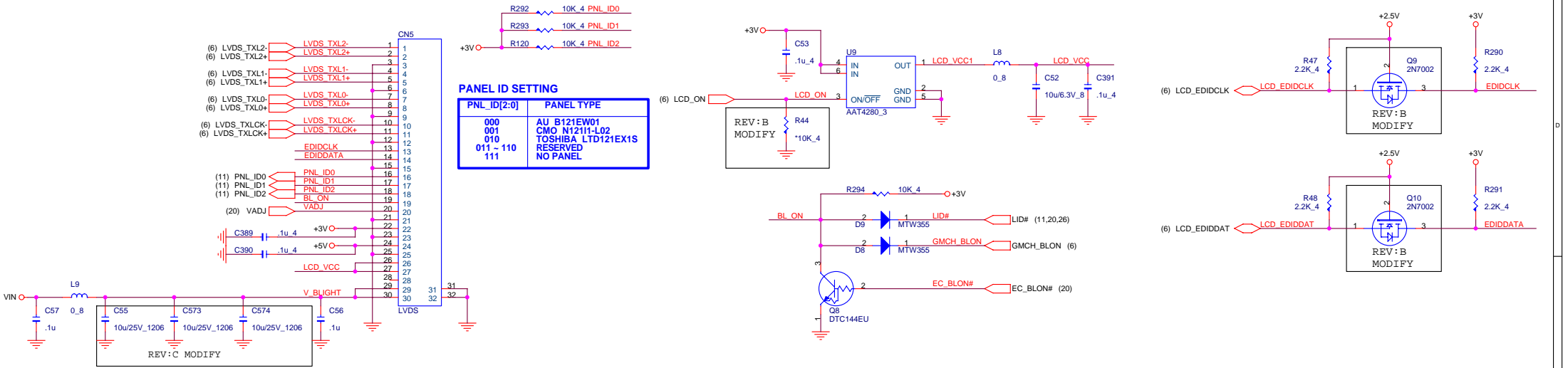


DDR2 TERMINATOR

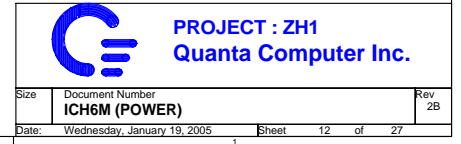


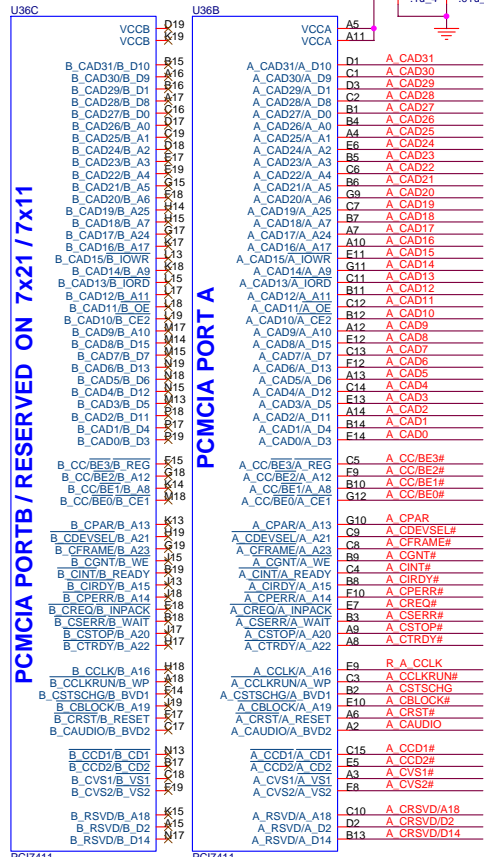
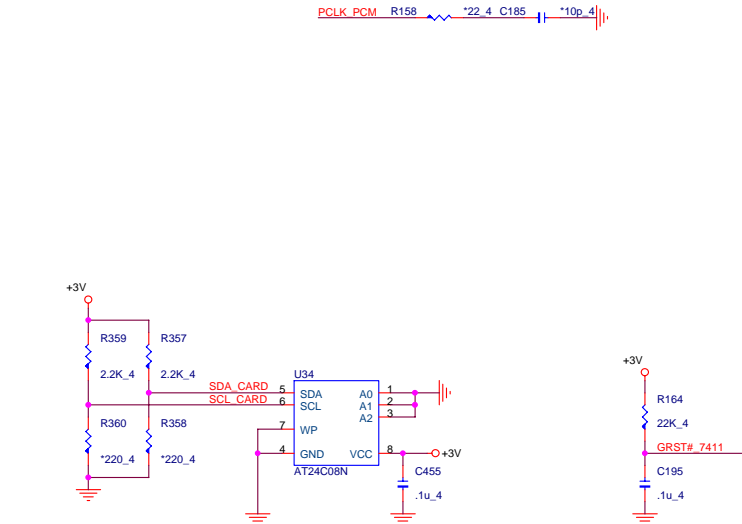
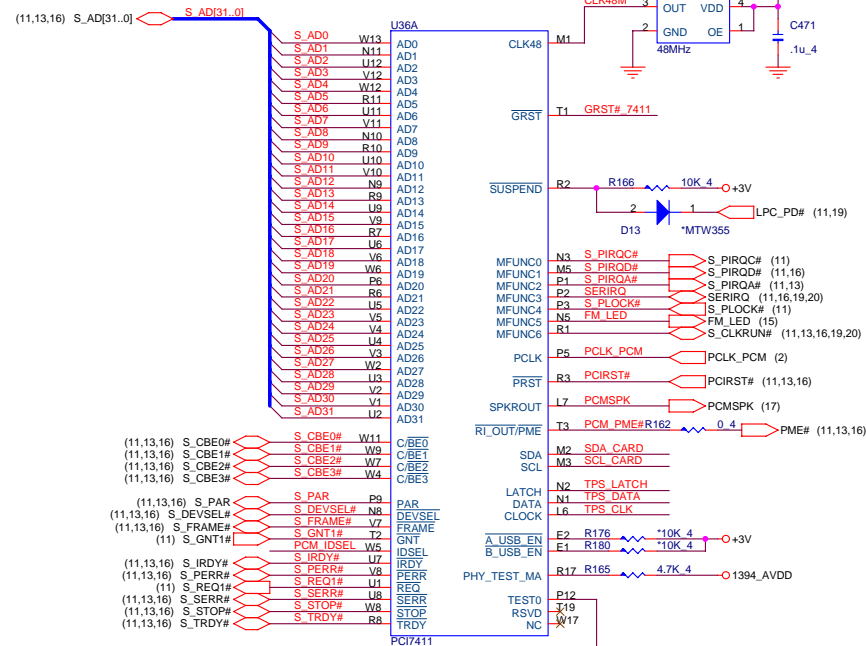
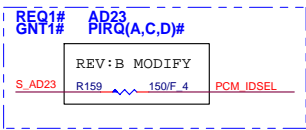
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	DDR2 SO-DIMM & TERMINATOR	3A
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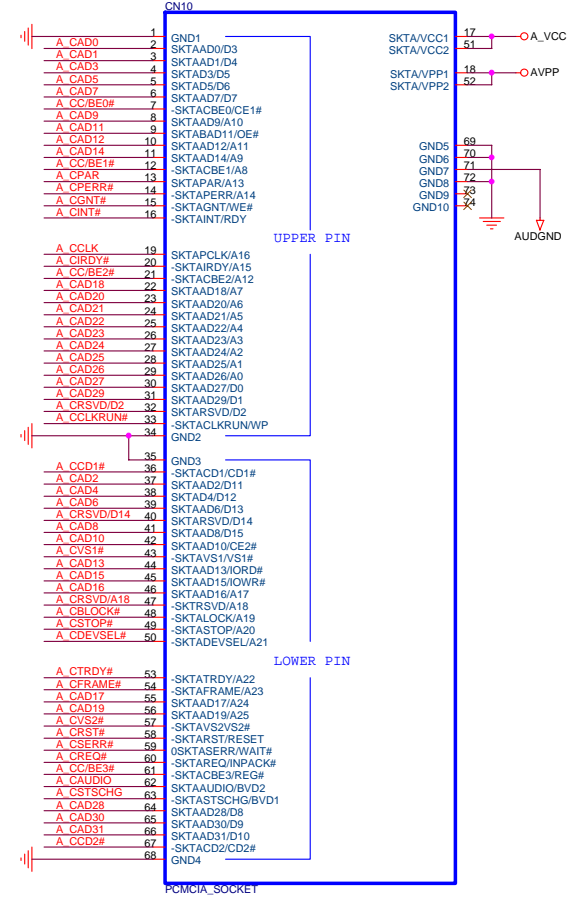




PCMCIA PORTB / RESERVED ON 7x21 / 7x11

PCMCIA PORT A

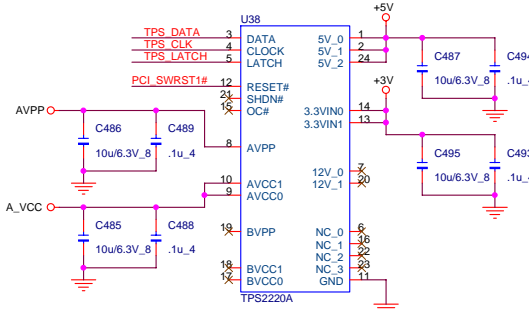
R A_CCLK R190 33.4 A_CCLK



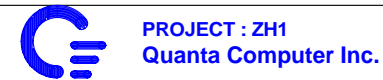
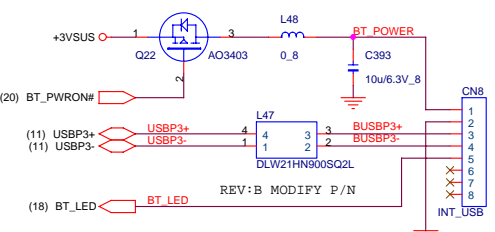
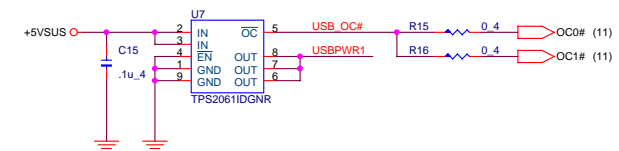
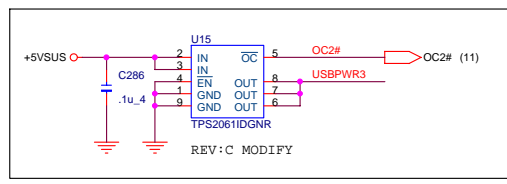
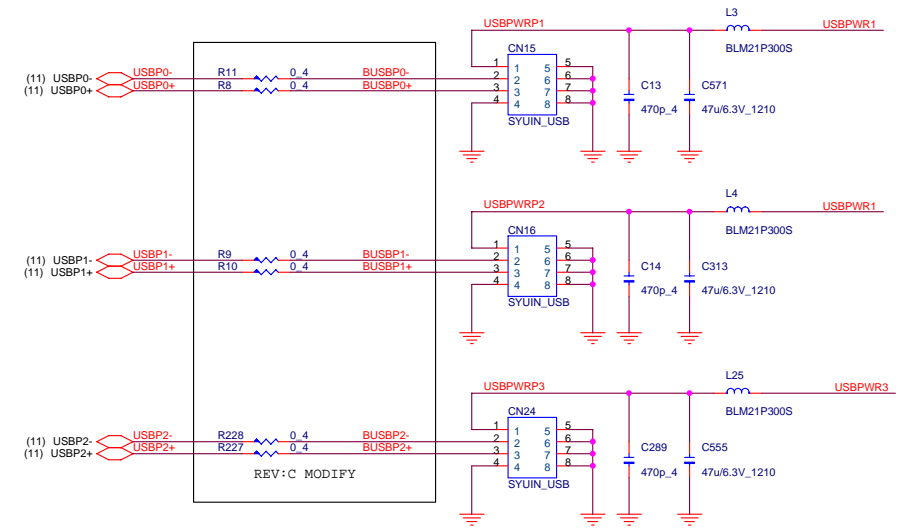
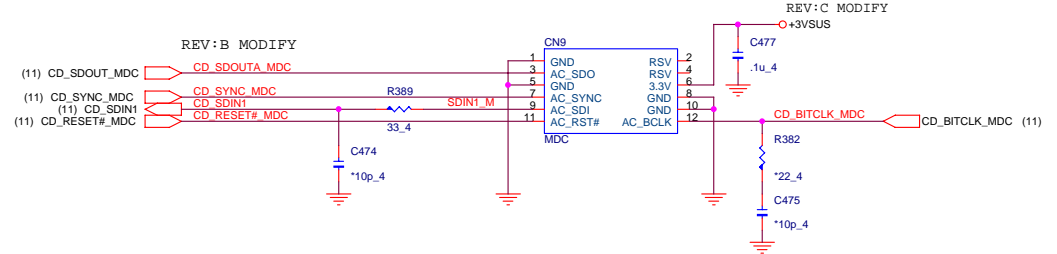
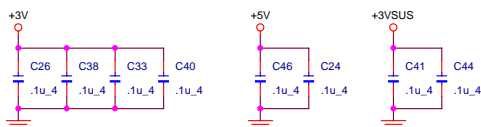
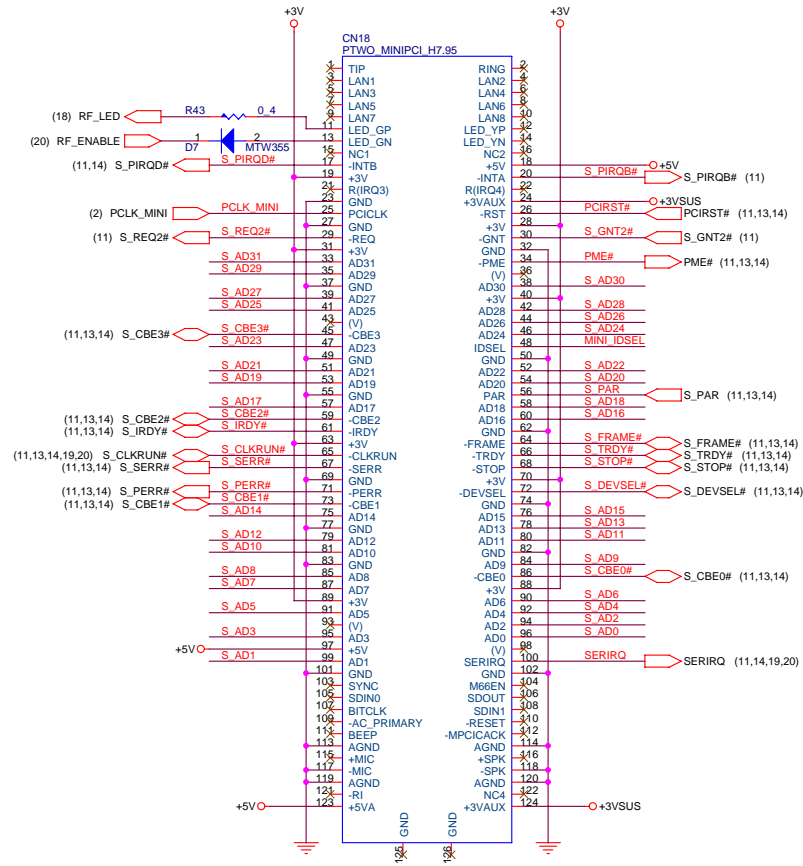
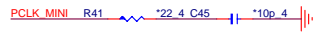
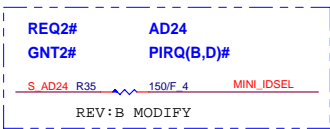
UPPER PIN

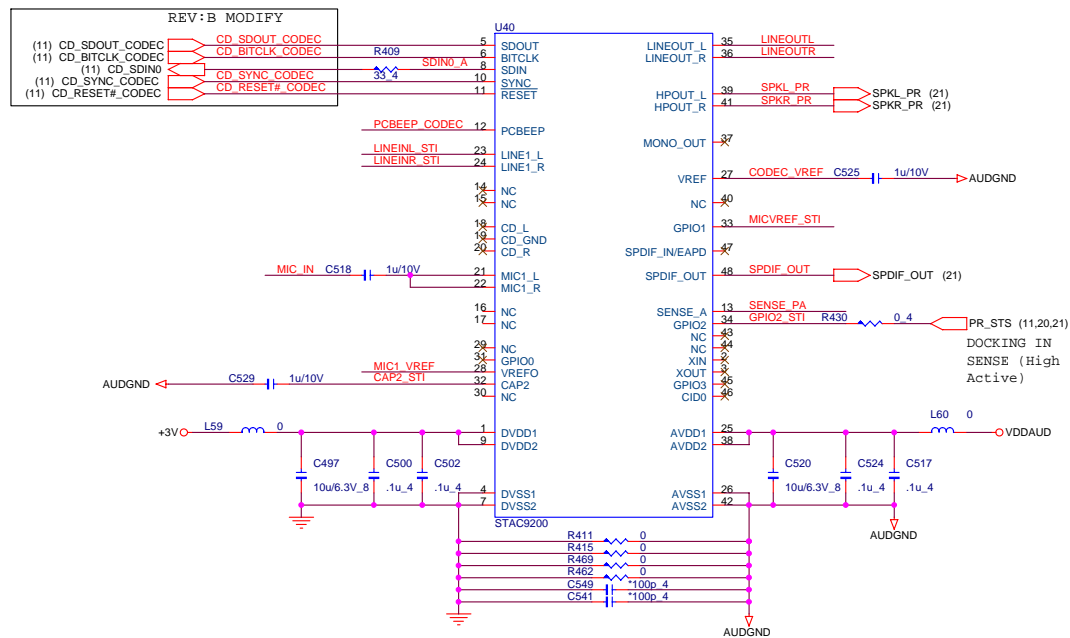
LOWER PIN

PCMCIA SOCKET

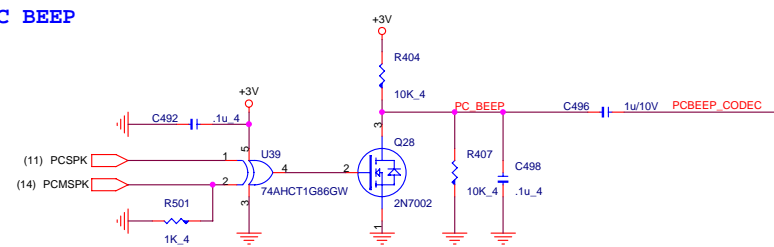


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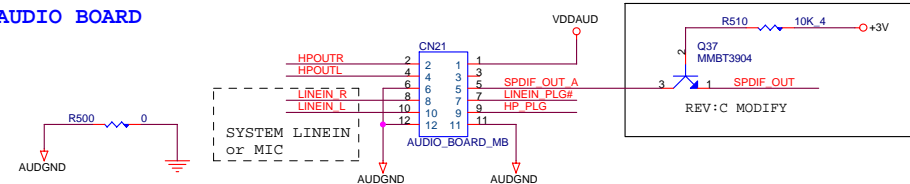




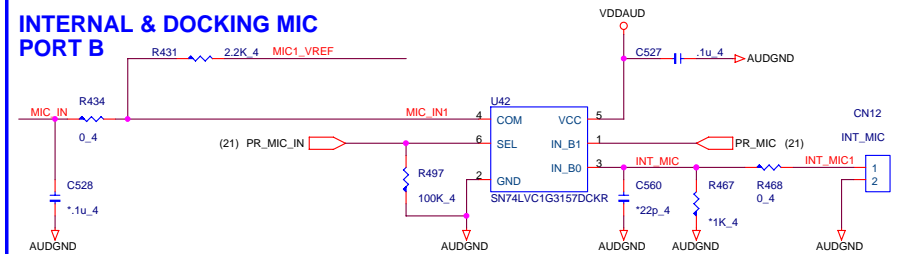
PC BEEP



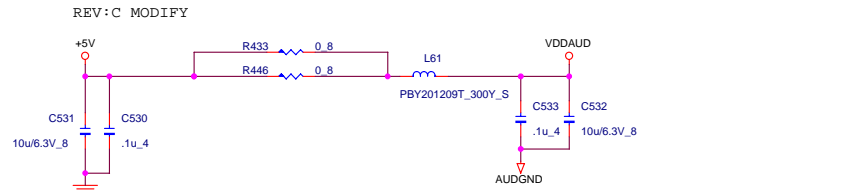
AUDIO BOARD



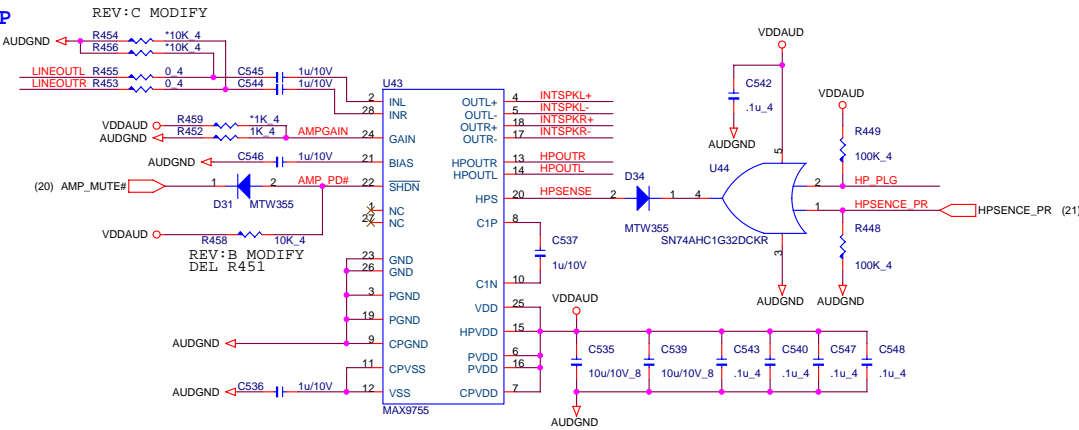
INTERNAL & DOCKING MICROPHONE
PORT B



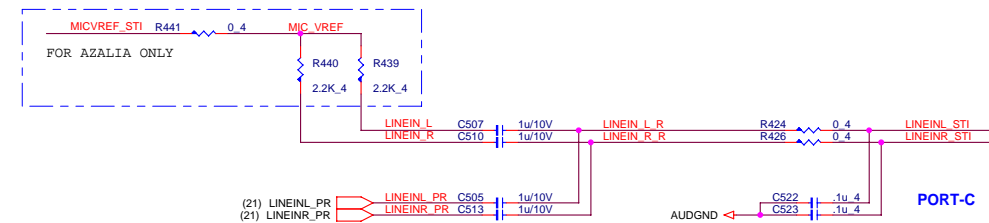
AUDIO POWER



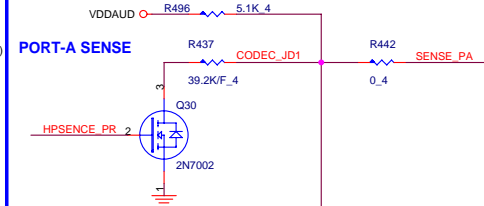
AMP



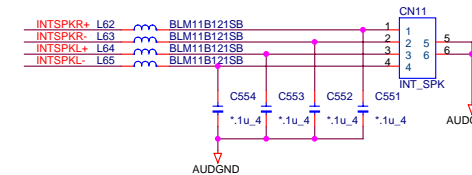
LINEIN PORT C



PORT-A SENSE

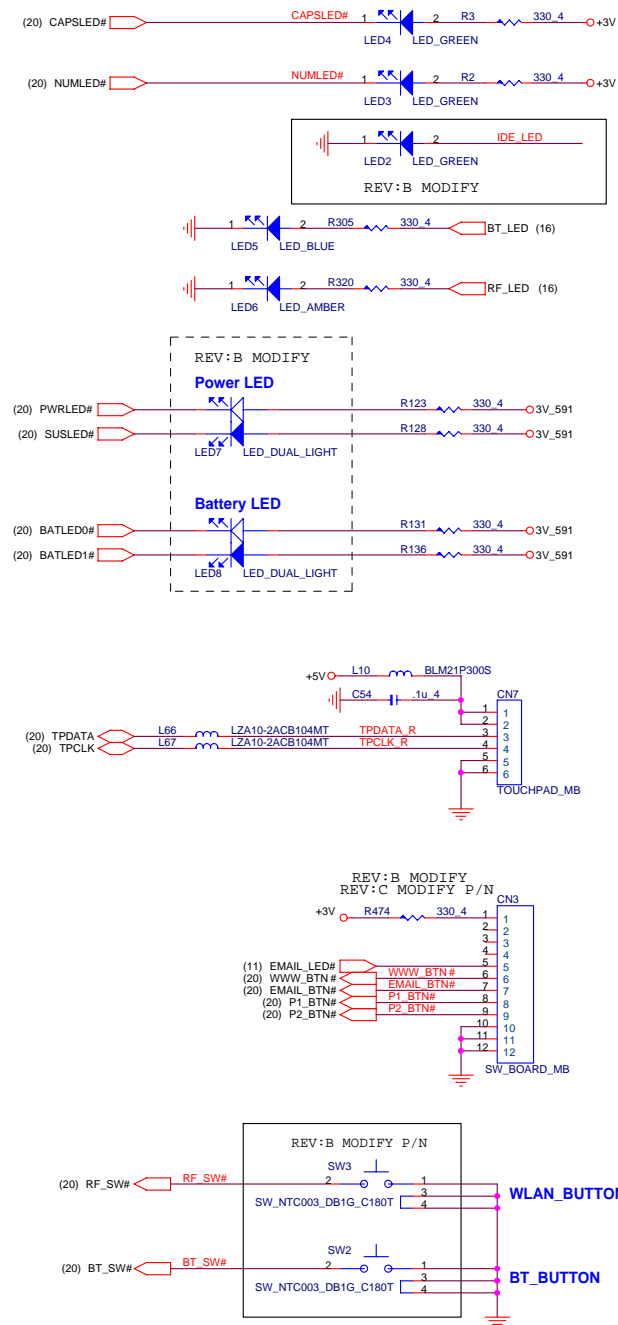
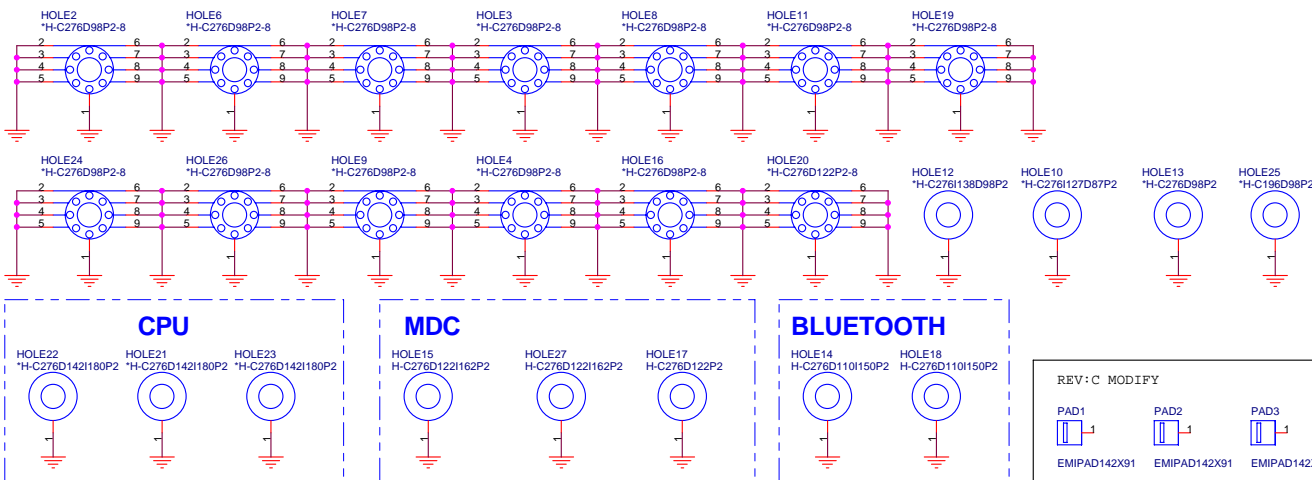
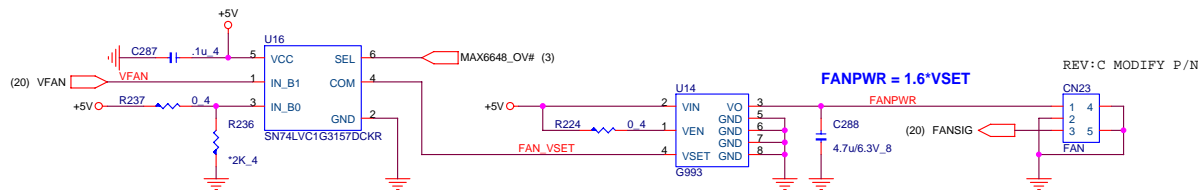
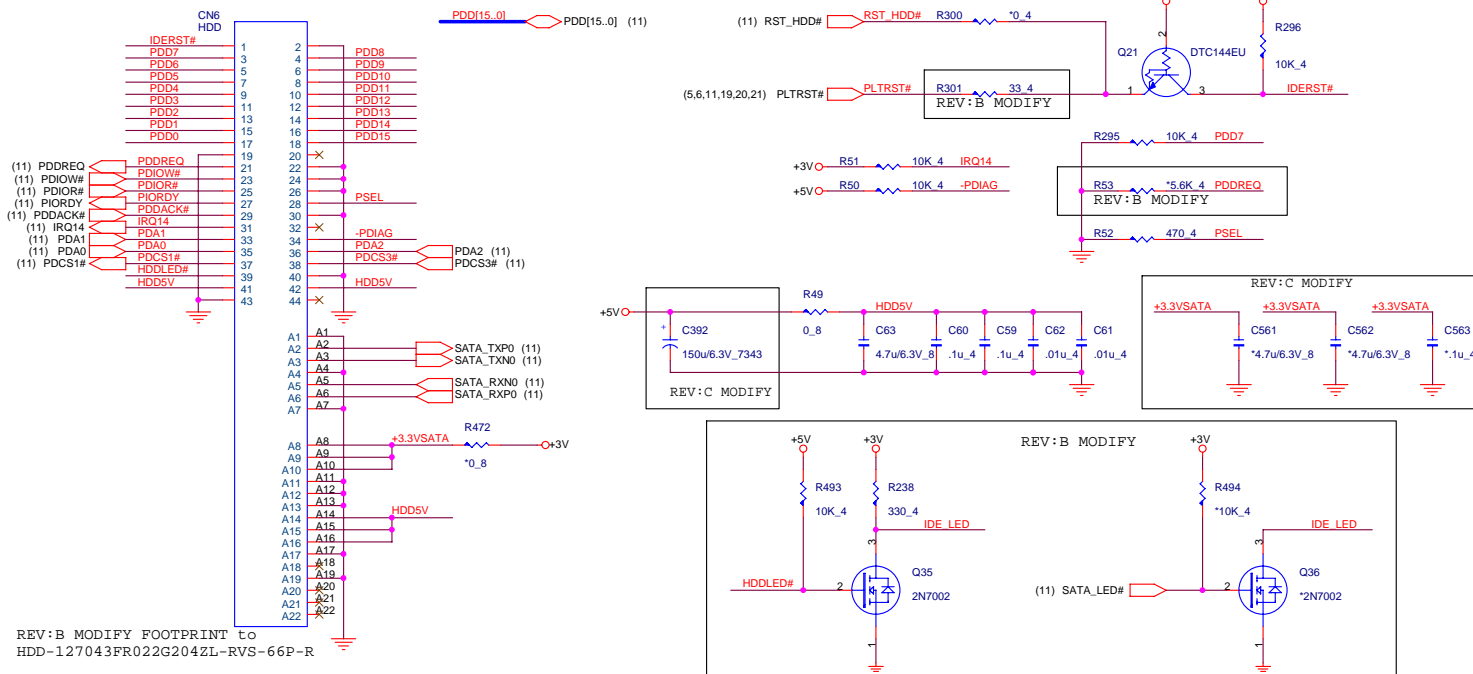


PORT-C SENSE

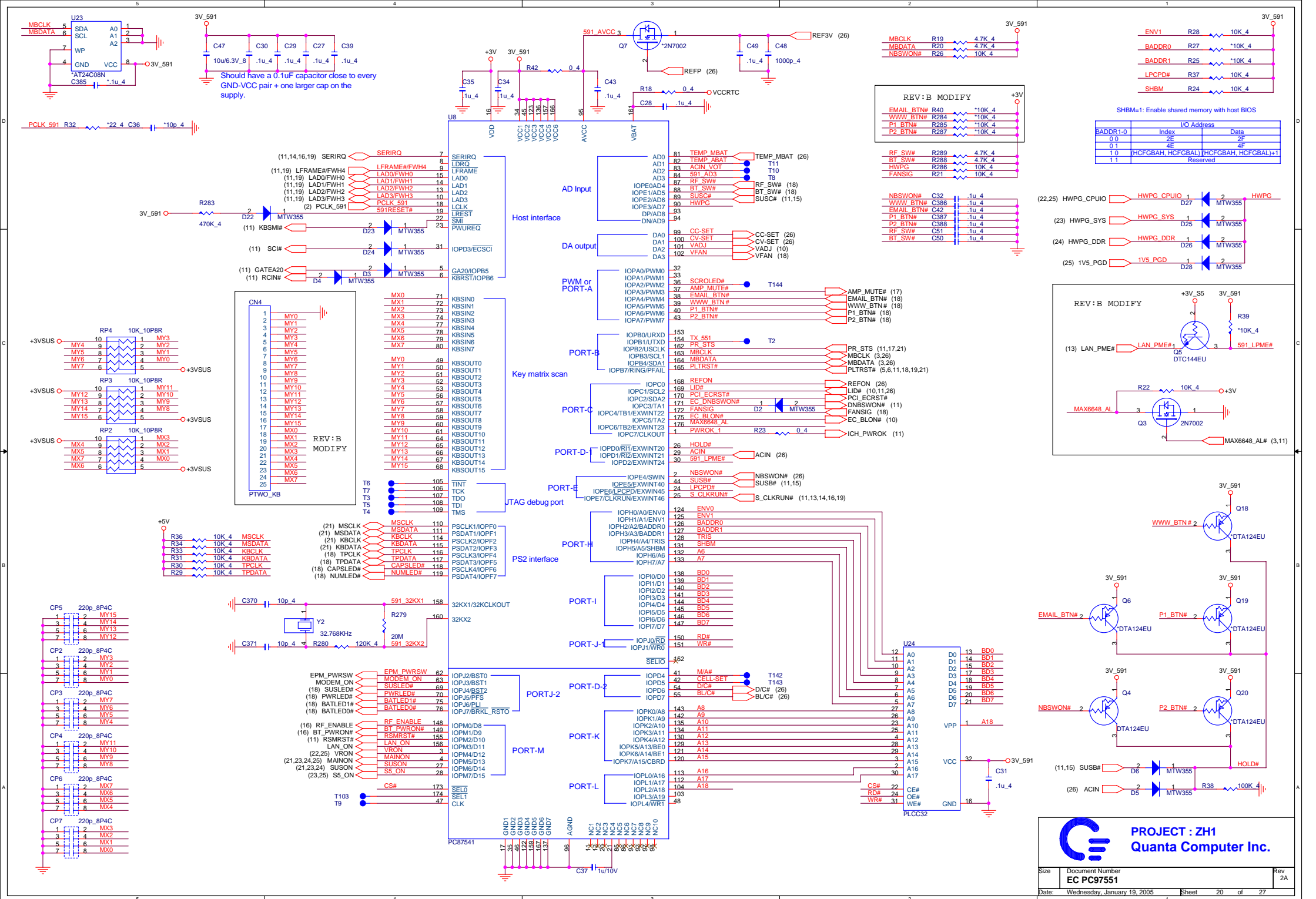


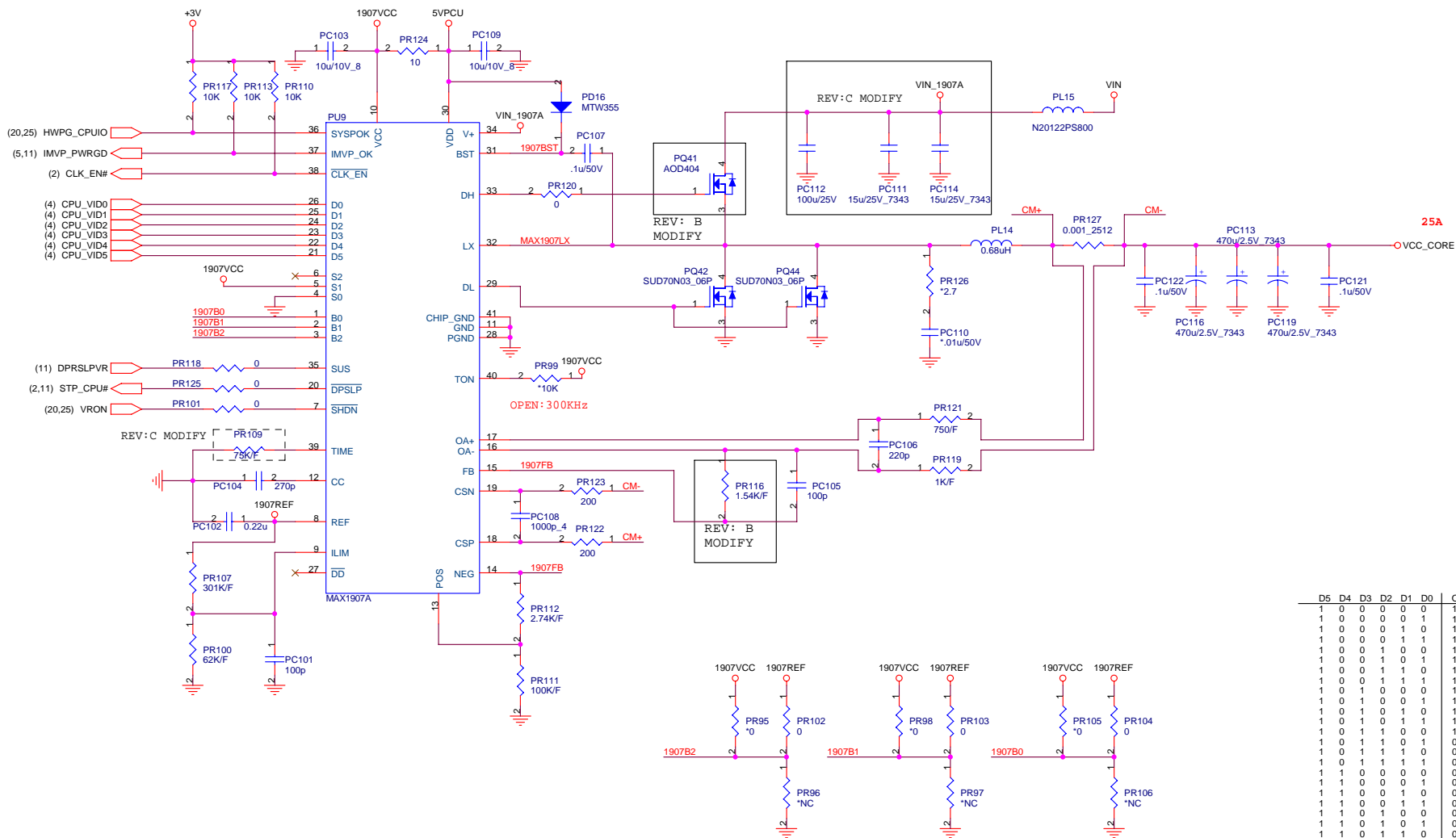
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SUSPEND MODE (SUS=HIGH)

S2	S1	S0	Output
✓ OPEN	VCC	GND	0.748V

VCC_BOOT

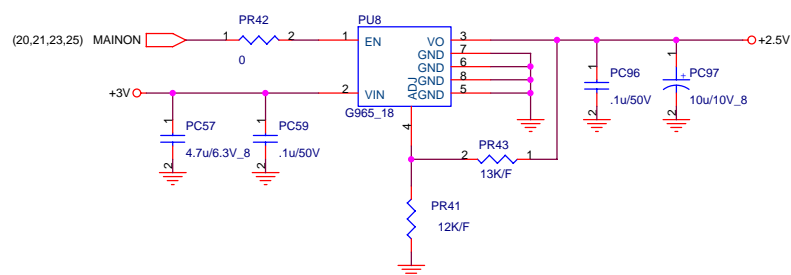
B2	B1	B0	Output
GND	GND	GND	1.708V
REF	REF	REF	1.372V
OPEN	OPEN	OPEN	1.036V
VCC	VCC	VCC	0.700V
REF	VCC	VCC	1.212V

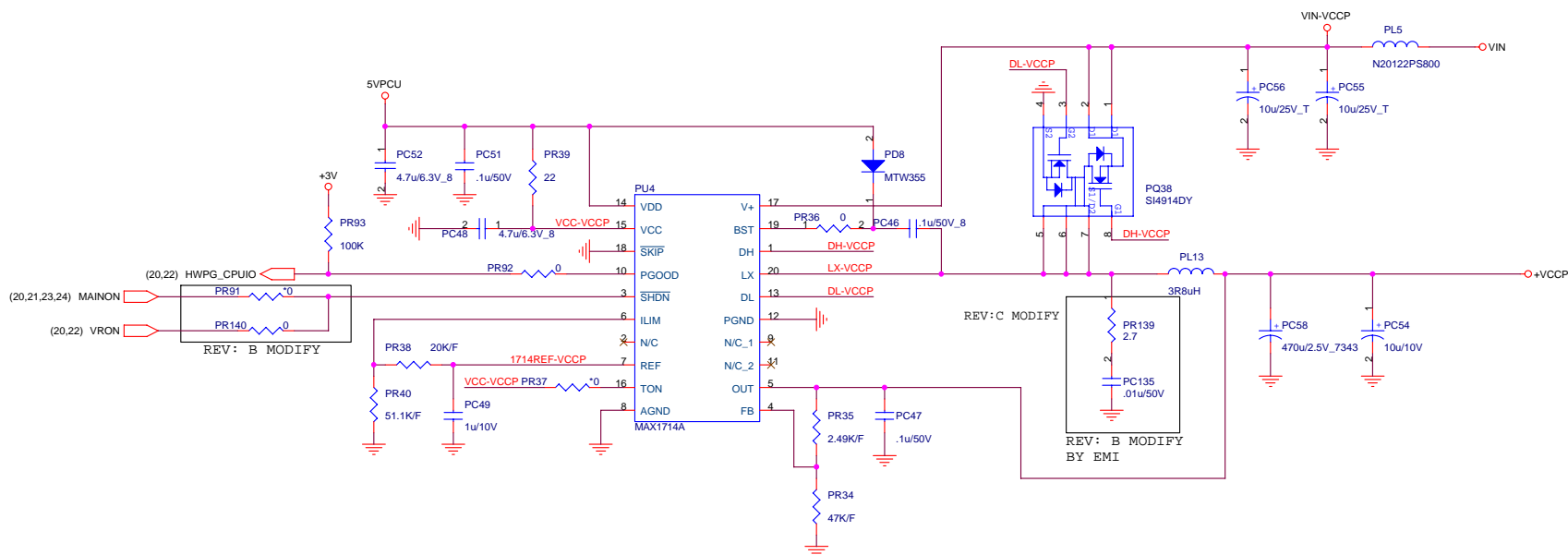
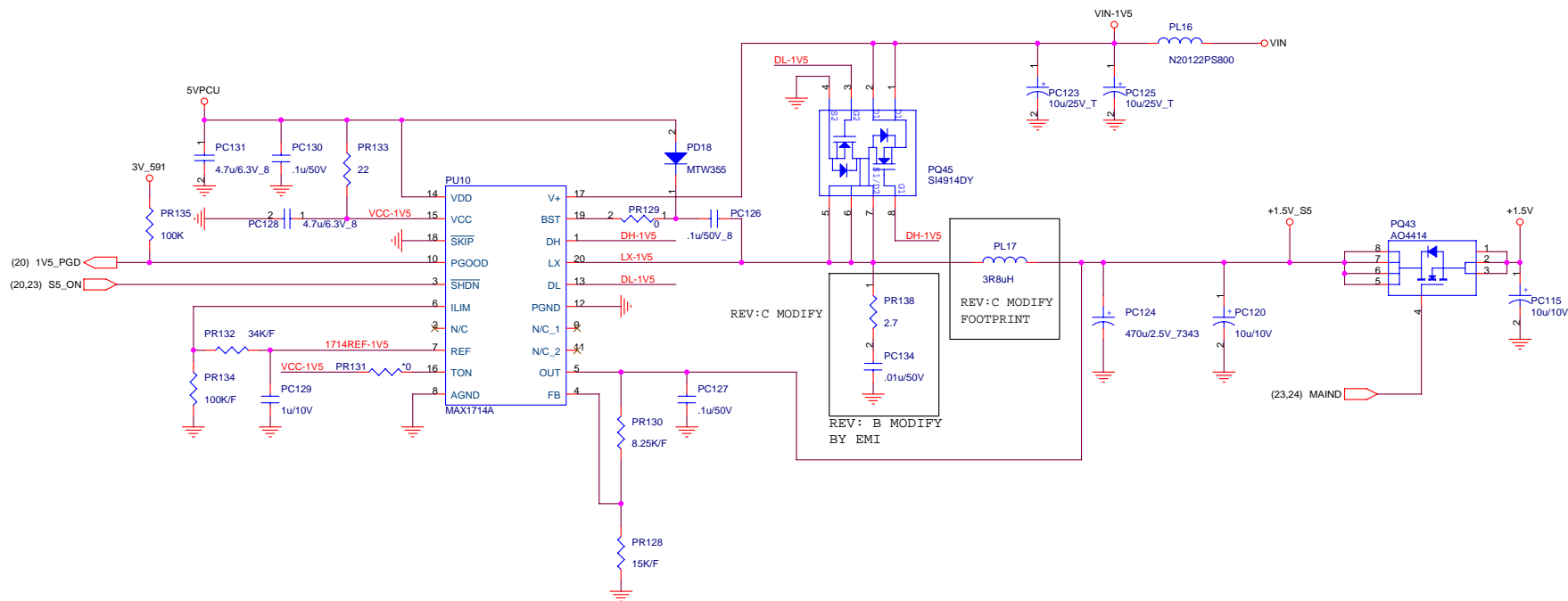
D5	D4	D3	D2	D1	D0	Output	D5	D4	D3	D2	D1	D0	Output
1	0	0	0	0	0	1.196V	0	0	0	0	0	0	1.708V
1	0	0	0	0	1	1.180V	0	0	0	0	0	1	1.692V
1	0	0	0	1	0	1.164V	0	0	0	0	1	0	1.676V
1	0	0	0	1	1	1.148V	0	0	0	0	1	1	1.660V
1	0	0	1	0	0	1.132V	0	0	0	1	0	0	1.644V
1	0	0	1	0	1	1.116V	0	0	0	1	0	1	1.628V
1	0	0	1	1	0	1.100V	0	0	0	1	1	0	1.612V
1	0	0	1	1	1	1.084V	0	0	0	1	1	1	1.596V
1	0	1	0	0	0	1.068V	0	0	1	0	0	0	1.580V
1	0	1	0	0	1	1.052V	0	0	1	0	0	1	1.564V
1	0	1	0	1	0	1.036V	0	0	1	0	1	0	1.548V
1	0	1	0	1	1	1.020V	0	0	1	0	1	1	1.532V
1	0	1	1	0	0	1.004V	0	0	1	1	0	0	1.516V
1	0	1	1	0	1	0.988V	0	0	1	1	1	0	1.500V
1	0	1	1	1	0	0.972V	0	0	1	1	1	1	1.484V
1	0	1	1	1	1	0.956V	0	0	1	1	1	1	1.468V
1	1	0	0	0	0	0.940V	0	1	0	0	0	0	1.452V
1	1	0	0	0	1	0.924V	0	1	0	0	1	0	1.436V
1	1	0	0	1	0	0.908V	0	1	0	0	1	1	1.420V
1	1	0	0	1	1	0.892V	0	1	0	0	1	1	1.404V
1	1	0	1	0	0	0.876V	0	1	0	1	0	0	1.388V
1	1	0	1	0	1	0.860V	0	1	0	1	1	0	1.372V
1	1	0	1	1	0	0.844V	0	1	0	1	1	1	1.356V
1	1	0	1	1	1	0.828V	0	1	0	1	1	1	1.340V
1	1	1	0	0	0	0.812V	0	1	1	0	0	0	1.324V
1	1	1	0	0	1	0.796V	0	1	1	0	0	1	1.308V
1	1	1	0	1	0	0.780V	0	1	1	0	1	0	1.292V
1	1	1	0	1	1	0.764V	0	1	1	0	1	1	1.276V
1	1	1	1	0	0	0.748V	0	1	1	1	0	0	1.260V
1	1	1	1	0	1	0.732V	0	1	1	1	0	1	1.244V
1	1	1	1	1	0	0.716V	0	1	1	1	1	0	1.228V
1	1	1	1	1	1	0.700V	0	1	1	1	1	1	1.212V



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	CPU CORE (MAX1907)	3A
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	+1.5V / +VCCP	3A
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