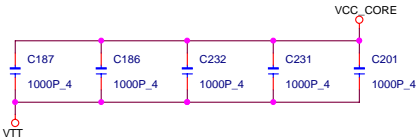
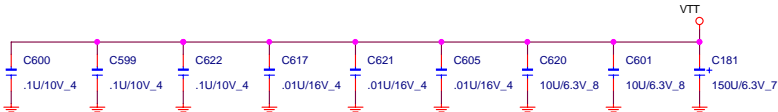
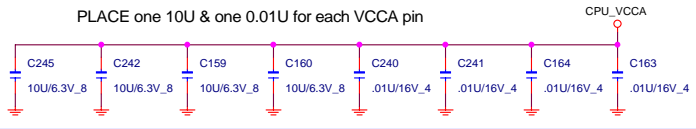


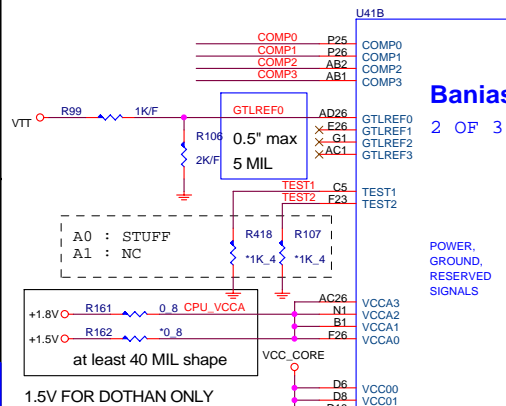
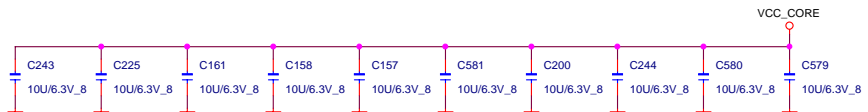
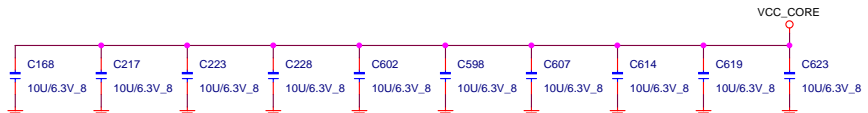
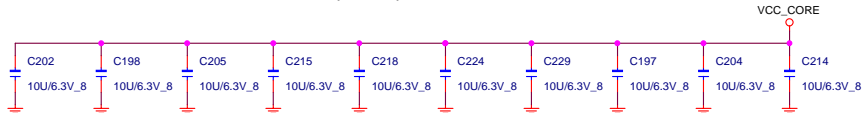
COMPO ~ 4
max length
500 MIL



PLACE one 10U & one 0.01U for each VCCA pin

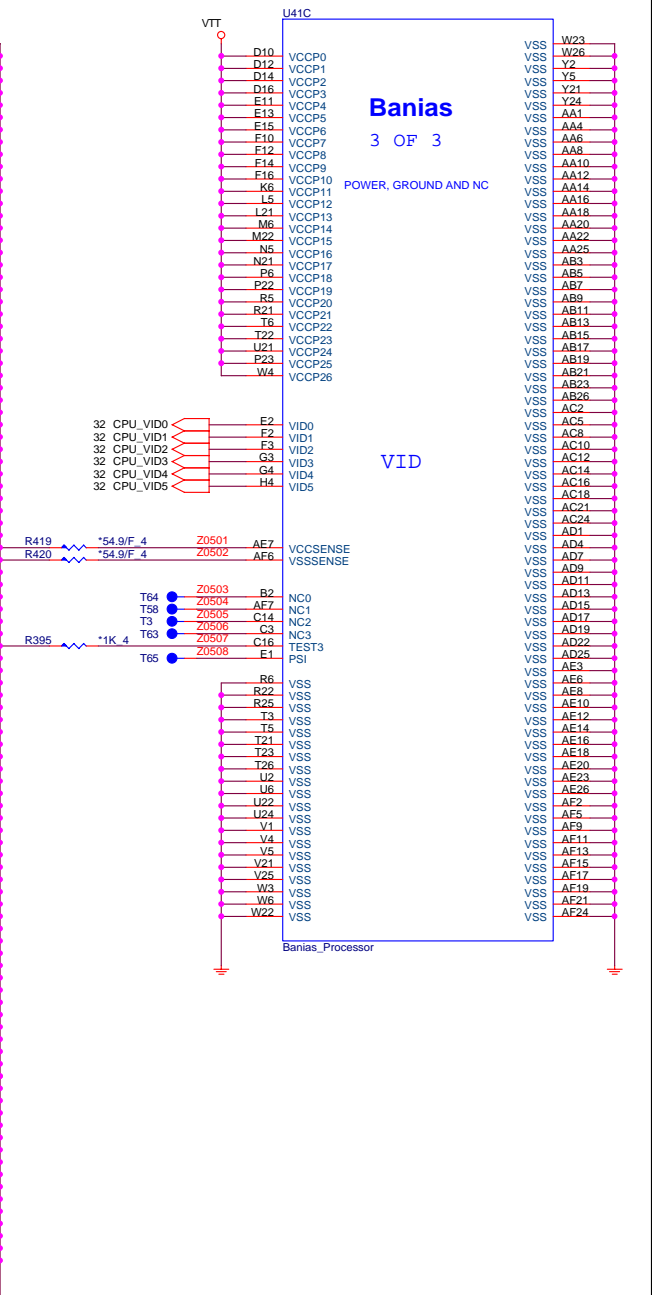
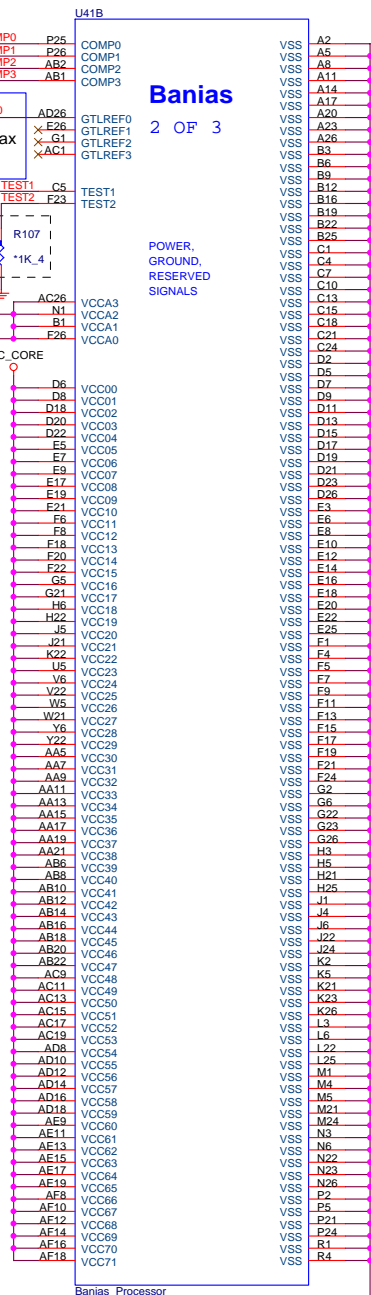


10U/6.3V/X5R(CC0805) *30



Banias
2 OF 3

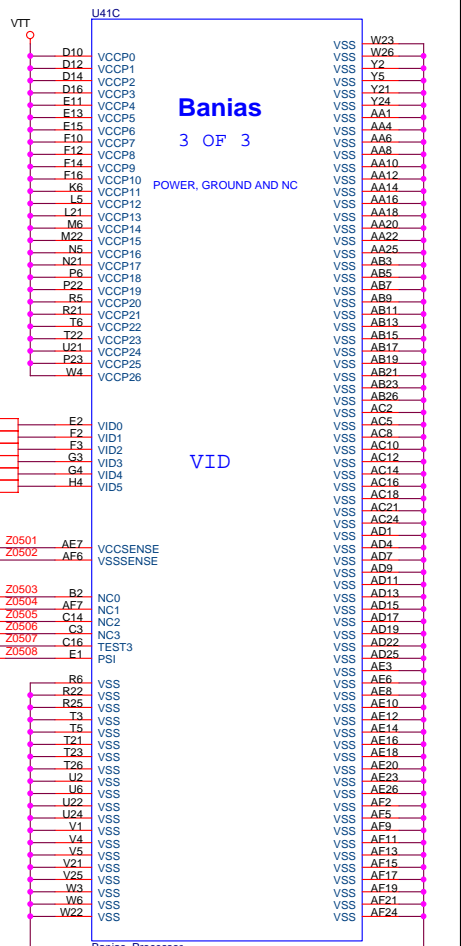
POWER,
GROUND,
RESERVED
SIGNALS

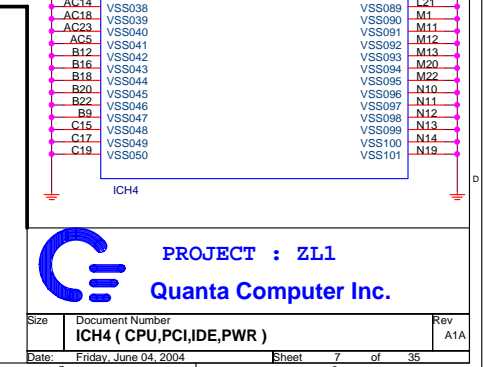
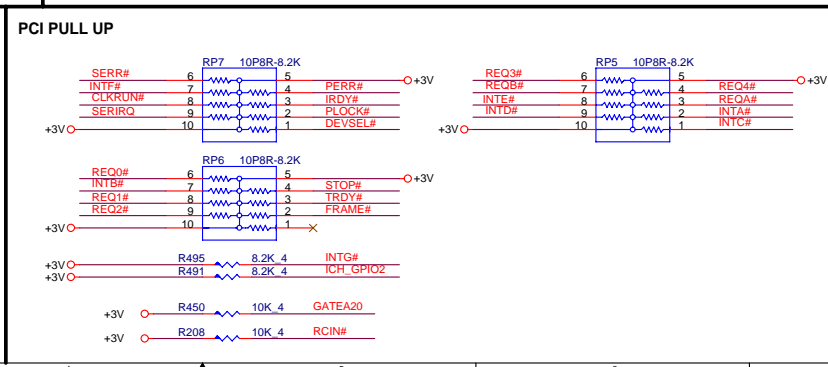
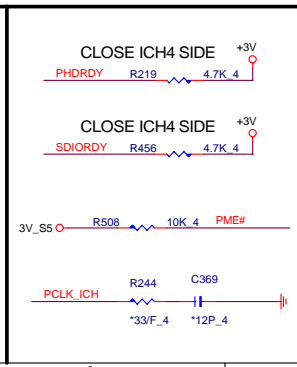
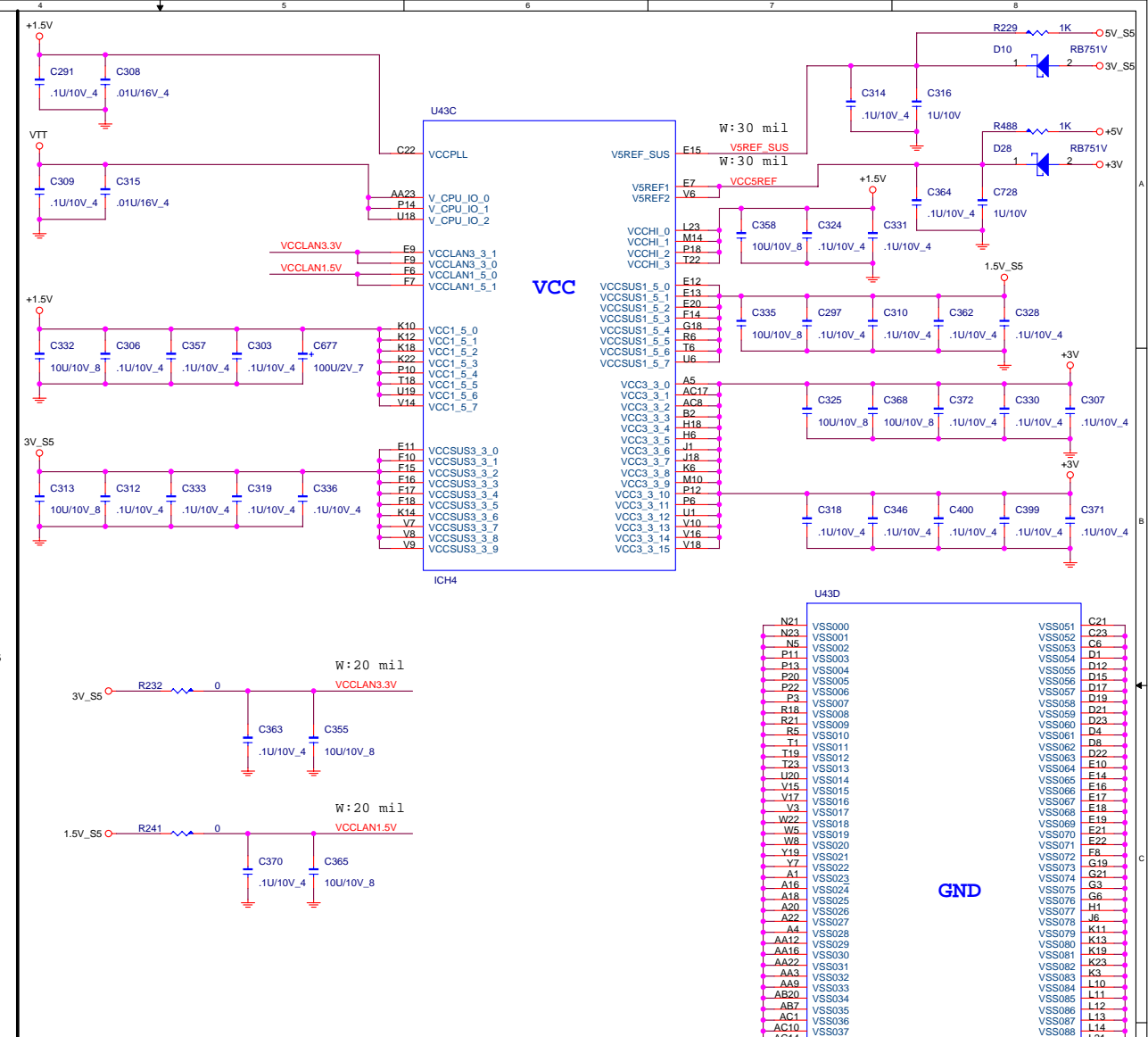


Banias
3 OF 3

POWER, GROUND AND NC

VID



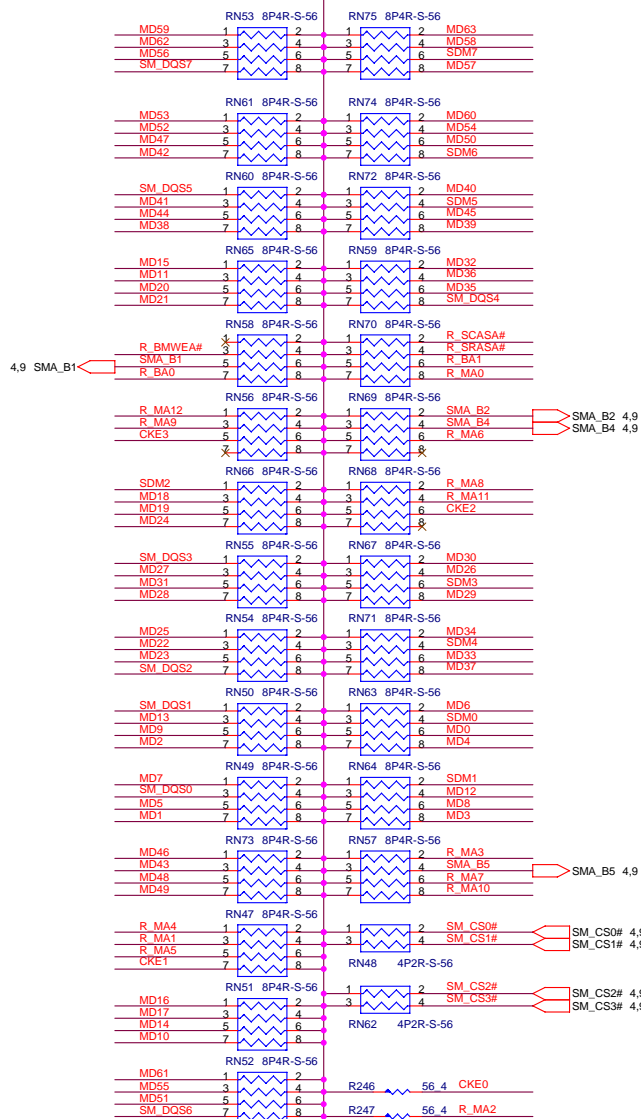




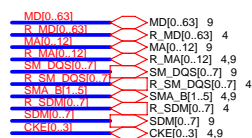
Quanta Computer Inc.

Size	Document Number DDR SO-DIMM (200P)	Rev A1A
Date:	Friday, June 04, 2004	Sheet 9 of 35

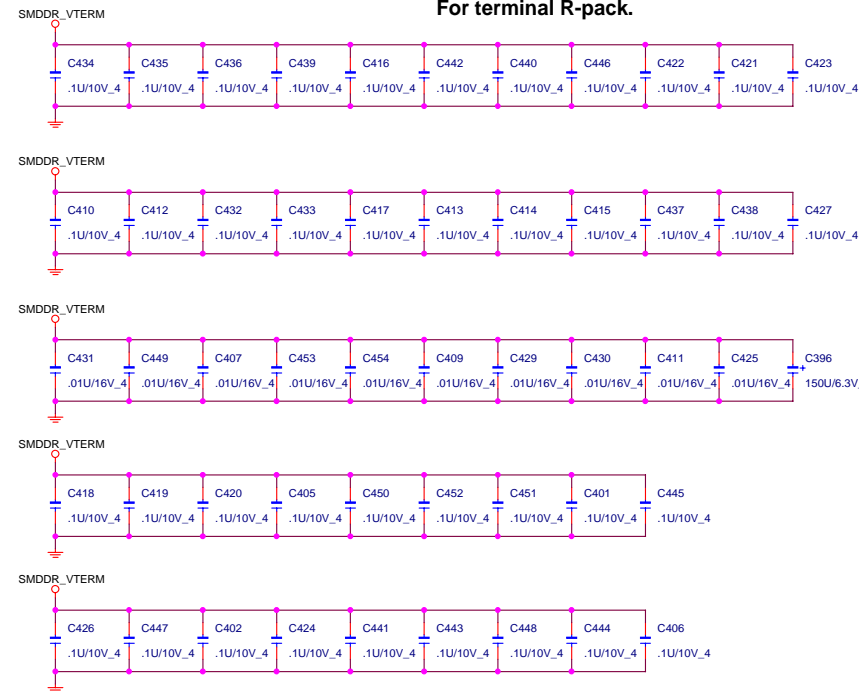
SMDDR_VTERM



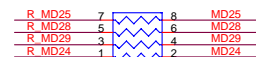
Damping put BOT, termination put TOP



For terminal R-pack.



RN37 8P4R-S-10



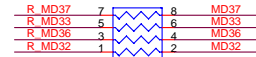
RN36 8P4R-S-10



RN35 8P4R-S-10



RN21 8P4R-S-10



RN20 8P4R-S-10



RN28 8P4R-S-10



RN42 8P4R-S-10



RN34 8P4R-S-10



RN41 8P4R-S-10



RN40 8P4R-S-10



RN33 8P4R-S-10



RN32 8P4R-S-10



RN31 8P4R-S-10



RN30 8P4R-S-10



RN39 8P4R-S-10



RN38 8P4R-S-10



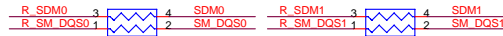
RN44 8P4R-S-10



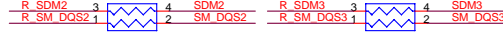
RN43 8P4R-S-10



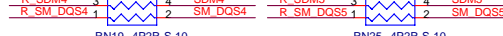
RN46 8P4R-S-10



RN29 4P2R-S-10



RN23 4P2R-S-10



RN19 4P2R-S-10



RN26 4P2R-S-10



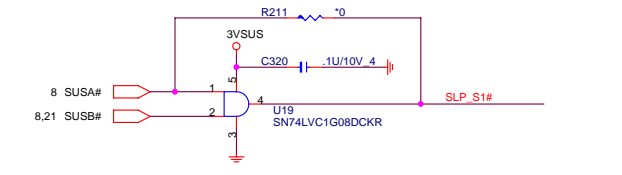
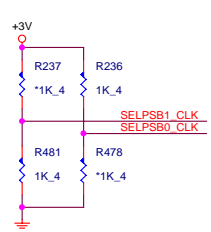
RN45 4P2R-S-10



PROJECT : ZL1
Quanta Computer Inc.

Size	Document Number	Rev
	DDR TERMINATION	A1A
Date	Friday, June 04, 2004	Sheet 10 of 35

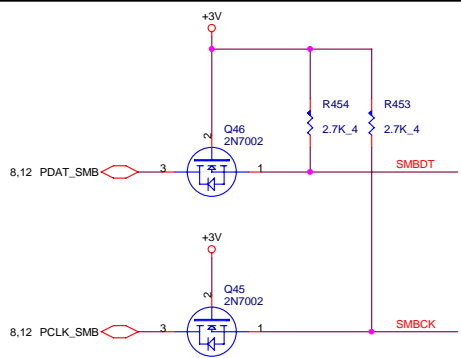
S2	S1	S0	CPU	3V66[0..4]	3V66_5/66IN
1	0	0	66	66IN	66 Input
1	0	1	100	66IN	66 Input
1	1	0	200	66IN	66 Input
1	1	1	133	66IN	66 Input
0	0	0	66	66	66
0	0	1	100	66	66
0	1	0	200	66	66
0	1	1	133	66	66



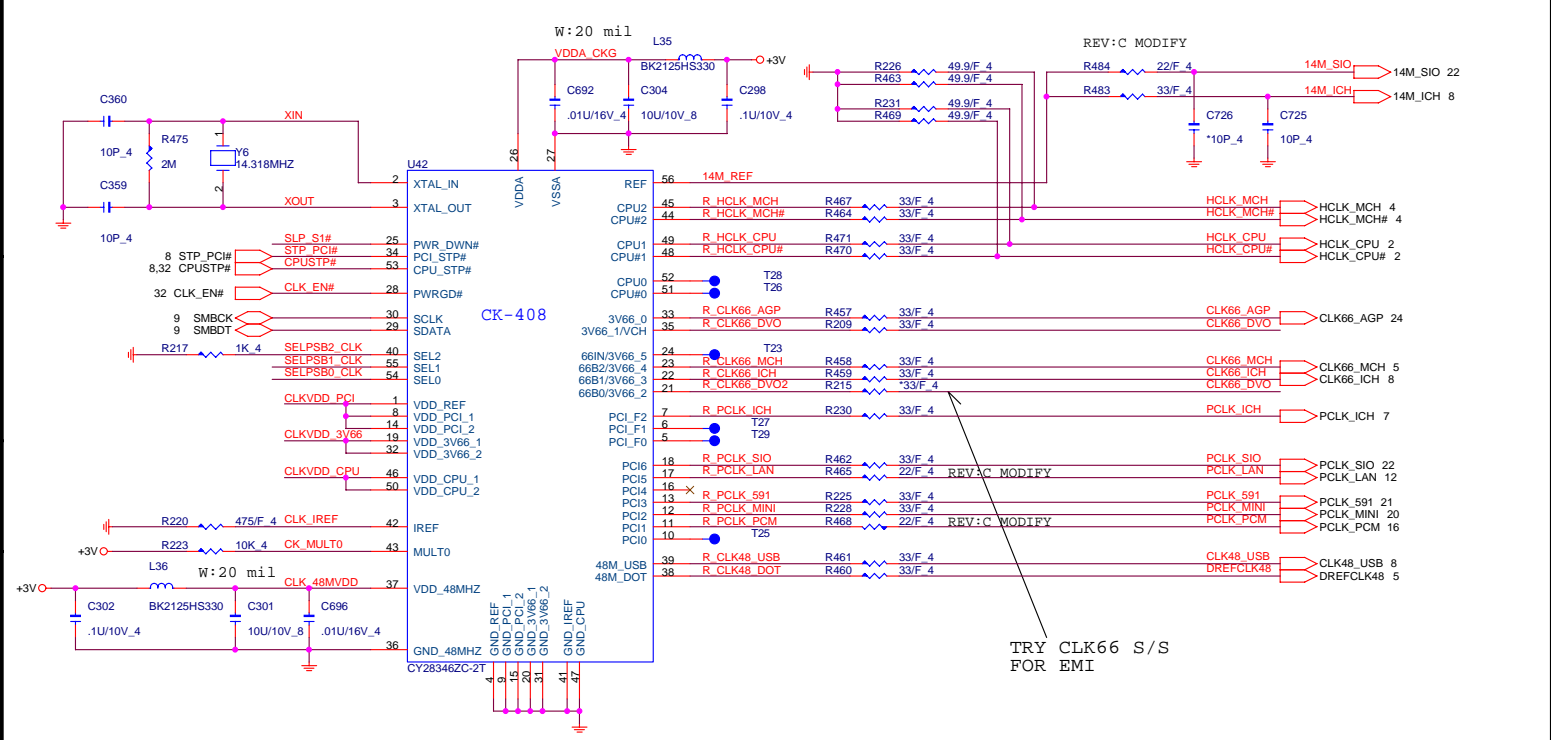
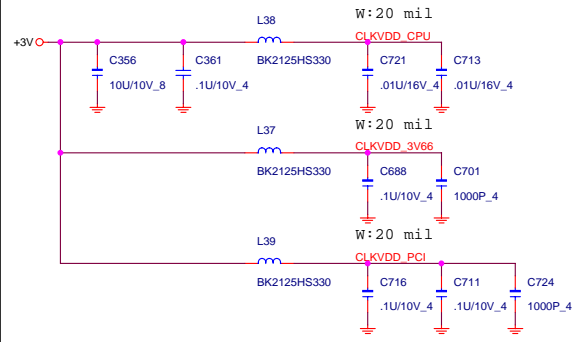
SMBus Byte 0 , Bit 5 = 0	3V66_1/VCH	66 MHz	W	S/S
SMBus Byte 0 , Bit 5 = 1	48 MHz	W/O	S/S	

Byte 0 , Bit 7 = 0 Disable Spread Spectrum
Byte 0 , Bit 7 = 1 Enable Spread Spectrum

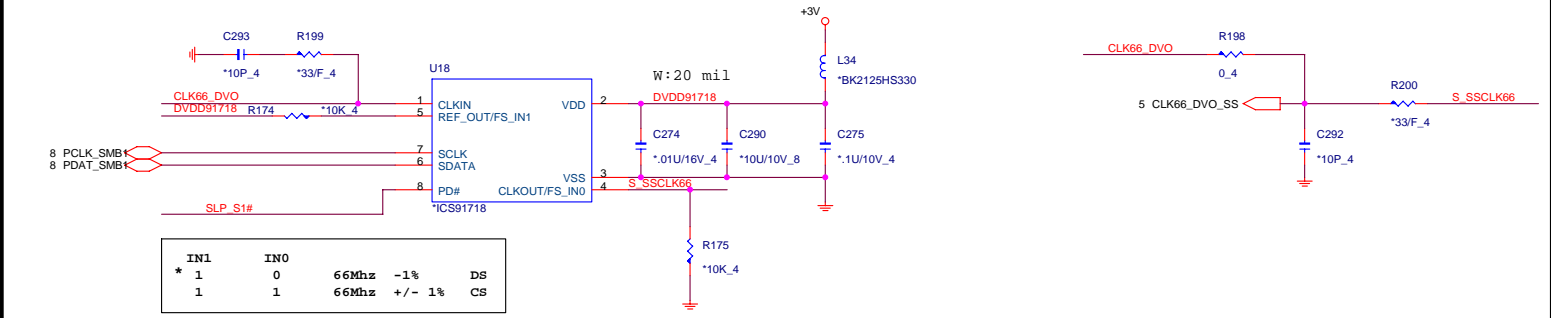
Byte 4 Bit 7	Byte 5 Bit 7	Byte 5 Bit 6	Spread Mode	Spread %
SS2	SS1	SS0		
0	0	0	DOWN	+0.00 , -0.25
0	0	1	DOWN	+0.00 , -0.50
0	1	0	DOWN	+0.00 , -0.75
0	1	1	DOWN	+0.00 , -1.00
1	0	0	CENTER	+0.13 , -0.13
1	0	1	CENTER	+0.25 , -0.25
1	1	0	CENTER	+0.37 , -0.37
1	1	1	CENTER	+0.50 , -1.50



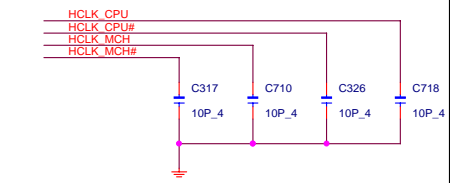
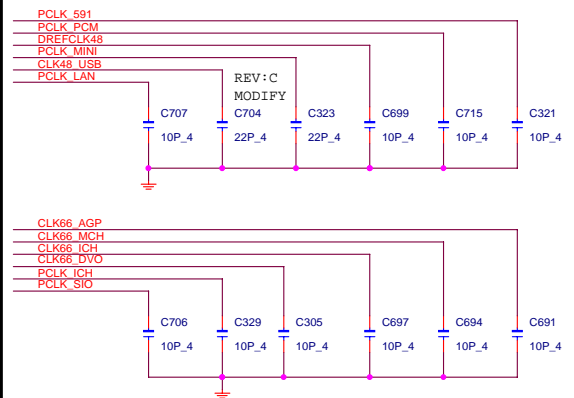
These are for backdrive issue

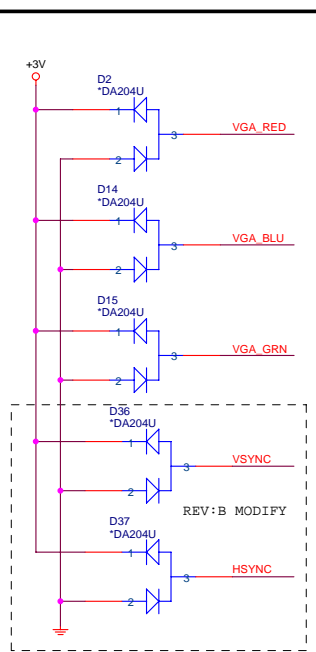
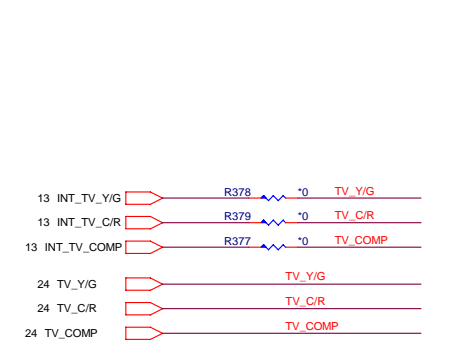
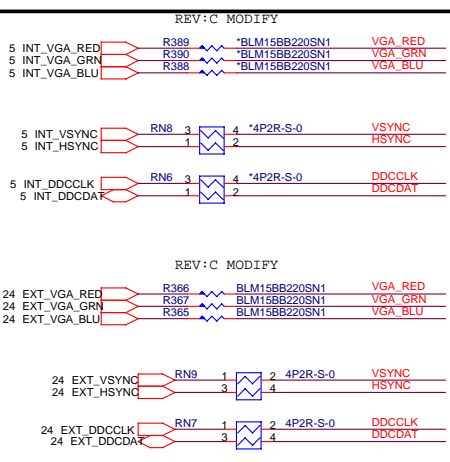
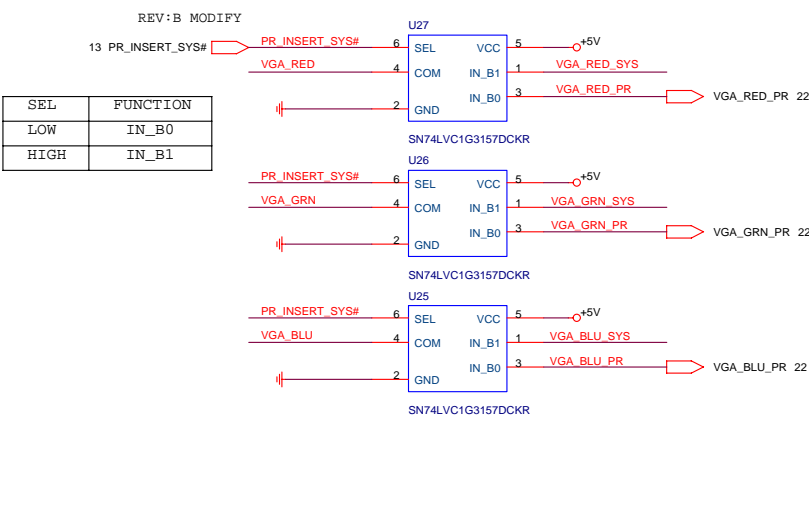


TRY CLK66 S/S
FOR EMI

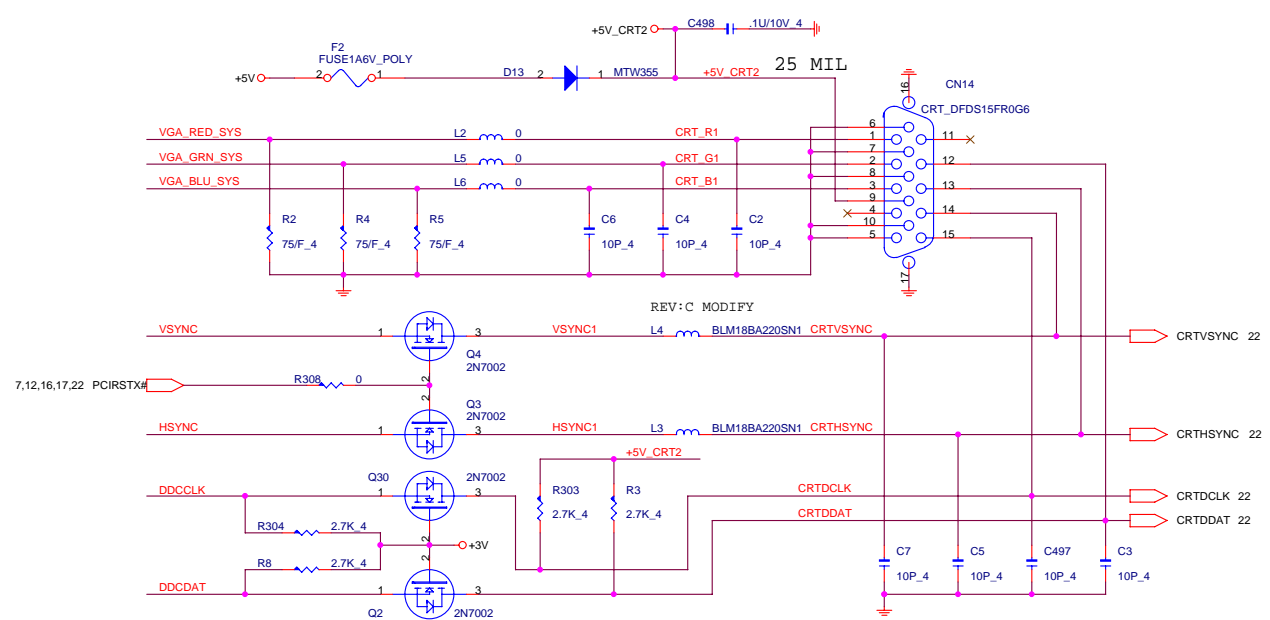


IN1	IN0			
* 1	0	66Mhz	-1%	DS
1	1	66Mhz	+/- 1%	CS

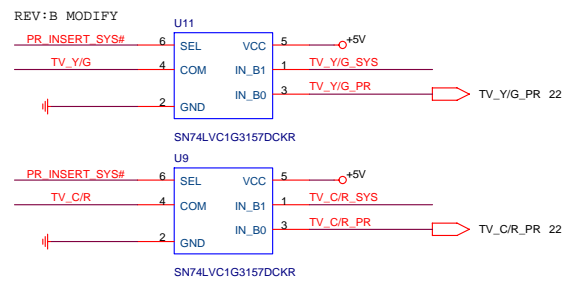
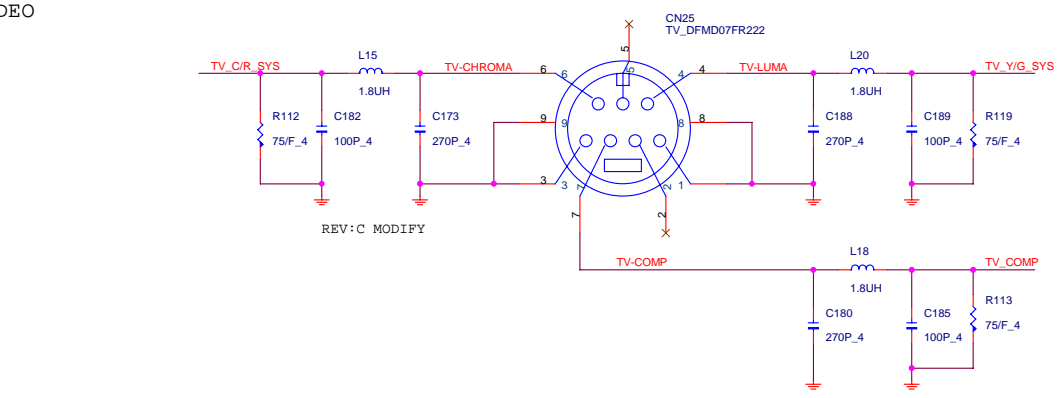




CRT PORT

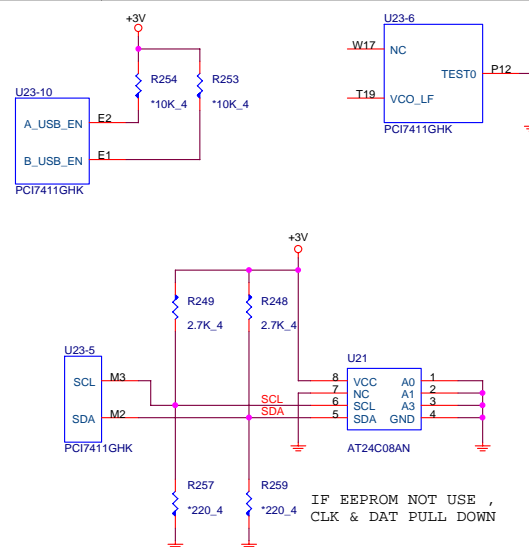
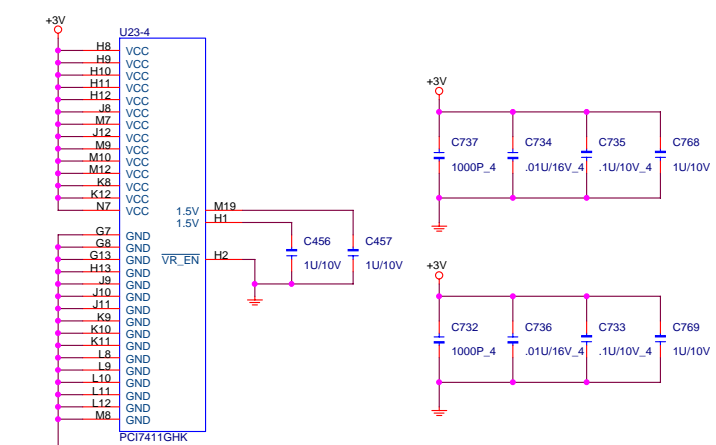
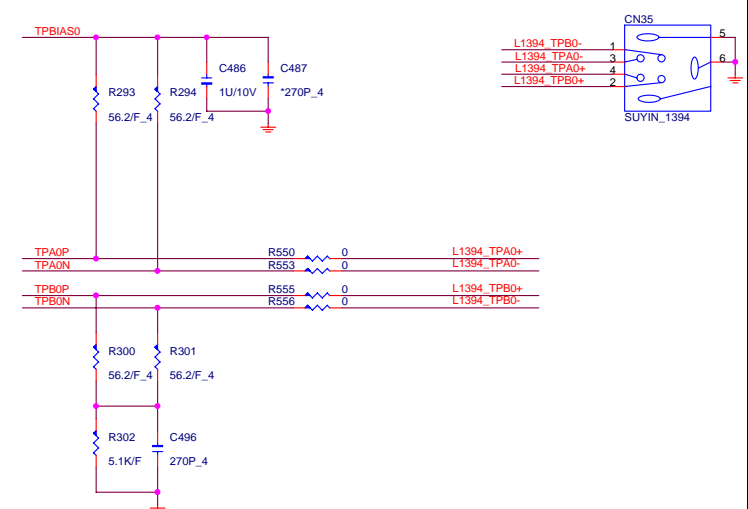
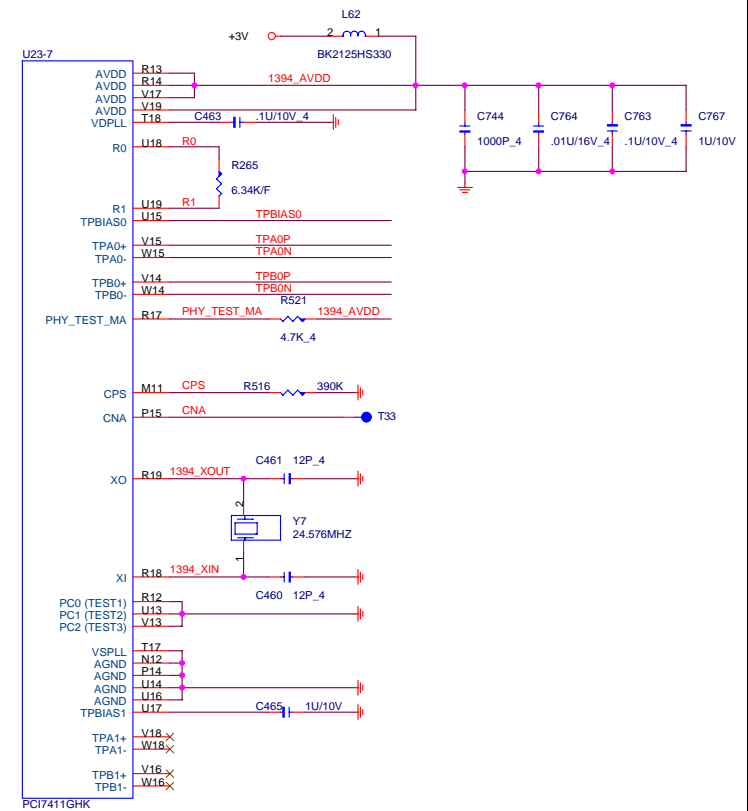
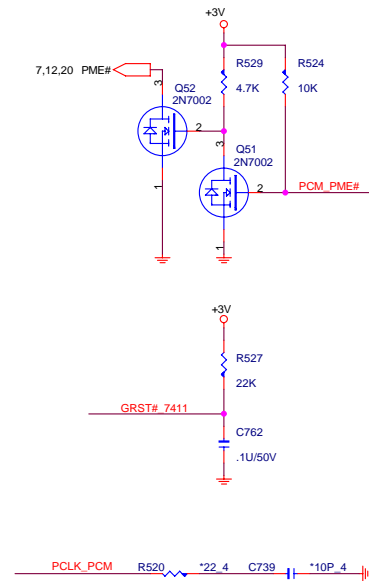
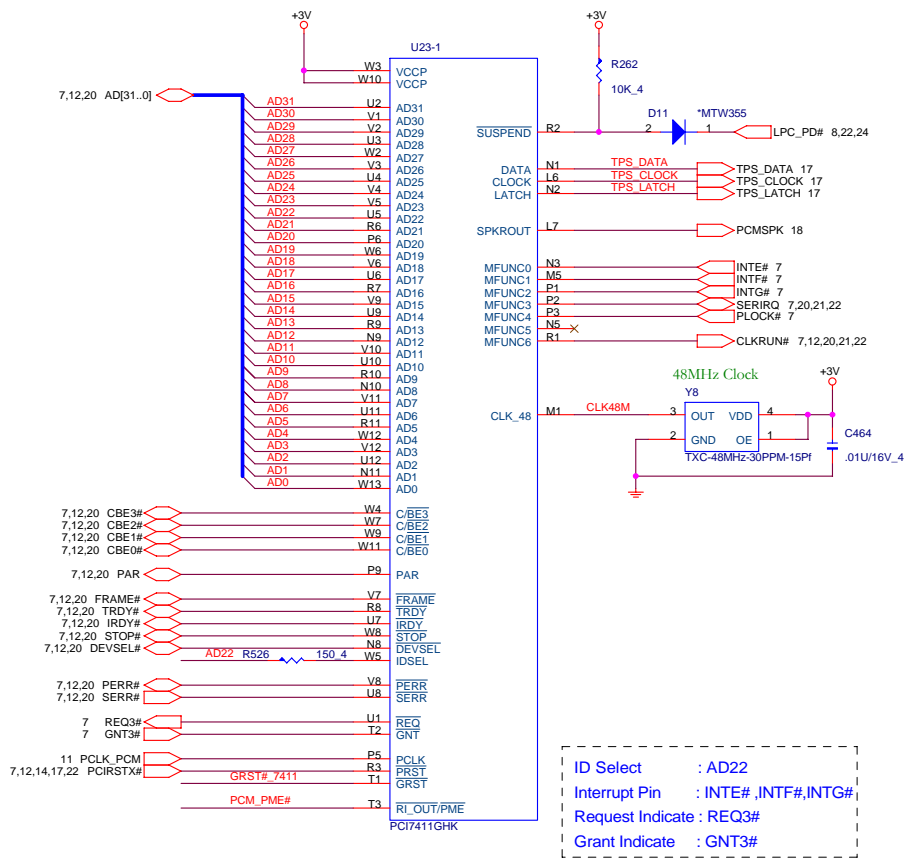


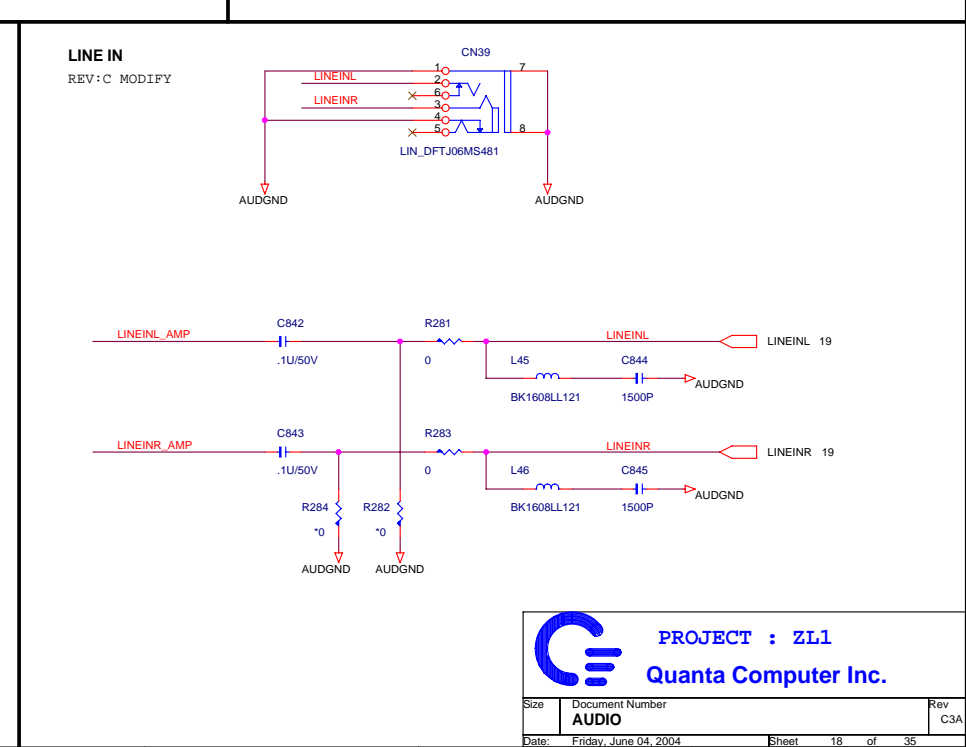
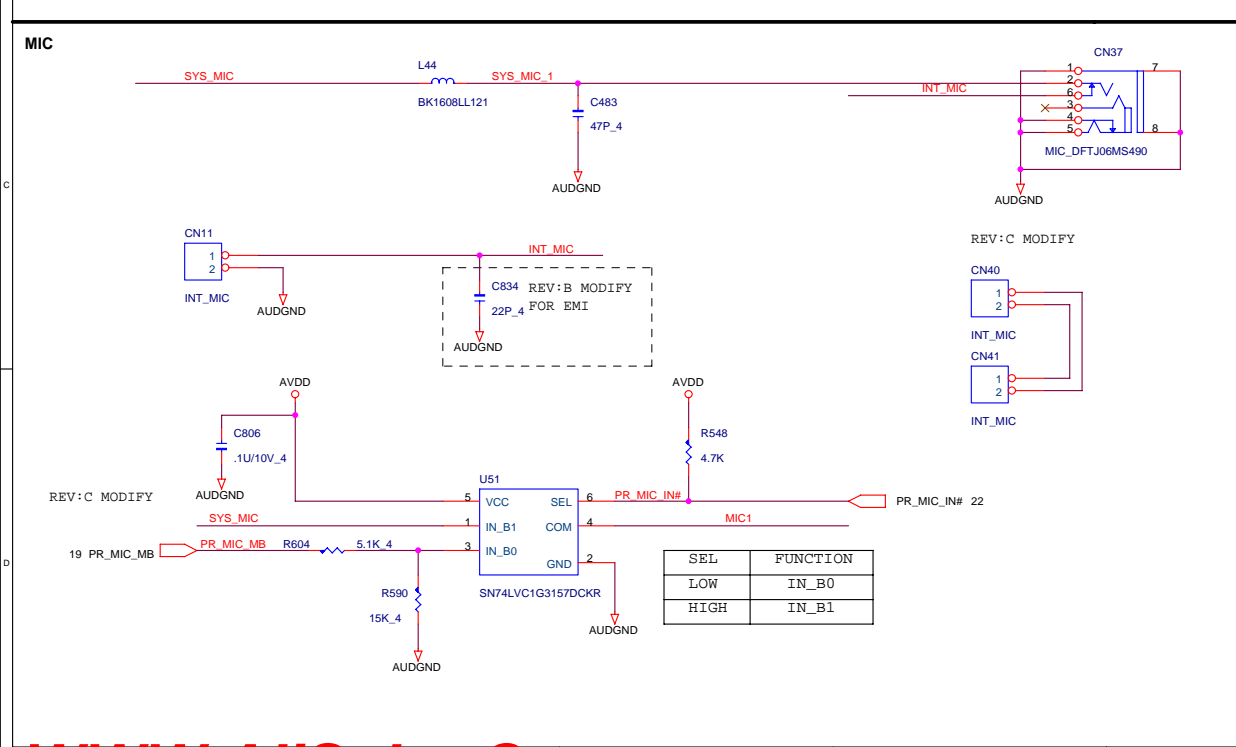
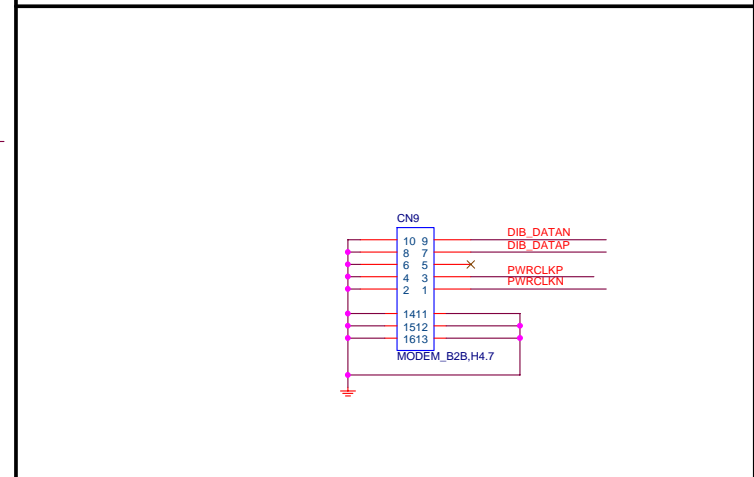
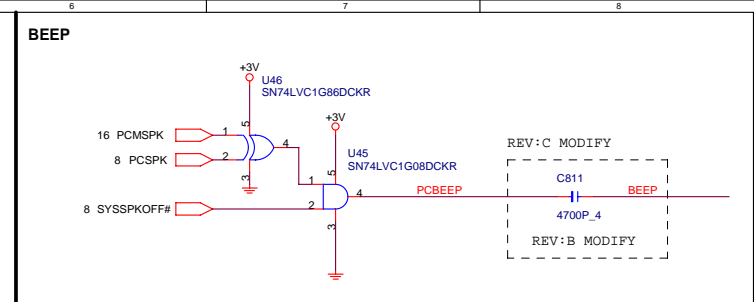
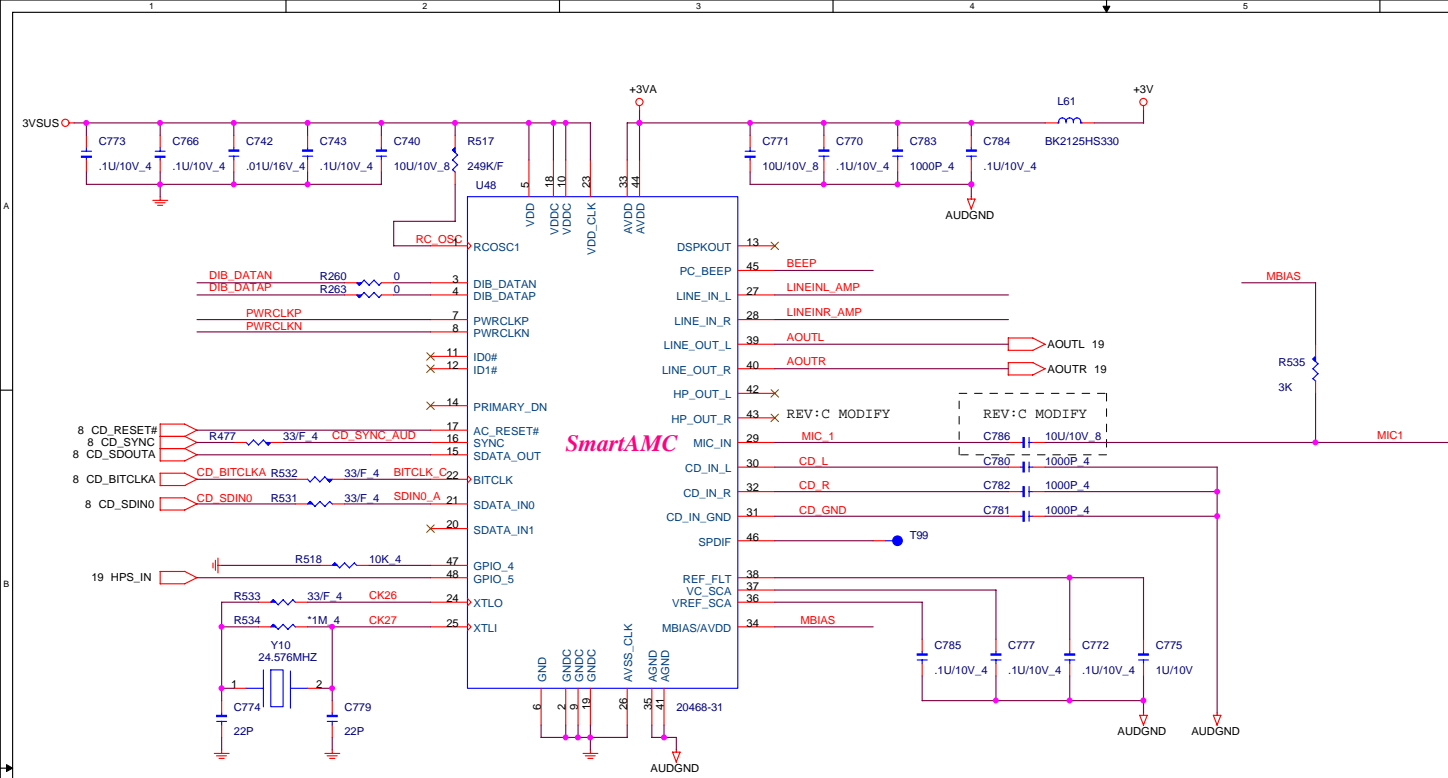
S-VIDEO



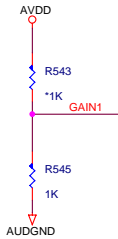
PROJECT : ZL1
Quanta Computer Inc.

Size	Document Number	Rev
	CRT & S-VIDEO	C3A
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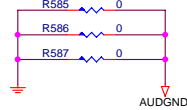
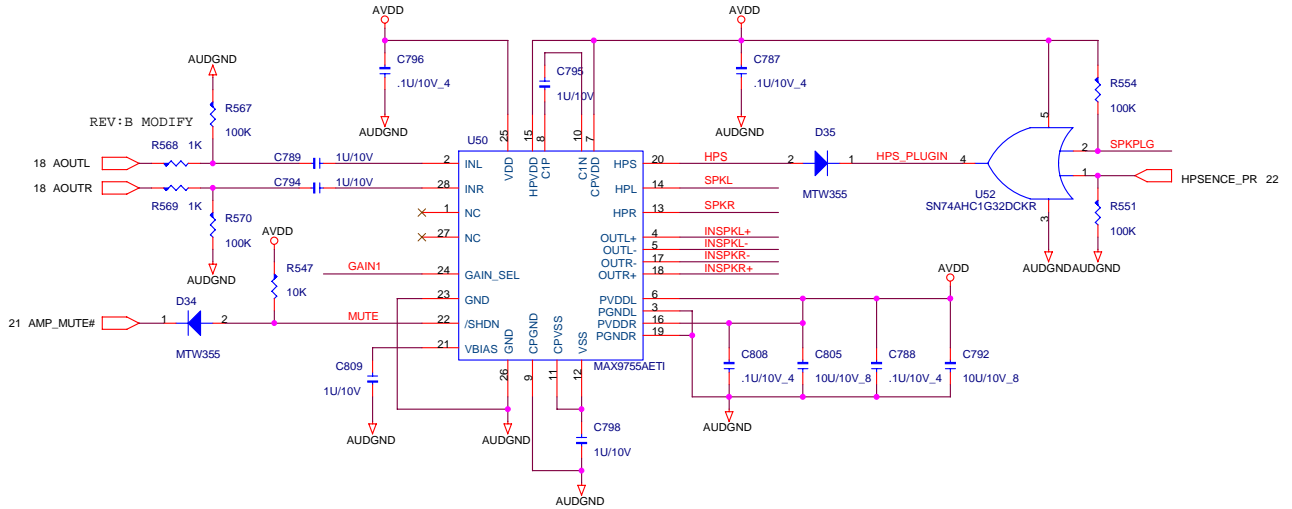
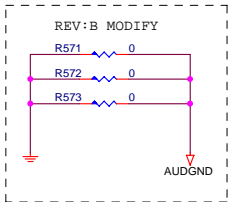
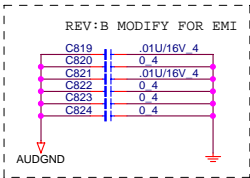
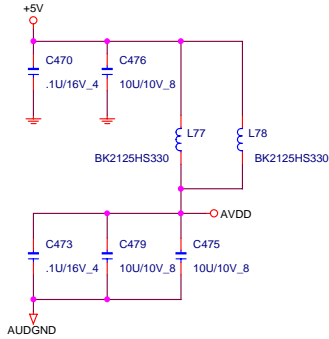




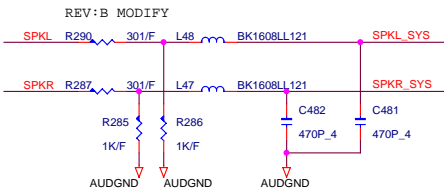
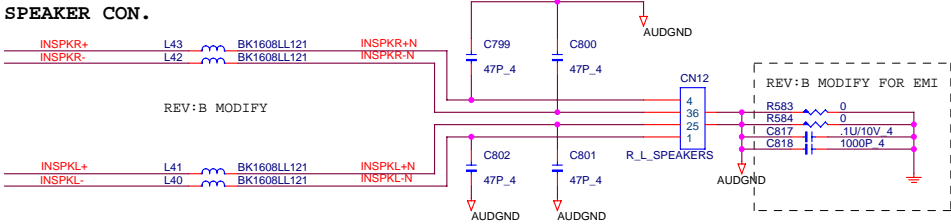
GAIN1	SPKR MODE	HP MODE
0	10.5	3
1	9	0



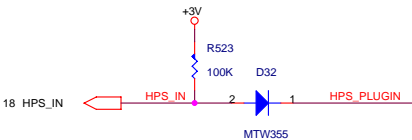
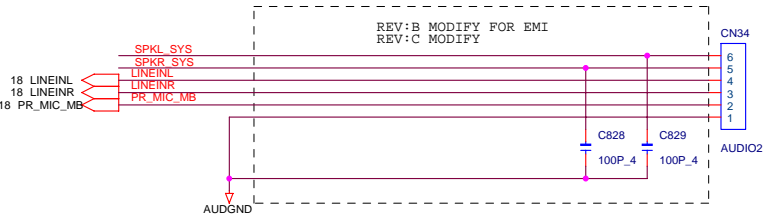
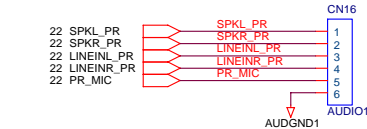
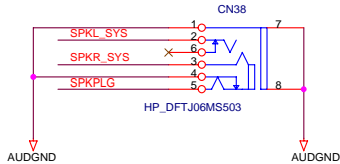
AMP POWER



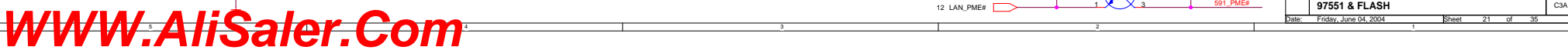
SPEAKER CON.



LINE OUT

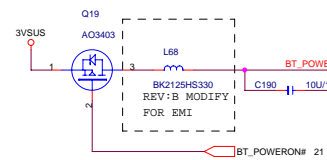


PROJECT : ZL1
Quanta Computer Inc.

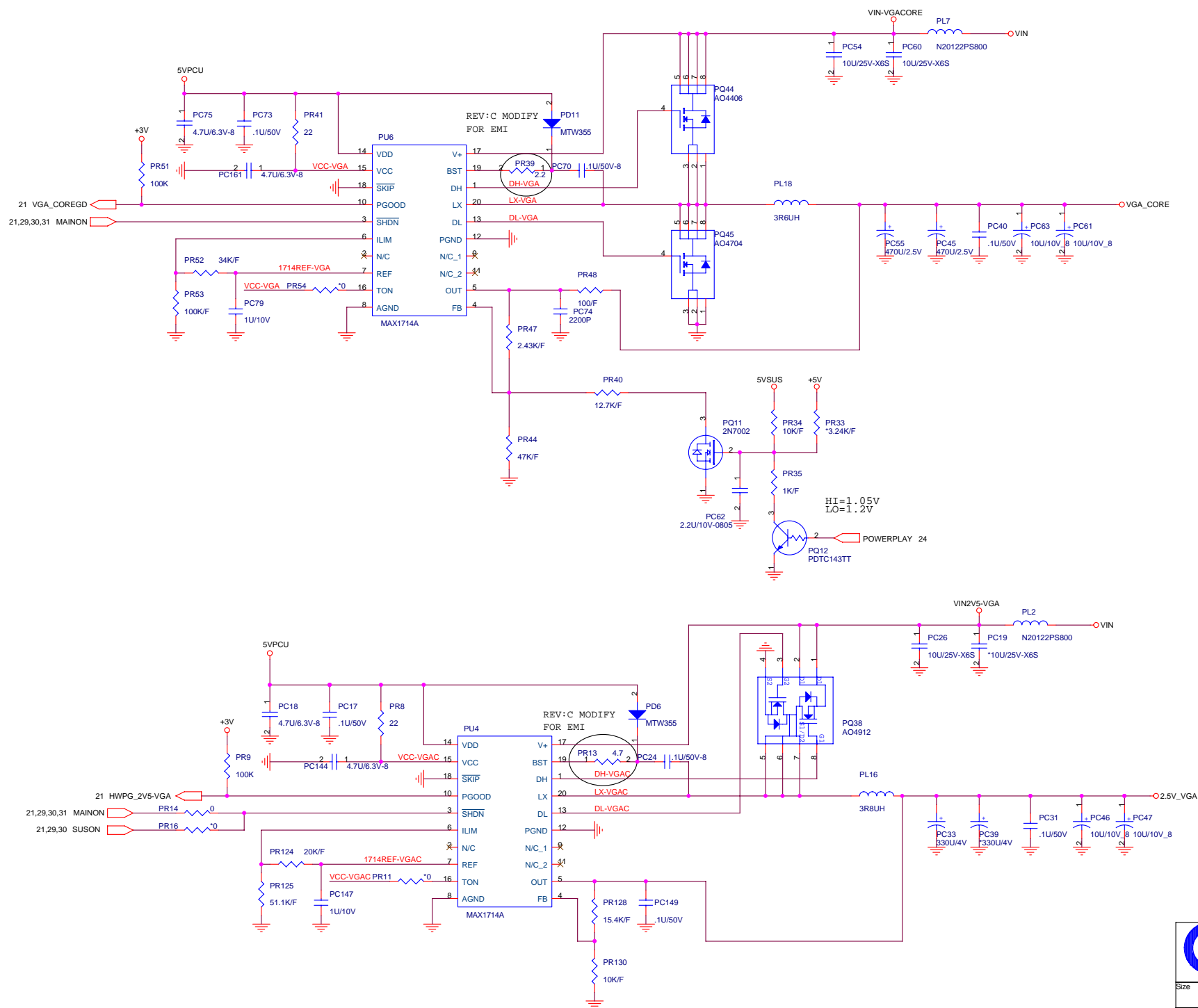
[illegible]

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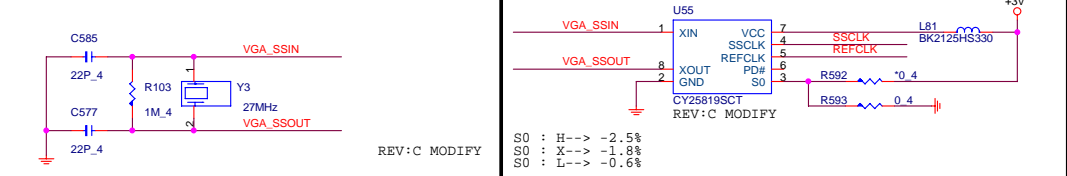
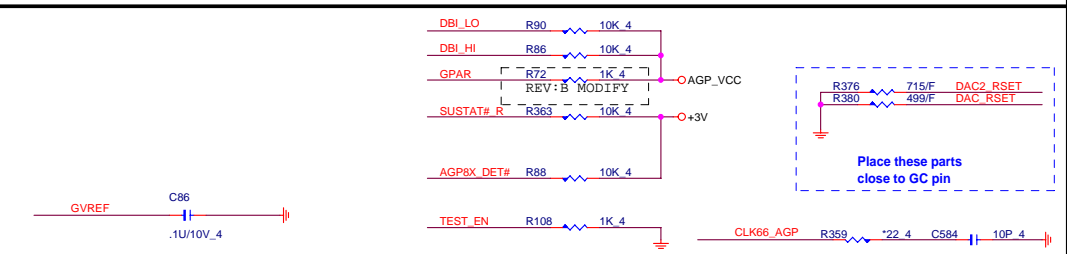
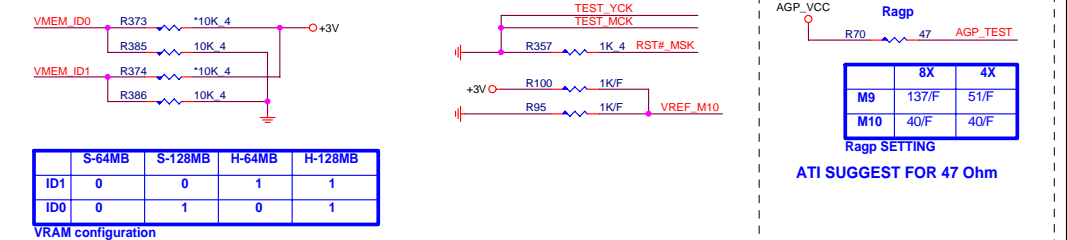
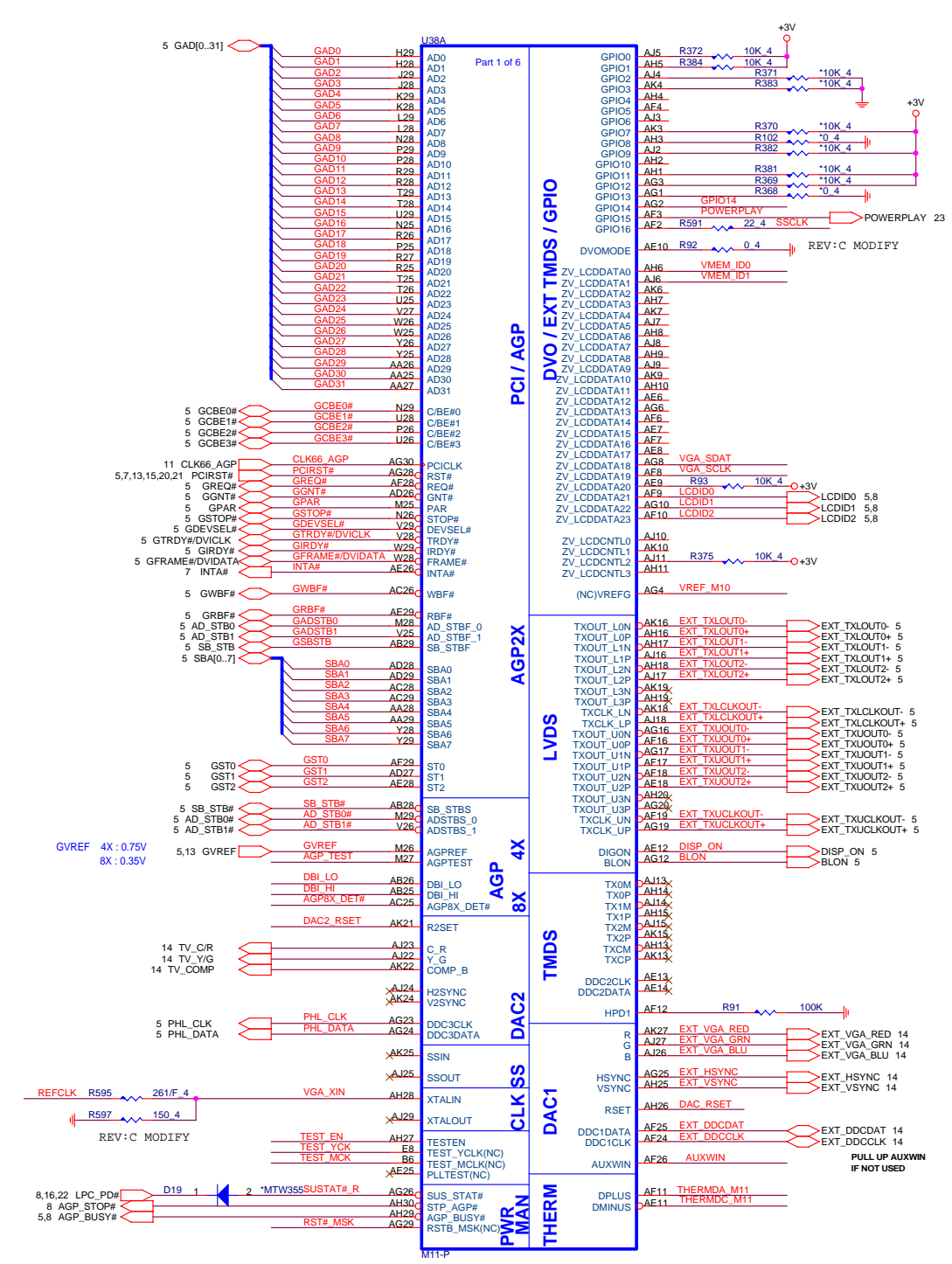
MINI-PCI



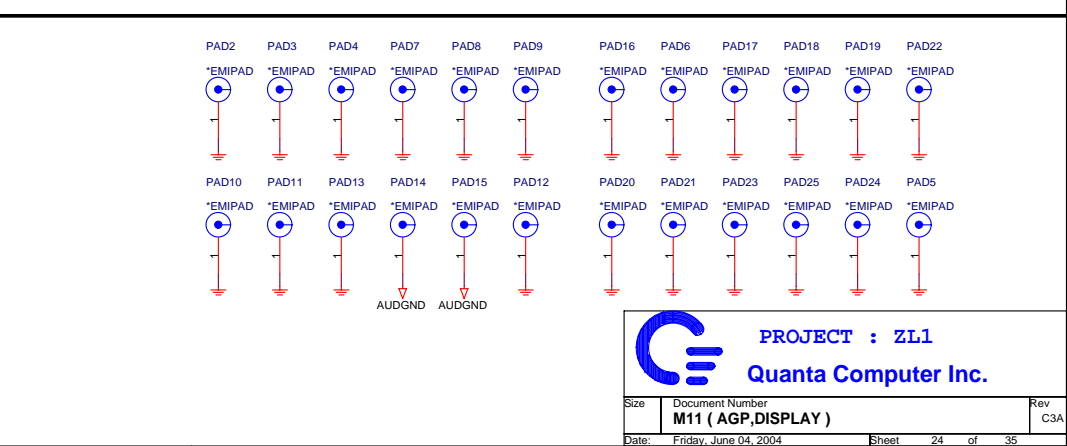
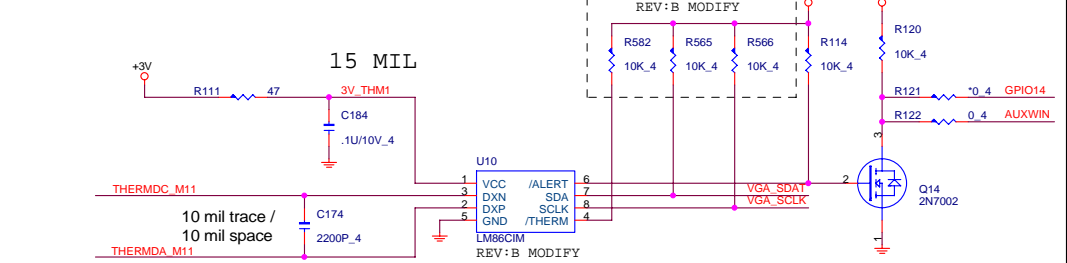
M11 Core & BATTERY



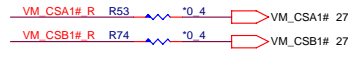
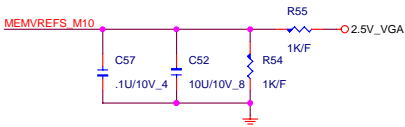
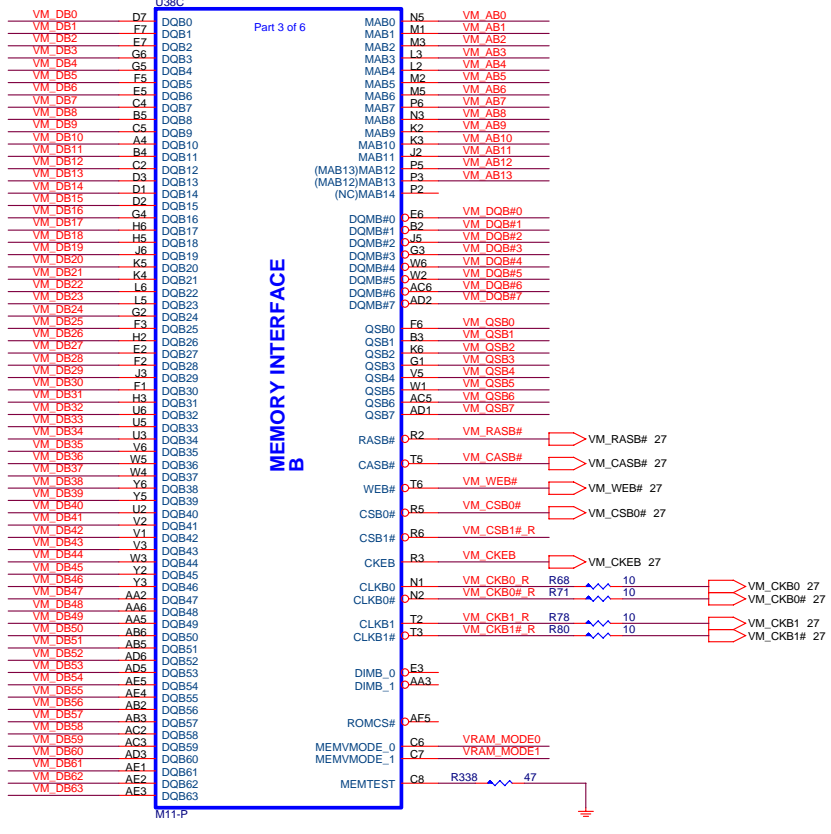
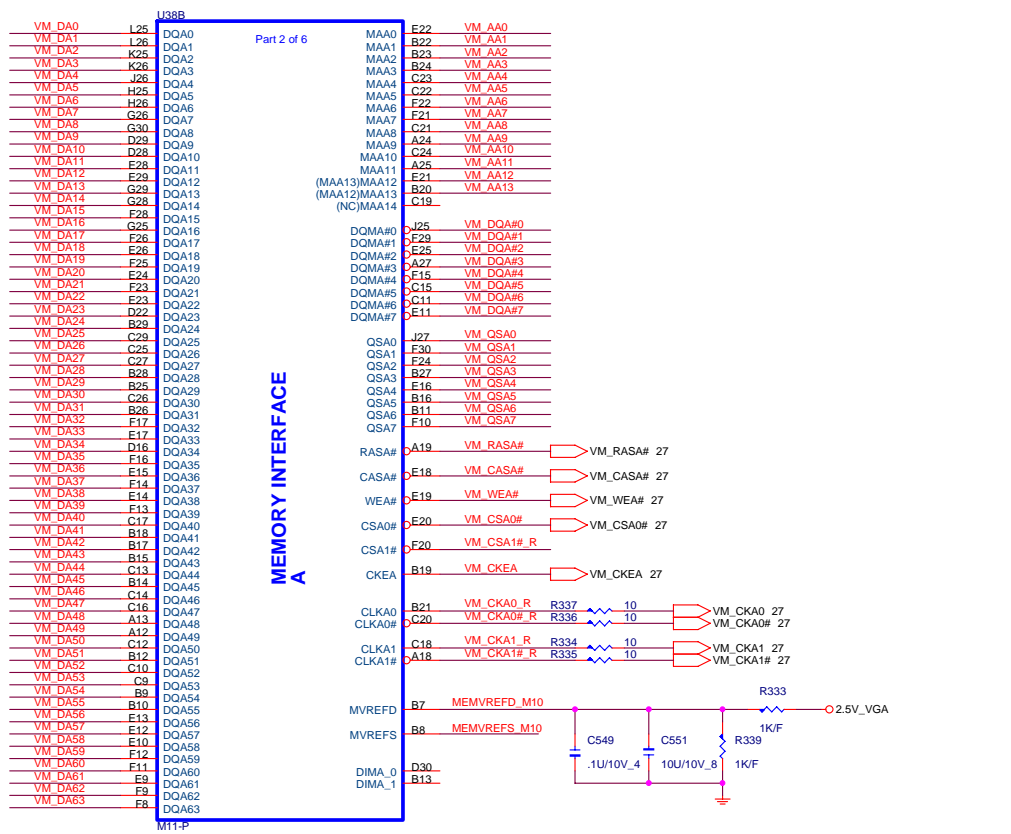
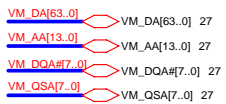
VGA(AGP,DISPLAY)



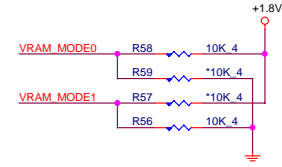
Thermal Monitor



VGA(MEMORY I/F)



Mount these parts when use
hynix 256Mb(8Mx32) VRAM
chip



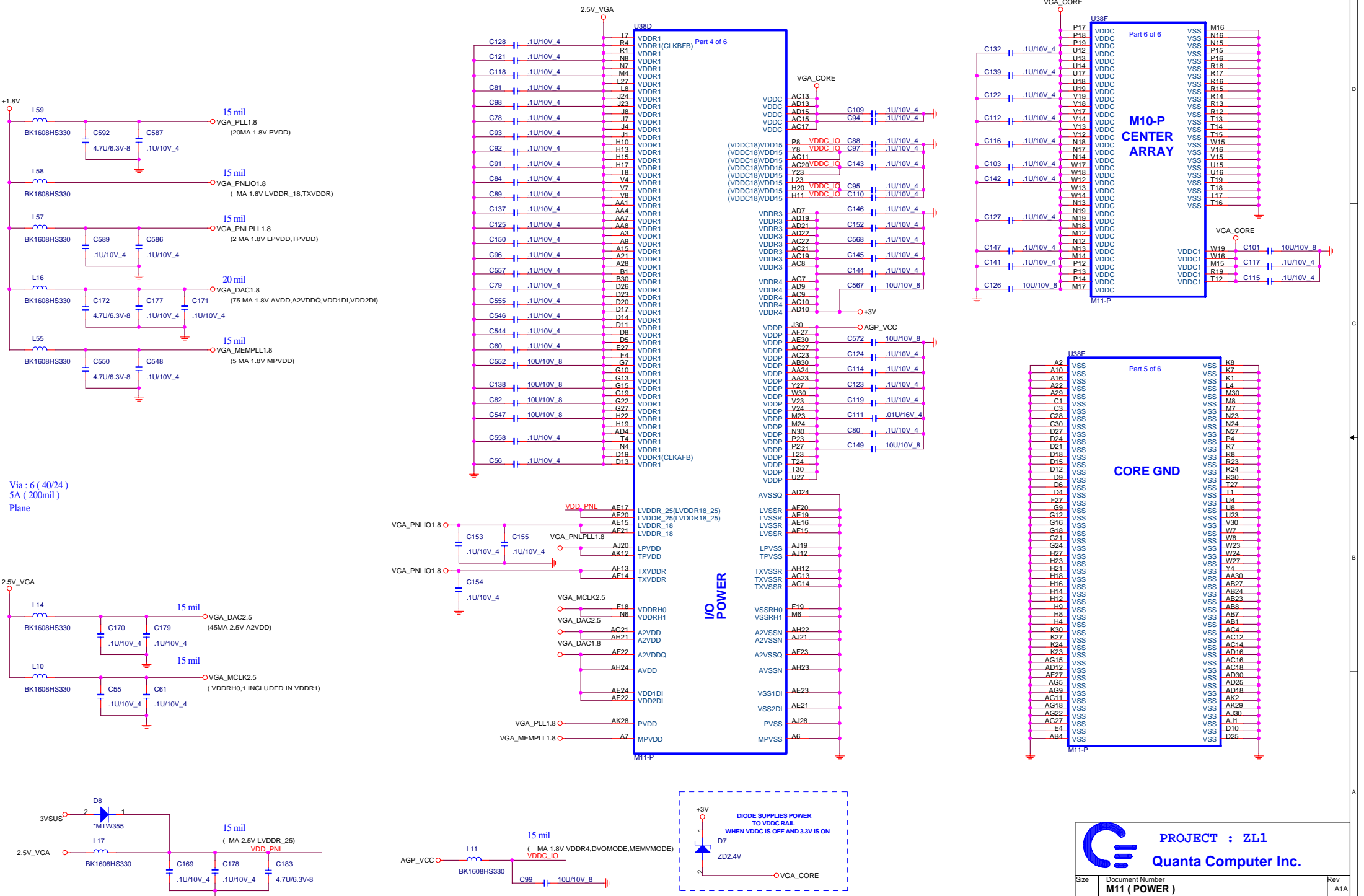
	VRAM_MODE1	VRAM_MODE0
2.5V	0	1
1.8V	1	0
2.8V	1	1

VRAM type setting

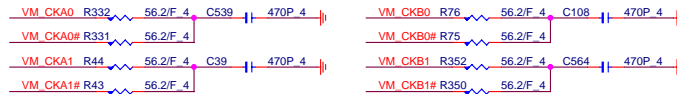


PROJECT : ZL1
Quanta Computer Inc.

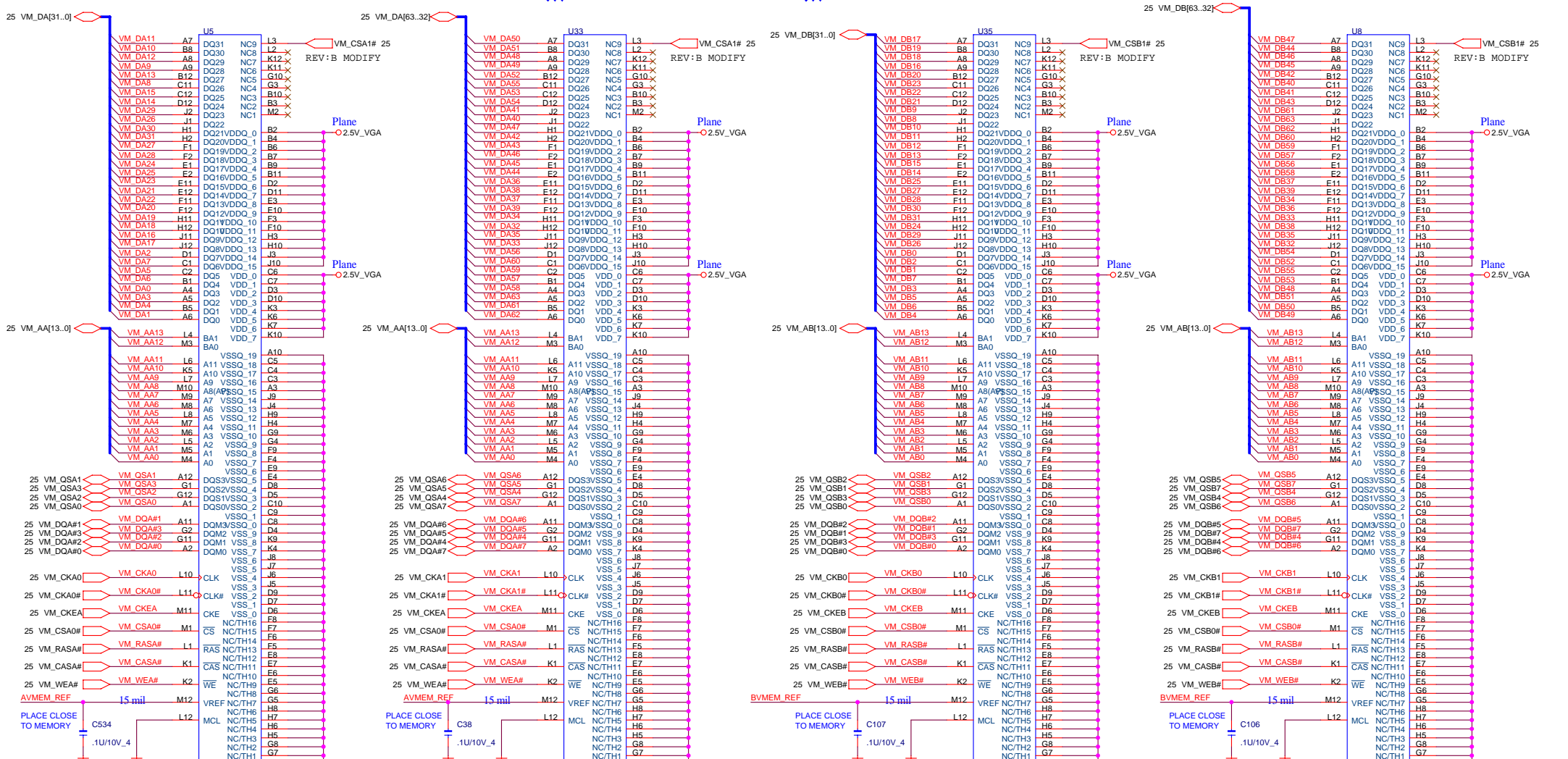
VGA(POWER)



VRAM Channel A



VRAM Channel B

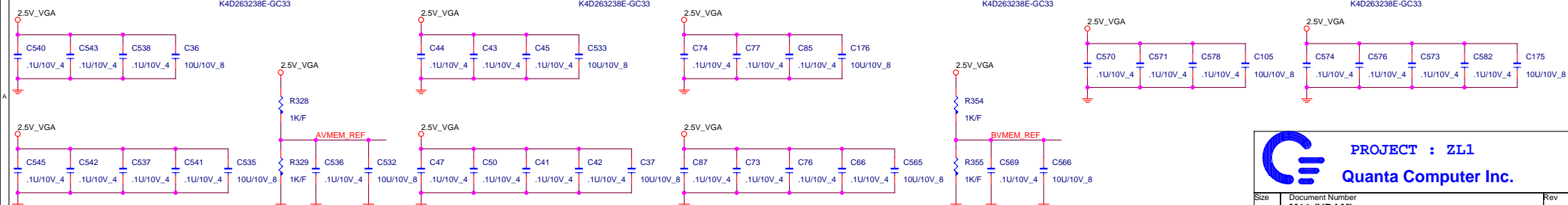


4Mx32 or 8Mx32
K4D263238E-GC33

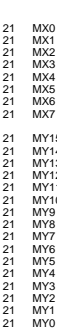
4Mx32 or 8Mx32
K4D263238E-GC33

4Mx32 or 8Mx32
K4D263238E-GC33

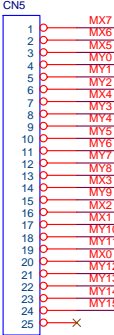
4Mx32 or 8Mx32
K4D263238E-GC33



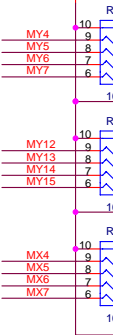
INT K/B



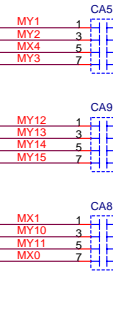
REV:C MODIFY



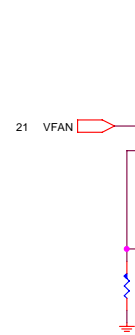
3V_ALWAYS



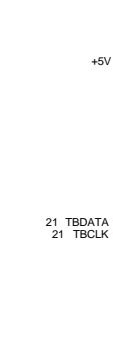
REV:C MODIFY



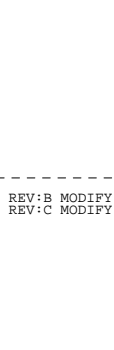
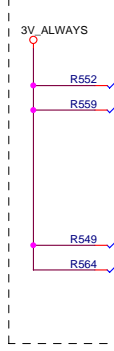
FAN CONTROL



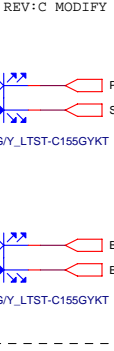
TOUCH PAD



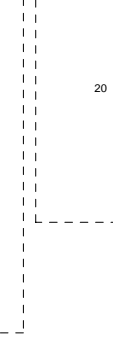
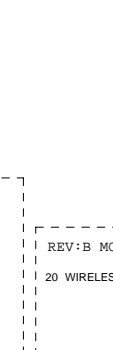
REV:C MODIFY



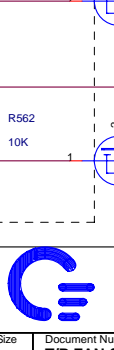
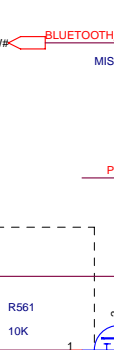
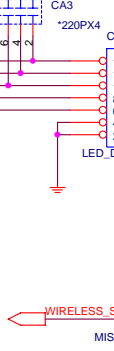
REV:C MODIFY



REV:C MODIFY

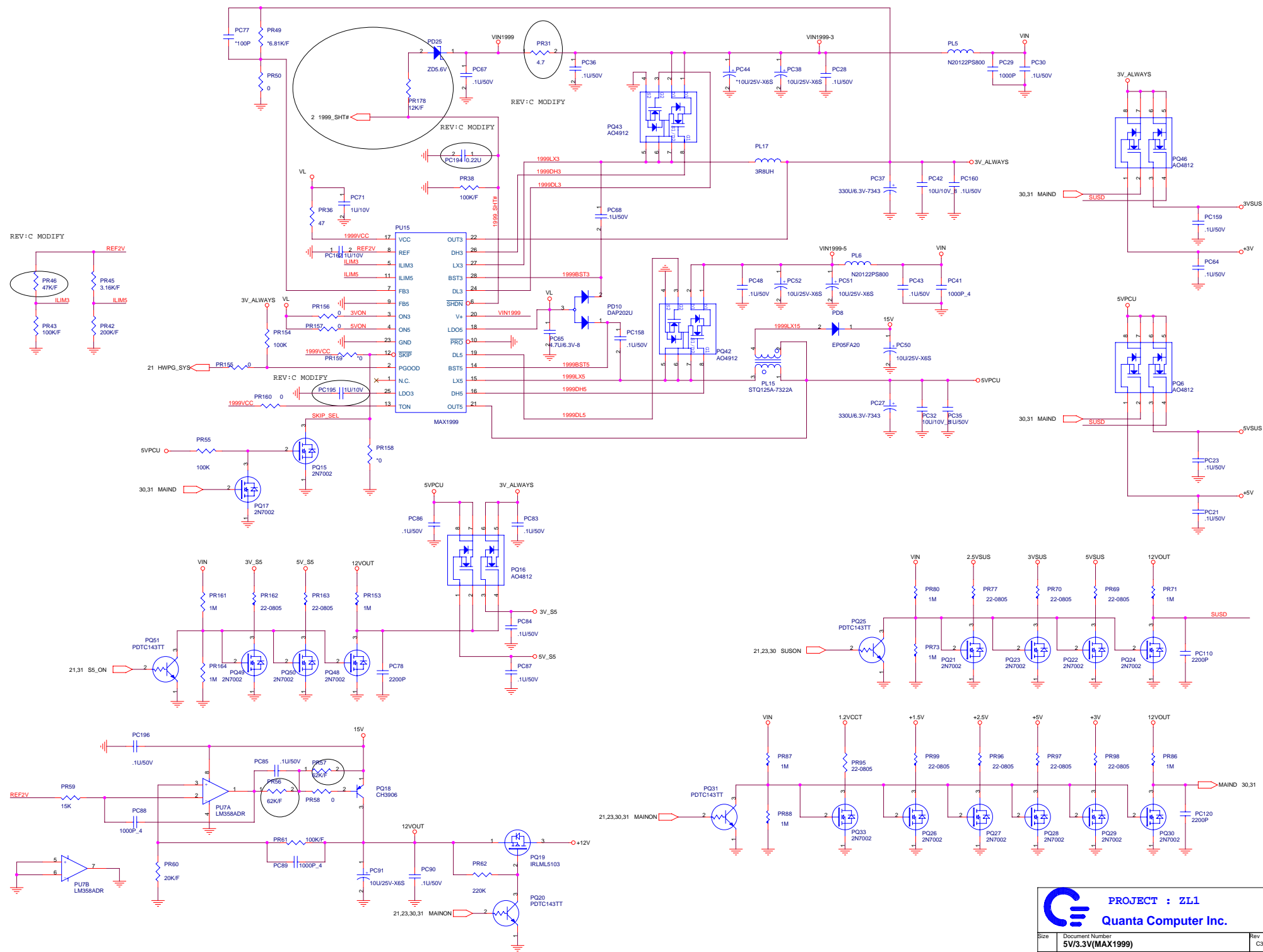


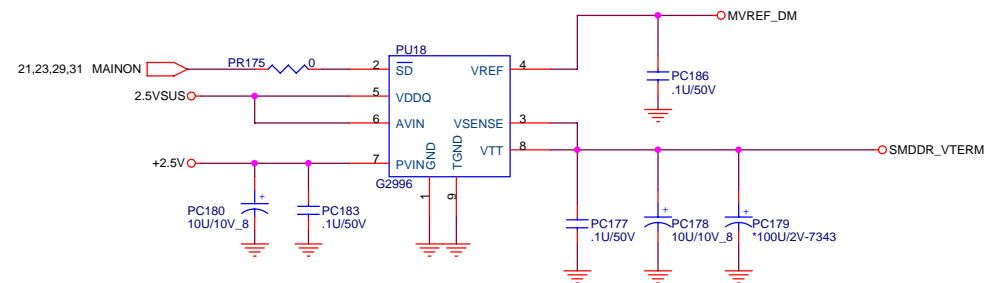
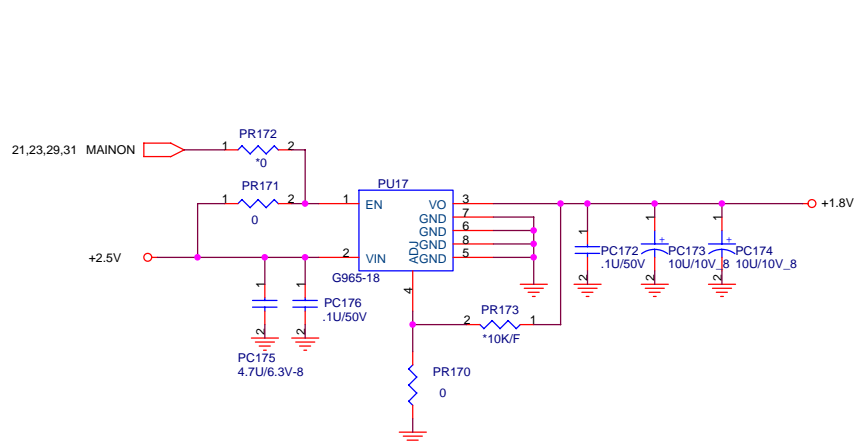
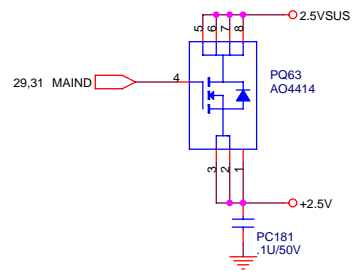
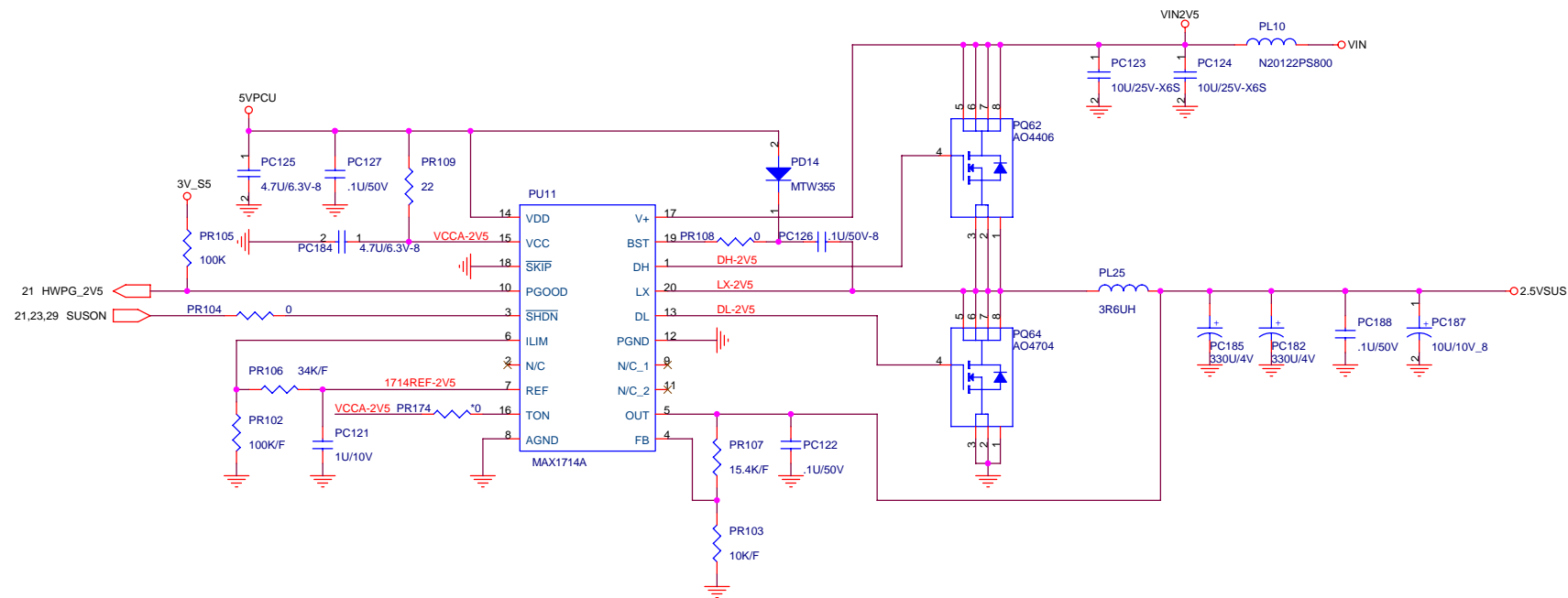
REV:C MODIFY



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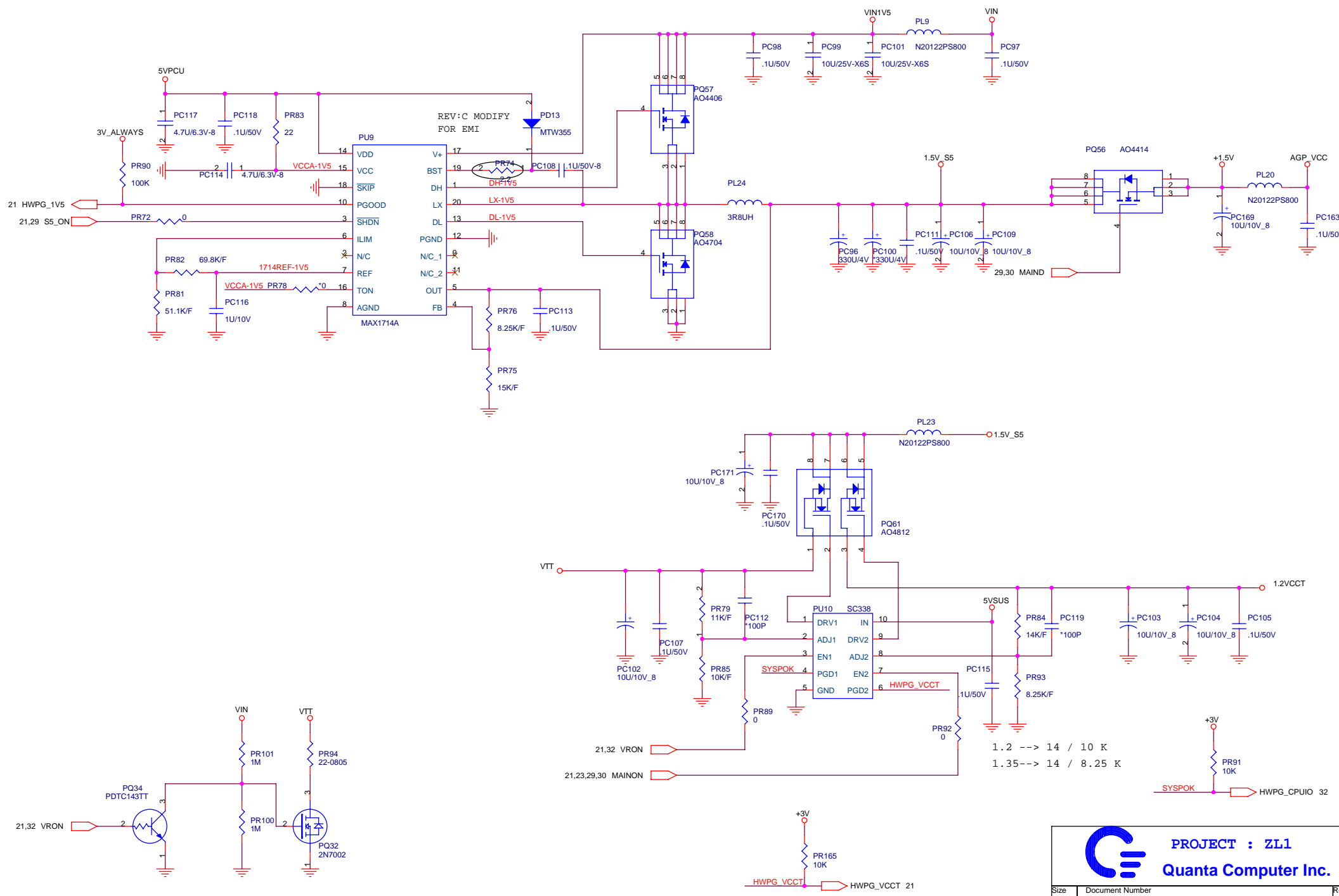
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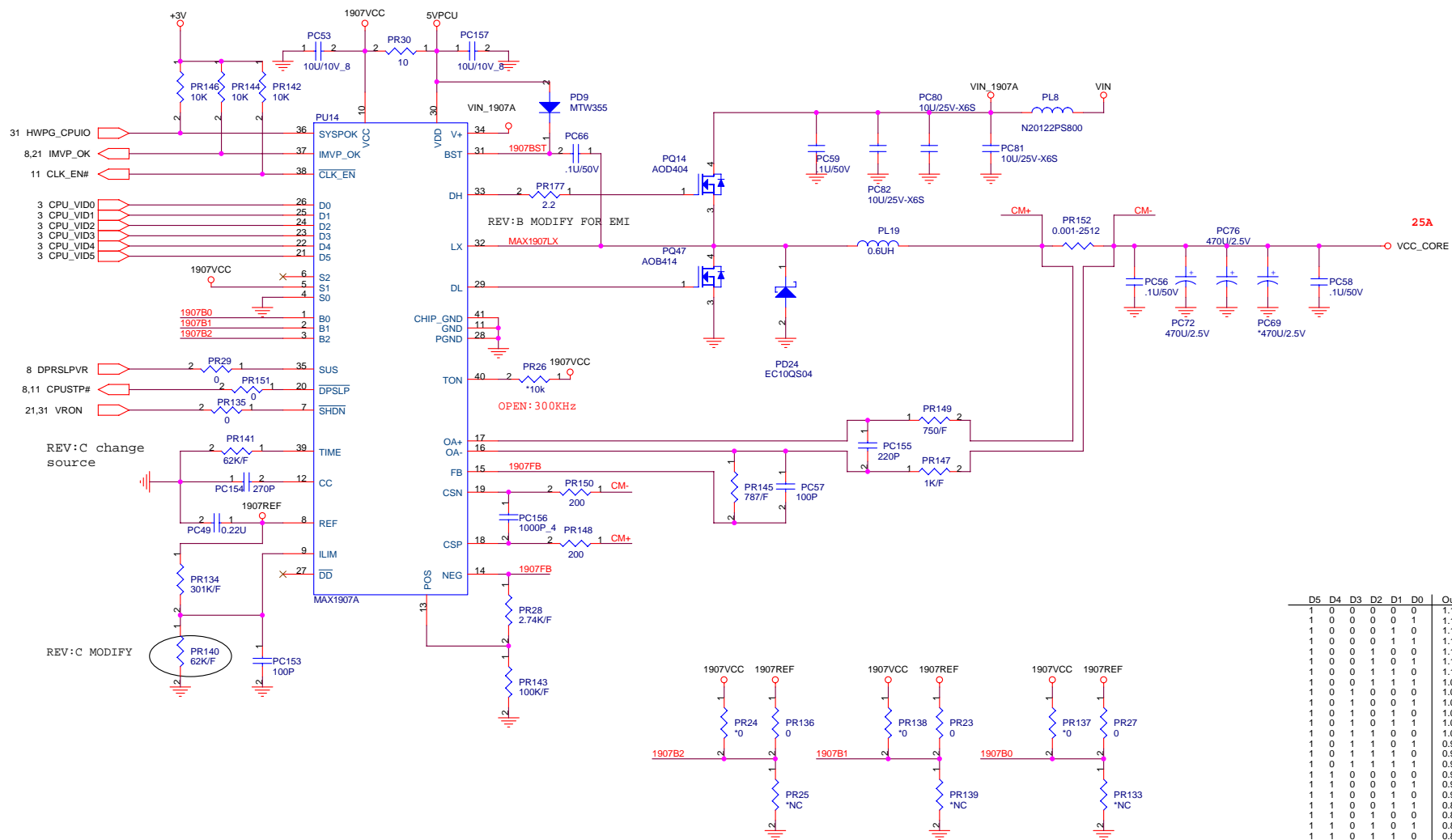
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D5	D4	D3	D2	D1	D0	Output	D5	D4	D3	D2	D1	D0	Output
1	0	0	0	0	0	1.196V	0	0	0	0	0	0	1.708V
1	0	0	0	0	1	1.180V	0	0	0	0	0	1	1.692V
1	0	0	0	1	0	1.164V	0	0	0	0	1	0	1.676V
1	0	0	0	1	1	1.148V	0	0	0	0	1	1	1.660V
1	0	0	1	0	0	1.132V	0	0	0	1	0	0	1.644V
1	0	0	1	0	1	1.116V	0	0	0	1	0	1	1.628V
1	0	0	1	1	0	1.100V	0	0	0	1	1	0	1.612V
1	0	0	1	1	1	1.084V	0	0	0	1	1	1	1.596V
1	0	1	0	0	0	1.068V	0	0	1	0	0	0	1.580V
1	0	1	0	0	1	1.052V	0	0	1	0	0	1	1.564V
1	0	1	0	1	0	1.036V	0	0	1	0	1	0	1.548V
1	0	1	0	1	1	1.020V	0	0	1	0	1	1	1.532V
1	0	1	1	0	0	1.004V	0	0	1	1	0	0	1.516V
1	0	1	1	0	1	0.988V	0	0	1	1	0	1	1.500V
1	0	1	1	1	0	0.972V	0	0	1	1	1	0	1.484V
1	0	1	1	1	1	0.956V	0	0	1	1	1	1	1.468V
1	1	0	0	0	0	0.940V	0	1	0	0	0	0	1.452V
1	1	0	0	0	1	0.924V	0	1	0	0	1	0	1.436V
1	1	0	0	1	0	0.908V	0	1	0	0	1	0	1.420V
1	1	0	0	1	1	0.892V	0	1	0	0	1	1	1.404V
1	1	0	1	0	0	0.876V	0	1	0	1	0	0	1.388V
1	1	0	1	0	1	0.860V	0	1	0	1	0	1	1.372V
1	1	0	1	1	0	0.844V	0	1	0	1	1	0	1.356V
1	1	0	1	1	1	0.828V	0	1	0	1	1	1	1.340V
1	1	1	0	0	0	0.812V	0	1	1	0	0	0	1.324V
1	1	1	0	0	1	0.796V	0	1	1	0	0	1	1.308V
1	1	1	0	1	0	0.780V	0	1	1	0	1	0	1.292V
1	1	1	0	1	1	0.764V	0	1	1	0	1	1	1.276V
1	1	1	1	0	0	0.748V	0	1	1	1	0	0	1.260V
1	1	1	1	0	1	0.732V	0	1	1	1	0	1	1.244V
1	1	1	1	1	0	0.716V	0	1	1	1	1	0	1.228V
1	1	1	1	1	1	0.700V	0	1	1	1	1	1	1.212V

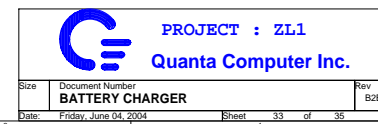
SUSPEND MODE (SUS=HIGH)				
	S2	S1	S0	Output
✓	OPEN	VCC	GND	0.748V

VCC_BOOT				
	B2	B1	B0	Output
✓	GND	GND	GND	1.708V
	REF	REF	REF	1.372V
	OPEN	OPEN	OPEN	1.036V
	VCC	VCC	VCC	0.700V
	REF	VCC	VCC	1.212V

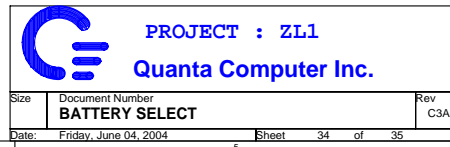


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MODEL:		REV:	CHANGE LIST:	PAGE	FROM	TO
ZL1 MotherBoard	B2B	PAGE2 . 1.UN-STUFF R392 FOR THERMTRIP 2. STUFF NUTS PAGE5 . 1.ADD +3V FOR LCD EDID 2.MODIFY C528 VALUE PAGE8 . 1.STUFF R204 FOR THERMTRIPS PAGE12. 1.ADD 10/100 CO LAYOUT CIRCUIT PAGE13. 1.ADD PR INSERT# FOR SYSTEM CIRCUIT 2.MODIFY LAN SWITCH POWER PAGE14. 1.ADD PR INSERT# FOR SYSTEM CIRCUIT PAGE15. 1.ADD RBAYID1 PULL DOWN RESISTOR PAGE18. 1.MODIFY FOR AUDIO QUALITY PAGE19. 1.MODIFY FOR AUDIO QUALITY PAGE20. 1.ADD EMI SOLUTION PAGE22. 1.MODIFY PR INSERT# PULL HIGH VOLTAGE PAGE23. 1.STUFF PC45 FOR VGA CORE 2. MODIFY 2.5V VGA POWER PAGE24. 1.MODIFY GPAR PULL HIGH RESISTOR 2. ADD PULL HIGH FOR I2C BUS PAGE27. 1.MODIFY CS PIN FOR 128MB VRAM PAGE28. 1.MODIFY LED CIRCUIT 2. MODIFY Q34 TO 2N7002 PAGE29. 1.MODIFY MAX6648_OV# CIRCUIT PAGE32. 1.MODIFY CPU CORE VOLTAGE LEVEL PAGE33. 1.MODIFY CHARGER CIRCUIT PAGE34. 1.MODIFY CHARGER CIRCUIT	1	A1A		
			2	B2B	C3A	
			3	A1A		
			4	A1A		
			5	B2B		
			6	A1A		
			7	A1A		
			8	B2B		
			9	A1A		
			10	A1A		
	C3A	PAGE02. 1.MODIFY MAX6648_AL# TO HIGH ACTIVE PAGE11. 1.MODIFY FOR SIGNAL QUALITY PAGE12. 1.MODIFY LAN LED 2. MODIFY LAN TRACE PULL HIGH PAGE13. 1.MODIFY LAN LED 2. MODIFY DVO_AVDD PAGE14. 1.MODIFY RGB BEAD VALUE PAGE16. 1.REMOVE COMMON MODE CHOKE PAGE17. 1.MODIFY 3 IN 1 CARD READER FOOTPRINT PAGE18. 1.MODIFY LINE IN SIGNAL PAGE19. 1.MODIFY LINE IN SIGNAL PAGE20. 1.MODIFY USB COMMON MODE CHOKE 2. MODIFY MINI-PCI 3V_S5 TO 3VSUS PAGE21. 1.MODIFY 97551 PIN 21 PAGE22. 1.MODIFY U30 PIN23 TO +5V PAGE23. 1.MODIFY FOR EMI SOLUTION PAGE24. 1.ADD EMI SOLUTION PAGE28. 1.MODIFY PWLED2 TO +3V 2. ADD TP ESD PROTECT 3. MODIFY LED CIRCUIT PAGE29. 1.MODIFY FOR POWER SUHTDOWN ISSUE PAGE31. 1.MODIFY FOR EMI SOLUTION PAGE32. 1.CHANGE SOURCE PAGE34. 1.MODIFY CHARGER CIRCUIT FOR COST DOWN	11	A1A	C3A	
			12	B2B	C3A	
			13	B2B	C3A	
			14	B2B	C3A	
			15	B2B		
			16	A1A	C3A	
			17	A1A	C3A	
			18	B2B	C3A	
			19	B2B	C3A	
			20	B2B	C3A	
			21	A1A	C3A	
			22	B2B	C3A	
			23	B2B	C3A	
			24	B2B	C3A	
			25	A1A		
			26	A1A		
			27	B2B		
			28	B2B	C3A	
			29	B2B	C3A	
			30	A1A		
			31	A1A	C3A	
			32	B2B	C3A	
			33	B2B		
			34	B2B	C3A	
			35	B2B	C3A	



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CHANGE LIST

MB ASSY'S P/N : 31ZL1MB0004

APPROVE BY: JIM HSU

PROJECT LEADER: JIM HSU

DRAWING BY:JACKY CHENG

DOCUMENT NO: 204

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