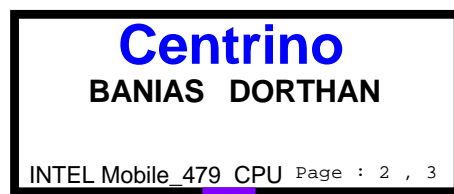
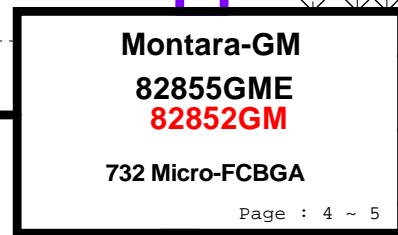


# ED1

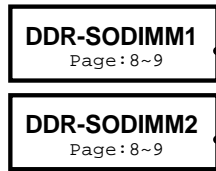


HA#[0..31],  
HD#[0..63]

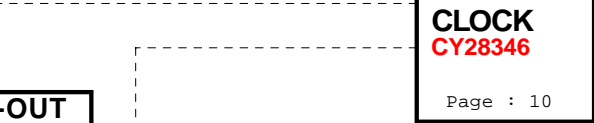
**HOST  
BUS**



CLK\_SDRAM0~5,  
CLK\_SDRAM0~5#



266MHZ DDR



HCLK\_CPU, HCLK\_CPU#

CLK66\_MCH

HCLK\_MCH, HCLK\_MCH#

DREFCLK48

CLK66\_ICH

CLK66\_ICH

HUB I/F 66(266)MHZ

33MHZ, 3.3V  
PCI BUS

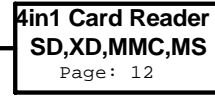
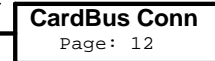
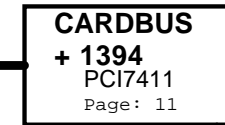
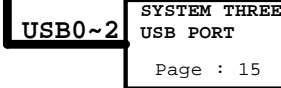
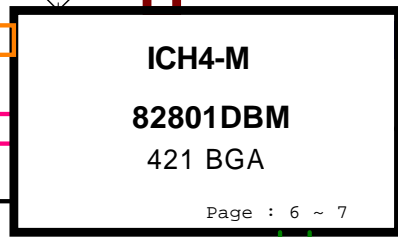
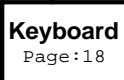
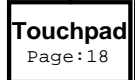
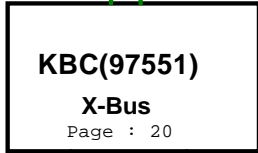
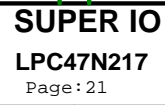
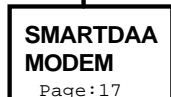
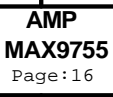
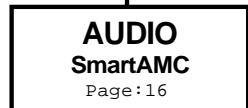


ATA 66/100



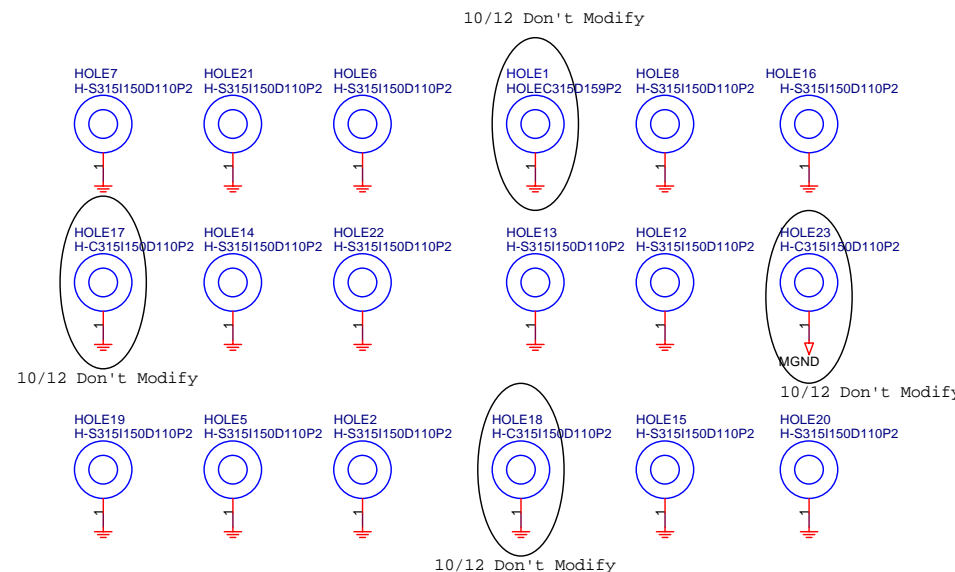
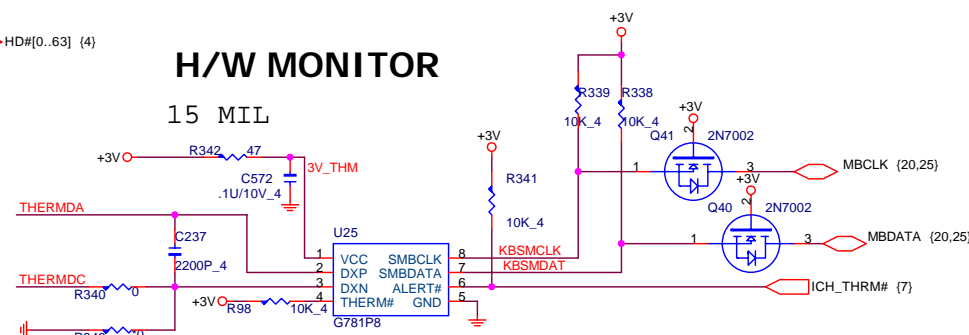
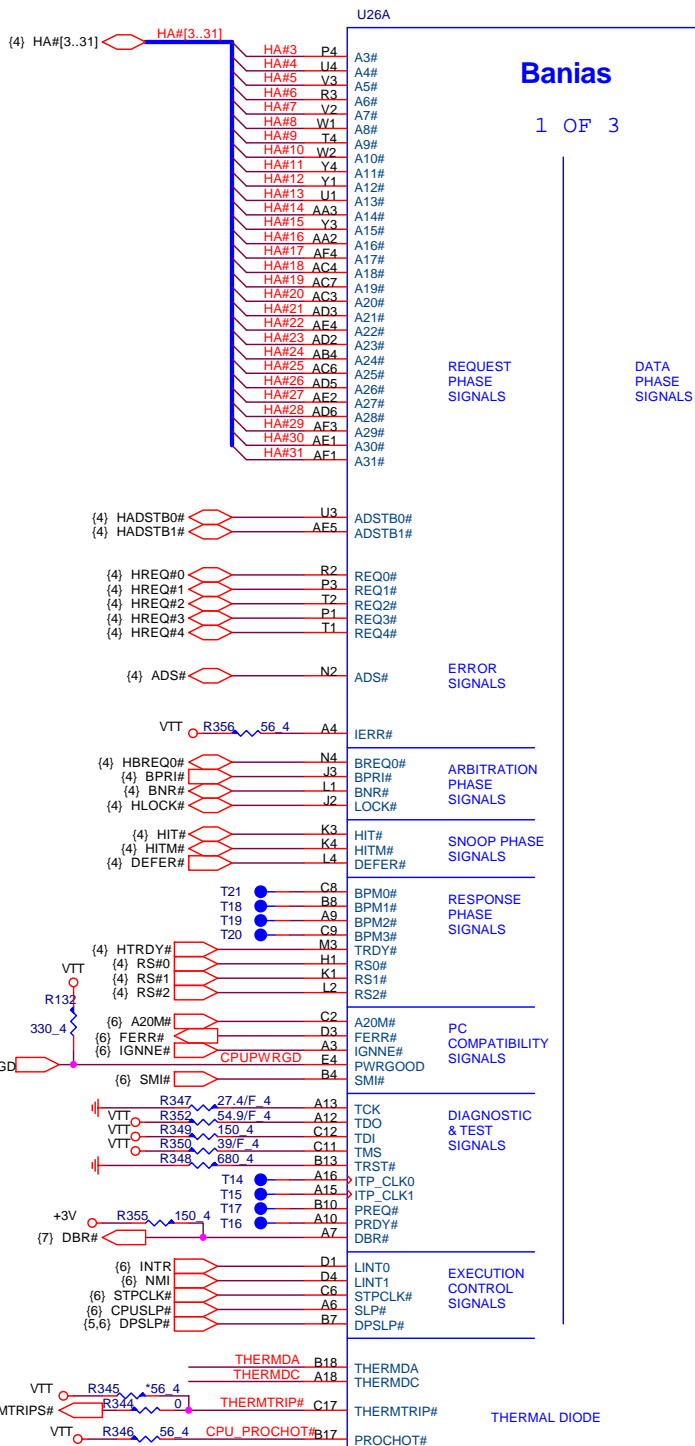
ATA 66/100

AC97

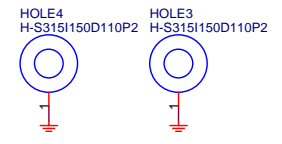


**PROJECT : ED1**  
**Quanta Computer Inc.**

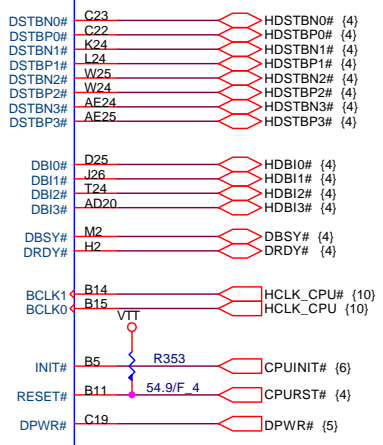
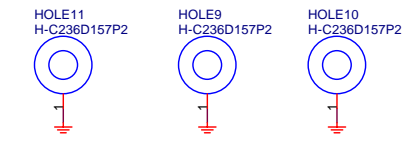
Size	Document Number	Rev
	<b>BLOCK DIAGRAM</b>	13H
Date:	Thursday, November 25, 2004	Sheet 1 of 25



**TV-Board fixing Nut**



**CPU SOCKET**



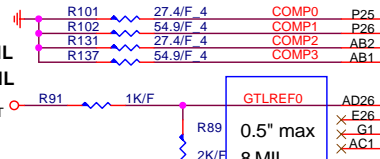
**PROJECT : ED1**  
**Quanta Computer Inc.**

Size: \_\_\_\_\_ Document Number: **CPU ( HOST )** Rev: I3H

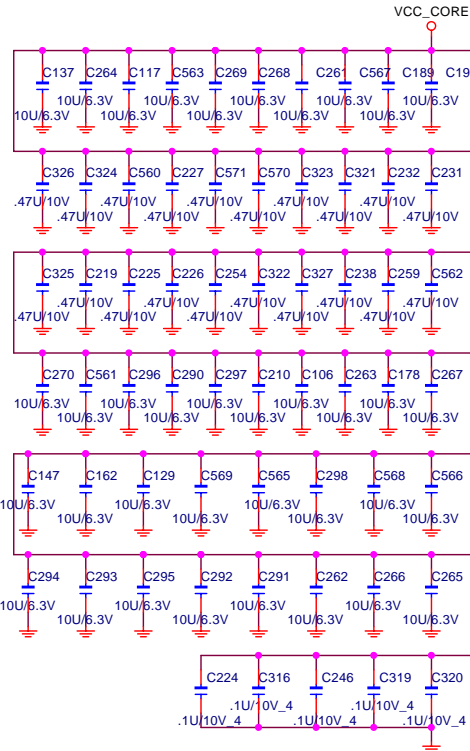
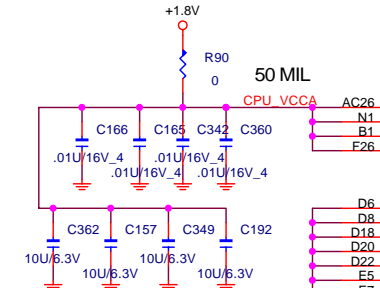
Date: Thursday, November 25, 2004 Sheet: 2 of 25

COMP0 ~ 4  
max length  
500 MIL

27.4/F\_4 with 18 MIL  
54.9/F\_4 with 5 MIL



PLACE one 10U & one 0.01U for each VCCA pin

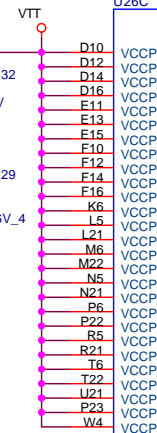
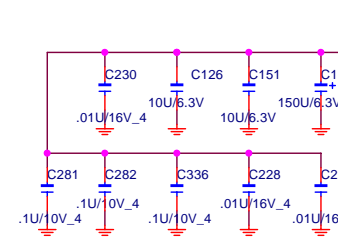
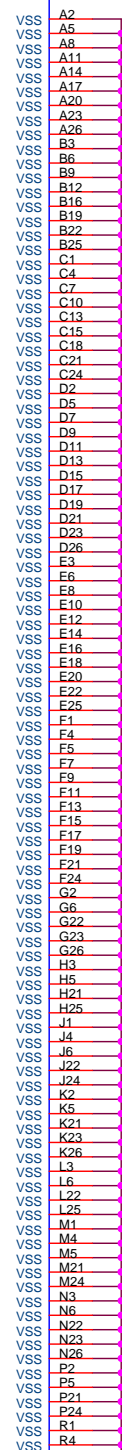


U26B

COMP0
COMP1
COMP2
COMP3

**Banias**  
2 OF 3

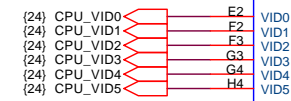
POWER,  
GROUND,  
RESERVED  
SIGNALS



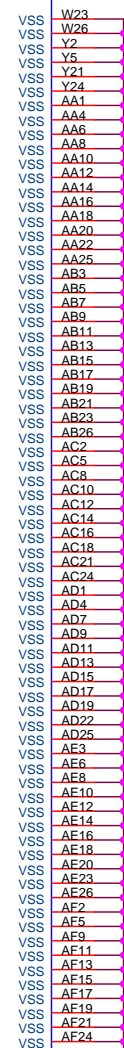
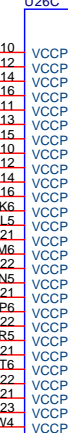
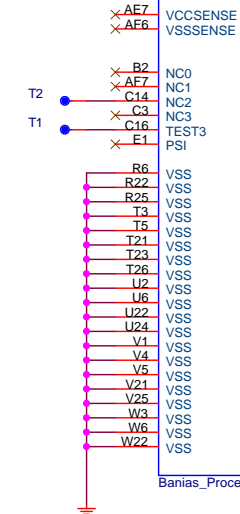
**Banias**

3 OF 3

10 POWER, GROUND AND NC  
11



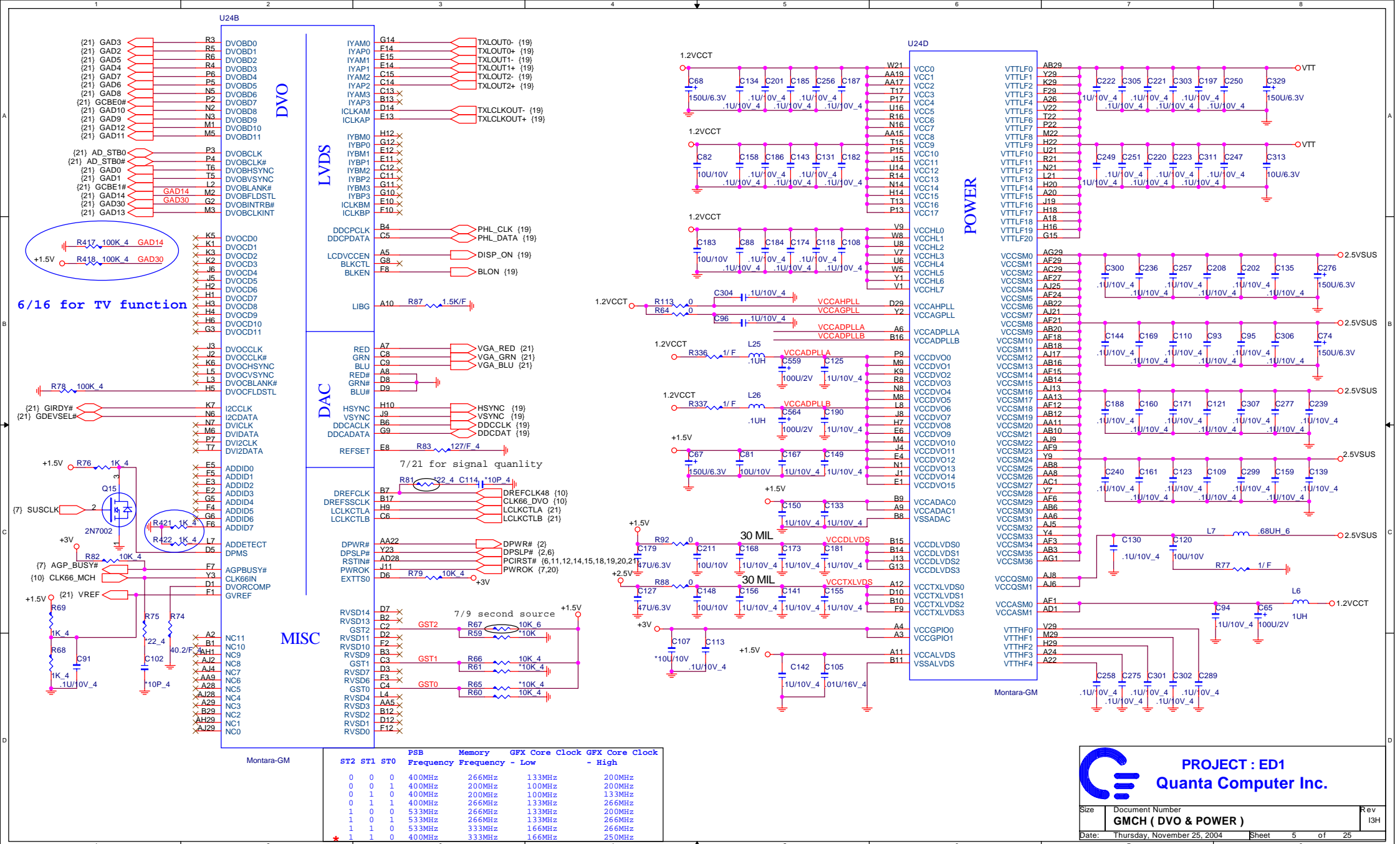
VID



**PROJECT : ED1**  
**Quanta Computer Inc.**

Size	Document Number <b>CPU ( POWER )</b>	Rev I3H
Date:	Thursday, November 25, 2004	Sheet 3 of 25









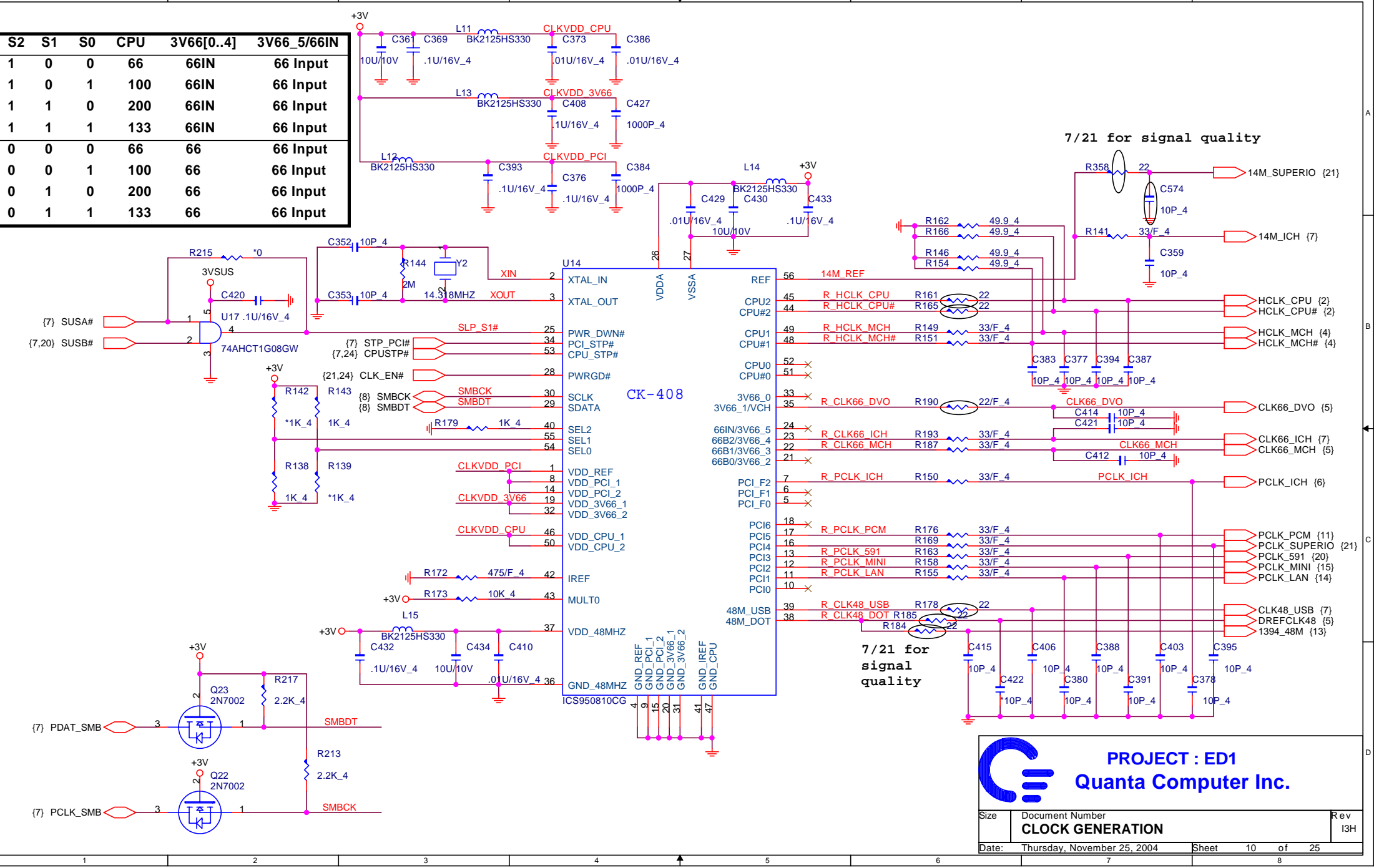









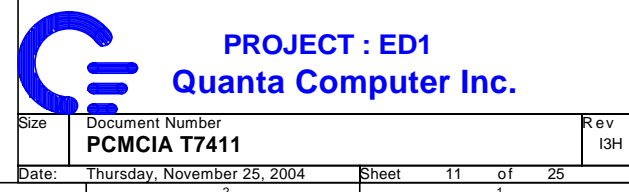
S2	S1	S0	CPU	3V66[0..4]	3V66_5/66IN
1	0	0	66	66IN	66 Input
1	0	1	100	66IN	66 Input
1	1	0	200	66IN	66 Input
1	1	1	133	66IN	66 Input
0	0	0	66	66	66 Input
0	0	1	100	66	66 Input
0	1	0	200	66	66 Input
0	1	1	133	66	66 Input





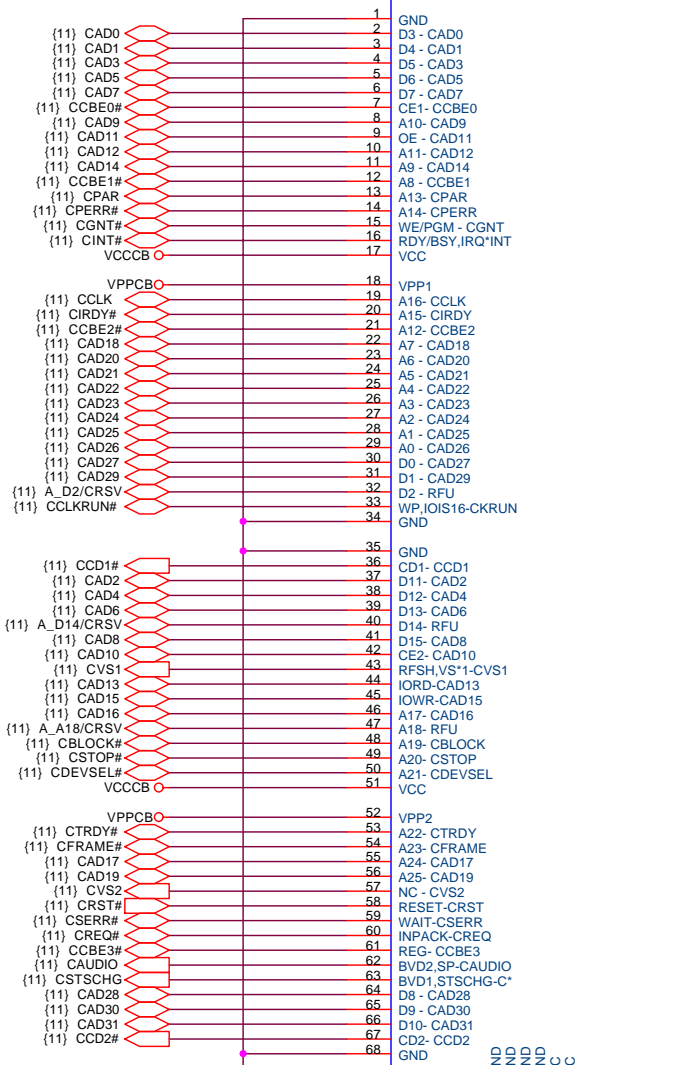
**PROJECT : ED1**  
**Quanta Computer Inc.**

Size	Document Number	Rev
	<b>CLOCK GENERATION</b>	I3H
Date:	Thursday, November 25, 2004	Sheet 10 of 25



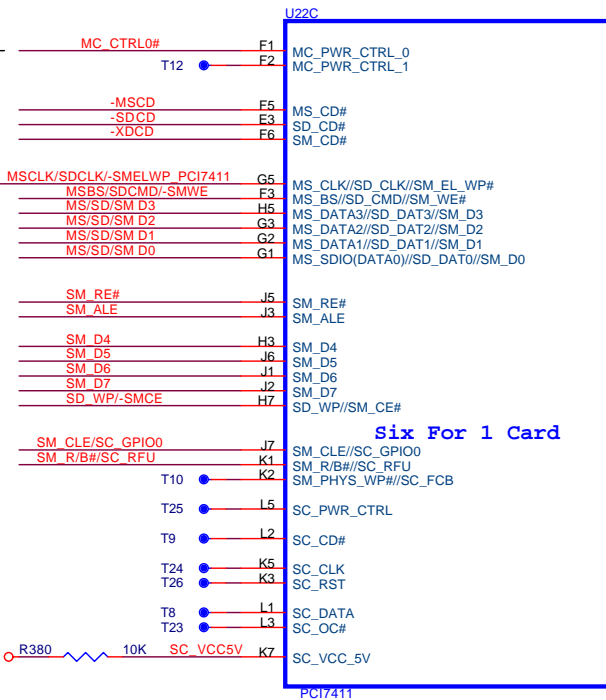
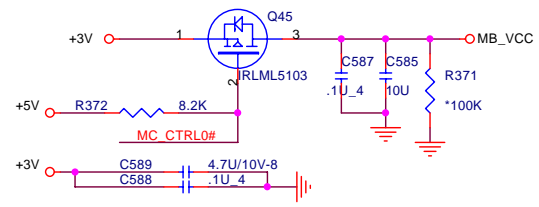
# PCMCIA SOCKET

MC\_PWR\_CTRL low active(default)  
 , or change register to high active

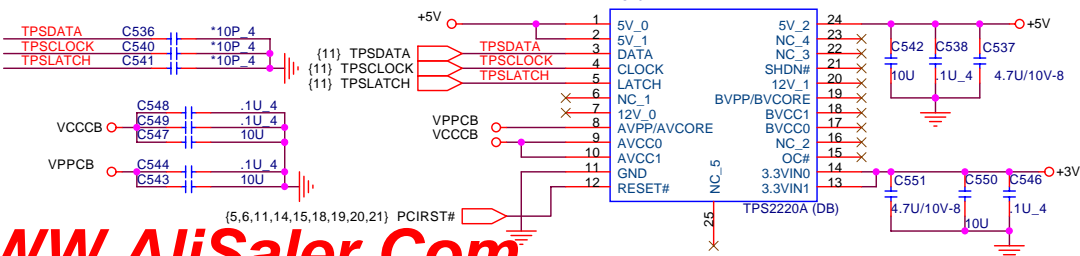
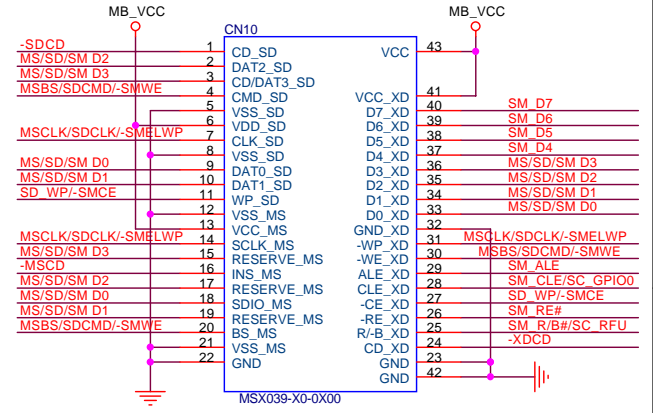


- Flash Media Layout Guidelines:
1. Signal traces should be 60 Ohm +/- 10%.
  2. All signal traces should be routed with equal propagation delay, and with trace lengths as short as practical.
  3. A 56 Ohm damping resistor for MS\_CLK and SD\_CLK should be placed near the PCI7411 source.

IRLML5103  
Rds(on)=0.6

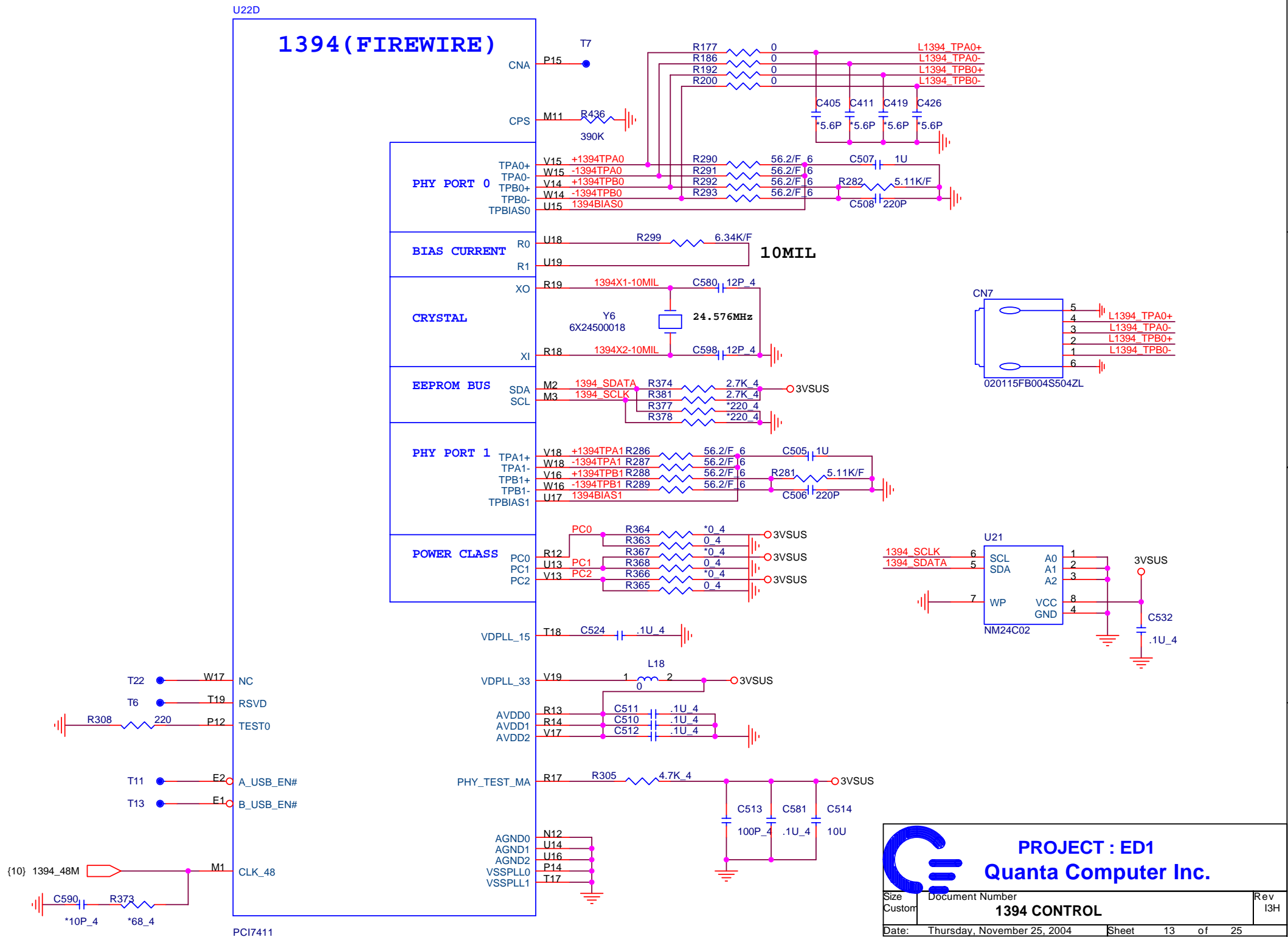


Six For 1 Card



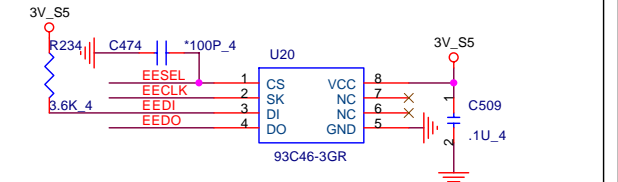
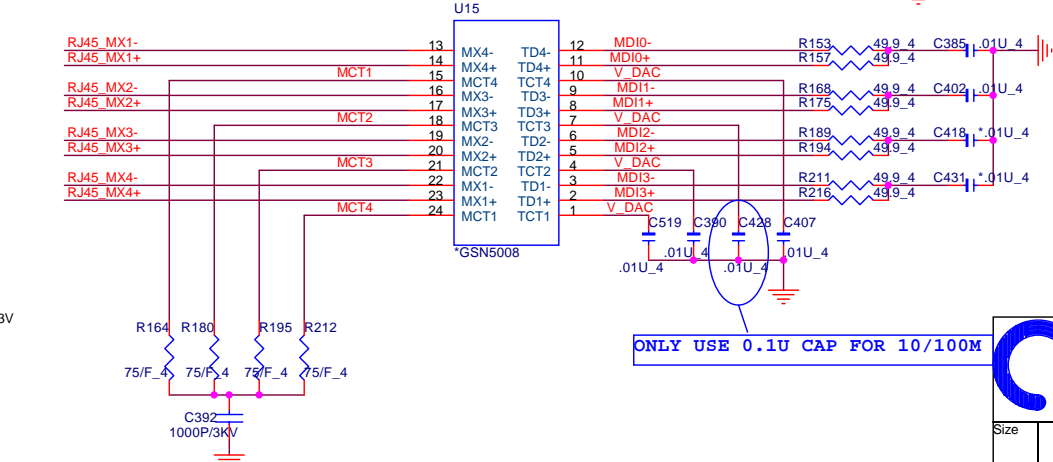
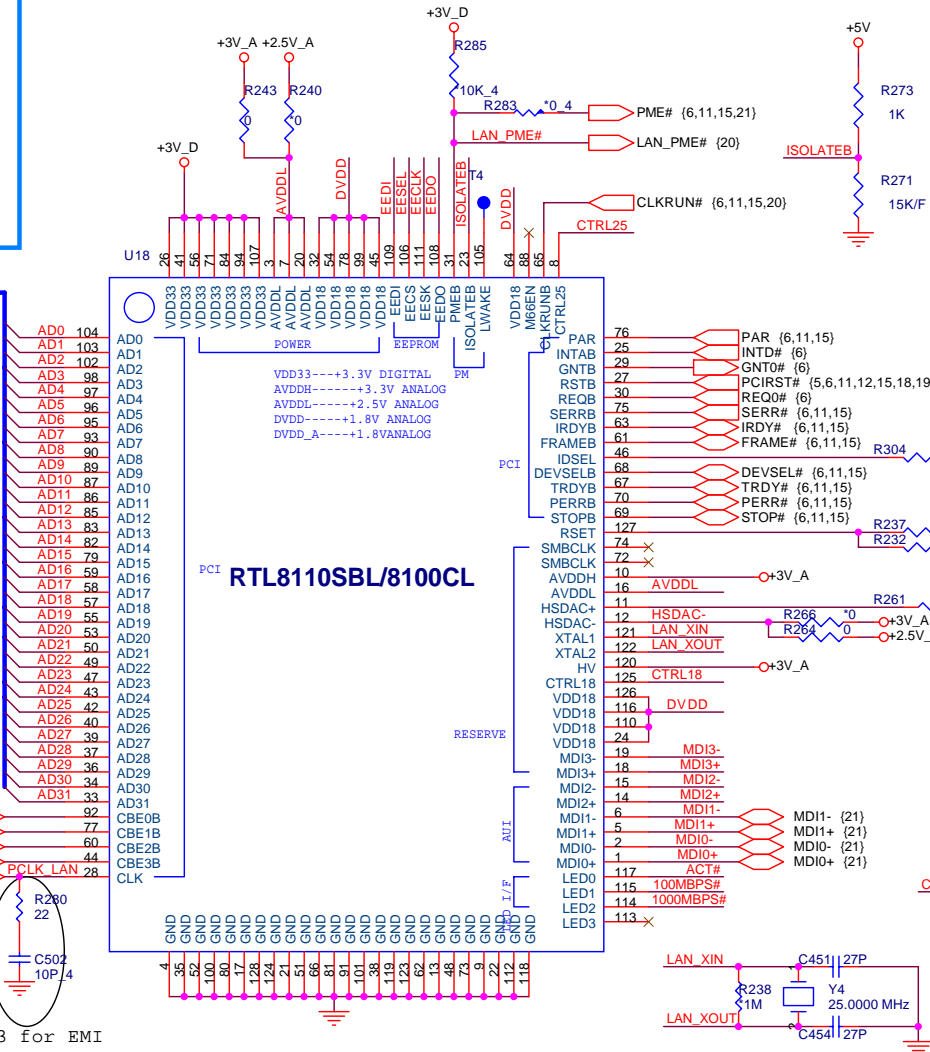
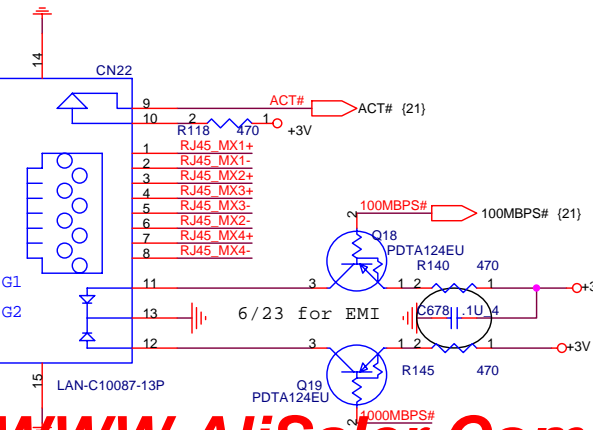
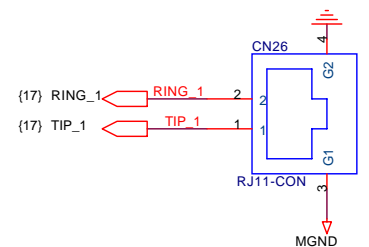
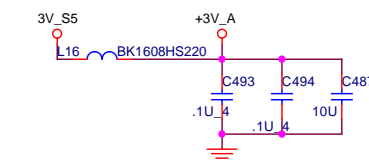
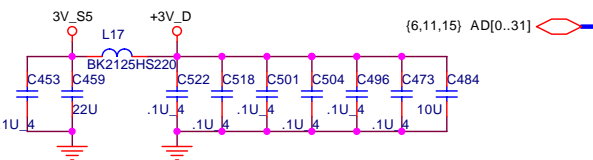
PROJECT : ED1  
Quanta Computer Inc.

Size	Document Number	Rev
Custom	PCMCIA SOCKET/4-IN-1	I3H
Date:	Thursday, November 25, 2004	Sheet 12 of 25

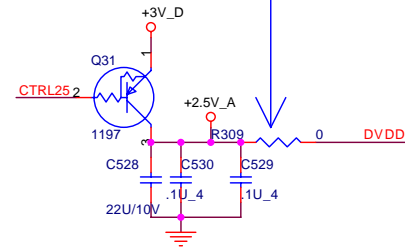




AS	RTL8110SBL(Giga-Lan)	AVDDL	CONNECT	+2.5V_A
	RTL8100L(10,100M-Lan)	AVDDL	CONNECT	+3V_A
AS	RTL8110SBL(Giga-Lan)	HSDAC-	CONNECT	+3V_A
	RTL8100L(10,100M-Lan)	HSDAC-	CONNECT	+2.5V_A
AS	RTL8110SBL(Giga-Lan)	DVDD	CONNECT	+1.8V_D
	RTL8100L(10,100M-Lan)	DVDD	CONNECT	+2.5V_A

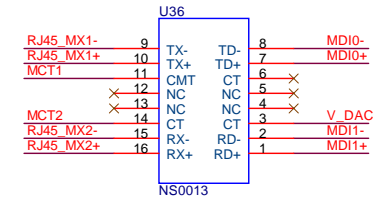
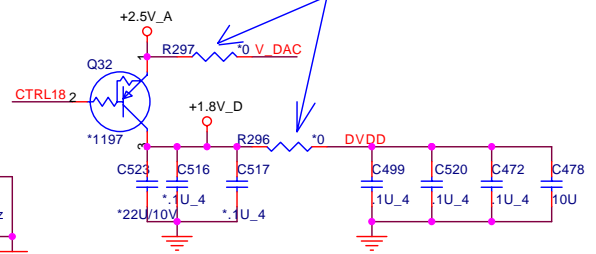


```
AS RTL8110SBL(10,100,1000M)--without mount
RTL8100L(10,100) --have mount
```



```
RTL8110SBL(10,100,1000M)--mount 2.49K
RTL8100L(10,100)          --mount 5.6K
```

```
AS RTL8110SBL(10,100,1000M)--have mount
    RTL8100L(10,100)           --without mount
```



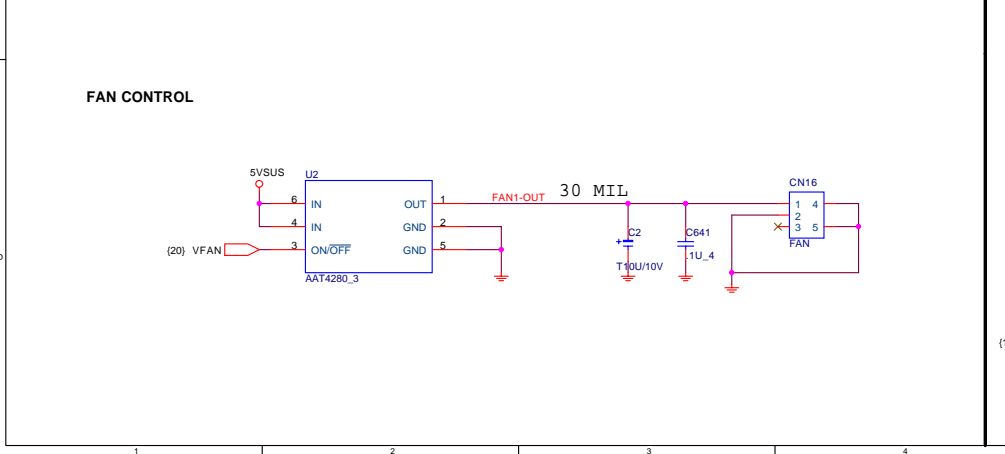
ONLY USE 0.1U CAP FOR 10/100M

 PROJECT : ED1  
Quanta Computer Inc.

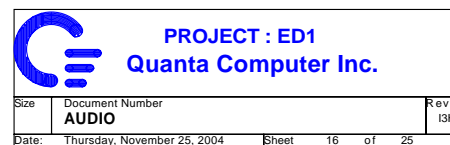
Size	Document Number <b>LAN RTL8110SBL/8100CL</b>	Rev I3H
Date:	Thursday, November 25, 2004	Sheet 14 of 25

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5

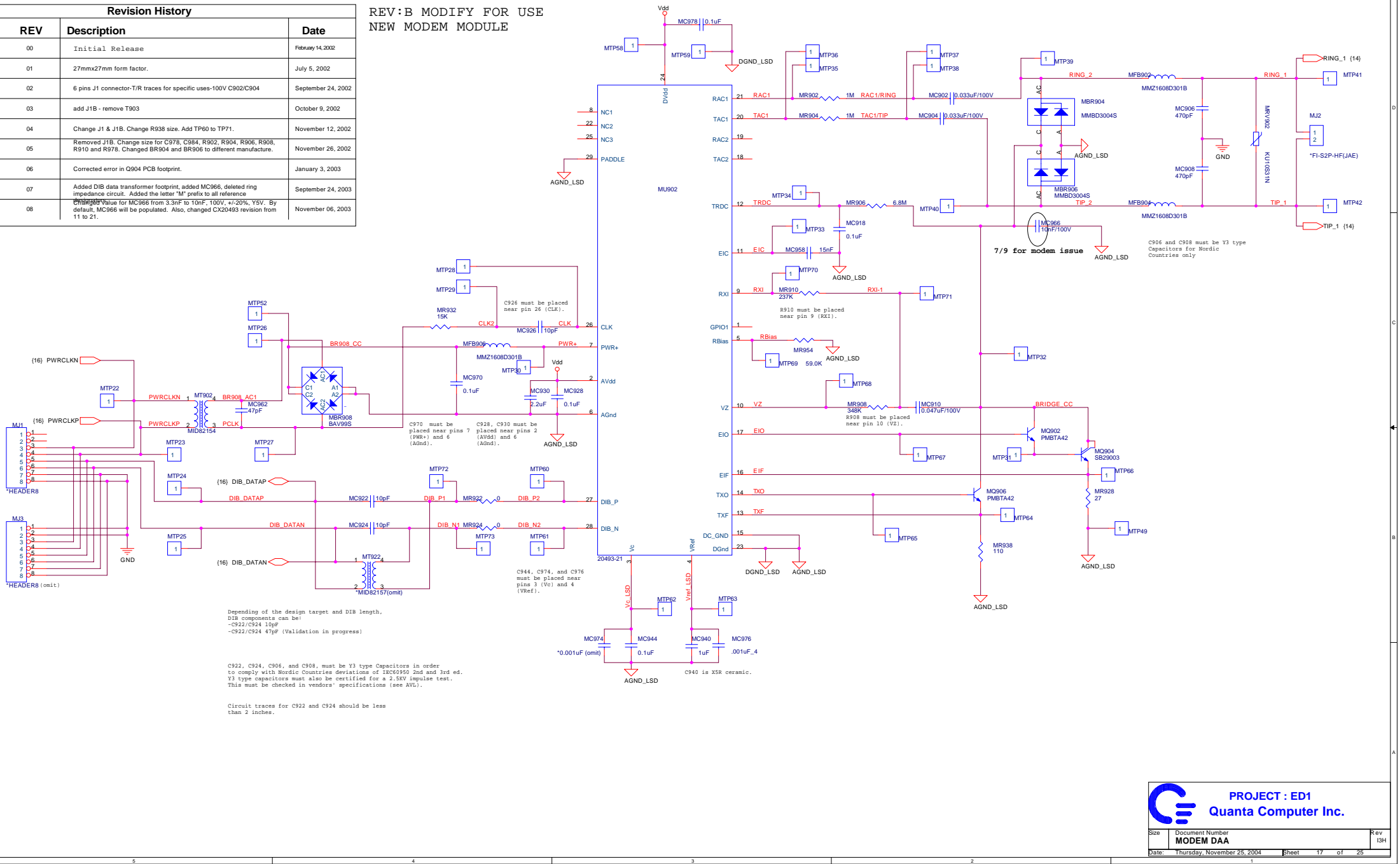


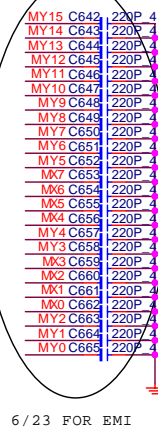
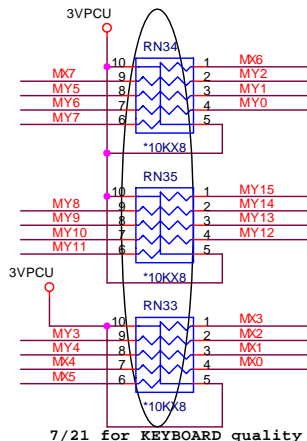
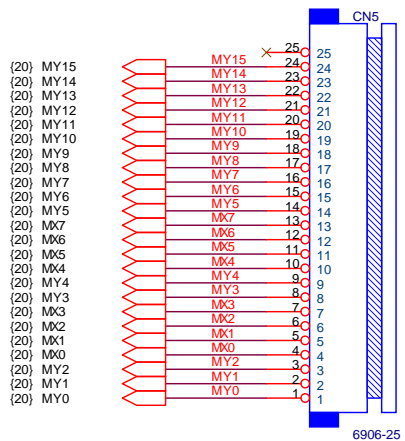
**WWW.AliSaler.Com**



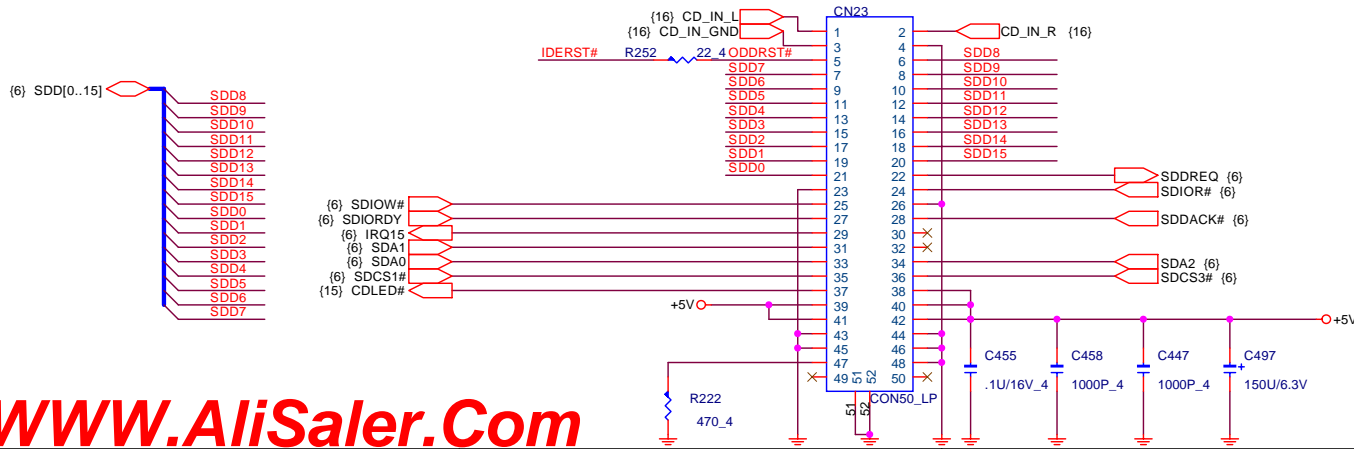
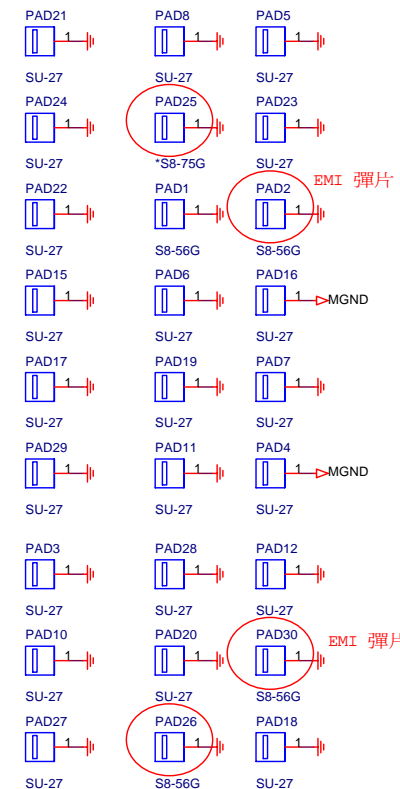
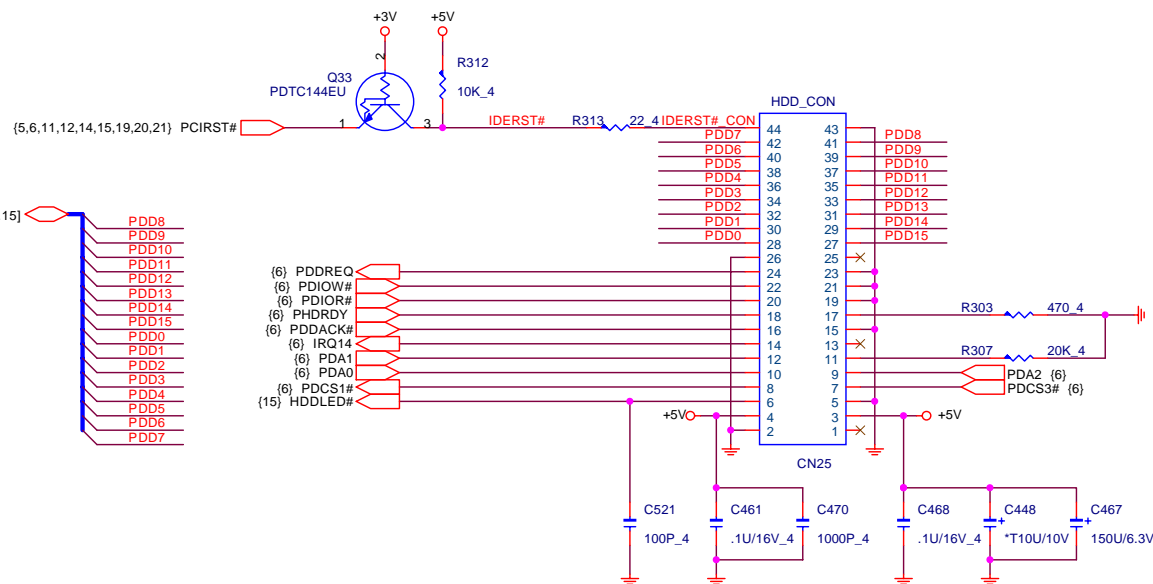
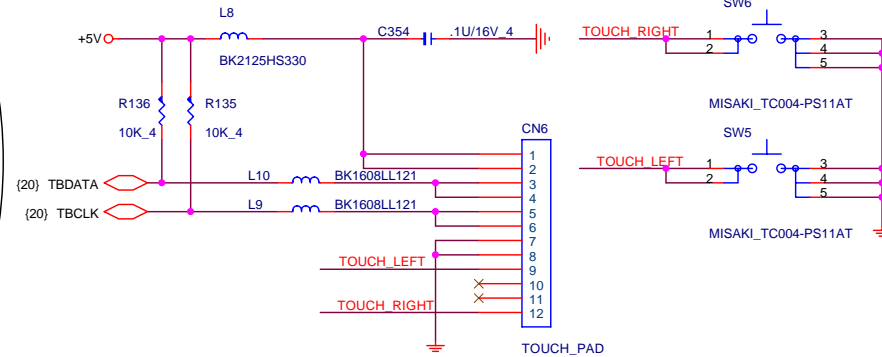
Revision History		
REV	Description	Date
00	Initial Release	February 14, 2002
01	27mmx27mm form factor.	July 5, 2002
02	6 pins J1 connector-T/R traces for specific uses-100V C902/C904	September 24, 2002
03	add J1B - remove T903	October 9, 2002
04	Change J1 & J1B. Change R938 size. Add TP60 to TP71.	November 12, 2002
05	Removed J1B. Change size for C978, C984, R902, R904, R906, R908, R910 and R978. Changed BR904 and BR906 to different manufacture.	November 26, 2002
06	Corrected error in Q904 PCB footprint.	January 3, 2003
07	Added DIB data transformer footprint, added MC966, deleted ring impedance circuit. Added the letter "M" prefix to all reference designators.	September 24, 2003
08	Changed value for MC966 from 3.3nF to 10nF. 100V, +/-20%, Y5V. By default, MC966 will be populated. Also, changed CX20493 revision from 11 to 21.	November 06, 2003

REV:B MODIFY FOR USE  
NEW MODEM MODULE





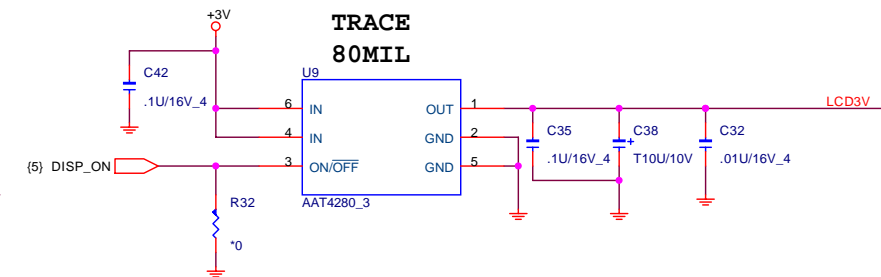
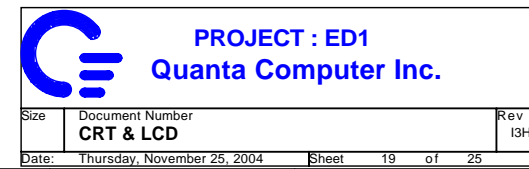
## TOUCH PAD



**PROJECT : ED1**  
**Quanta Computer Inc.**

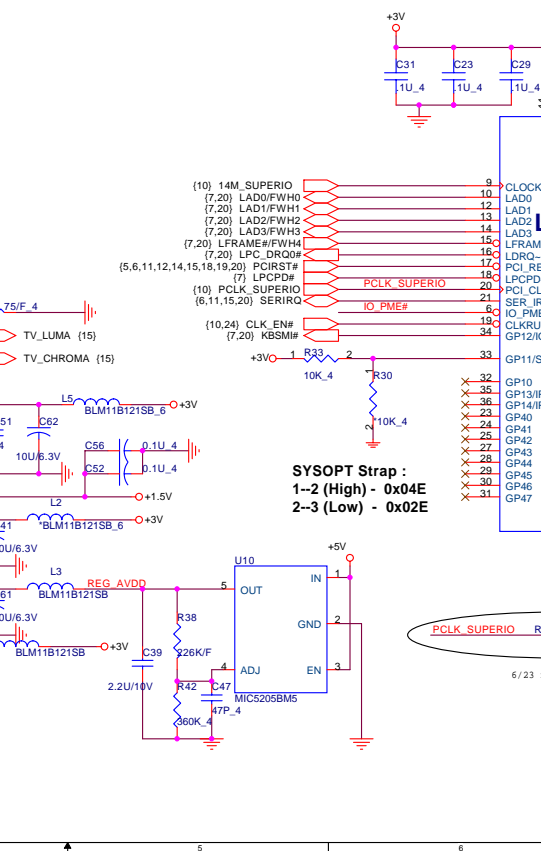
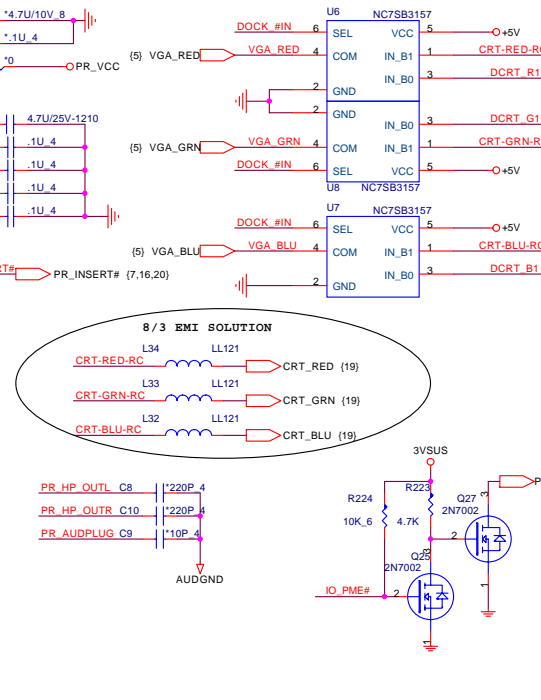
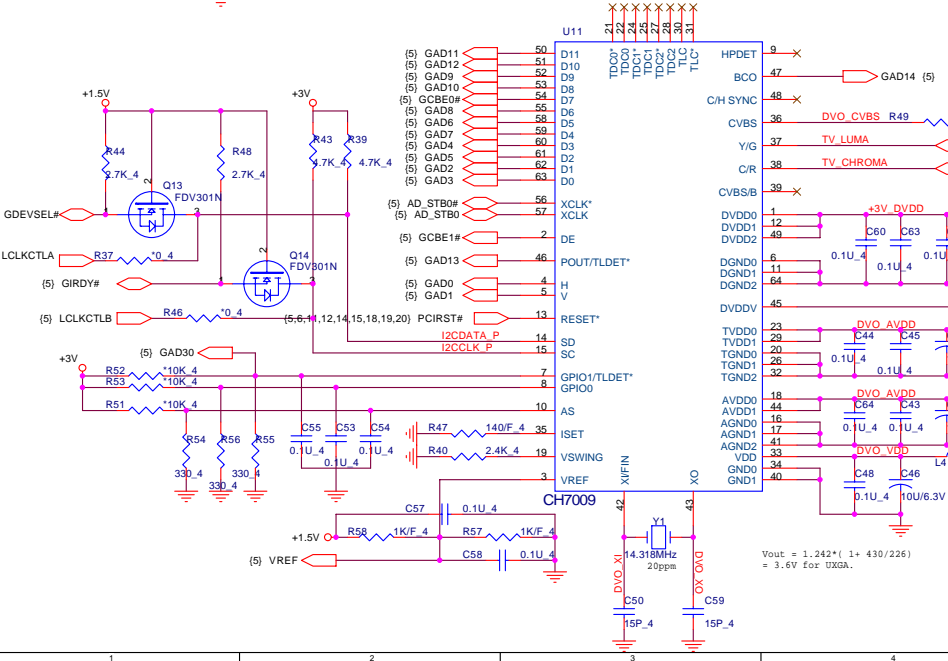
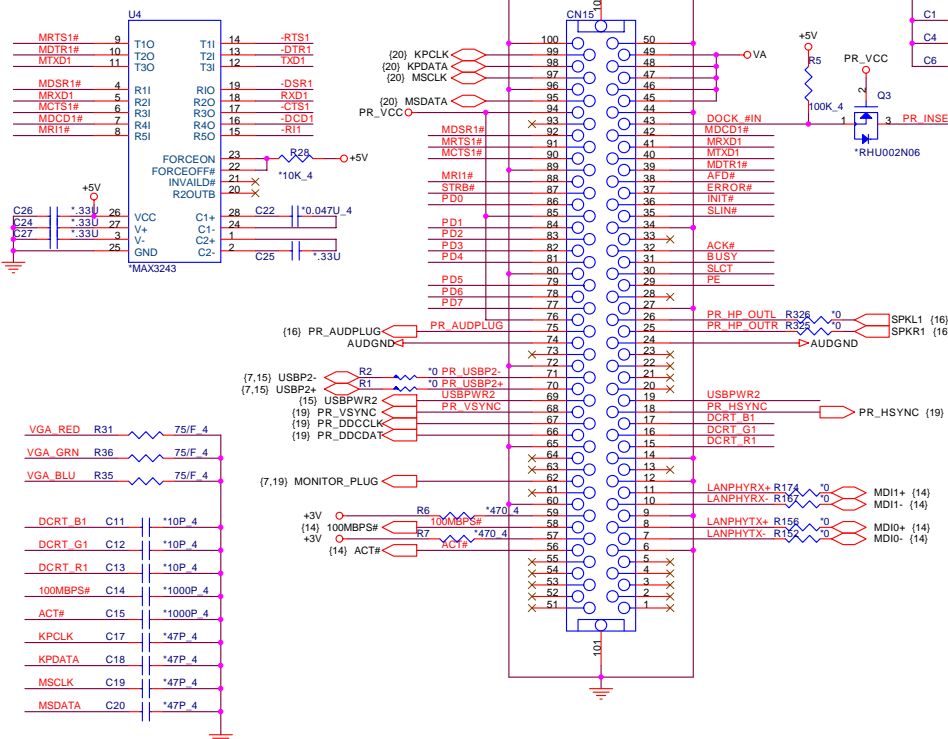
Size	Document Number	Rev
	<b>HDD &amp; CDROM &amp; KB</b>	13H
Date:	Thursday, November 25, 2004	Sheet 18 of 25



[illegible]



# PORT REPLICATOR Connector



## FIR

