

JUL'07

Clock Generator  
MLF68 15

Intel  
Penryn LV/ULV SFF  
Processor 3, 4

Thermal  
Sensor  
LM26 5

ITP  
Conn.  
6

# Caramel-1 Block Diagram

07251-1-0709

Project Code: 91.4Y401.001  
PCB(Raw Card):

## PCB Layer Stackup

L1:TOP  
L2:Signal 1  
L3:GND  
L4:Signal 2  
L5:VCC  
L6:GND  
L7:Signal 3  
L8:BOTTOM

## Battery Charger/Selector

ADP3808 43

INPUTS	OUTPUTS
DOCK_PWR20_F	M-BAT-PWR S-BAT-PWR

## System DC/DC

TPS51221 49

VINT20	VCC5M VCC3M
--------	----------------

## CPU DC/DC

VT1311 50

VINT20	VCCPUCORE
--------	-----------

## GMCH GFX CORE

VT451&VT450 51

VINT20	VCCGFXCORE
--------	------------

## VCC1R05AMT/VCC1R5A

VT351 52, 53

VCC5V_OUT	VCC1R05AMT VCC1R5A
-----------	-----------------------

## VCC0R75AMT

MAX1510 54

VCC1R5A	VCC0R75AMT
---------	------------

## VCC1R8B

BD3550 55

VCC3M	VCC1R8B
-------	---------

## VCC1R05AUX/1R8AUX

BD3550 56

VCC3M	VCC1R05AUX VCC1R8AUX
-------	-------------------------

&lt;Variant Name&gt;

緯創資通

**Wistron Corporation**  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title

## Block Diagram

Size  
A3

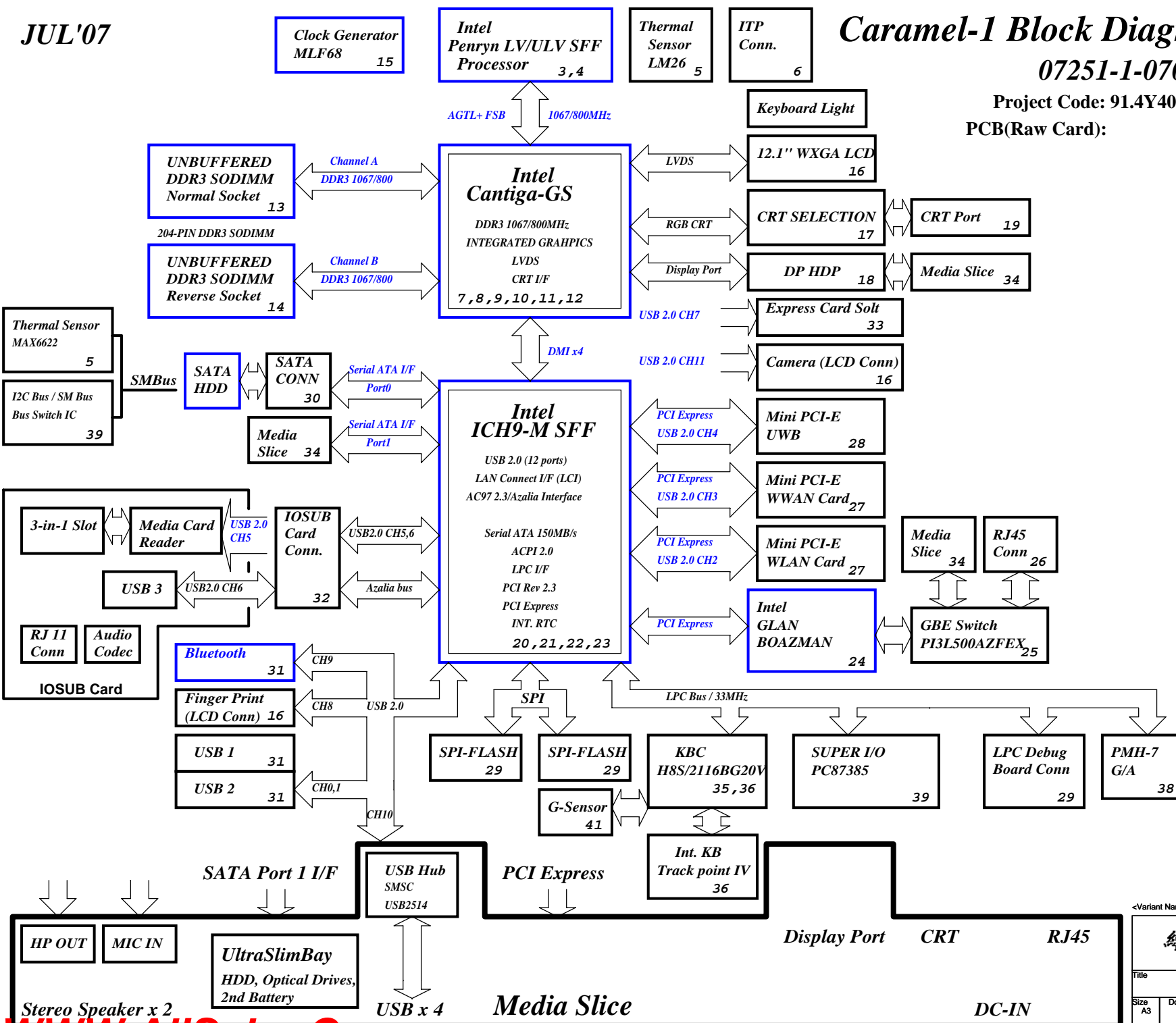
Document Number

Caramel-1

Rev  
SC

Date: Monday, July 14, 2008

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## RESISTOR

Symbol name	Value	Tolerance (J: 5%, F: 1%, D: 0.5%, B: 0.1 %)	Rating 0402=> 1/16W, 25V 0603 => 1/16W, 75V 0805 => 1/10W, 100V	Size 2=>0402, 3=>0603, 5=>0805, 6=>1206, 0=>1210
10KR3	10K Ohm	If no letter, it means J: 5%	1/16W, 75V	0603
33D3R5	33.3 Ohm	If no letter, it means J: 5%	1/10W, 100V	0805
1KR3F	1K Ohm	F: 1%	1/16W, 75V	0603

The naming rule is value + R + size + tolerance  
 For the value, it can be read by the number before R. (R means resistor)  
 For the tolerance, it can be read from the last letter.  
 For the rating, we don't show on the symbol name.  
 For the size, R2=>0402, R3=>0603, R5=>0805,....

## CAPACITOR

Symbol name	Value	Tolerance (M: +/-20, K: +/-10, Z: +80/-20)	Rating	Size 2=>0402, 3=>0603, 5=>0805, 6=>1206, 0=>1210
SCD1U10V2MX-1	0.1uF	M/X5R	10V	0402
SC10U6D3V5MX	10uF	M/X5R	6.3V	0805
SC2D2U16V5ZY	2.2uF	Z/Y5V	16V	0805

The naming rule is  
 Capacitor type + value + rating + size + tolerance + material  
 SCD1U10V2MX-1  
 SCD => SMT Ceramic, TC=> POS cap or SP cap  
 D1U => 0.1uF  
 10V => the voltage rating is 10V  
 2=> 0402, 3=>0603, 5=>0805  
 M=>tolerance M, K, Z  
 X=> X7R/X5R, Y=> Y5V  
 -1 => symbol version, nonsense to EE characteristic

## PLANAR\_ID[3..0]

ICH8-M GPIO <sub>n</sub>	39	38	37	36	Planar ID Version	Planar PCB Version
PLANAR_ID <sub>n</sub>	3	2	1	0		
	0	0	0	0	Caramel-1 Pre-DV/DV	SA
	0	0	0	1	Caramel-1 SIV	SB
	0	0	1	0	Caramel-1 SIT	SC
	0	0	1	1	Caramel-1 SVT	
	0	1	0	0		
	0	1	0	1		
	0	1	1	0		
	0	1	1	1		

## EC HISTORY

[illegible]

## PCI TABLE

DEVICE	IDSEL	IRQ (Default)	REQ# / GNT#
MINIPCI SLOT	AD18	F, G	REQ# 3/ GNT#
CARDBUS R5C811	AD16	SERIRQ	REQ#0 / GNT#
USB UHCI	AD29	A, C, D	
USB 2.0 EHCI	AD29	H	
DMI-to-PCI/ AC97 Modem/ AC97 Audio	AD30	B B	
LPC Bridge IDE SATA SMBus	AD31	C C B	
PCI Express	AD28	A, B, C, D	

<Variant Name>

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Taipei Hsien 221, Taiwan, R.O.C.

Title
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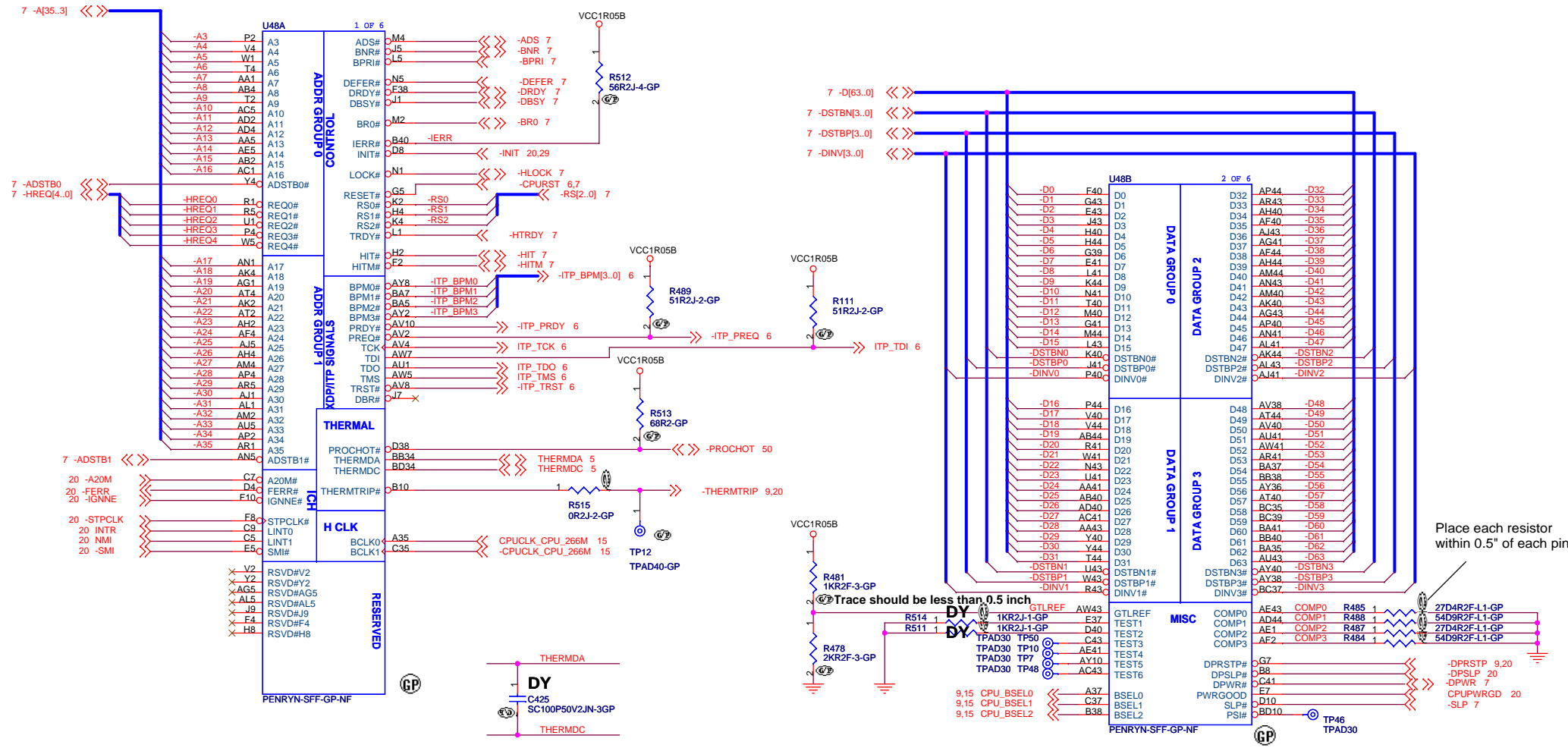
## Reference

Size	Document Number
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### Caramel-1

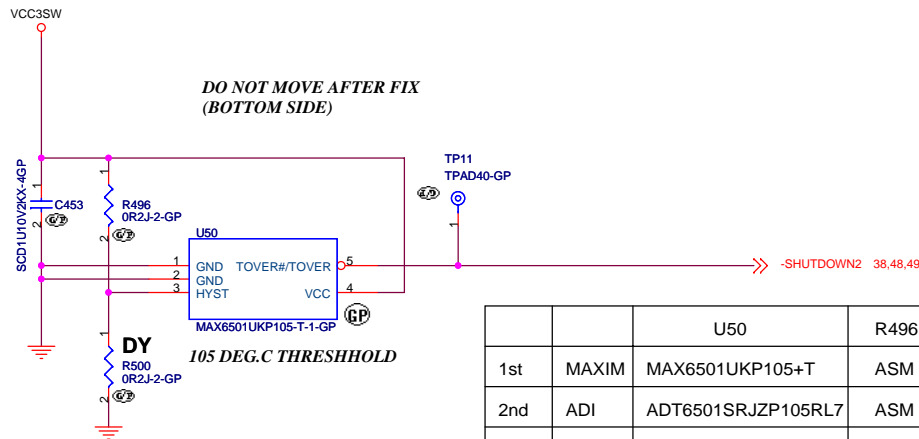
Rev

Sheet 2 of 62

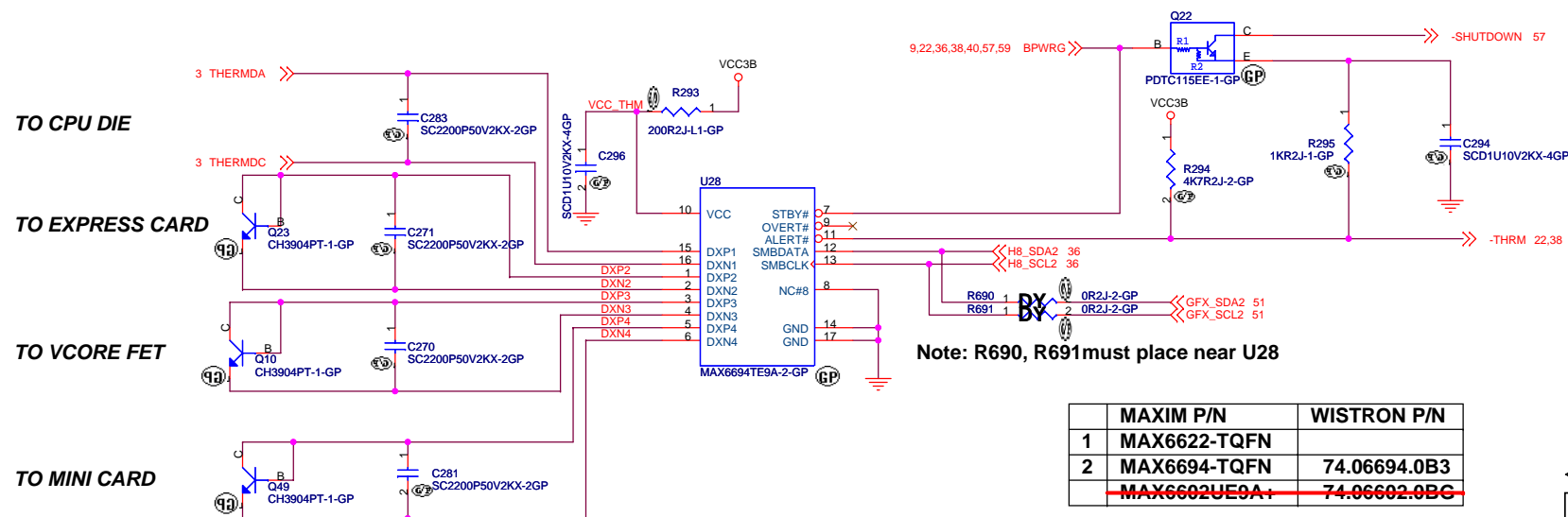




# Thermal Sensor for CPU



**THESE CAPS MUST BE PLACED AS CLOSE AS POSSIBLE TO MAX6602**



	MAXIM P/N	WISTRON P/N
1	MAX6622-TQFN	
2	MAX6694-TQFN	74.06694.0B3
	<del>MAX6602UE9A-1</del>	<del>74.06602.0B3</del>

**Layout Comment :**

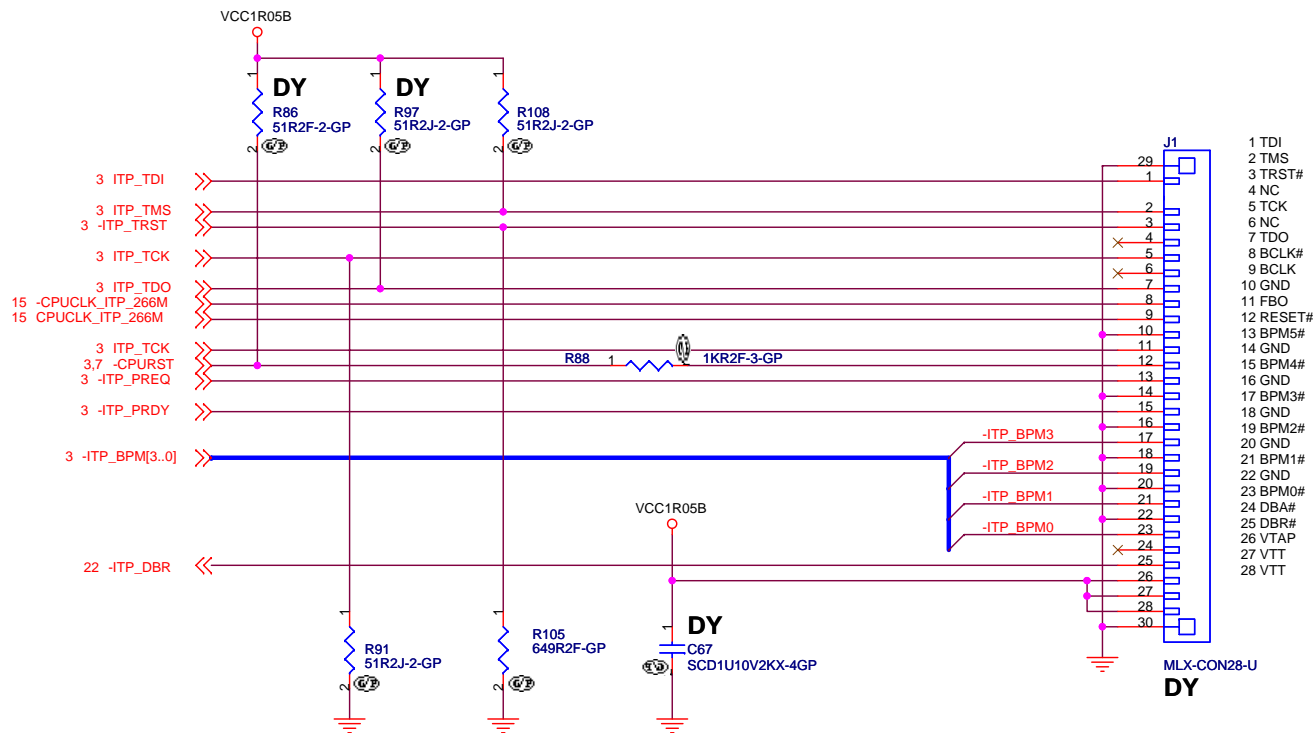
- (1) Thermal sensor trace lines should not be overlapped with other high frequency trace lines in other layers.
- (2) Also, it should not be overlapped with large amplitude trace lines either.

**PLACE UNDER PC-CARD SLOT**  
H8 I2C Bus 2 ADDRESS : 9AH  
TEST PAD FOR BOARD MFG TEST

<Variant Name>

**緯創資通 Wistron Corporation**  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.

Title			<b>THERMAL SENSOR</b>		
Size	Document Number		Caramel-1		Rev
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Date:	Monday, July 14, 2008		Sheet	5	of 62



- 1 TDI
- 2 TMS
- 3 TRST#
- 4 NC
- 5 TCK
- 6 NC
- 7 TDO
- 8 BCLK#
- 9 BCLK
- 10 GND
- 11 FBO
- 12 RESET#
- 13 BPM5#
- 14 GND
- 15 BPM4#
- 16 GND
- 17 BPM3#
- 18 GND
- 19 BPM2#
- 20 GND
- 21 BPM1#
- 22 GND
- 23 BPM0#
- 24 DBA#
- 25 DBR#
- 26 VTAP
- 27 VTT
- 28 VTT

(\*1) TCK SIGNAL IS BRANCHED AT CPU's PIN

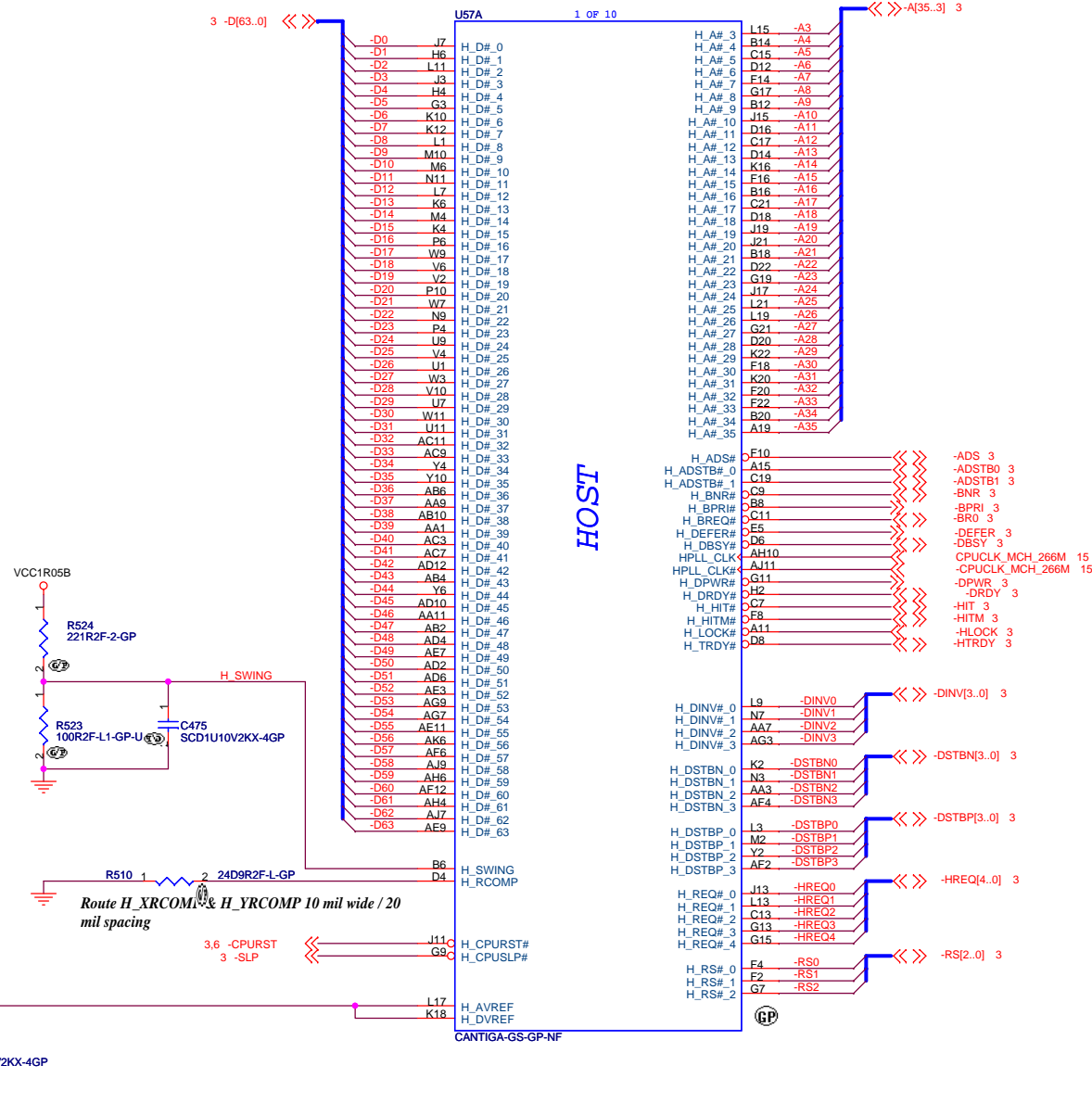
(\*2) CPURST# SIGNAL IS BRANCHED AT GMCH's PIN

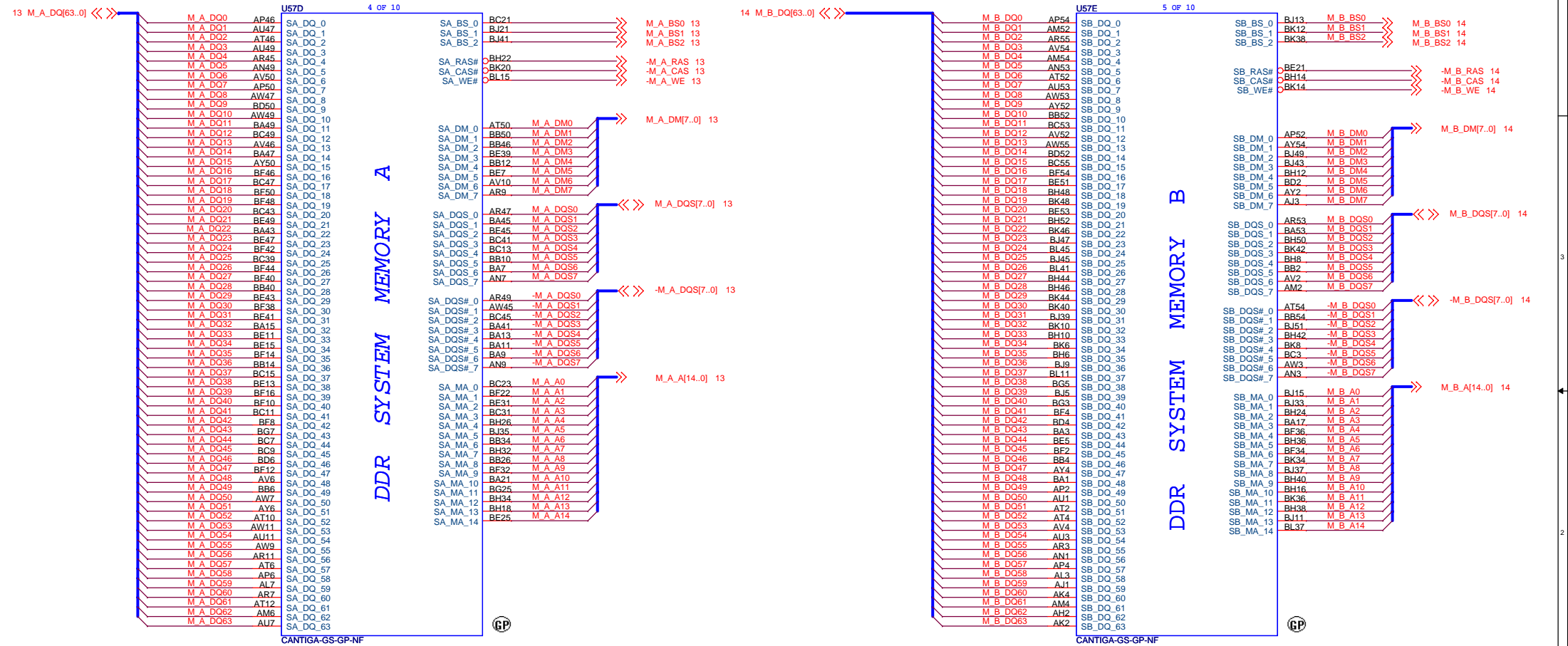
Ref Des	For ITP-XDP
J1	NO_ASM-->ASM
C67	NO_ASM-->ASM
R88	NO_ASM-->1K 5% ASM
R105	ASM (No Change)
R86	ASM-->NO_ASM
R97	ASM (No Change)
R91	ASM-->51 5% ASM
R108	ASM-->51 5% ASM

<Variant Name>

<b>緯創資通</b>		<b>Wistron Corporation</b> 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title <b>ITP_XDP CONN</b>			
Size	Document Number	Rev	
Custom	<b>Caramel-1</b>	<b>SC</b>	
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Route H\_XSWING & H\_YSWING  
10 mil wide / 20 mil spacing

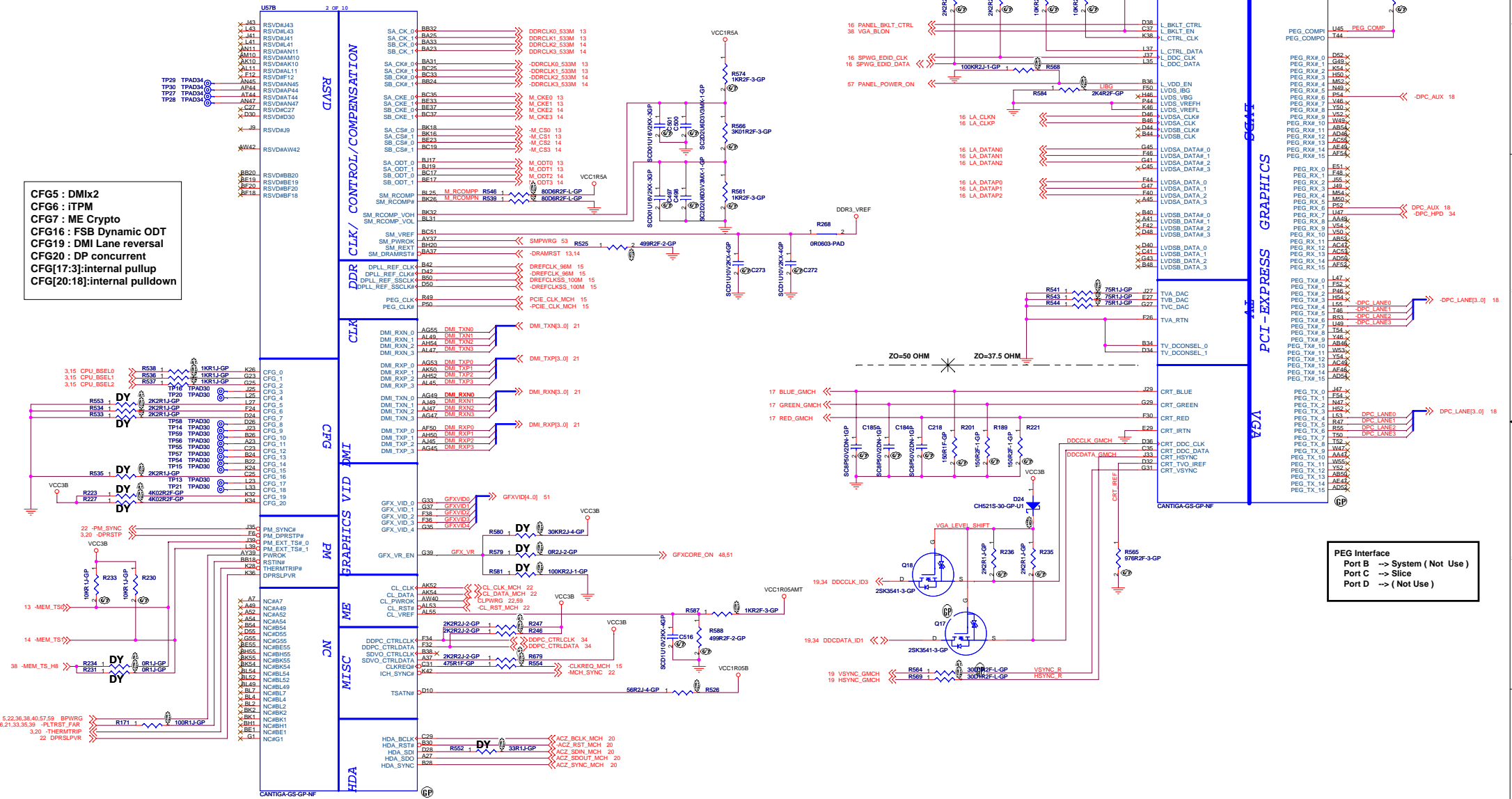




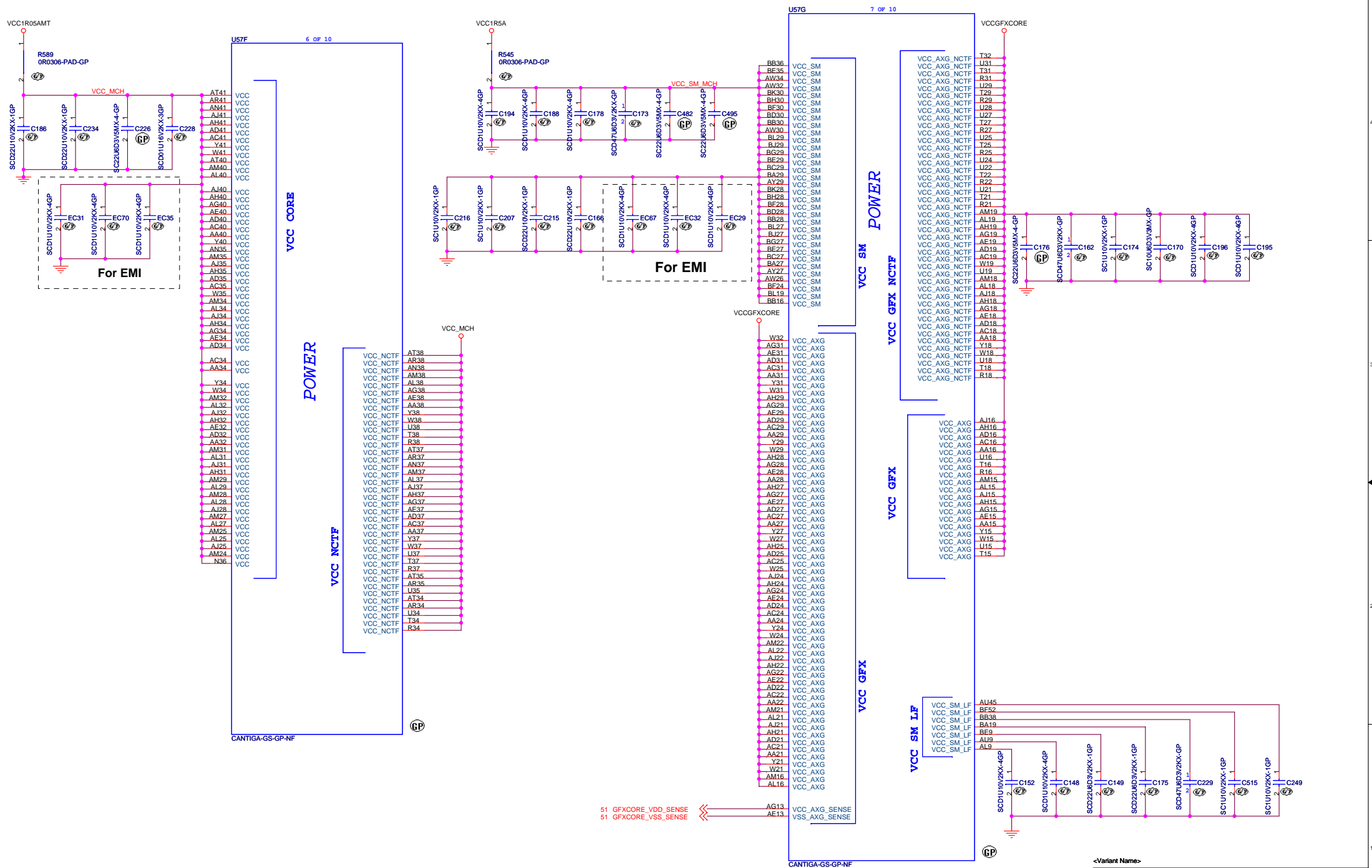


## ME DEBUG PORT PIN OUT TABLE

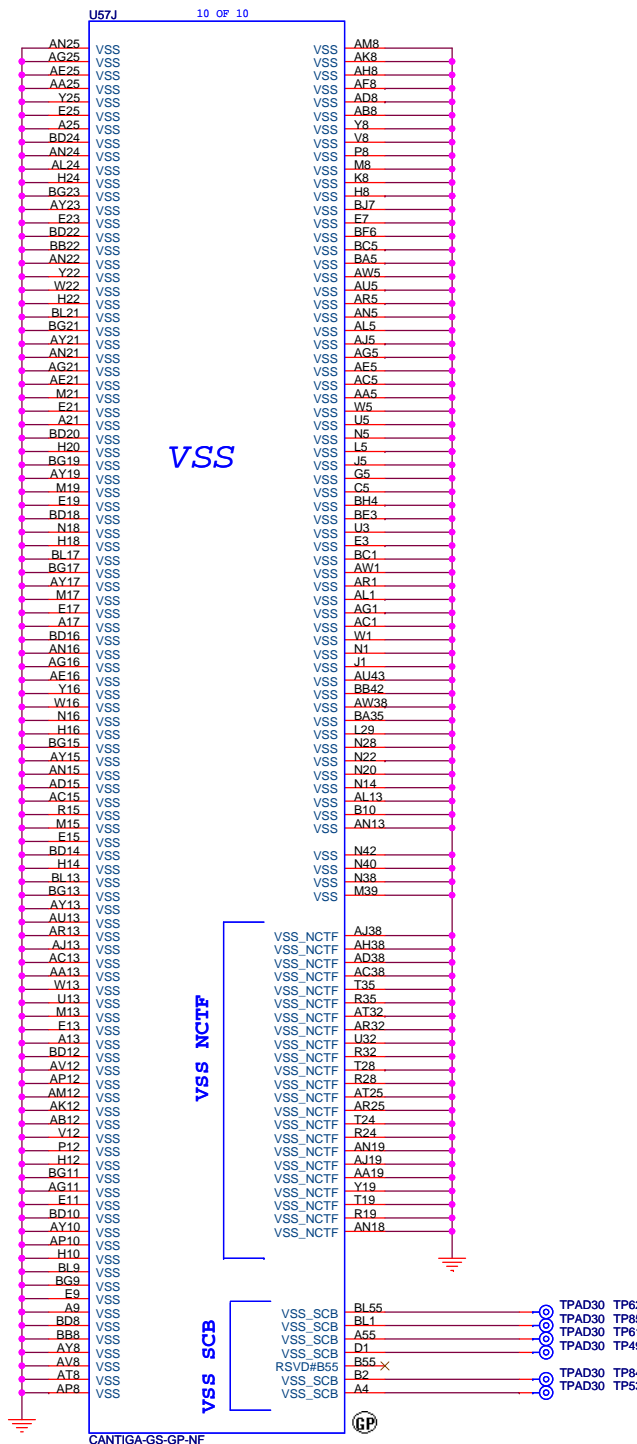
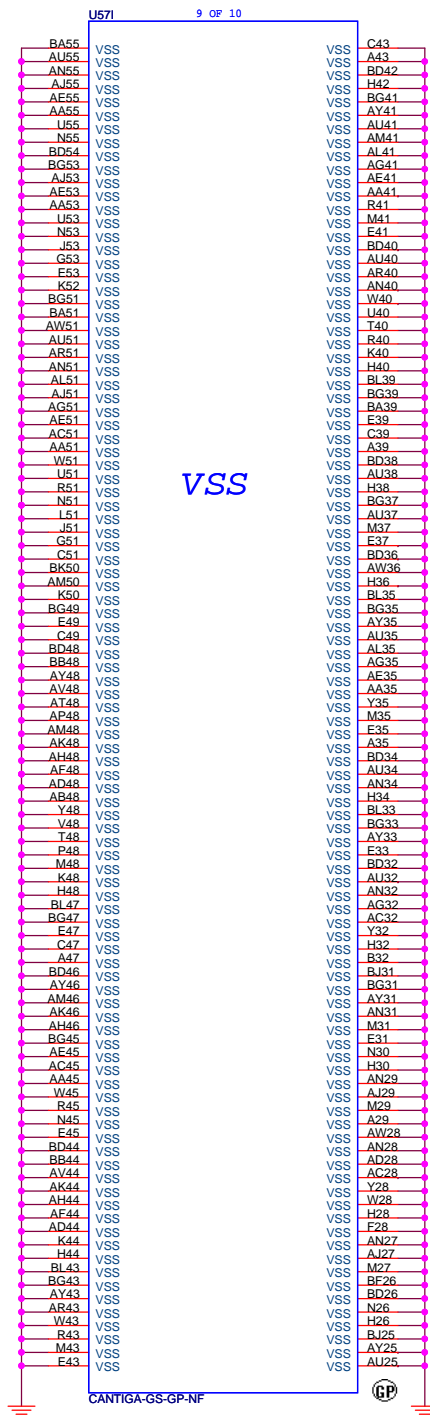
RESERVED#AN45	ME_JTAG_TCK
RESERVED#AP44	ME_JTAG_TDI
RESERVED#AT44	ME_JTAG_TDO
RESERVED#AN47	ME_JTAG_TMS



&lt;Variant Name&gt;







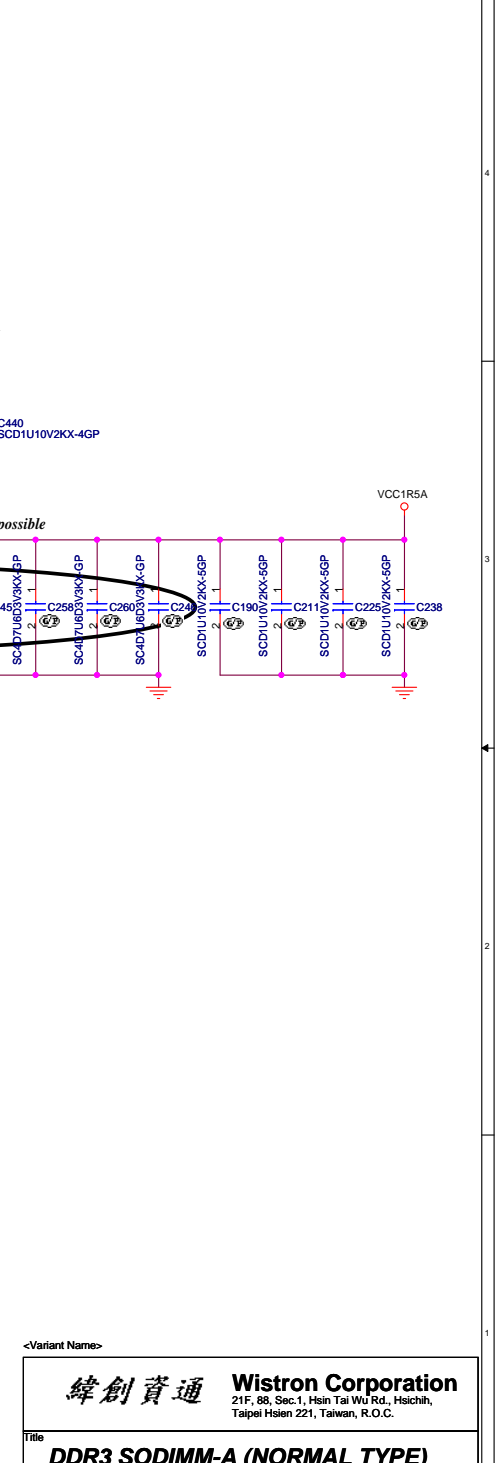
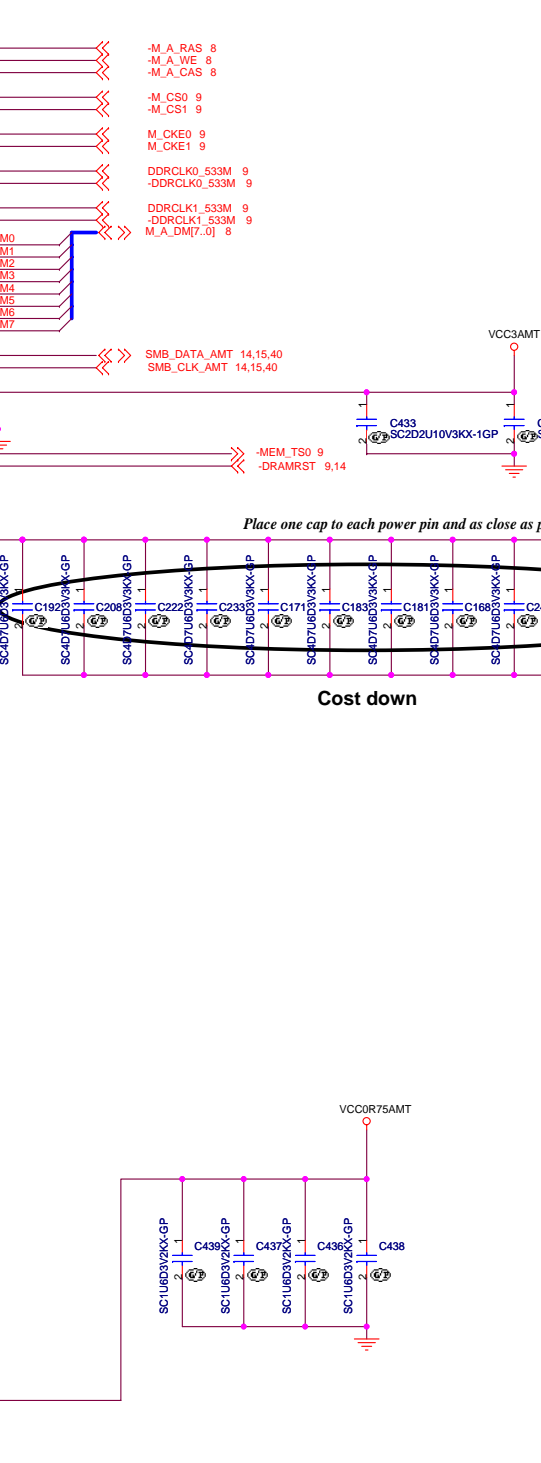
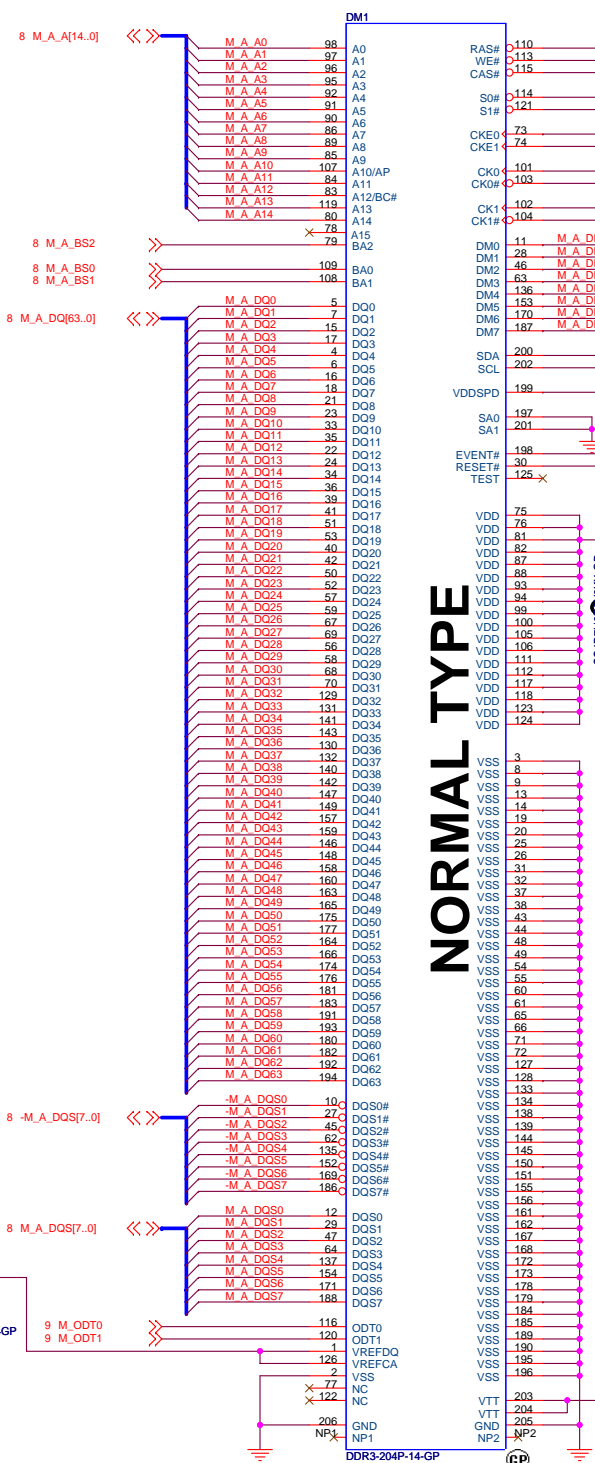
<Variant Name>

**緯創資通** **Wistron Corporation**  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title: **Cantiga-GS (6/6):GND**

Size: A3 Document Number: **Caramel-1** Rev: SC

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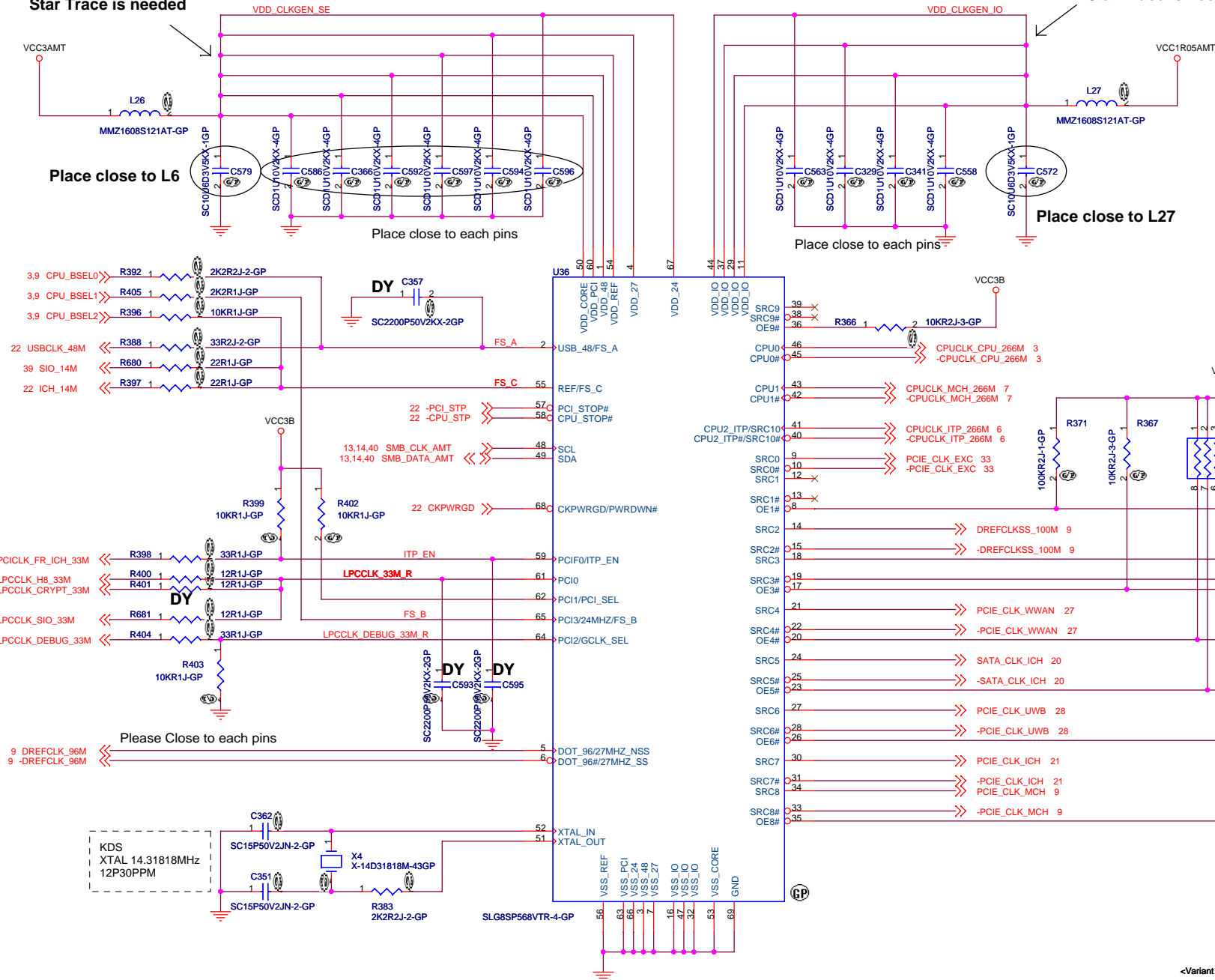






Star Trace is needed

Star Trace is needed



SERC#	OE#	DEVICE
0		EXPRESS
1	OE1#	SLICE
2		GFX
3	OE3#	WLAN
4	OE4#	WWAN
5	OE5#	SATA
6	OE6#	UWB
7		ICH
8	OE8#	MCH
9	OE9#	Reserved
10		ITP

[Source Cadidate]

SILEGO SLG8SP568V 71.08568.C03

<Variant Name>

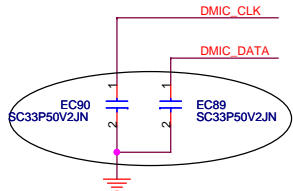
緯創資通 Wistron Corporation  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title		
CLOCK GEN(CK505)		
Size A3	Document Number	Rev SC
Date: Monday, July 14, 2008	Sheet 15 of 62	

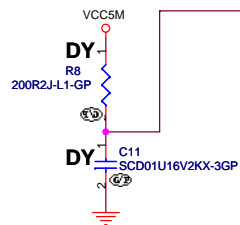
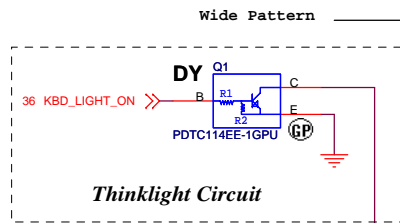
# LCD / Inverter Connector

<Comment>

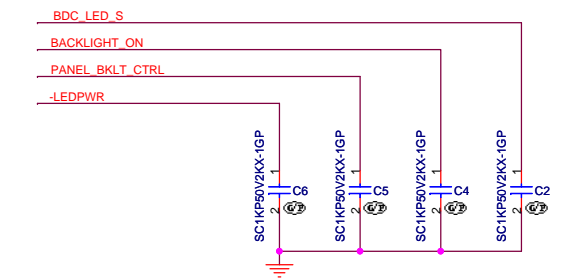
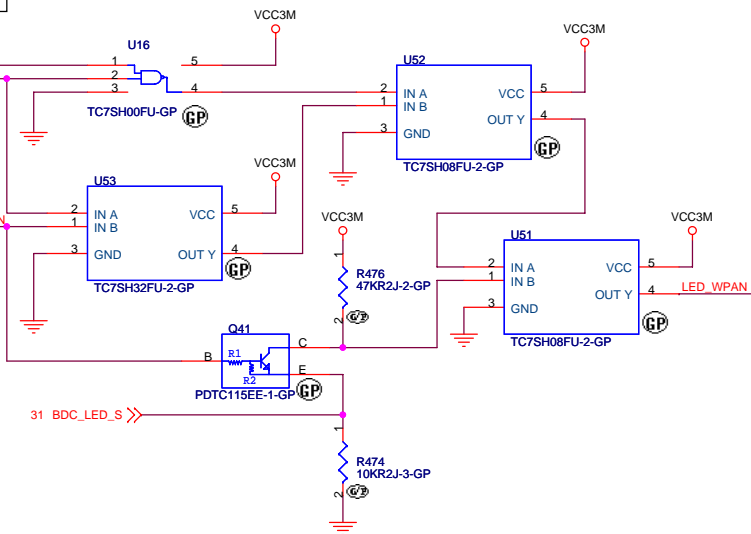
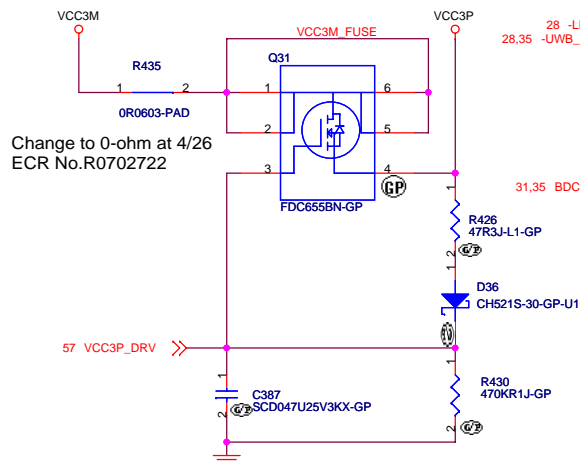
R19, R18, R2, R3, R4, R14, R5 and R16  
will be removed after SDV evaluation.



Please place EC89,EC90 near LCD CONN.



AND GATE
U51 U52 : TC7SH08FU
OR GATE
U53 : TC7SH32FU
NAND GATE
U16 : TC7SH00FU

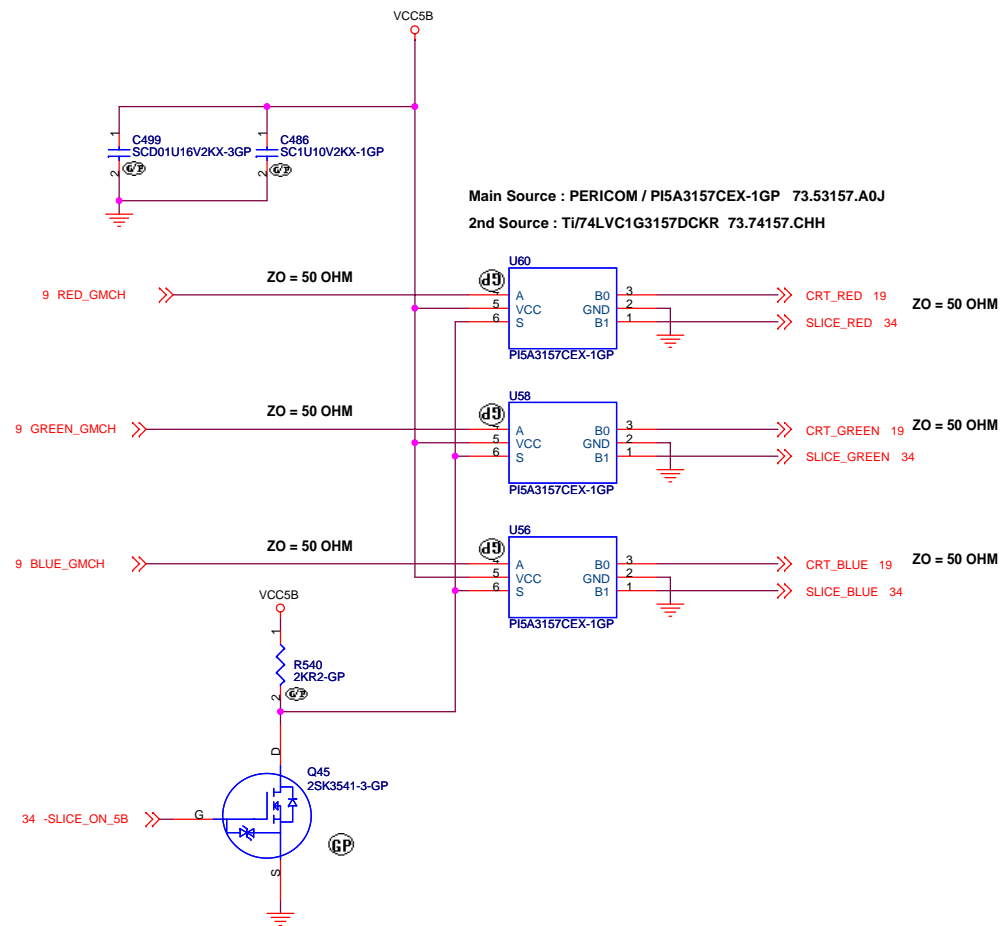


<Variant Name>

**緯創資通 Wistron Corporation**  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title			
LCD CONNECTOR			
Size	Document Number	Rev	
A3	Caramel-1	SC	
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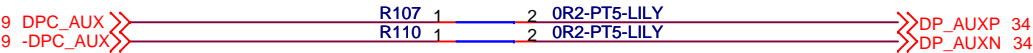
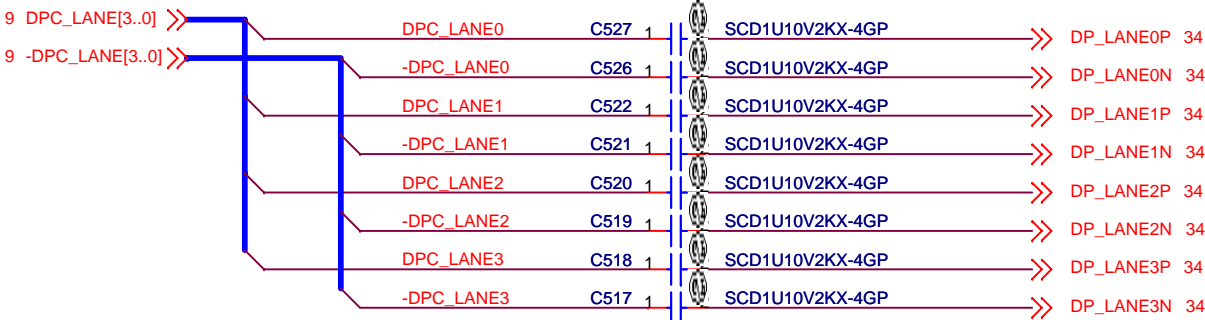


<Variant Name>

緯創資通 Wistron Corporation  
 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
 Taipei Hsien 221, Taiwan, R.O.C.

Title			<b>CRT SELECTOR</b>	
Size	Document Number	Caramel-1		Rev
A3				SC
Date:	Monday, July 14, 2008	Sheet	17	of 62

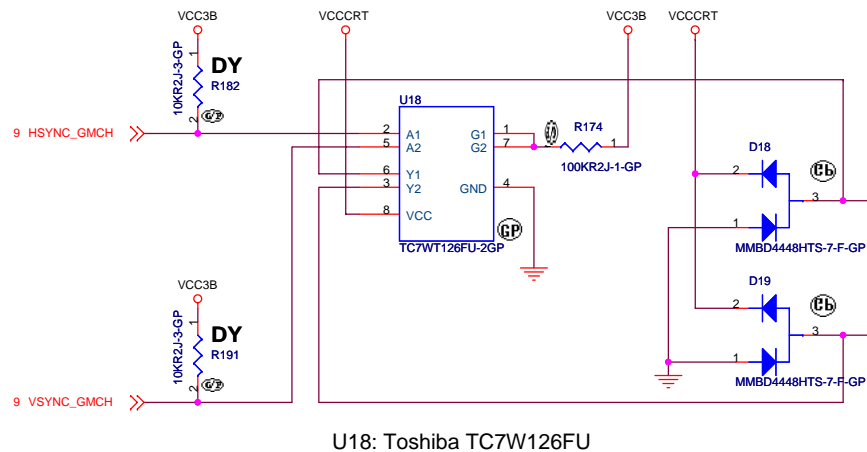
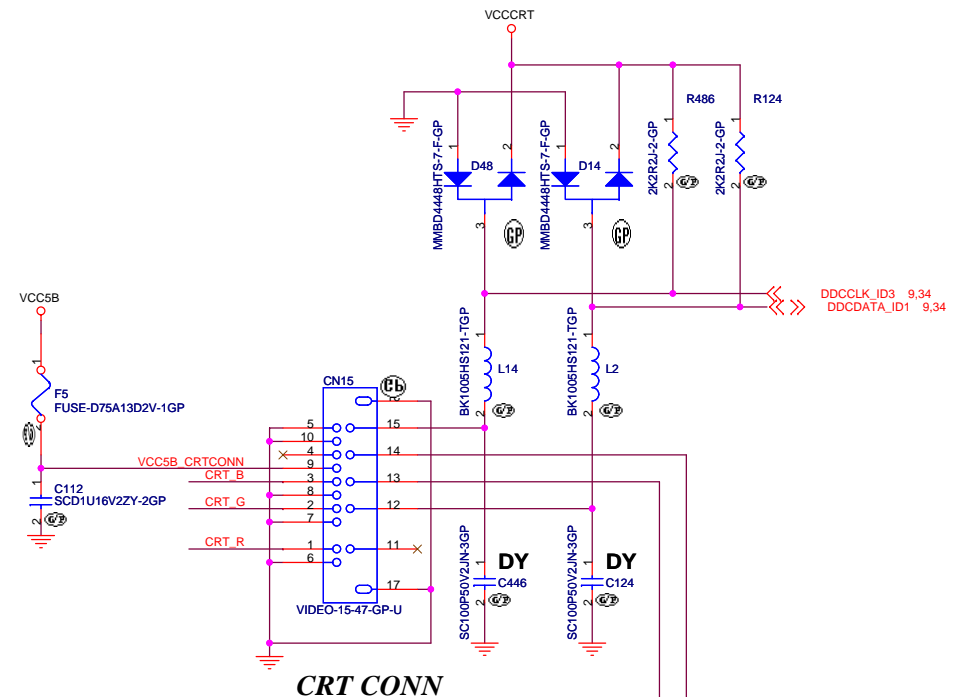
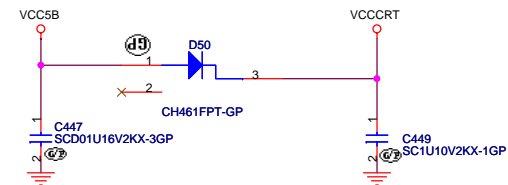
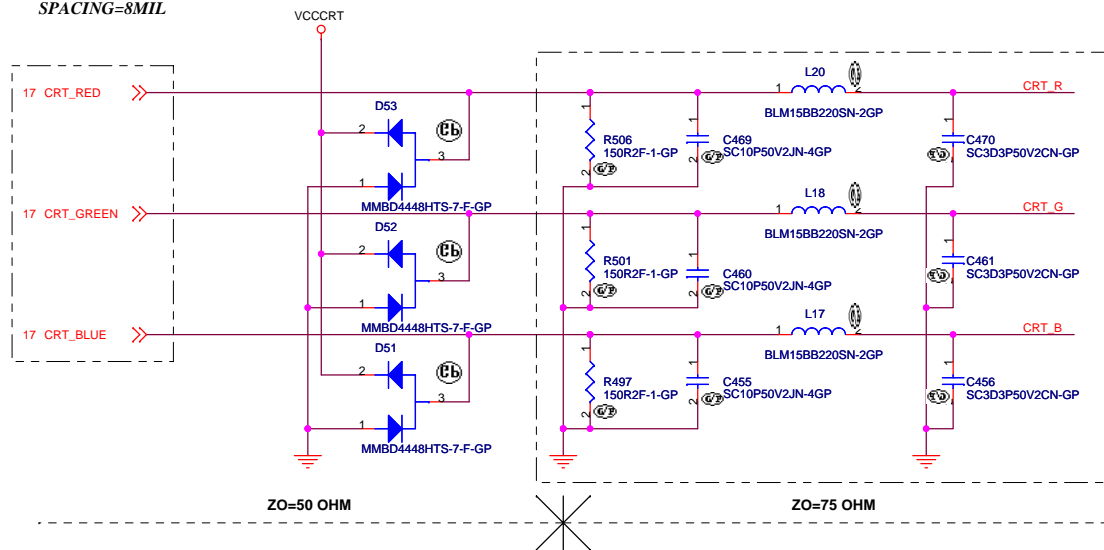
System DP: GMCH to SLICE Connector



<Variant Name>

<div>緯創資通</div> <div>Wistron Corporation</div> <div>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</div>		
Title Display Port AC Coupling		
Size A4	Document Number Caramel-1	Rev SC
Date: Monday, July 14, 2008	Sheet 18 of 62	

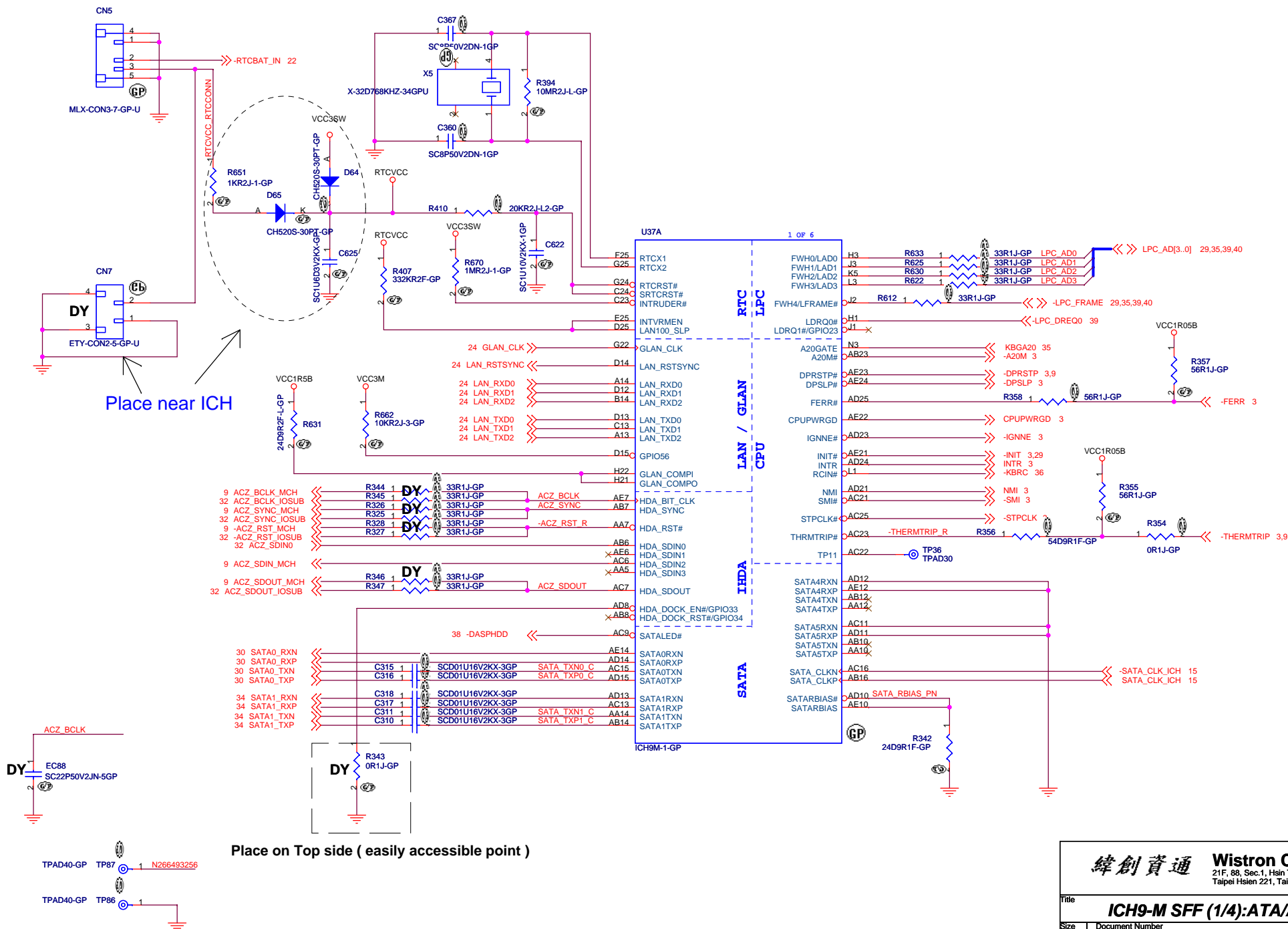
GND GUARDING  
EACH SIGNAL WIDTH DEPENDS ON ZO(TRACE IMPEDANCE)  
SPACING=8MIL



U18: Toshiba TC7W126FU

SLICE\_VSYNC 34  
SLICE\_HSYNC 34

<b>緯創資通</b> Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title	
Size	
A3	
Document Number	
Caramel-1	
Date: Monday, July 14, 2008	
Sheet 19 of 62	
Rev SC	

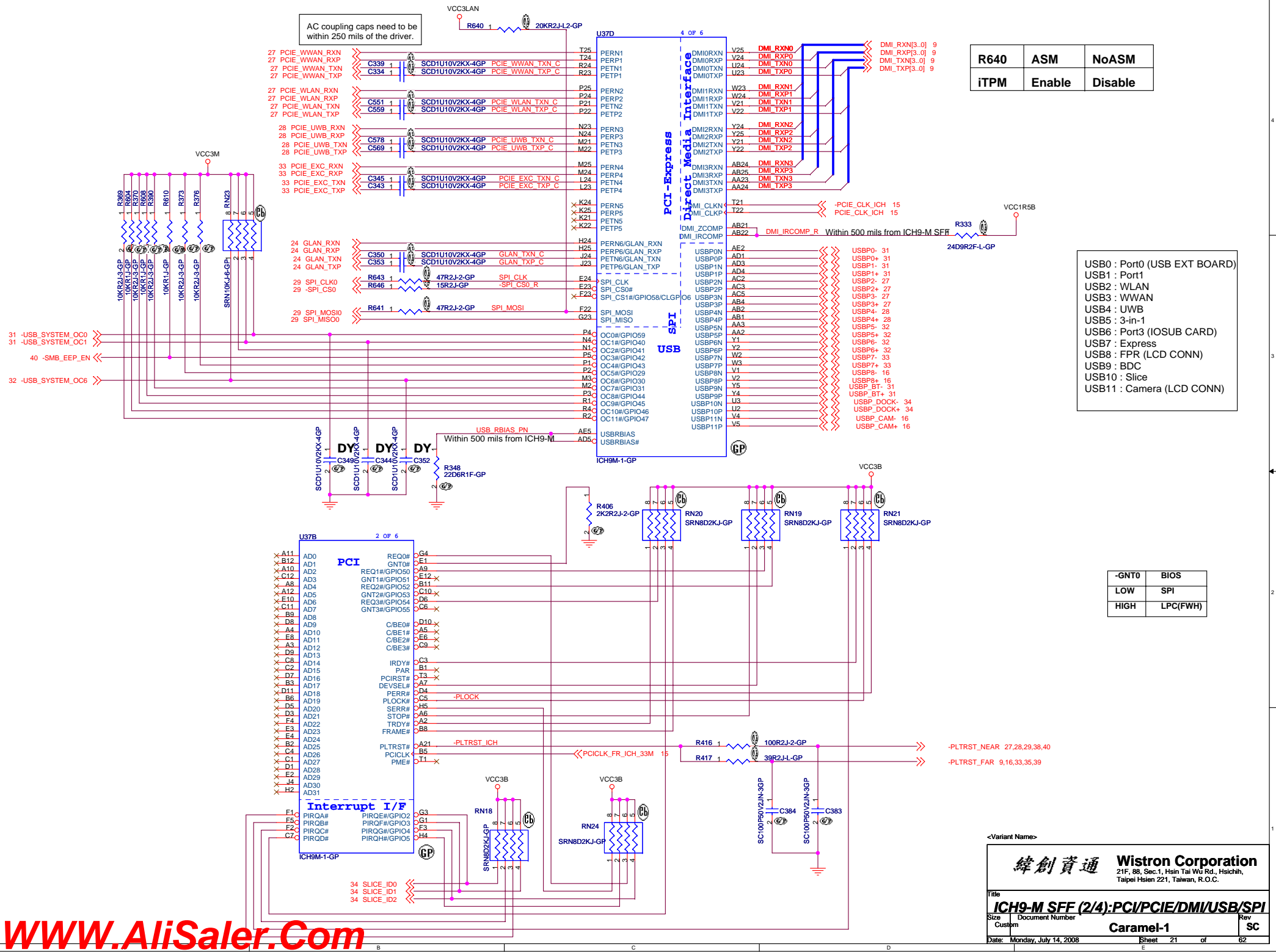


緯創資通 Wistron Corporation  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title ICH9-M SFF (1/4):ATA/AC97/LPC

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R640	ASM	NoASM
ITPM	Enable	Disable

USB0 : Port0 (USB EXT BOARD)  
 USB1 : Port1  
 USB2 : WLAN  
 USB3 : WWAN  
 USB4 : UWB  
 USB5 : 3-in-1  
 USB6 : Port3 (IOSUB CARD)  
 USB7 : Express  
 USB8 : FPR (LCD CONN)  
 USB9 : BDC  
 USB10 : Slice  
 USB11 : Camera (LCD CONN)

-GNT0	BIOS
LOW	SPI
HIGH	LPC(FWH)

<Variant Name>

**緯創資通**

**Wistron Corporation**  
 21F, 88, Sec.1, Hsin Tai Wd Rd., Hsichih,  
 Taipei Hsien 221, Taiwan, R.O.C.

Title  
**ICH9-M SFF (2/4):PCI/PCIE/DMI/USB/SPI**

Size  
 Custom

Date: Monday, July 14, 2008

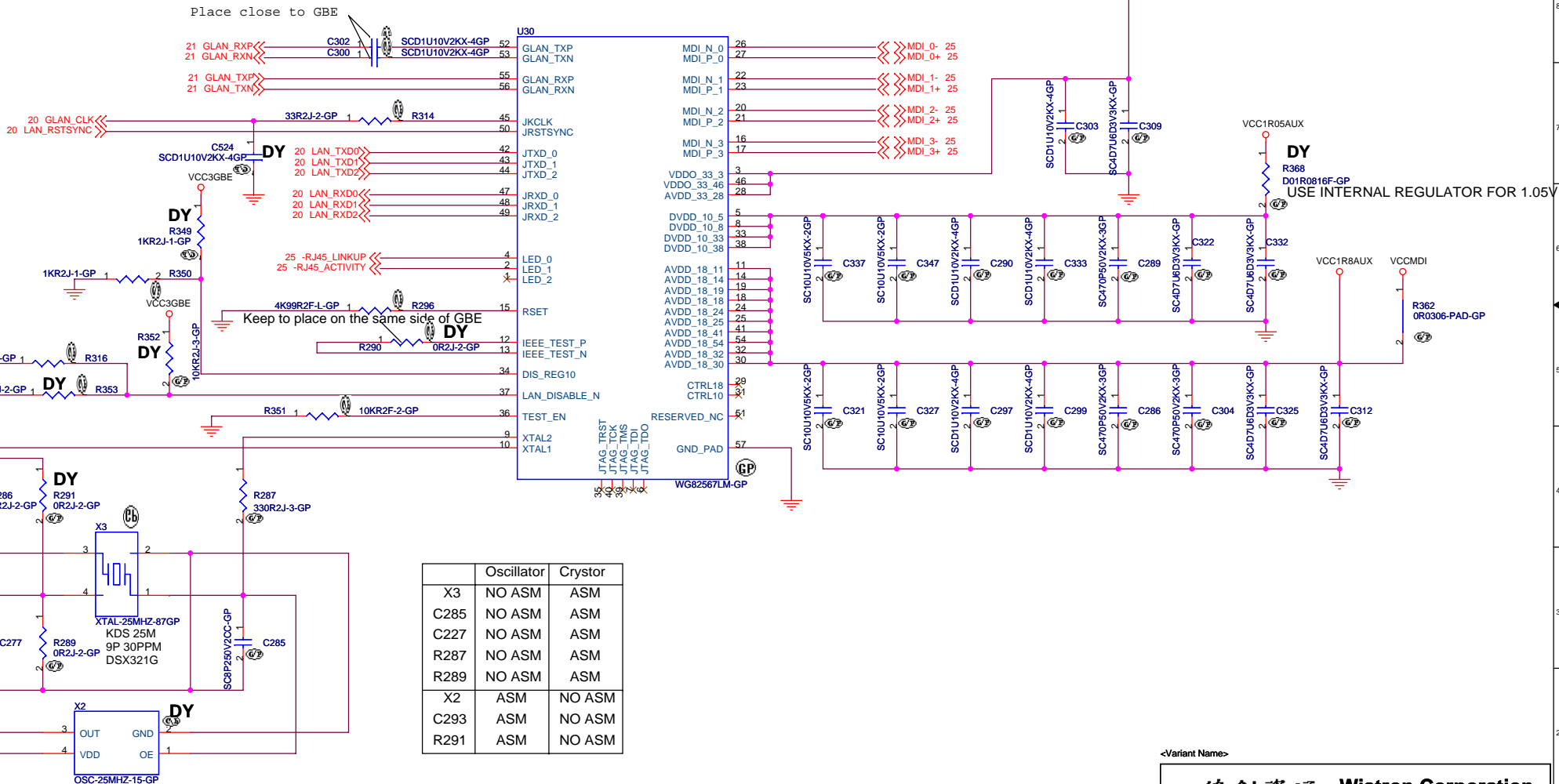
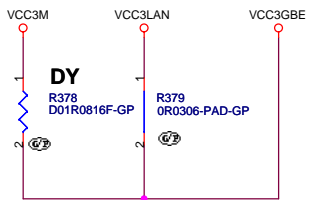
Document Number  
**Caramel-1**

Rev  
**SC**

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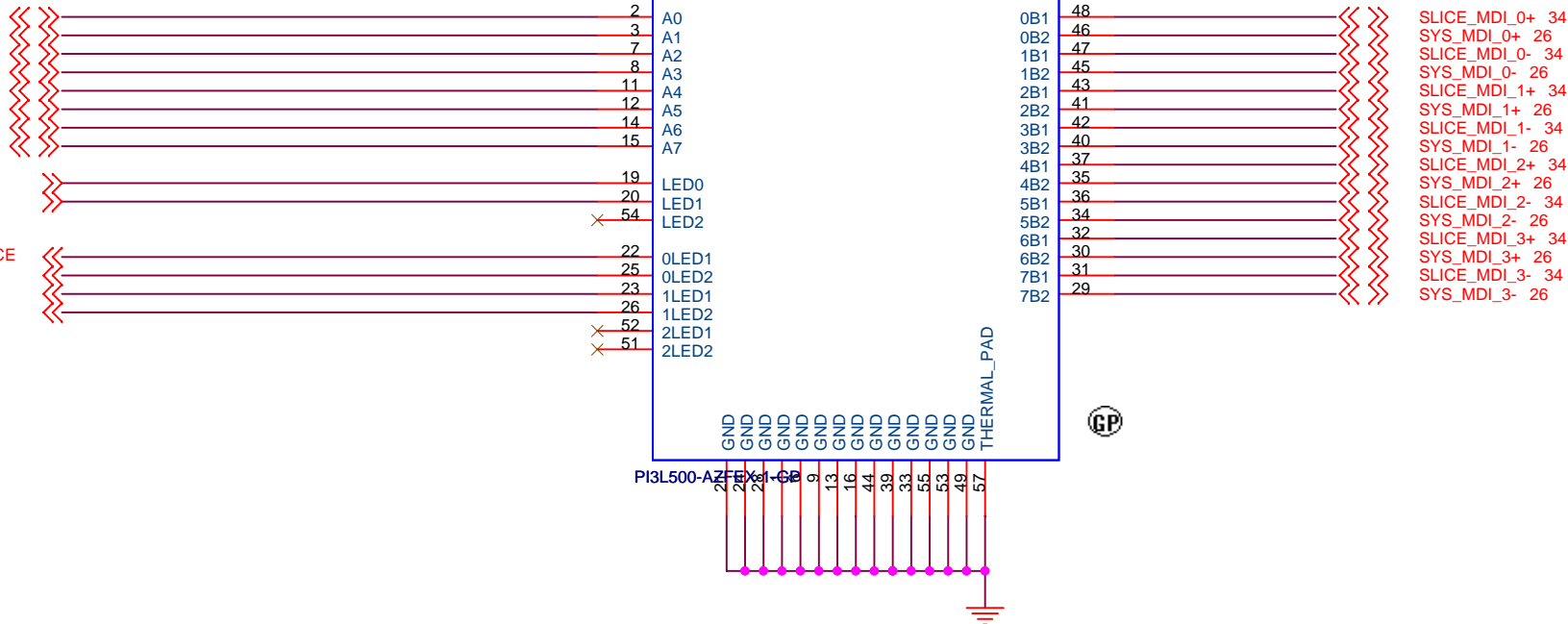
	Oscillator	Crystor
X3	NO ASM	ASM
C285	NO ASM	ASM
C227	NO ASM	ASM
R287	NO ASM	ASM
R289	NO ASM	ASM
X2	ASM	NO ASM
C293	ASM	NO ASM
R291	ASM	NO ASM

<Variant Name>

<b>緯創資通 Wistron Corporation</b> 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
<b>GBE BOAZMAN</b>	
Title Size A3 Date: Monday, July 14, 2008	Document Number <b>Caramel-1</b> Sheet 24 of 62
Rev <b>SC</b>	




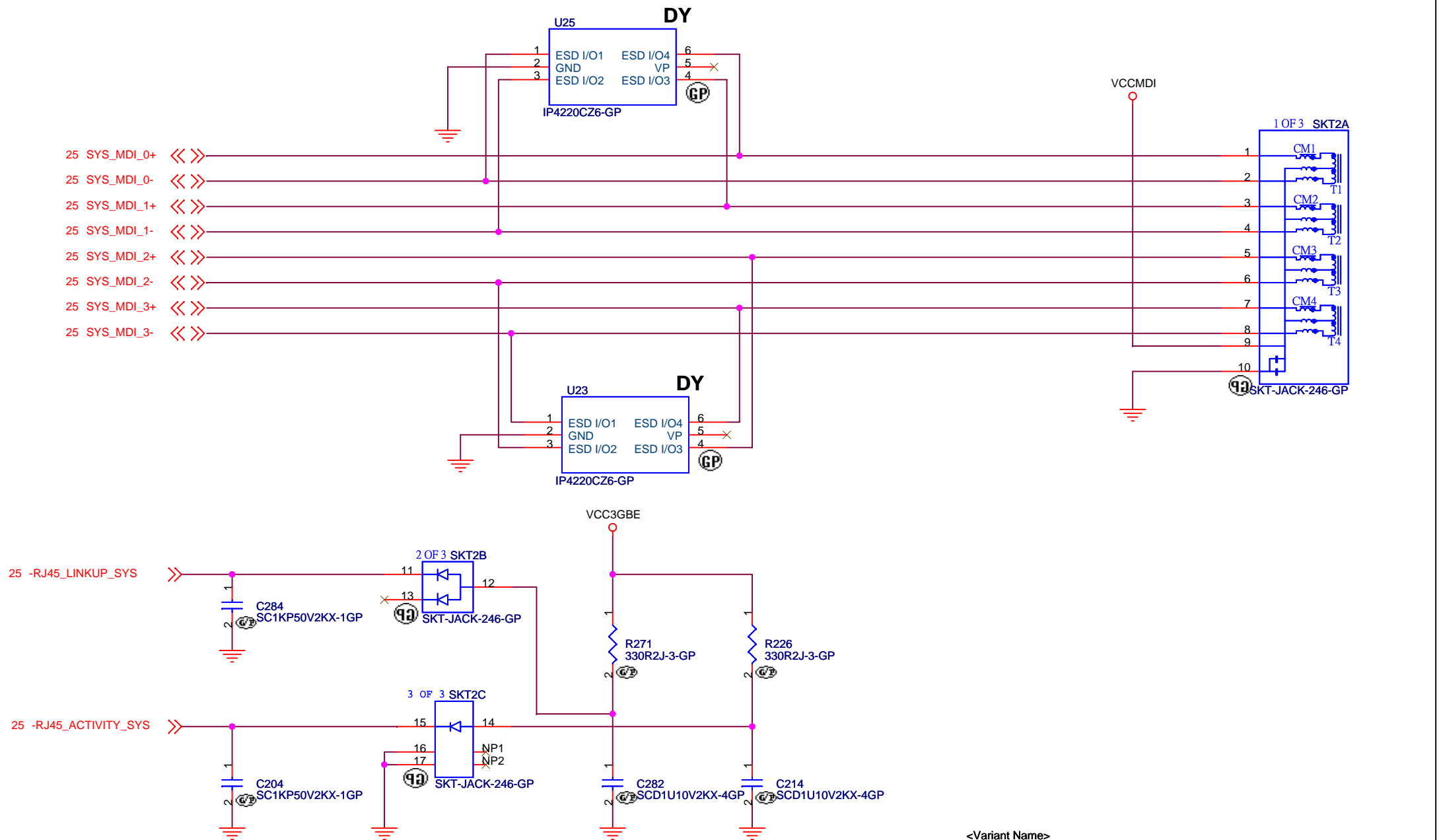
24 MDI\_0+  
24 MDI\_0-  
24 MDI\_1+  
24 MDI\_1-  
24 MDI\_2+  
24 MDI\_2-  
24 MDI\_3+  
24 MDI\_3-  
  
24 -RJ45\_ACTIVITY  
24 -RJ45\_LINKUP  
  
34 -RJ45\_ACTIVITY\_SLICE  
26 -RJ45\_ACTIVITY\_SYS  
34 -RJ45\_LINKUP\_SLICE  
26 -RJ45\_LINKUP\_SYS




		Vendor P/N	Wistron P/N
1st	Pericom	PI3L500AZFEX	73.3L500.003
2nd	TI	TS3L500AERHUR	73.3L500.A0V
3rd	MAXIM	MAX4927ETN+	74.04927.073

<Variant Name>

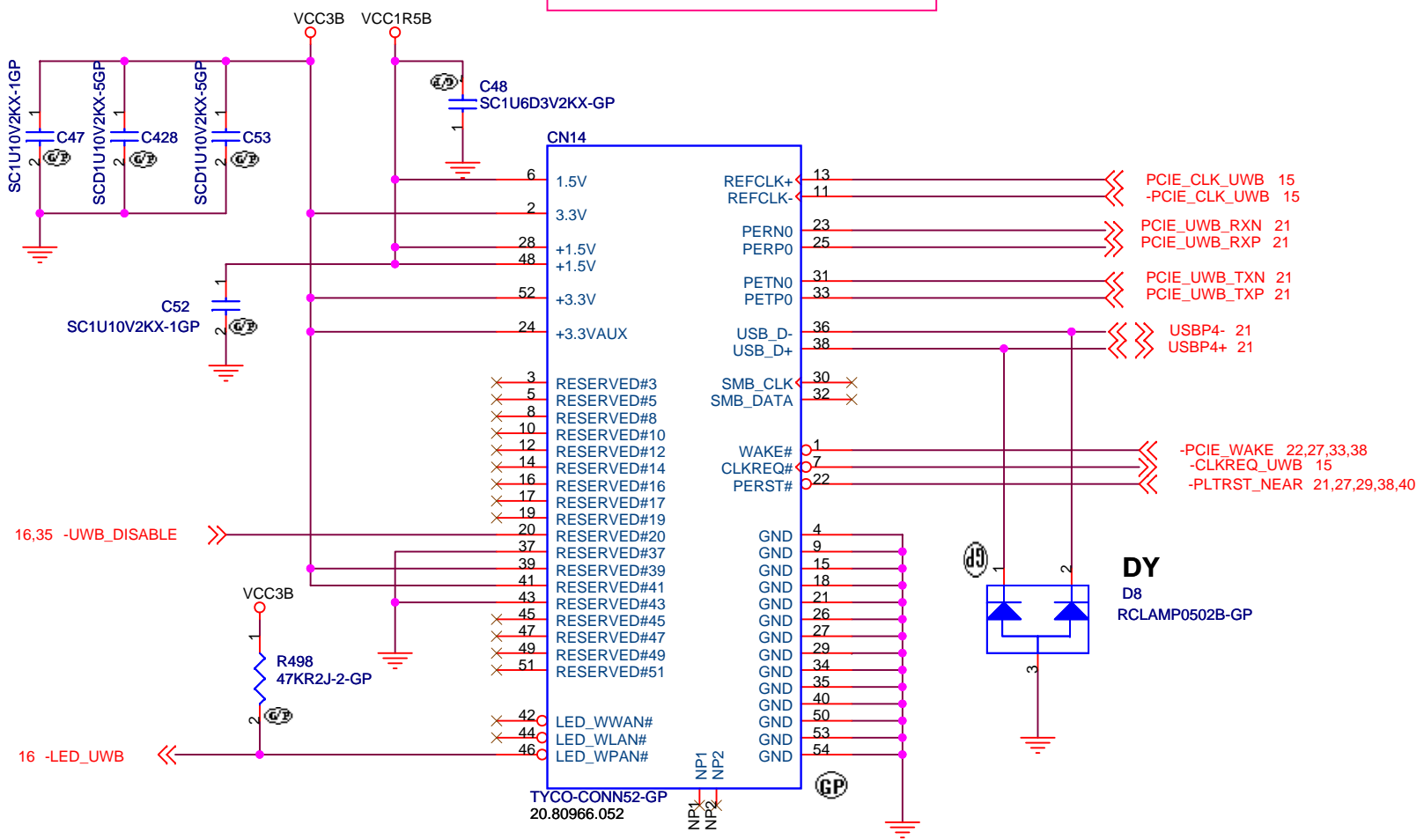
 <b>Wistron Corporation</b> 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title	
<b>GBE LAN SW</b>	
Size A4	Document Number <b>Caramel-1</b>
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Sheet 25 of 62	



<Variant Name>

		<b>Wistron Corporation</b> 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
<b>RJ45 CONN</b>			
Size A4	Document Number <b>Caramel-1</b>		Rev <b>SC</b>
Date:	Monday, July 14, 2008	Sheet 26 of 62	





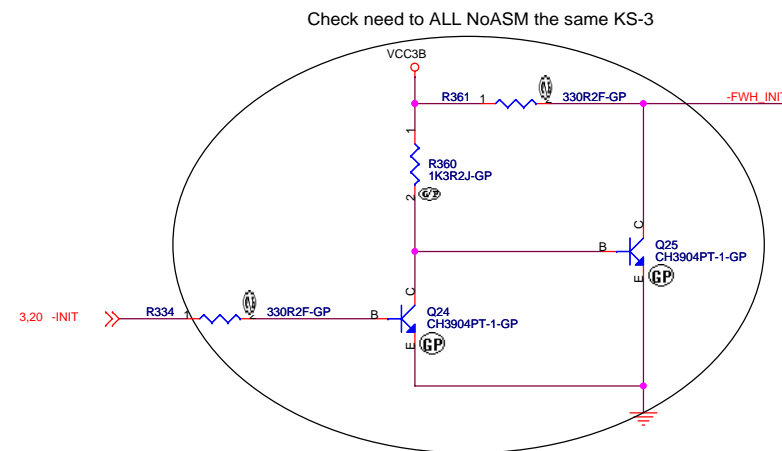
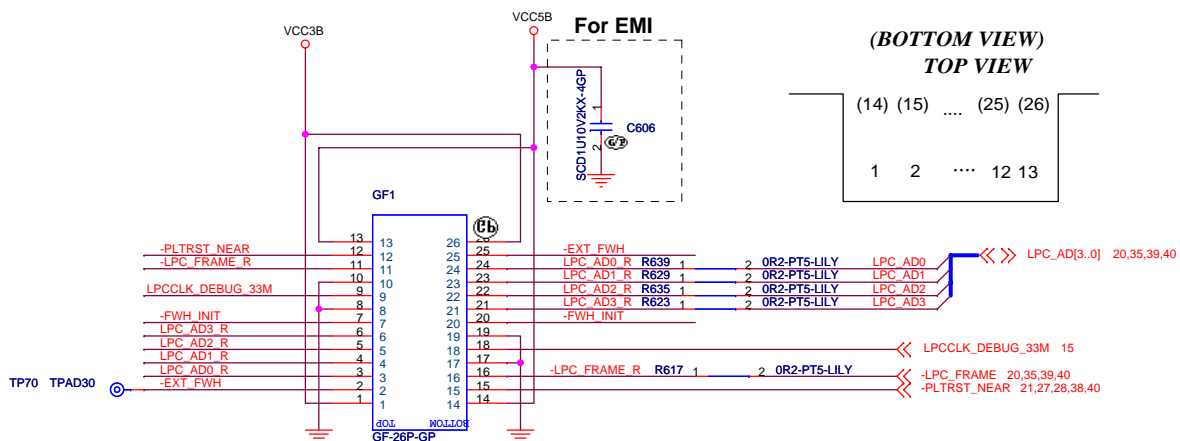
CN14  
1st: 20.80966.052(TYCO)  
2st:

DY  
D8  
RCLAMP0502B-GP

<Variant Name>

<b>緯創資通 Wistron Corporation</b> 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		
Title <b>MINI CARD SLOT 2</b>		
Size A4	Document Number <b>Caramel-1</b>	Rev <b>SC</b>
Date: Monday, July 14, 2008		Sheet 28 of 62

# Golden Finger for Debug Board



64Mbit SPI FLASH :

Macronix MX25L6405DZNI-12G: 72.25640.B01

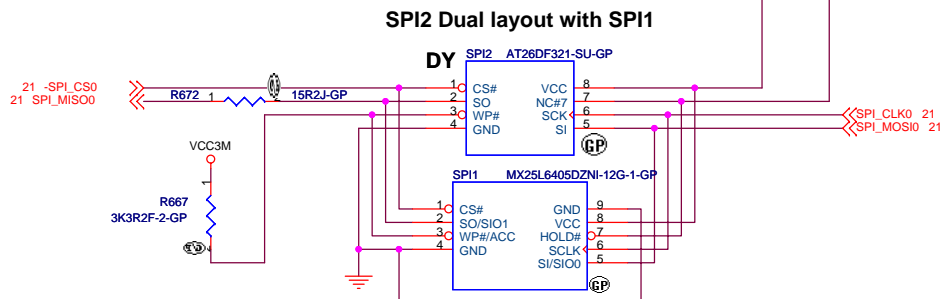
Winbond W25X64VZEIG

ATMEL XXXXXX

32Mbit SPI FLASH :

ATMEL/AT26DF321-SU:72.26321.A01

MXIC/MX25L3205DM2I-12G:72.25325.A01



SO8 and WSON8 are both supported!

<Variant Name>

緯創資通 Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title

SPI

Size

A3 Document Number

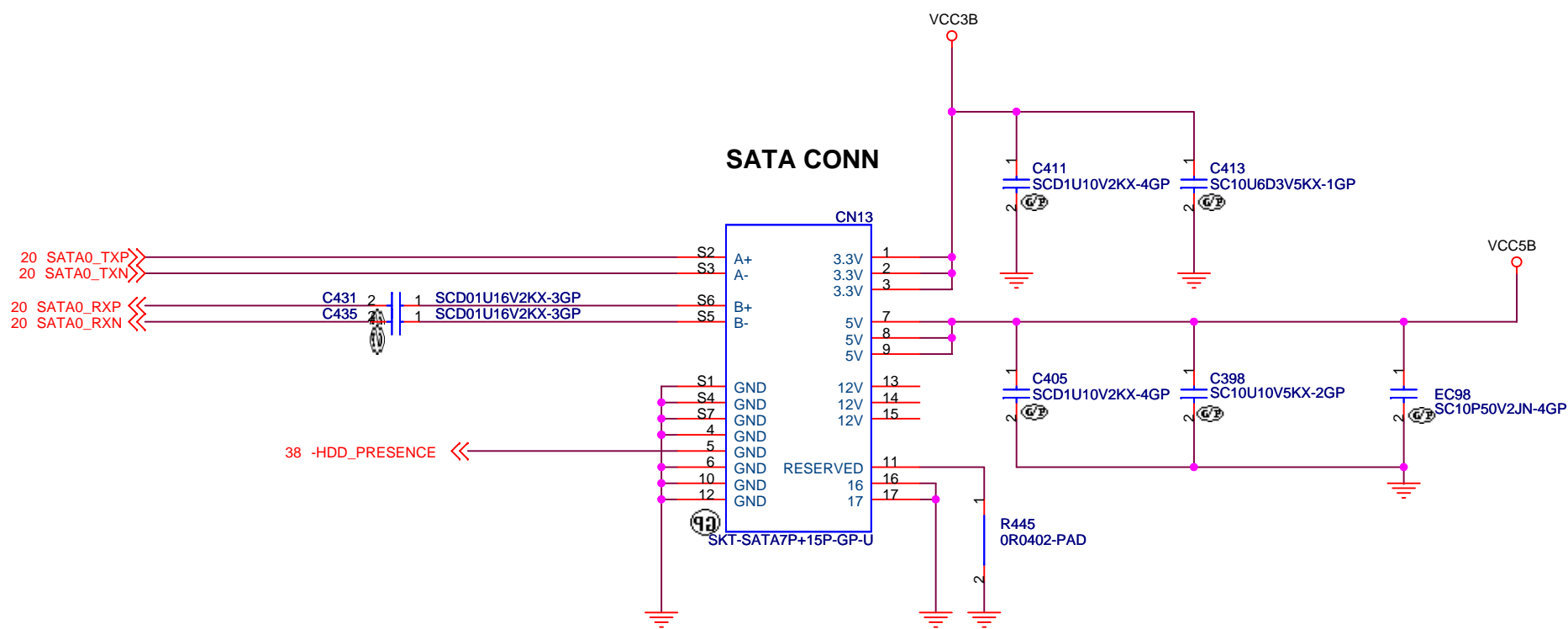
Caramel-1

Rev


SC

Date: Monday, July 14, 2008

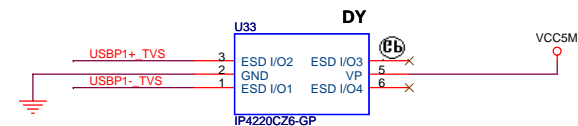
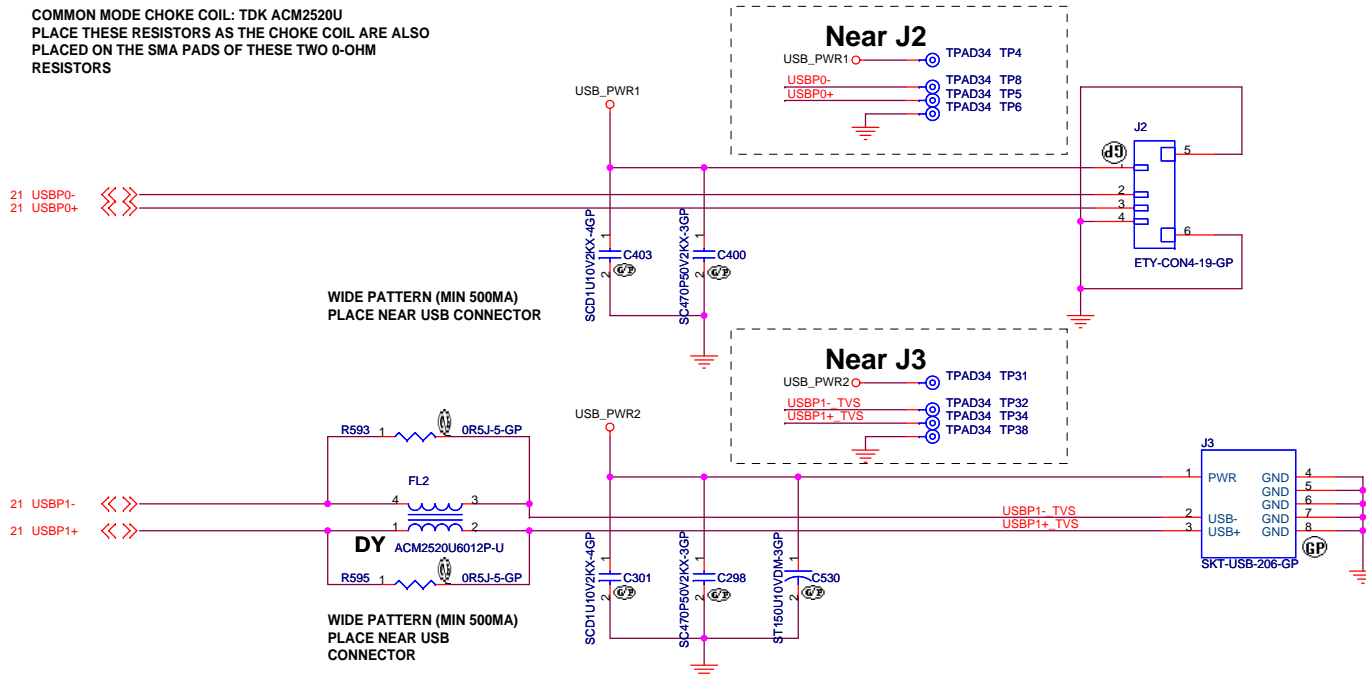
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<Core Design>

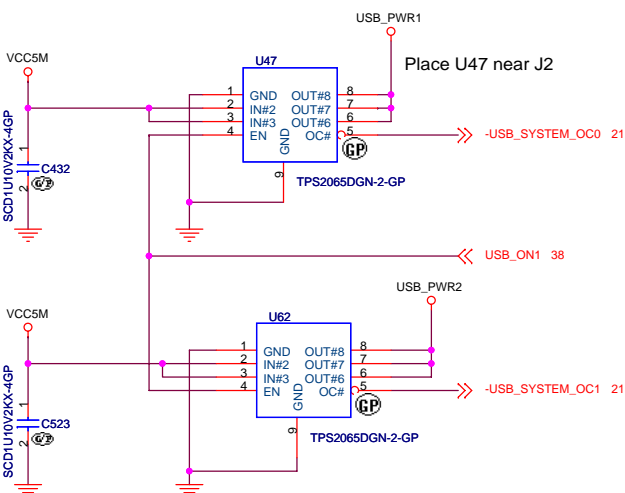
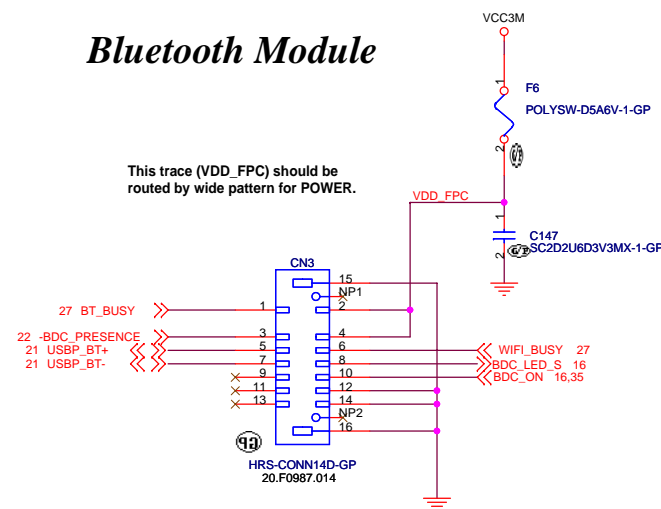
		<b>Wistron Corporation</b> 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
<b>2.5 inch SATA Connector</b>			
Size A4	Document Number <b>Caramel-1</b>		Rev <b>SC</b>
Date:	Monday, July 14, 2008	Sheet 30 of	62

COMMON MODE CHOKE COIL: TDK ACM2520U  
PLACE THESE RESISTORS AS THE CHOKE COIL ARE ALSO  
PLACED ON THE SMA PADS OF THESE TWO 0-OHM  
RESISTORS



## Bluetooth Module

This trace (VDD\_FPC) should be routed by wide pattern for POWER.



	TI P/N	WISTRON P/N
1	TPS2065DGN-GP	74.02065.079
2	TPS2065DGN-1-GP	74.02065.A79

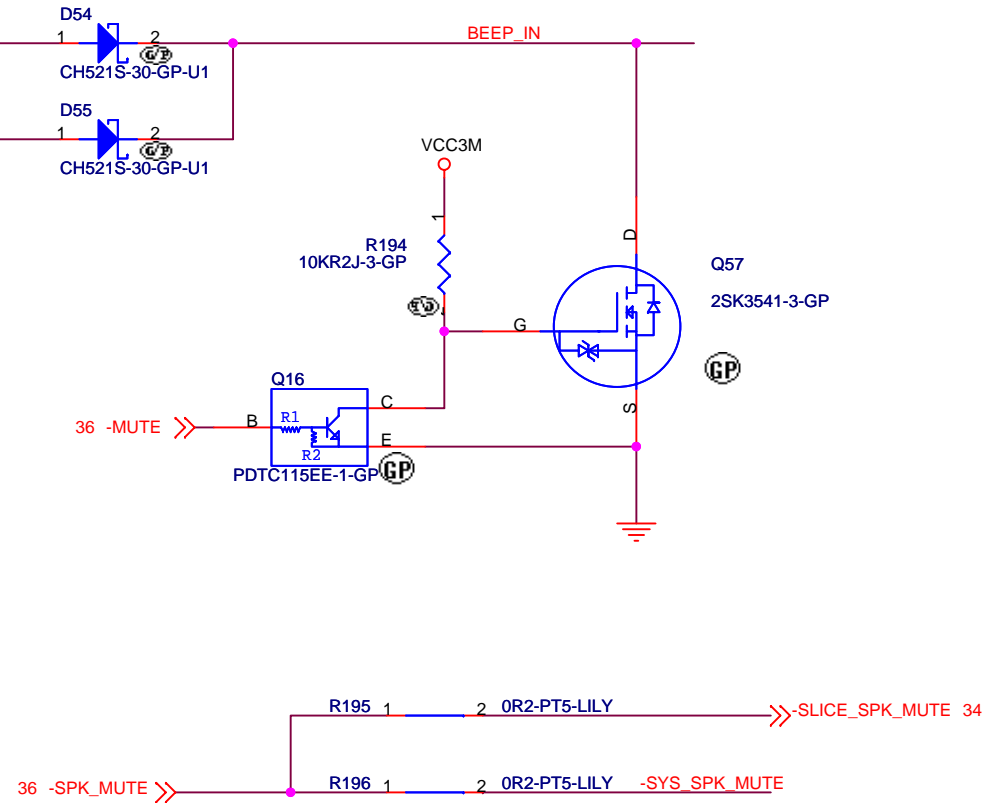
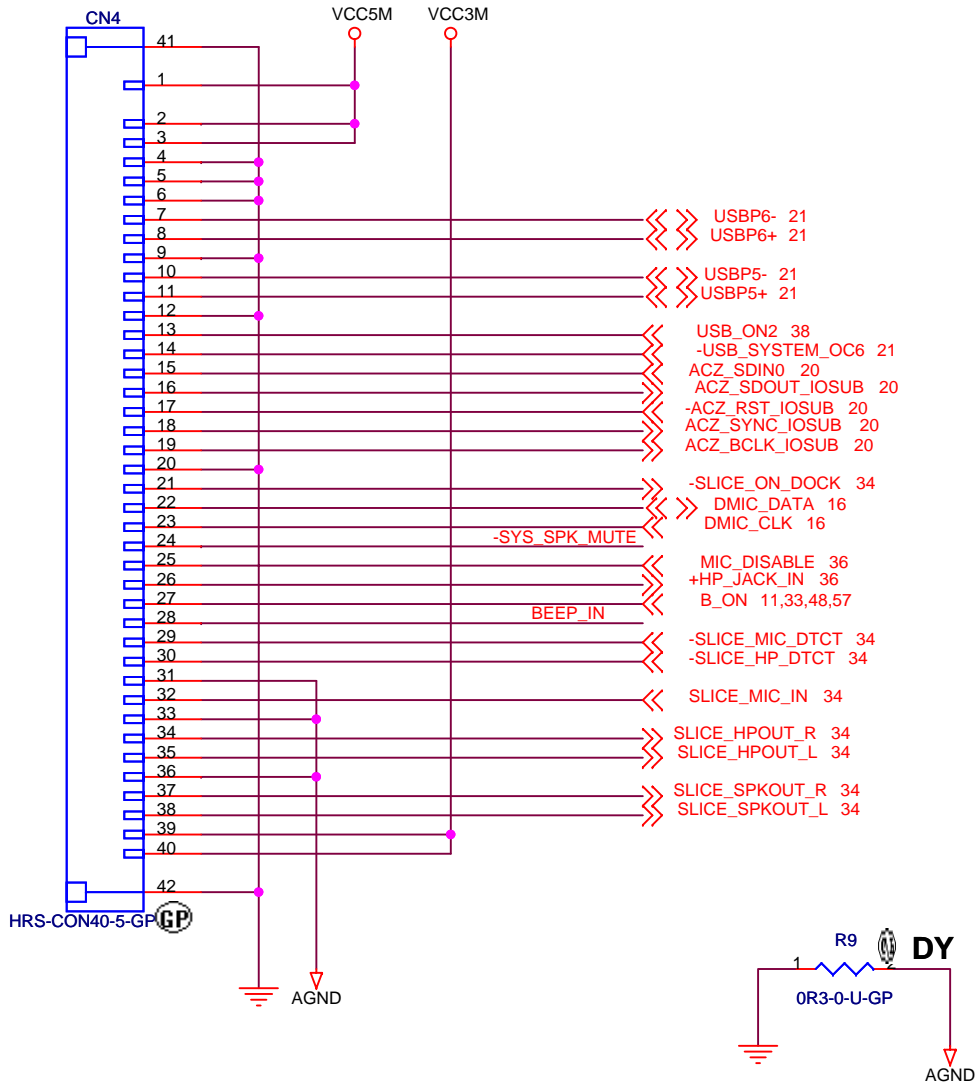
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**緯創資通 Wistron Corporation**  
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Taipei Hsien 221, Taiwan, R.O.C.

Title  
**USB POWER / CONNECTOR**

Size A3 Document Number  
**Caramel-1** Rev SC

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<Variant Name>

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Title

**SUB-CARD CONNECTOR**

Size  
A4

Document Number

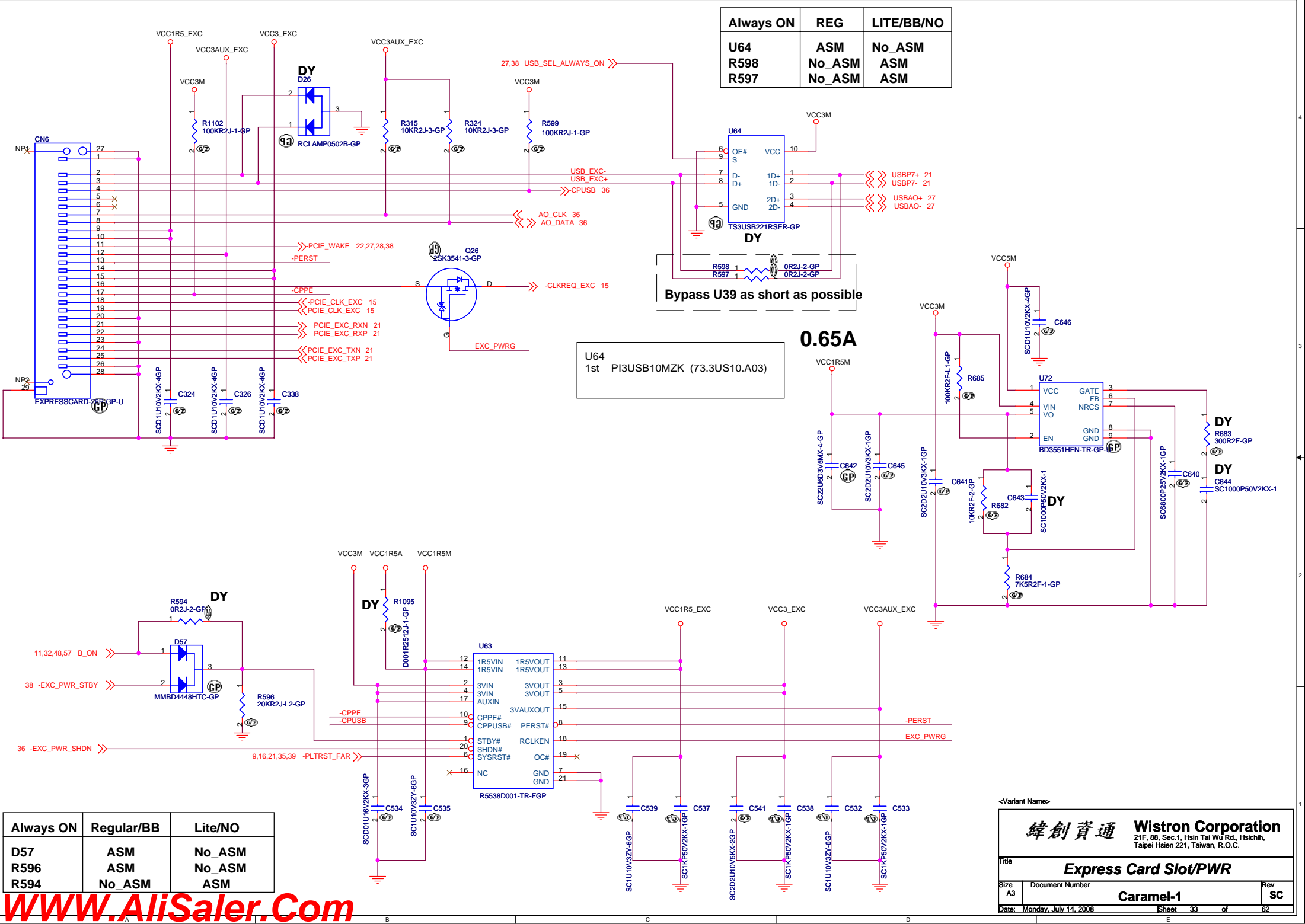
**Caramel-1**

Rev  
SC

Date: Monday, July 14, 2008

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Always ON	REG	LITE/BB/NO
U64	ASM	No_ASM
R598	No_ASM	ASM
R597	No_ASM	ASM

U64  
1st PI3USB10MZK (73.3US10.A03)

0.65A

Always ON	Regular/BB	Lite/NO
D57	ASM	No_ASM
R596	ASM	No_ASM
R594	No_ASM	ASM

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Express Card Slot/PWR

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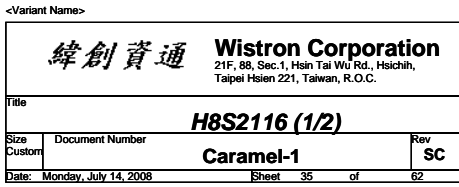
Monday, July 14, 2008

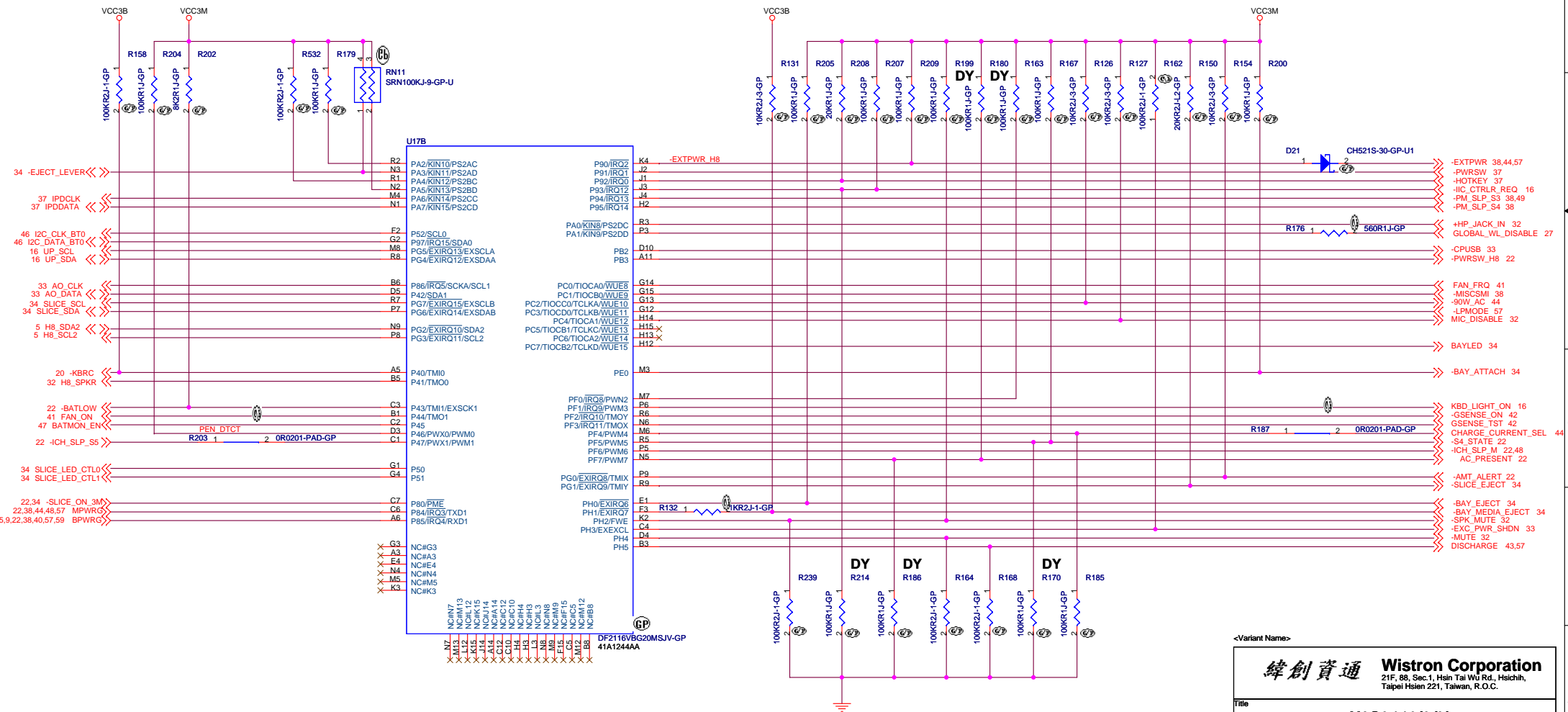
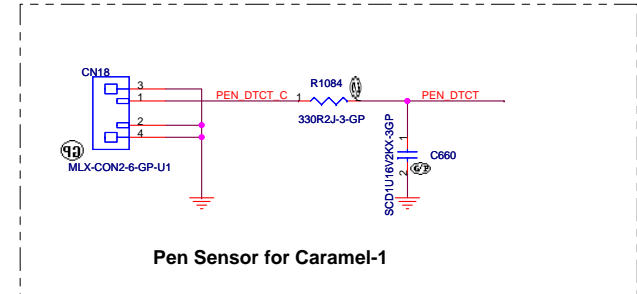
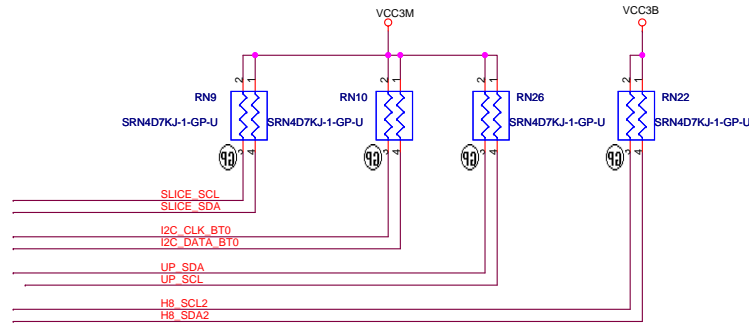
Sheet

Date

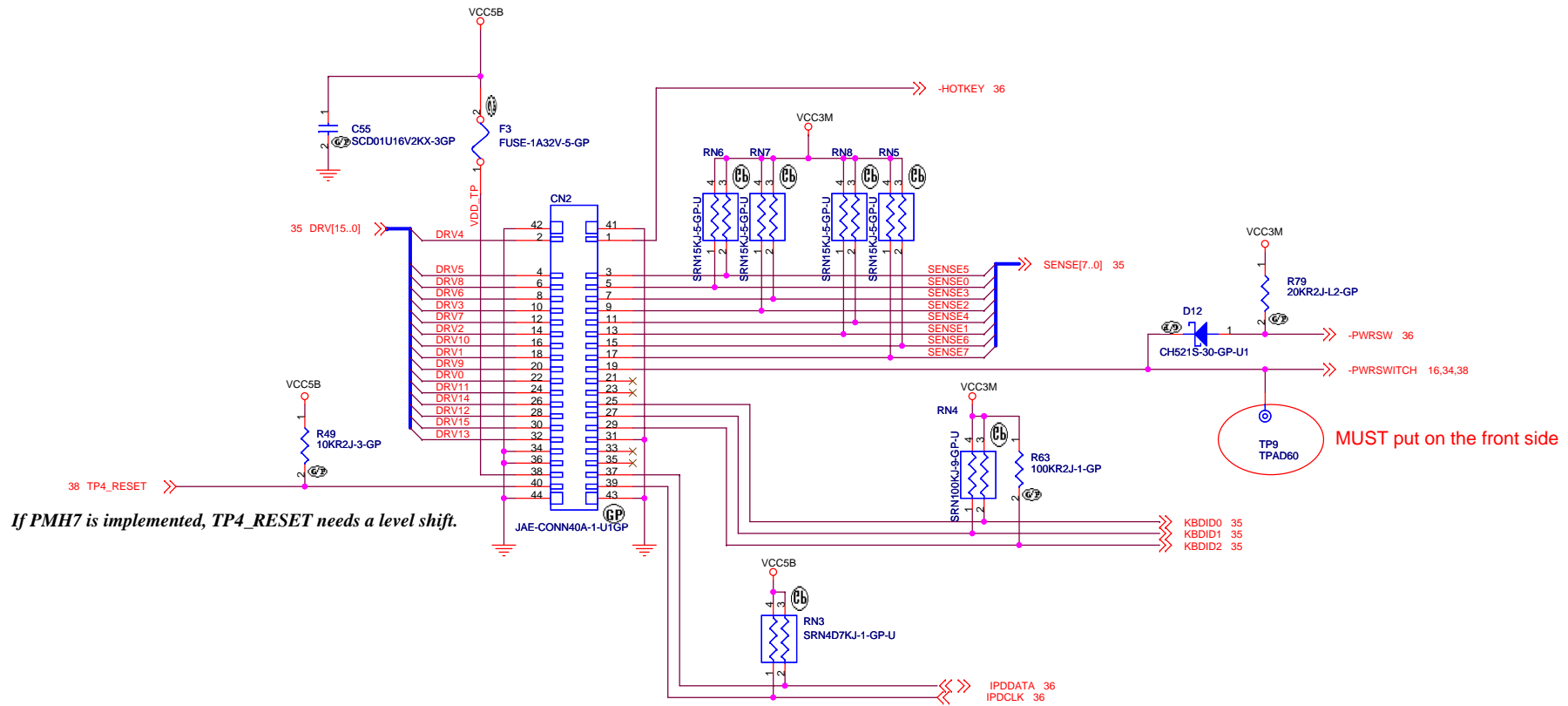
WWW.AliSaler.Com



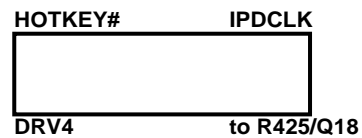





### *Keyboard Connector*



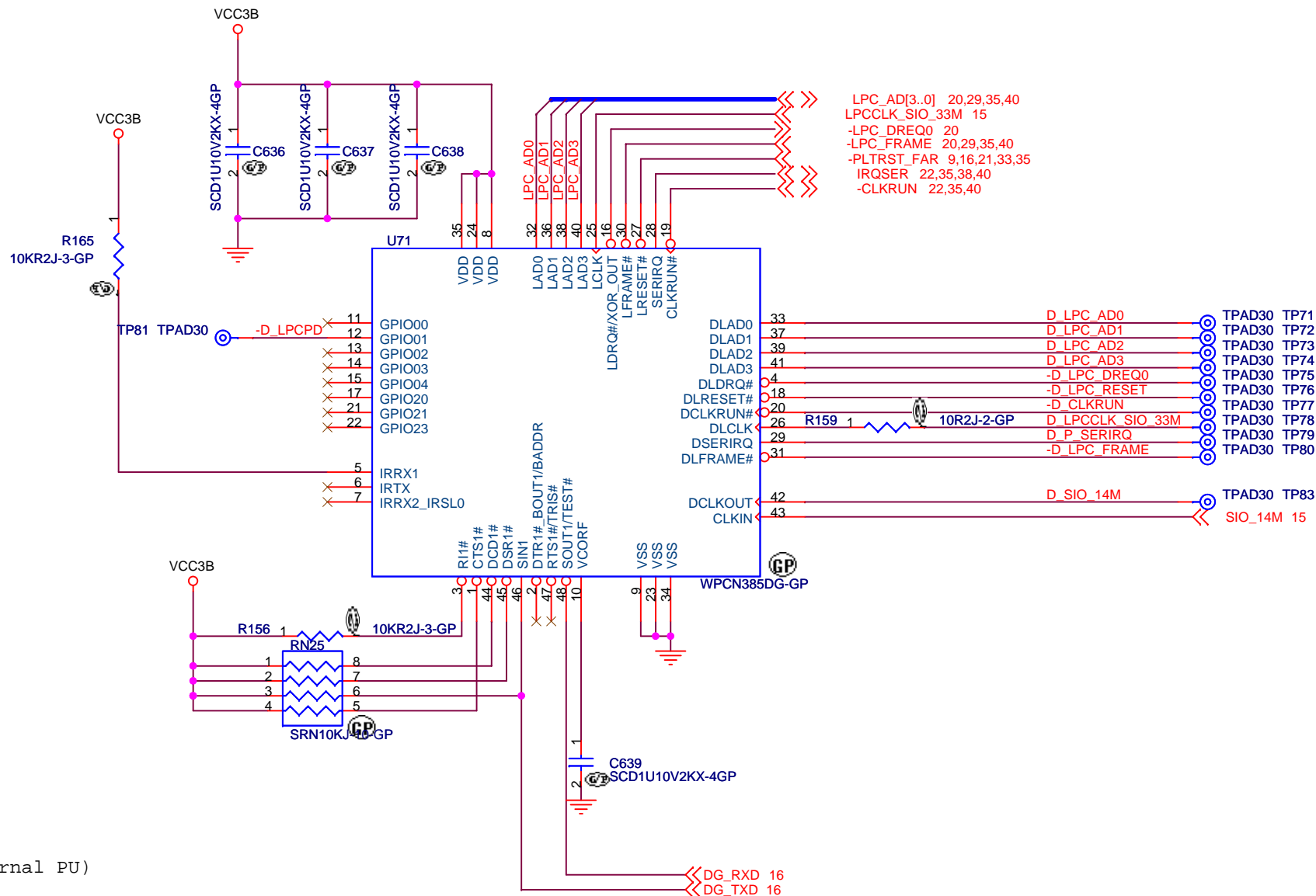
*If PMH7 is implemented, TP4\_RESET needs a level shift.*



### Keyboard Connector Top View

<Variant Name>			
		<b>Wistron Corporation</b> 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
<b>KEYBOARD CONN</b>			
Size A3	Document Number	Rev	
	<b>Caramel-1</b>	<b>SC</b>	
Date: Monday, July 14, 2008		Sheet 37 of	62

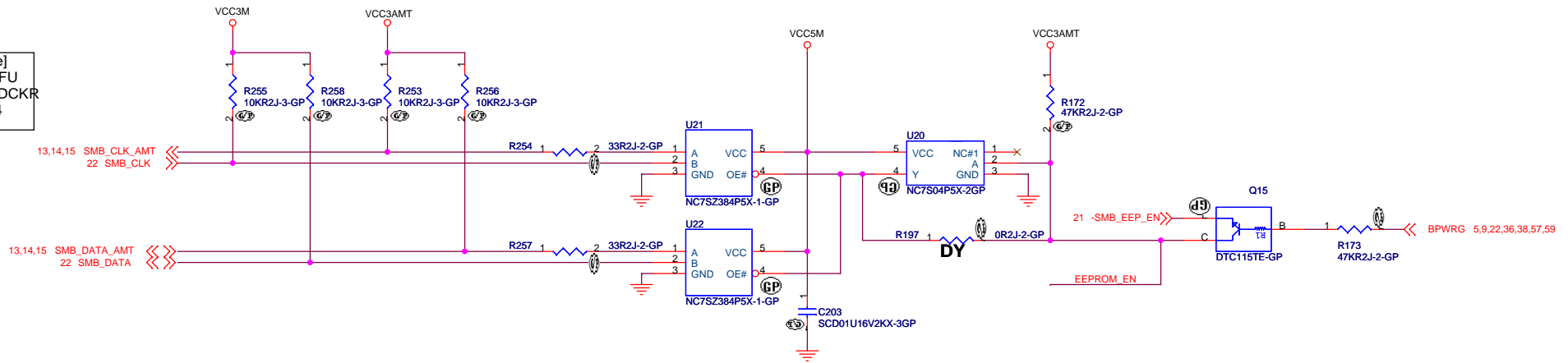




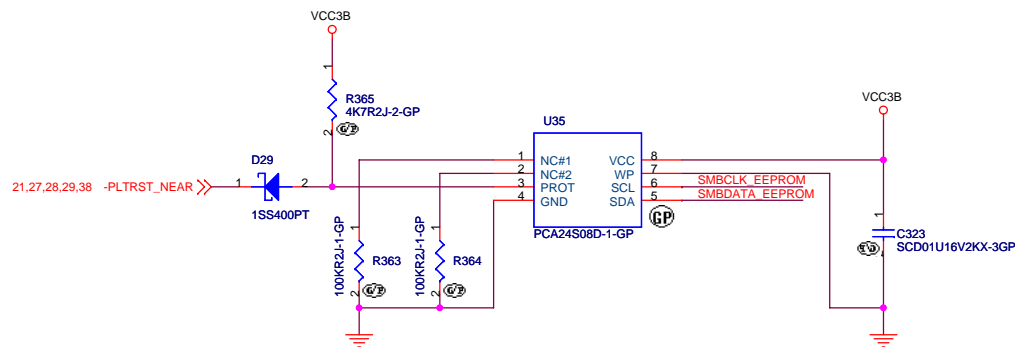
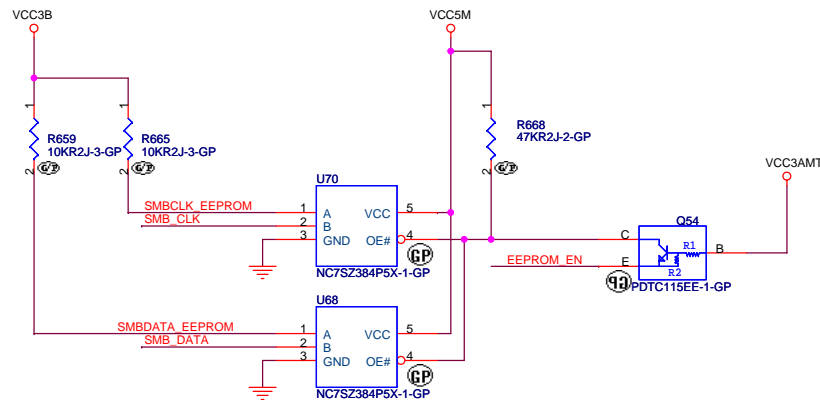
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<div>緯創資通</div>		<div>Wistron Corporation</div> <div>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</div>	
Title			
WPCN385SDG			
Size A4	Document Number  Caramel-1		Rev SC
Date:	Monday, July 14, 2008	Sheet 39 of	62

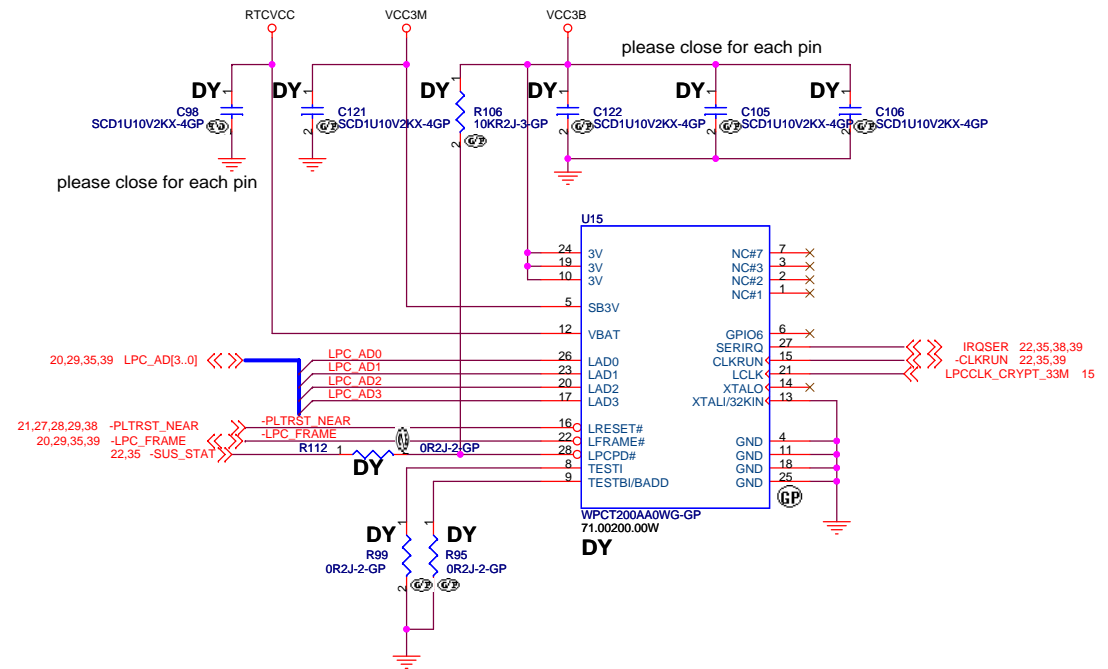
[Sourcing Candidate]  
Toshiba TC7SB384FU  
TI SN74CBT1G384DCKR  
Fairchild NC7SZ384



## EEPROM



## TCPA



<Core Design>

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Title			<b>EEPROM/TCPA</b>	
Size	Document Number	Caramel-1		Rev
A3				SC
Date:	Monday, July 14, 2008	Sheet	40	of 62



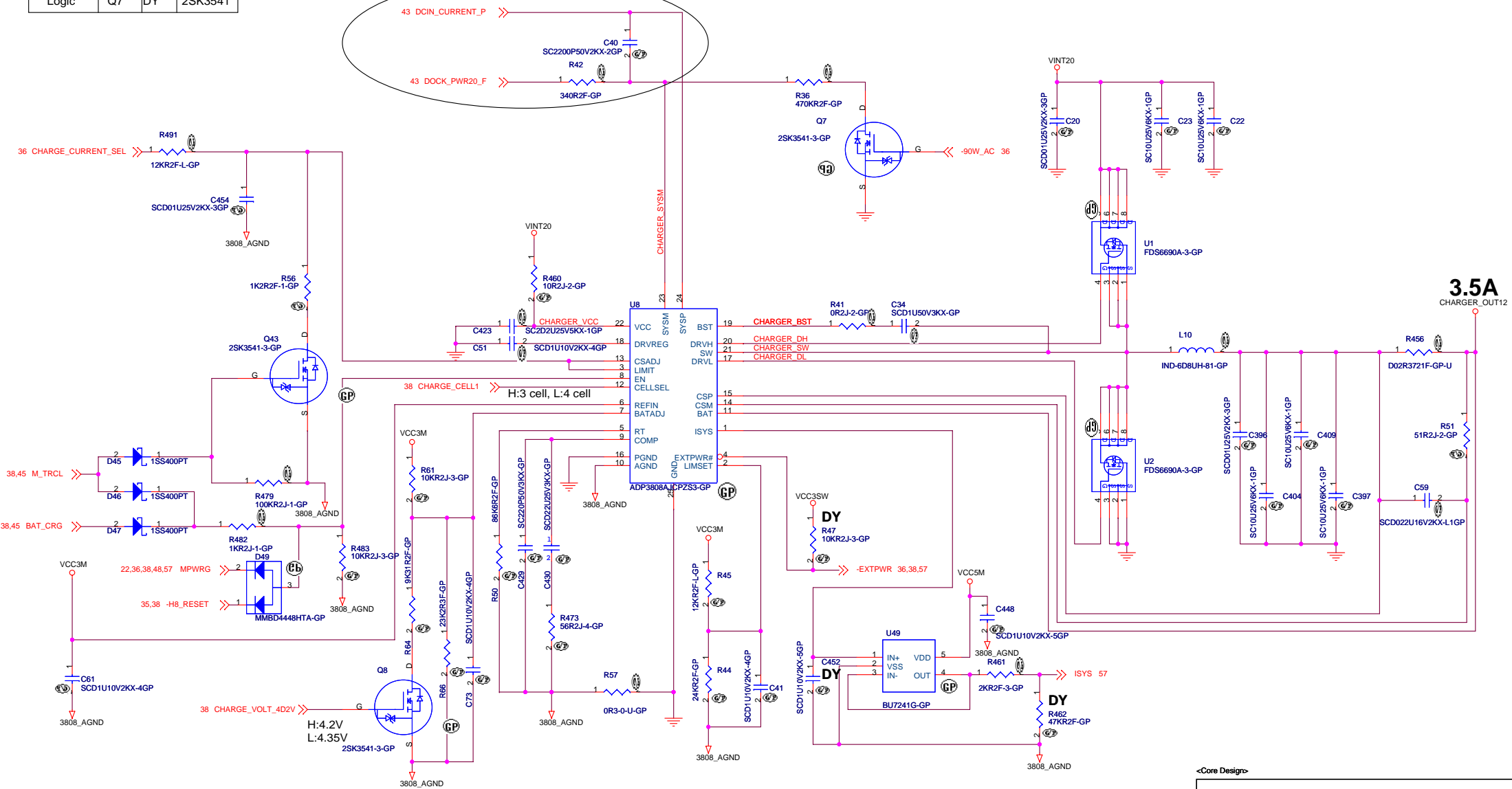






90W, 65W, auto selection mode			
MAX Power Setting	65W	90W	
-90W_AC	R36	DY	470K
Logic	Q7	DY	2SK3541

Keep these two signals as a pair routing !!

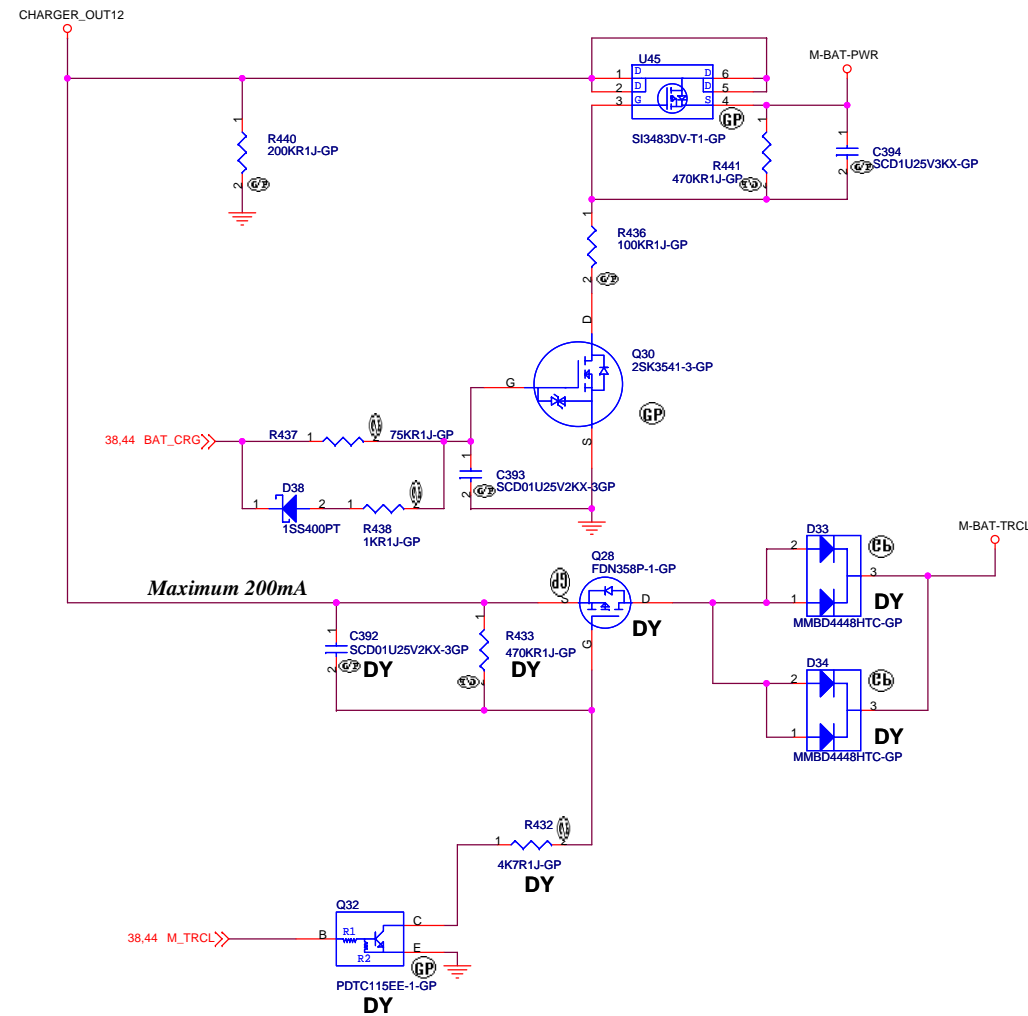


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Title			
<b>DC-IN AND CHARGER (2/2)</b>			
Size	Document Number		Rev
Custom			SC
Date: Monday, July 14, 2008	Sheet 44	of 62	

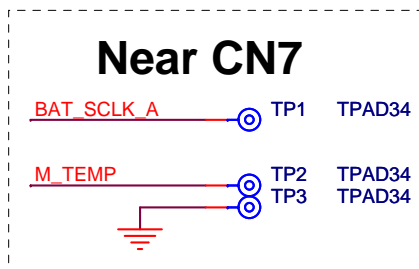
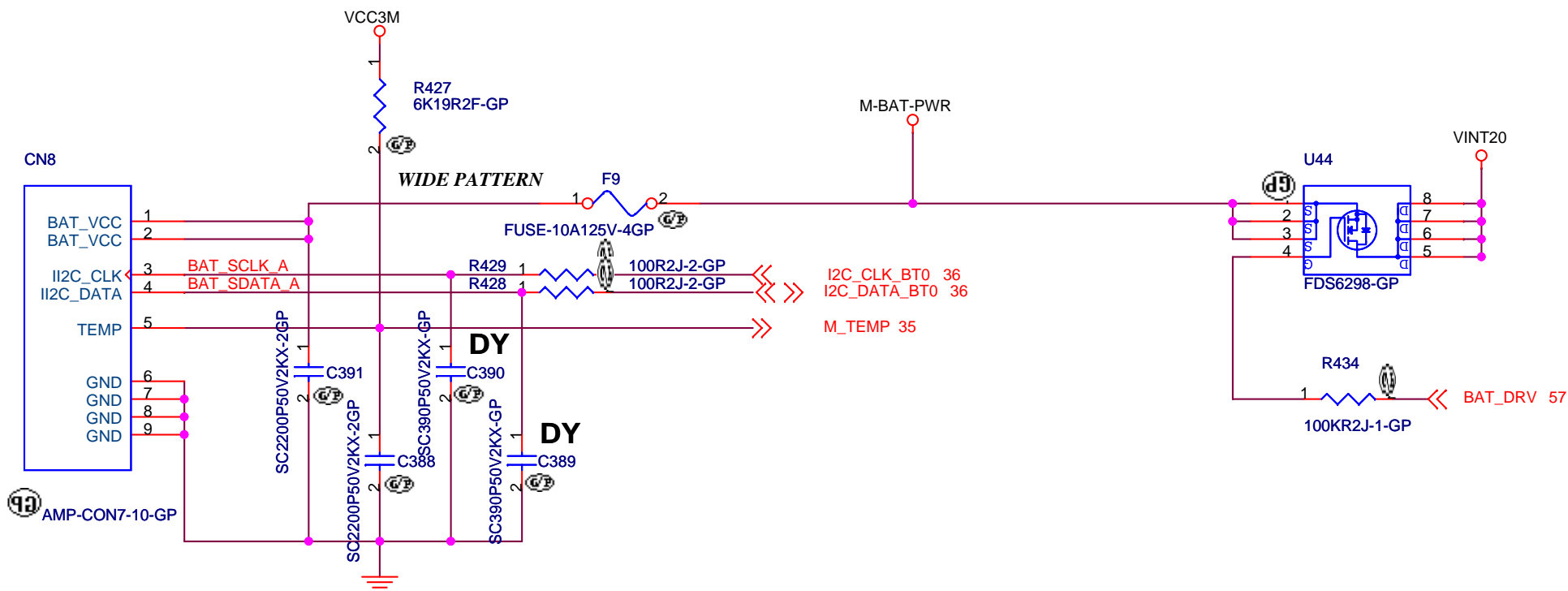


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Taipei Hsien 221, Taiwan, R.O.C.

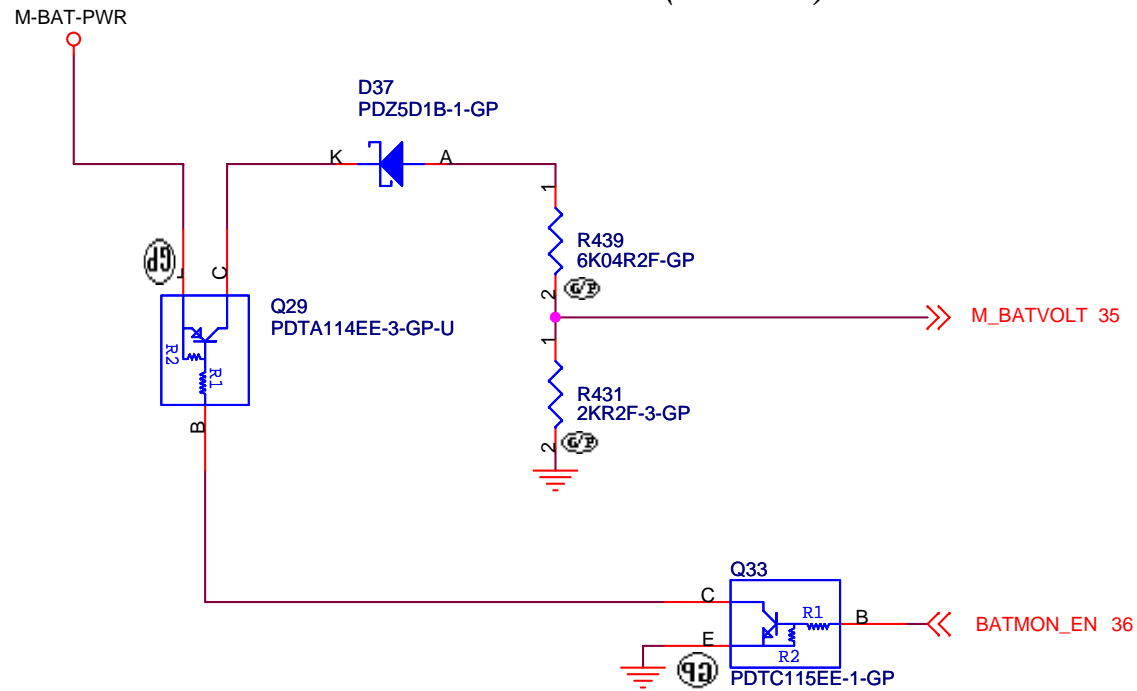
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CHARGER SELECT		
Size A3	Document Number Caramel-1	Rev SC
Date: Monday, July 14, 2008	Sheet 45 of 62	



<Core Design>

<div>緯創資通</div> <div>Wistron Corporation</div> <div>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</div>	
Title	
BATTERY INPUT	
Size A	Document Number Caramel-1
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$$V_{OUT} = 0.249 (V_{BAT} - 5)$$



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Title

**BATTERY MONITOR**

Size  
A

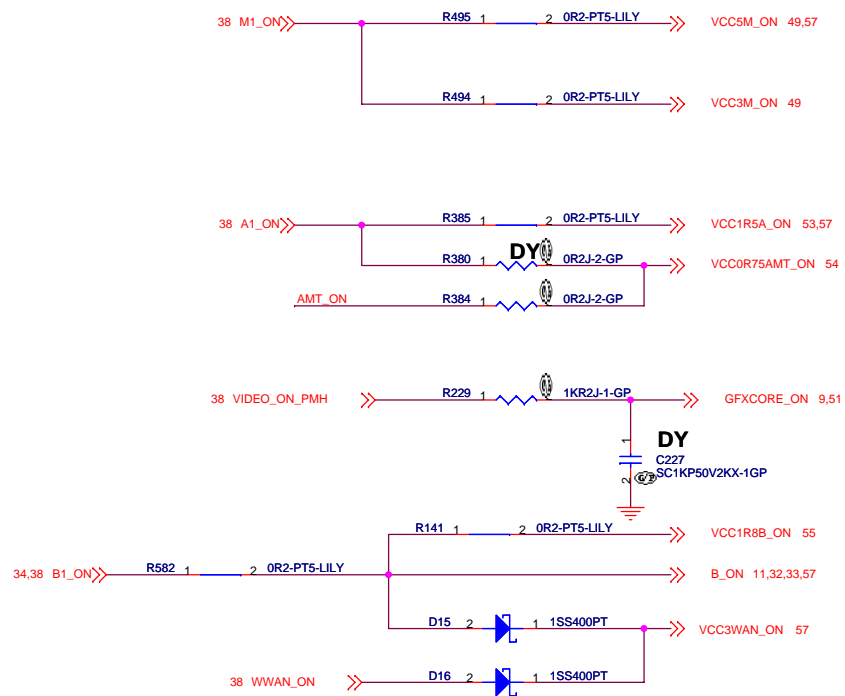
Document Number

**Caramel-1**

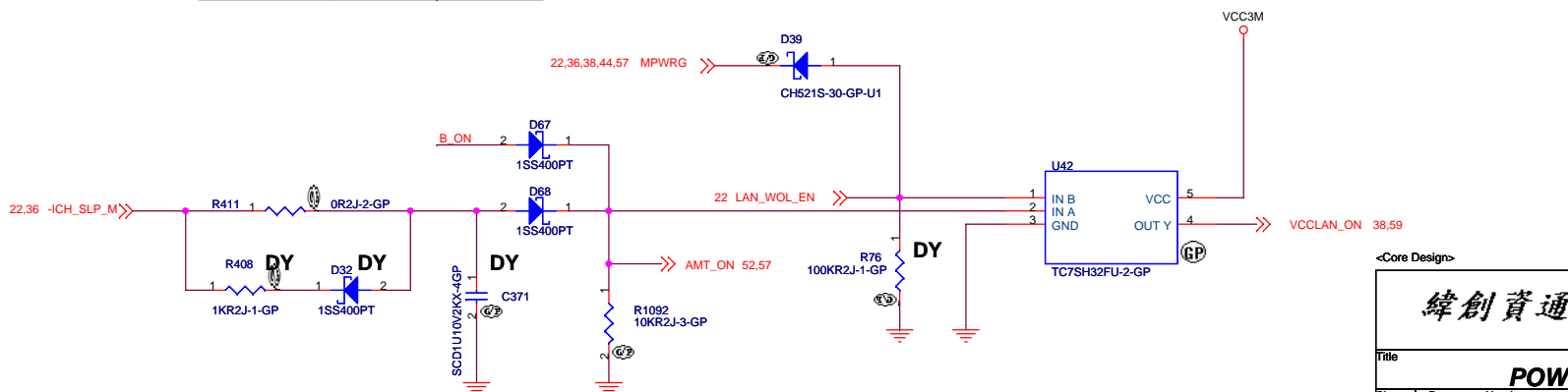
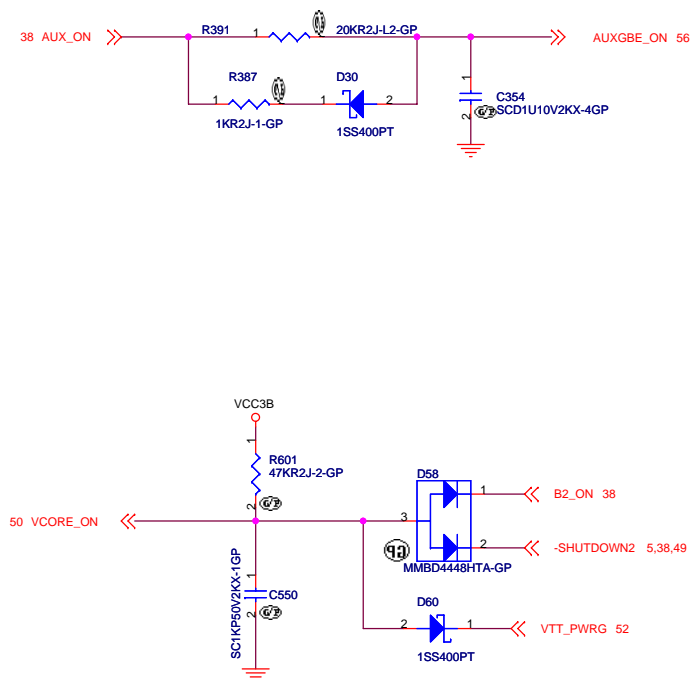
Rev  
**SA**

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Always ON	Enable	Disable
D15	ASM	No_ASM
D16	ASM	No_ASM



<Core Design>

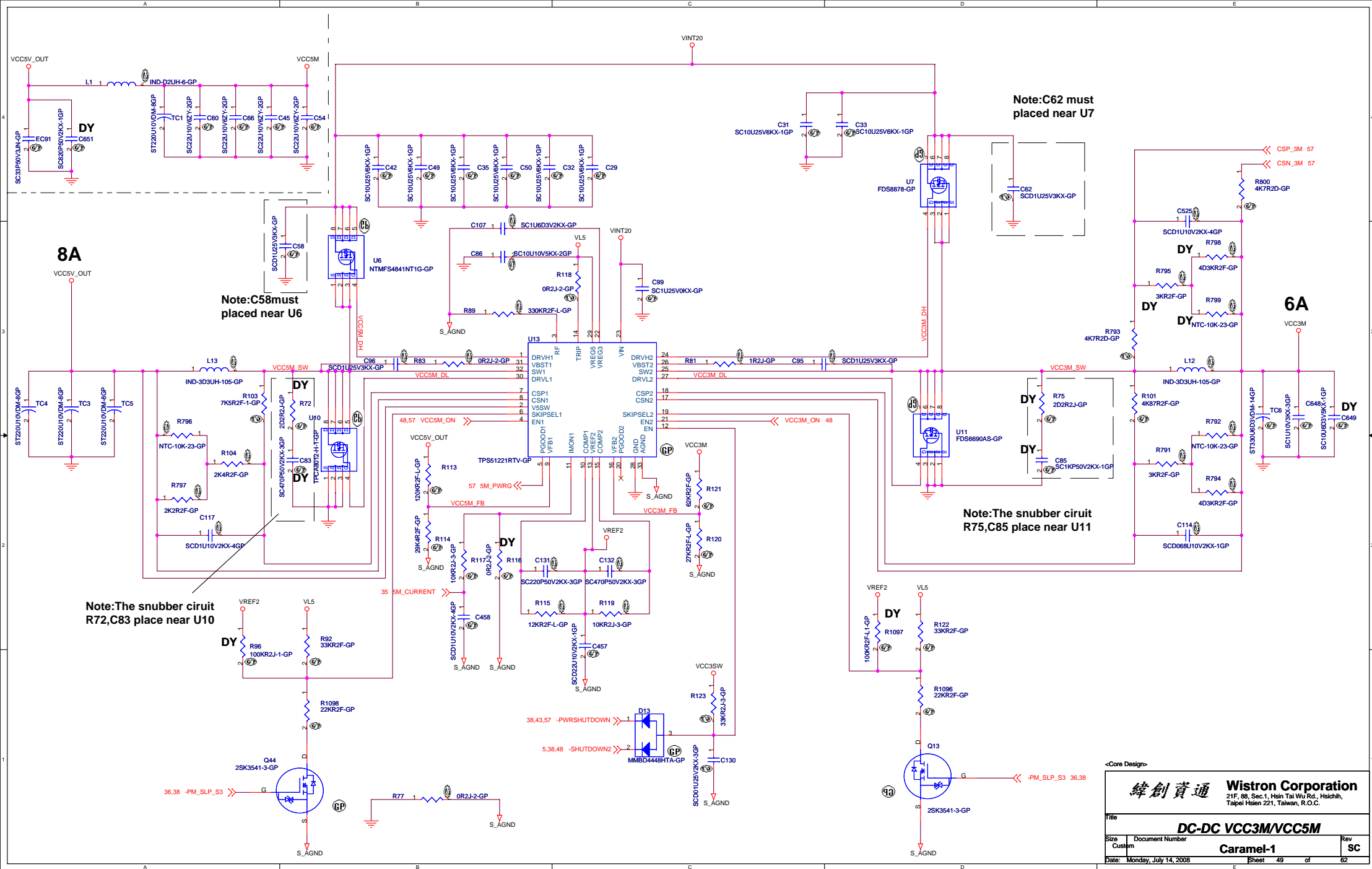
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21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

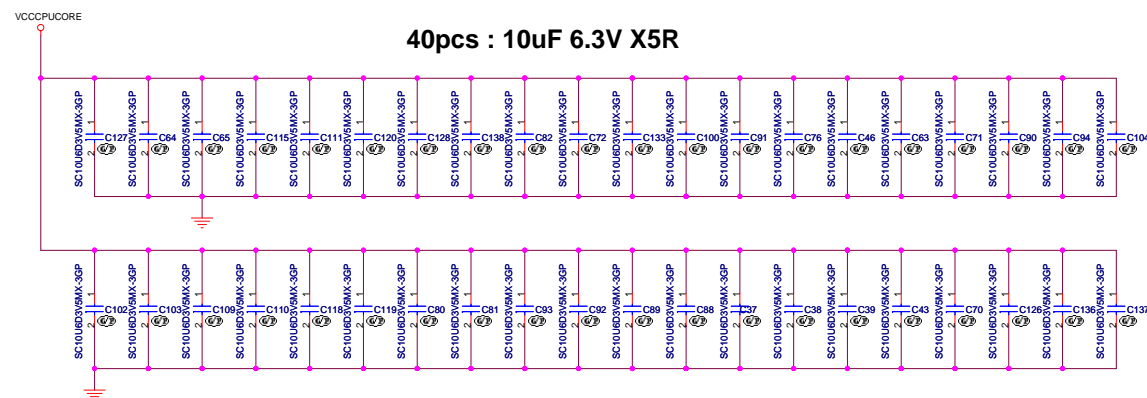
Title **POWER SEQUENCE**

Size A3 Document Number **Caramel-1** Rev SC

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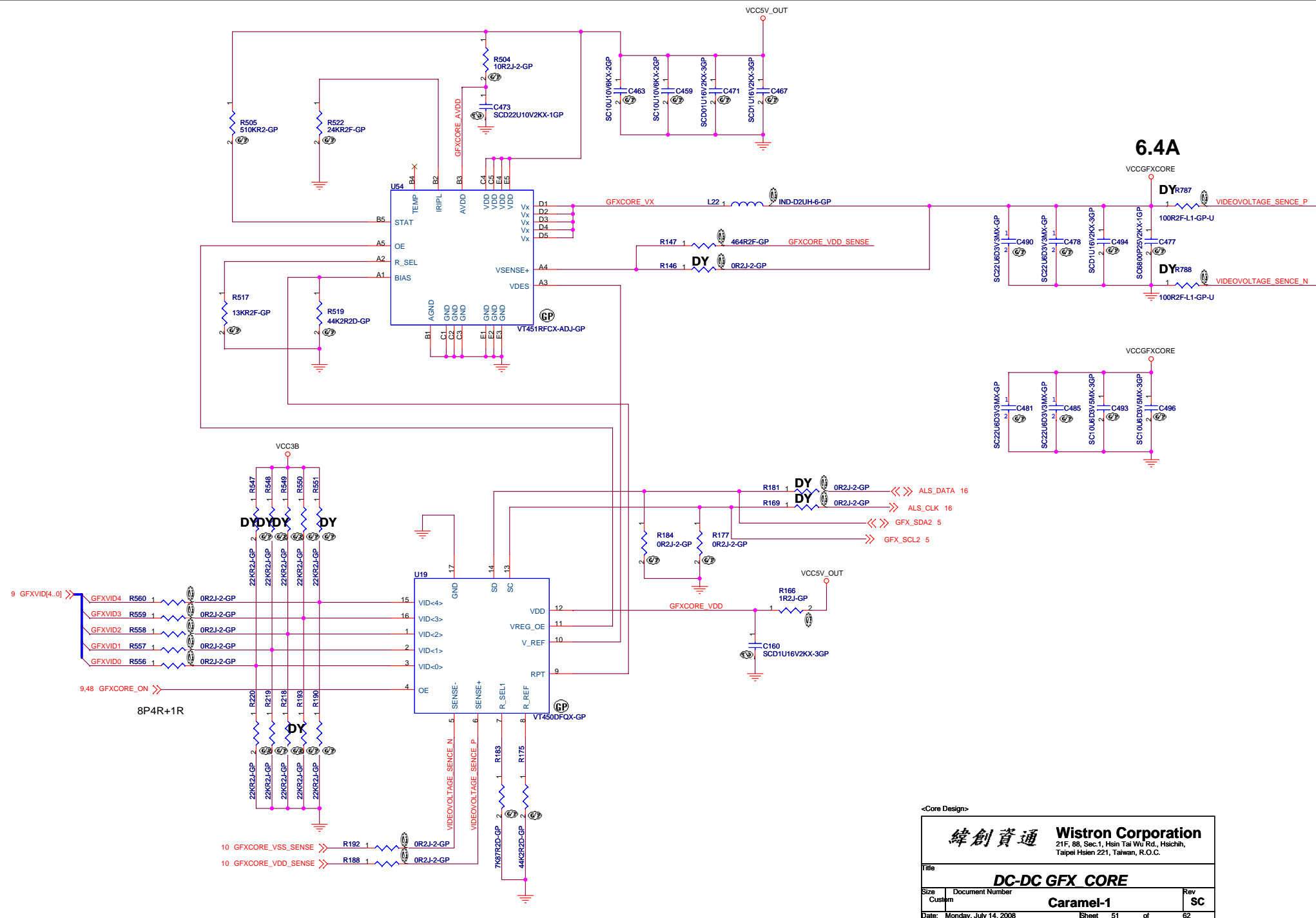




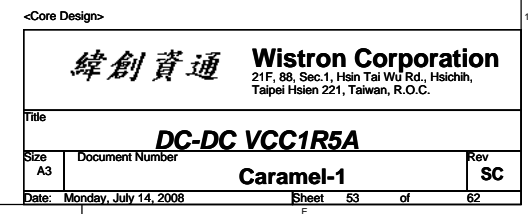
	Pre DV	DV
R455	12.1k ohm	11k ohm
R454	12.1k ohm	11k ohm



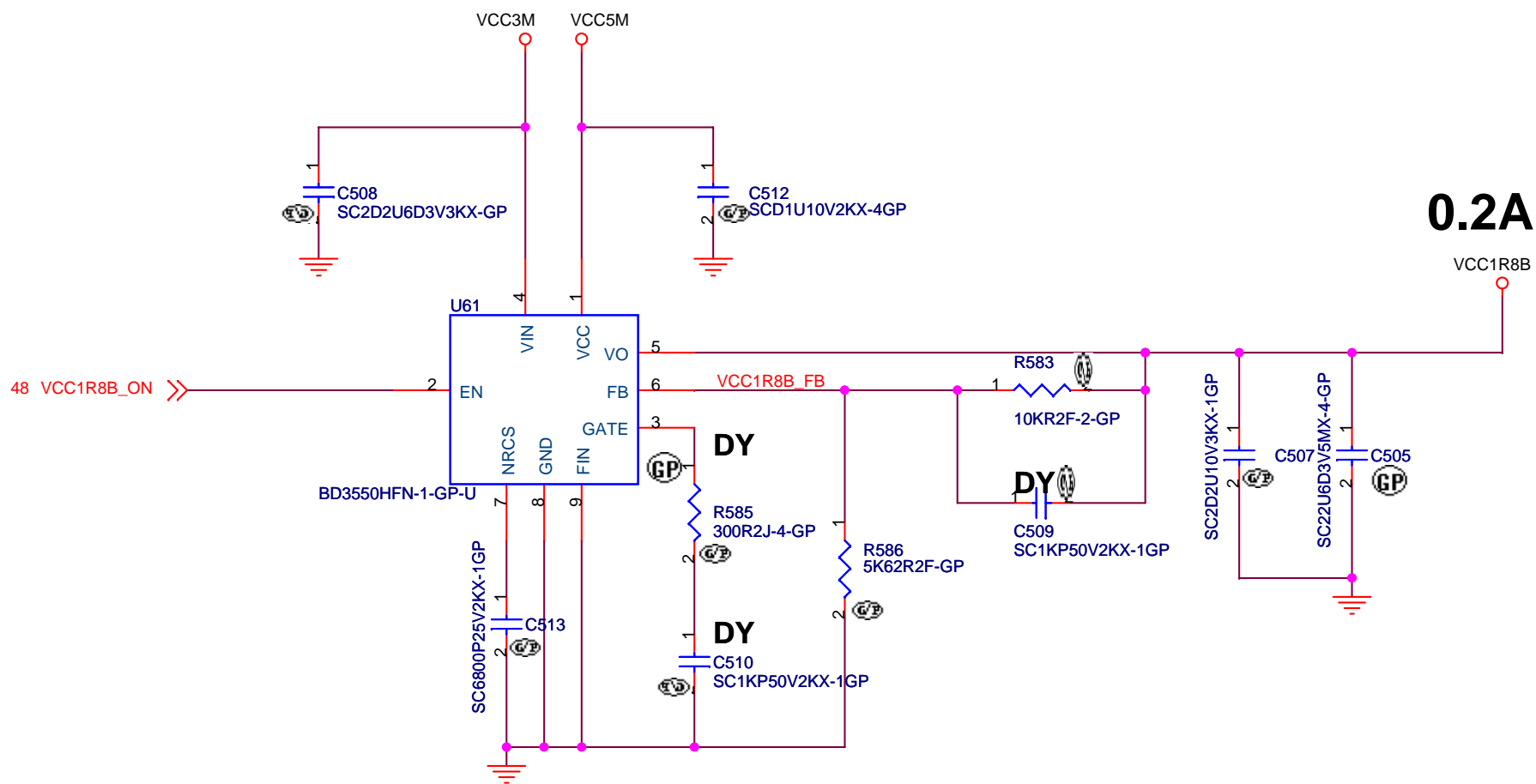
Title			
<b>DC-DC VCCCPUCORE LV/ULV</b>			
Size	Document Number	Rev	
Custom	<b>Caramel-1</b>	<b>SC</b>	
Date:	Monday, July 14, 2008	Sheet	50 of 62





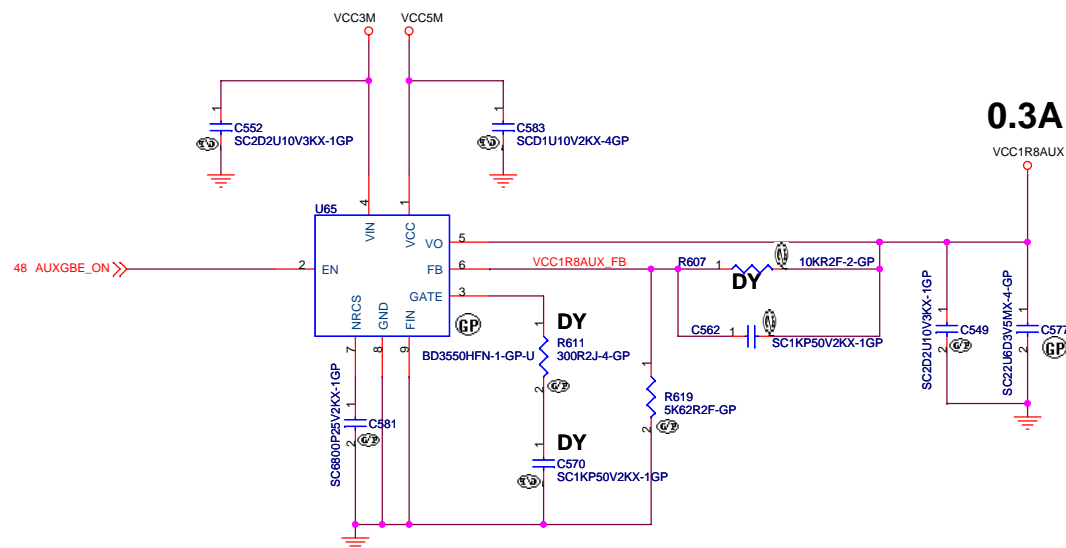
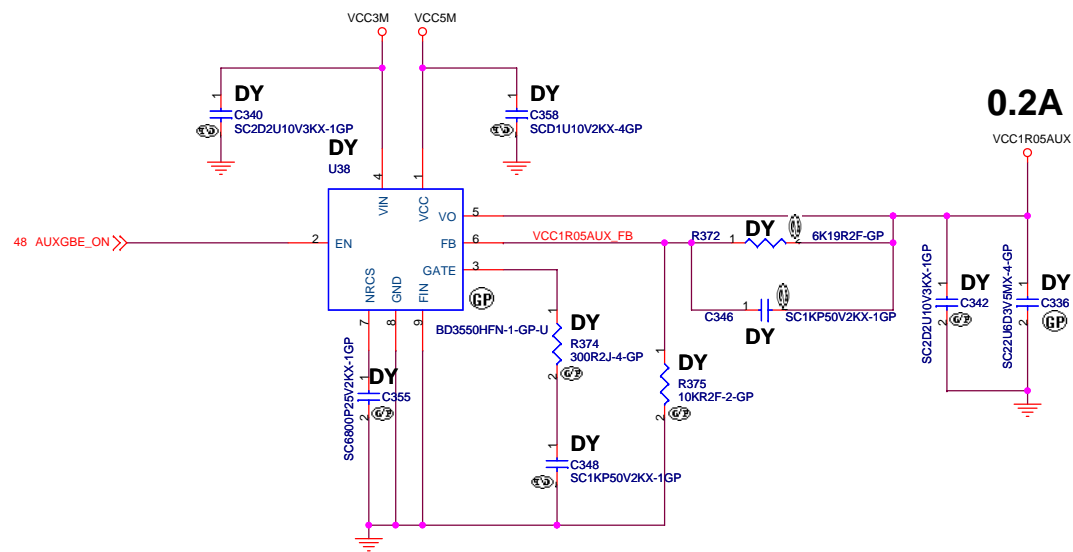






<Core Design>

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Title	<b>DC-DC VCC1R8B</b>
Size A	<div>Document Number</div> <div><b>Caramel-1</b></div>
Rev	<b>SC</b>
Date: Monday, July 14, 2008	Sheet 55 of 62



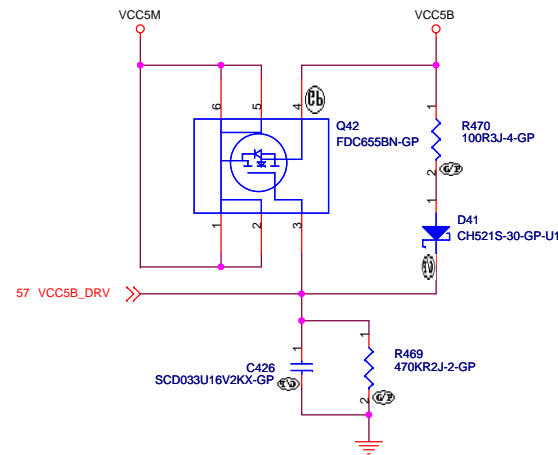
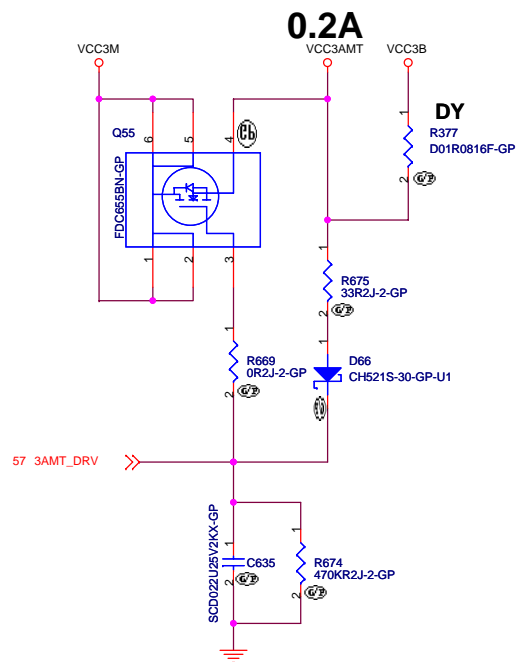
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Taipei Hsien 221, Taiwan, R.O.C.

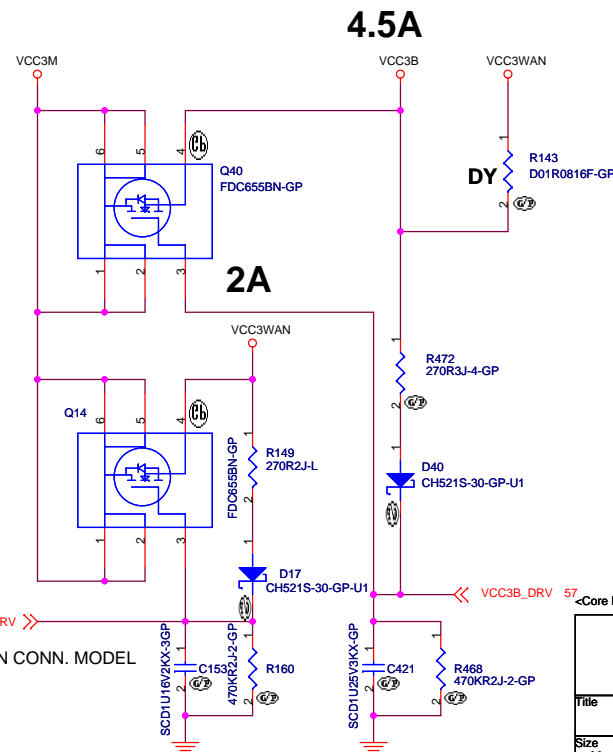
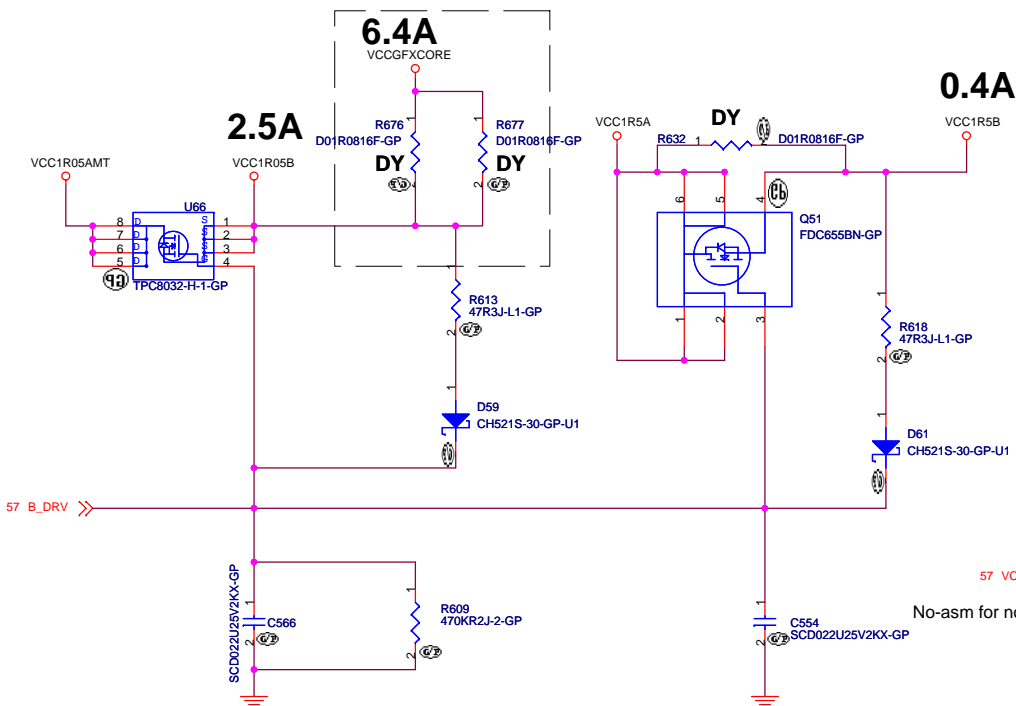
Title		DC-DC VCC1R05AUX/VCC1R8AUX	
Size	Document Number	Caramel-1	Rev
A3			SC
Date:	Monday, July 14, 2008	Sheet	56 of 62







Place near VCCGFXCORE logic



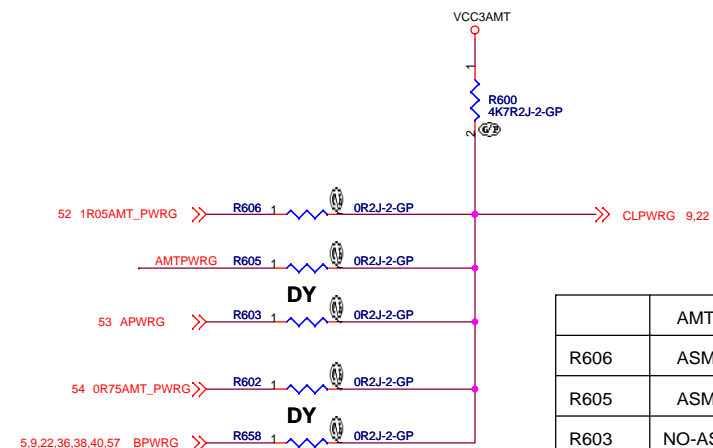
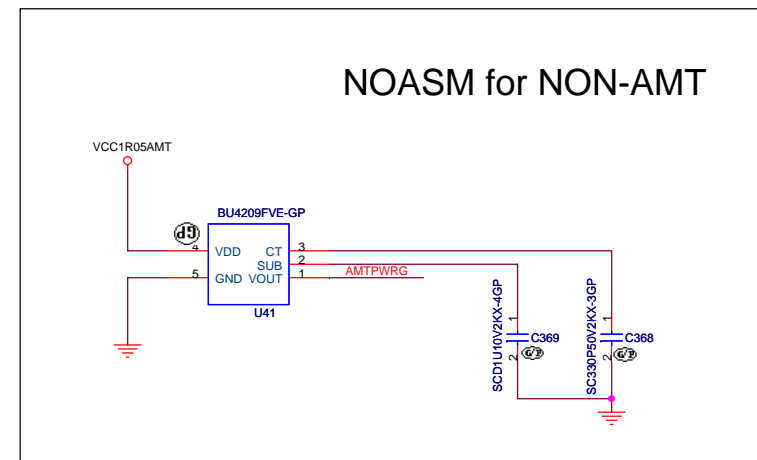
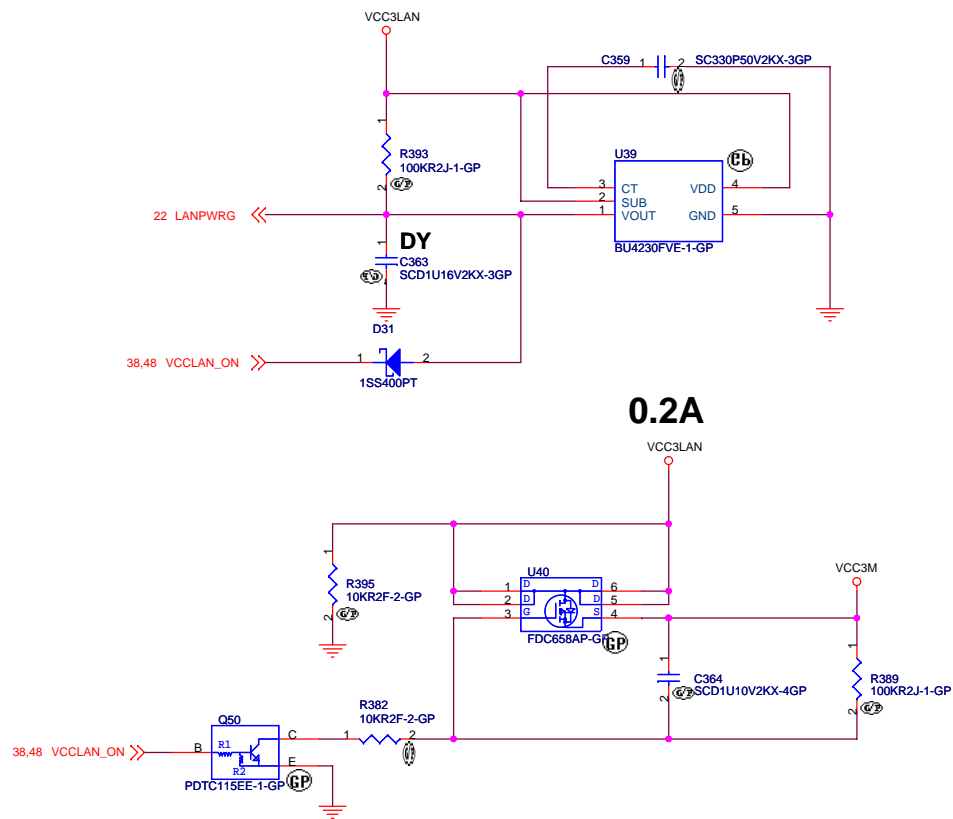
Always ON	Enable	Disable
Q14	ASM	No_ASM
R149	ASM	No_ASM
D17	ASM	No_ASM
R160	ASM	No_ASM
C153	ASM	No_ASM
R143	No_ASM	ASM

No-asm for no WWAN CONN. MODEL

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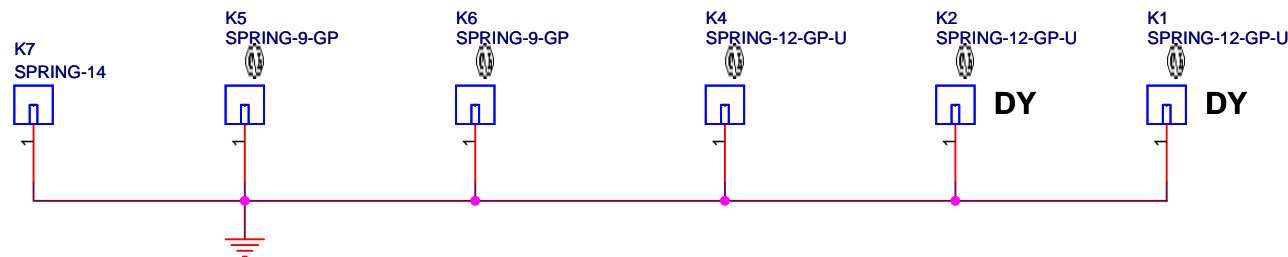
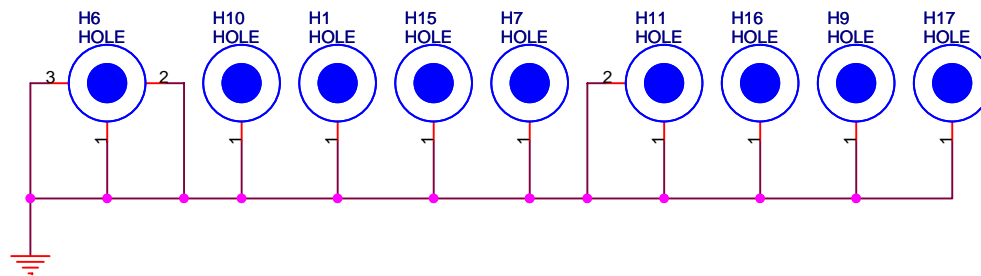
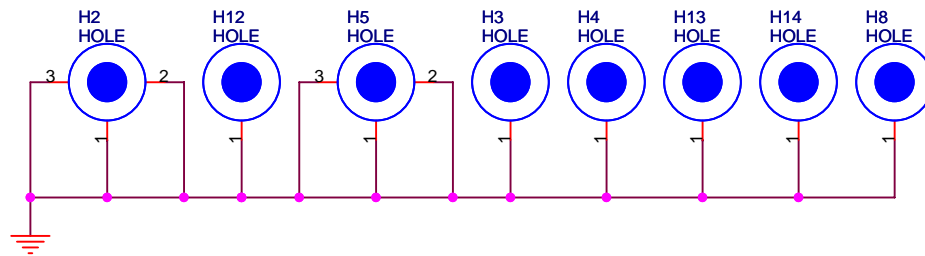
LOAD SW 1		
Size A3	Document Number	Rev SC
Caramel-1		
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	AMT	NON-AMT
R606	ASM	NO-ASM
R605	ASM	NO-ASM
R603	NO-ASM	NO-ASM
R602	ASM	NO-ASM
R600	ASM	NO-ASM
R658	NO-ASM	ASM

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<b>緯創資通</b>		<b>Wistron Corporation</b>	
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.			
Title		<b>LOAD SW 2</b>	
Size A3	Document Number	<b>Caramel-1</b>	Rev <b>SB</b>
Date: Monday, July 14, 2008		Sheet	59 of 62



<Core Design>

<div>緯創資通</div>		<div>Wistron Corporation</div> <div>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</div>	
Title			
HOLES/GND/PADS			
Size A4	Document Number Caramel-1		Rev SC
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# Comparison chart for AMT function

Item	Page	Location Netname	AMT	NON_AMT	Modify	Note
1	22	U37	ICH9-M Enhanced	ICH9-M		
	22	R424	NO_ASM	ASM		
	24	U30	BOAZMAN 82567LM			
	29	SPI1	64Mbit			
	49	R411	ASM	NO_ASM		
	52	R636	ASM	NO_ASM		
	59	R603	ASM	NO_ASM		
		R605	ASM	NO_ASM		
		R606	ASM	NO_ASM		
	58	R377	NO_ASM	ASM		
		Q55	ASM	NO_ASM		
		R669	ASM	NO_ASM		
		R675	ASM	NO_ASM		
		D66	ASM	NO_ASM		
		C635	ASM	NO_ASM		
		R674	ASM	NO_ASM		
		U66	ASM	NO_ASM		
		R613	ASM	NO_ASM		
		D59	ASM	NO_ASM		
		C566	ASM	NO_ASM		
		R609	ASM	NO_ASM		

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Title

**AMT FUNCTION CONTROL TABLE**

Size  
A3

Document Number

**Caramel-1**

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**SC**

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# Long power trace EMI decoupling caps



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Title			<b>EMI DECOUPLING</b>
Size	Document Number	Rev	
Custom		SA	
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