

August '06

# Pecan-1 Block Diagram

07234-2-Final

Project Code: 91.48Q01.001  
PCB(Raw Card): 48.48Q01.0SA

## PCB Layer Stackup

L1:TOP  
L2:Signal 1  
L3:GND  
L4:Signal 2  
L5:VCC  
L6:GND  
L7:Signal 3  
L8:BOTTOM

## Battery Charger/Selector

ADP3808	43
INPUTS	OUTPUTS
DOCK_PWR20_F	M-BAT-PWR
	S-BAT-PWR

## System DC/DC

TPS51221	48
VINT20	VCC5M
	VCC3M

## CPU DC/DC

VT1311	49
VINT20	VCCCPUCORE

## GMCH GFX CORE

VT451&VT450	50
VINT20	VCCGFXCORE

## VCCIR05AMT/VCCIR5A

VT351	51,52
VCC5V_OUT	VCCIR05AMT
	VCCIR5A

## VCC0R75AMT

MAX1510	53
VCCIR5A	VCC0R75AMT

## VCCIR8B

BD3550	54
VCC3M	VCCIR8B

## VCCIR05AUX/IR8AUX

BD3550	55
VCC3M	VCCIR05AUX
	VCCIR8AUX

<Variant Name>

緯創資通

**Wistron Corporation**  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title

## Block Diagram

Size

A3 Document Number

**Pecan-1**

Rev

-2

Date: Wednesday, August 06, 2008

Sheet 1 of 61

Clock Generator  
MLF68 15

Intel  
Penryn LV/ULV SFF  
Processor 3,4

Thermal Sensor  
LM26 5

ITP  
Conn. 6

UNBUFFERED  
DDR3 SODIMM  
Normal Socket 13

UNBUFFERED  
DDR3 SODIMM  
Reverse Socket 14

Intel  
Cantiga-GS  
DDR3 1067/800MHz  
INTEGRATED GRAHPICS  
LVDS  
CRT I/F  
7,8,9,10,11,12

Keyboard Light  
12.1" WXGA LCD 16  
CRT SELECTION 17  
DP HDP 18  
Express Card Solt 33  
Camera (LCD Conn) 16  
Mini PCI-E UWB 28  
Mini PCI-E WWAN Card 27  
Mini PCI-E WLAN Card 27  
Intel GLAN BOAZMAN 24

Thermal Sensor  
MAX6622 5

I2C Bus / SM Bus  
Bus Switch IC 39

SMBus  
SATA HDD

SATA CONN 30

Serial ATA I/F  
Port0

Serial ATA I/F  
Port1

Media Slice 34

Intel  
ICH9-M SFF  
USB 2.0 (12 ports)  
LAN Connect I/F (LCI)  
AC97 2.3/Azalia Interface  
Serial ATA 150MB/s  
ACPI 2.0  
LPC I/F  
PCI Rev 2.3  
PCI Express  
INT. RTC  
20,21,22,23

PCI Express  
USB 2.0 CH4  
PCI Express  
USB 2.0 CH3  
PCI Express  
USB 2.0 CH2  
PCI Express

3-in-1 Slot  
Media Card Reader  
USB 3  
USB2.0 CH5  
USB2.0 CH6  
RJ11 Conn  
Audio Codec  
IOSUB Card

IOSUB  
Card Conn. 32

USB2.0 CH5,6  
Azalia bus

Finger Print 31

Bluetooth  
(LCD Conn) 16

USB 1 31

USB 2 31

SPI-FLASH 29

SPI-FLASH 29

KBC  
H8S/2116BG20V 35,36

LPC Debug  
Board Conn 29

PMH-7  
G/A 38

G-Sensor 41

Int. KB  
Track point IV 36

SATA Port 1 I/F

USB Hub  
SMSC USB2514

PCI Express

Display Port

CRT

RJ45

HP OUT  
MIC IN

UltraSlimBay  
HDD, Optical Drives,  
2nd Battery

USB x 4

Media Slice

DC-IN

## RESISTOR

Symbol name	Value	Tolerance (J: 5%, F: 1%, D: 0.5%, B: 0.1 %)	Rating 0402>=> 1/16W, 25V 0603 => 1/16W, 75V 0805 => 1/10W, 100V	Size 2==>0402, 3==>0603, 5==>0805, 6==>1206, 0==>1210
10KR3	10K Ohm	If no letter, it means J: 5%	1/16W, 75V	0603
33D3R5	33.3 Ohm	If no letter, it means J: 5%	1/10W, 100V	0805
1KR3F	1K Ohm	F: 1%	1/16W, 75V	0603

The naming rule is value + R + size + tolerance  
 For the value, it can be read by the number before R. (R means resistor)  
 For the tolerance, it can be read from the last letter.  
 For the rating, we don't show on the symbol name.  
 For the size, R2=>0402, R3=>0603, R5=>0805.....

## CAPACITOR

Symbol name	Value	Tolerance (M: +/-20, K: +/-10, Z: +80/-20)	Rating	Size 2==>0402, 3==>0603, 5==>0805, 6==>1206, 0==>1210
SCD1U10V2MX-1	0.1uF	M/X5R	10V	0402
SC10U6D3V5MX	10uF	M/X5R	6.3V	0805
SC2D2U16V5ZY	2.2uF	Z/Y5V	16V	0805

The naming rule is  
Capacitor type + value + rating + size + tolerance + material  
SCD1U10V2MX-1  
SC=> SMT Ceramic, TC=> POS cap or SP cap  
D1U => 0.1uF  
10V => the voltage rating is 10V  
2=> 0402, 3=>0603, 5=>0805  
M=>tolerance M, K, Z  
X=> X7R/X5R, Y=> Y5V  
-1 => symbol version, nonsense to EE characteristic

## PLANAR\_ID[3..0]

ICH9-M GPIO#	39	38	37	36	Planar ID Version	Planar PCB Version
PLANAR_ID#	3	2	1	0		
	0	0	0	0	Pecan-1 Pre-DV	SA
	0	0	0	1	Pecan-1 DV	SB
	0	0	1	0	Pecan-1 SIV	SC
	0	0	1	1	Pecan-1 SIT	SD
	0	1	0	0	Pecan-1 SVT	-1
	0	1	0	1	Pecan-1 SOVP	-2
	0	1	1	0		
	0	1	1	1		

## EC HISTORY

[illegible]

## PCI TABLE

DEVICE	IDSEL	IRQ (Default)	REQ# / GNT#
MINIPCI SLOT	AD18	F, G	REQ# 3/ GNT#
CARDBUS R5C811	AD16	SERIRQ	REQ#0 / GNT#
USB UHCI	AD29	A, C, D	
USB 2.0 EHCI	AD29	H	
DMI-to-PCI/ AC97 Modem/ AC97 Audio	AD30	B B	
LPC Bridge IDE SATA SMBus	AD31	C C B	
PCI Express	AD28	A, B, C, D	

<Variant Name>

緯創資通

**Wistron Corporation**  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,

Title
-------

### **Reference**

Size	Document Number
------	-----------------

### Pecan-1

Rev

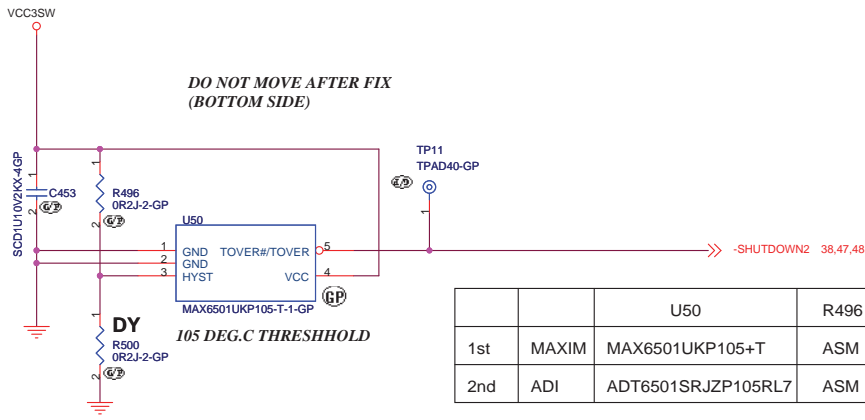
Date: Wednesday, August 06, 2008

Sheet 2 of 61





# Thermal Sensor for CPU



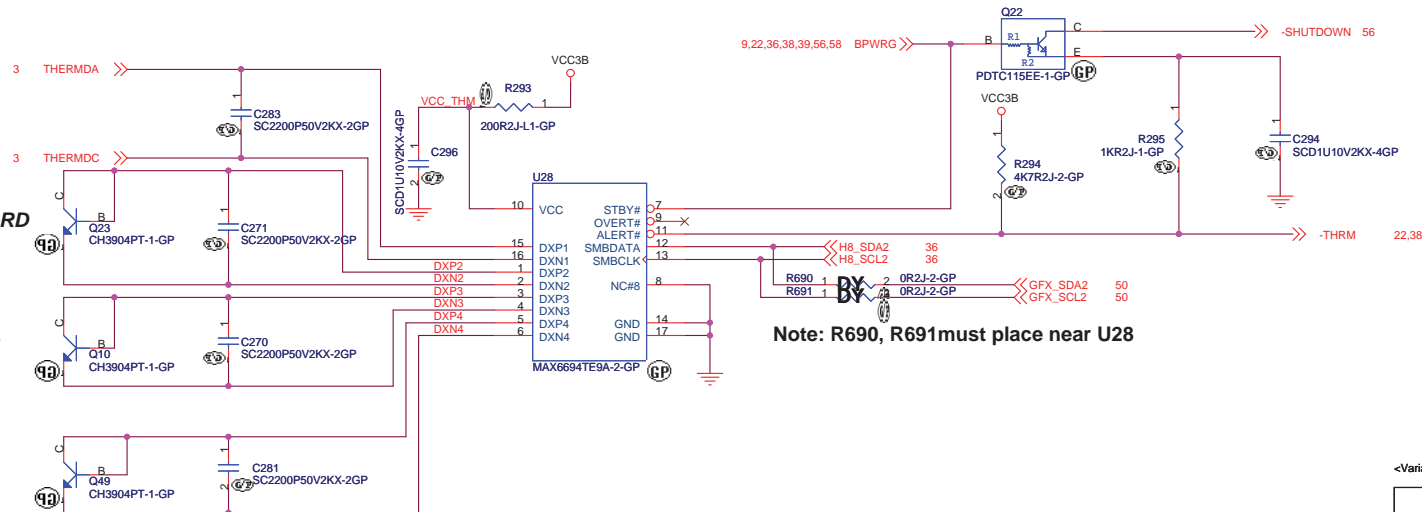
THESE CAPS MUST BE PLACED AS CLOSE AS POSSIBLE TO MAX6694

TO CPU DIE

TO EXPRESS CARD

TO VCORE FET

TO MINI CARD



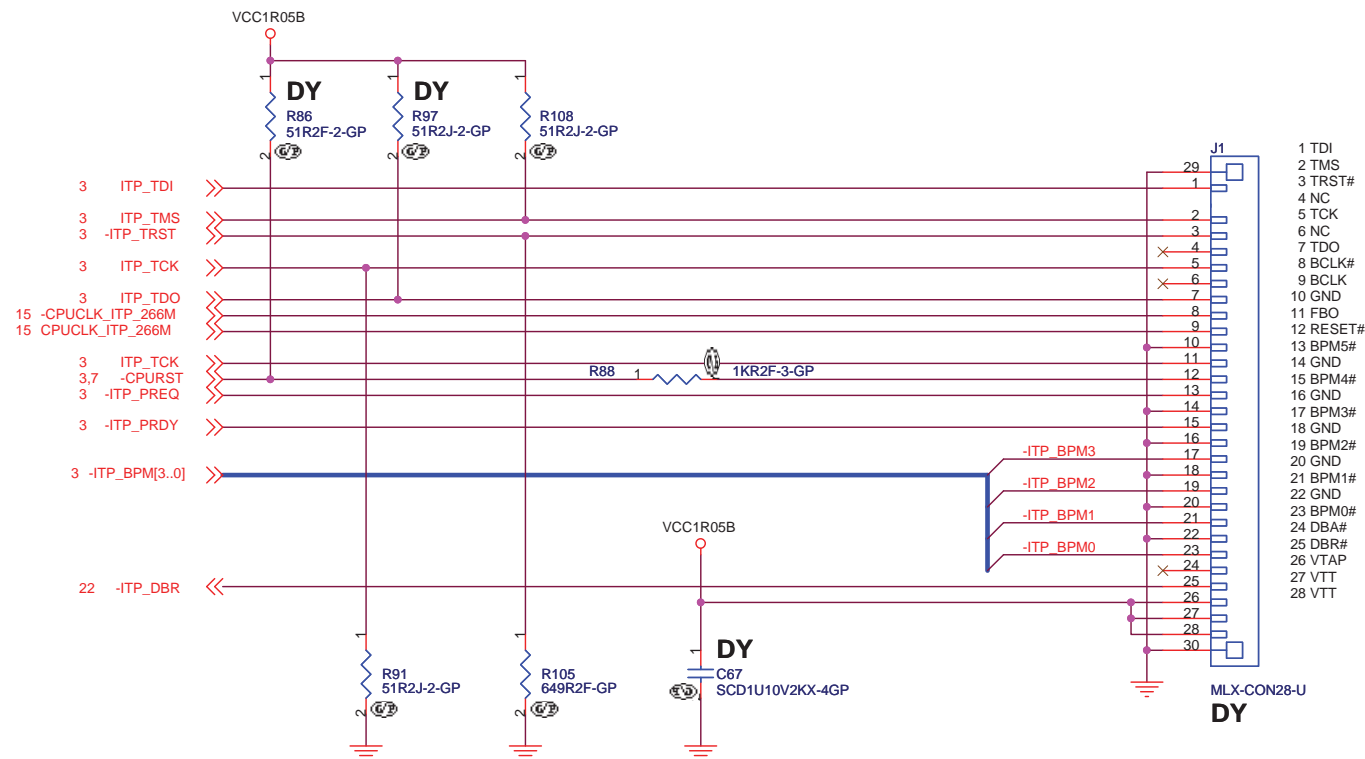
PLACE UNDER PC-CARD SLOT  
H8 I2C Bus 2 ADDRESS : 9AH  
TEST PAD FOR BOARD MFG TEST

Layout Comment :

- (1) Thermal sensor trace lines should not be overlapped with other high frequency trace lines in other layers.
- (2) Also, it should not be overlapped with large amplitude trace lines either.

<Variant Name>

<b>緯創資通</b>		<b>Wistron Corporation</b>	
		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
<b>THERMAL SENSOR</b>			
Size A3	Document Number		Rev -2
<b>Pecan-1</b>			
Date: Wednesday, August 06, 2008		Sheet 5 of	61



- 1 TDI  
2 TMS  
3 TRST#  
4 NC  
5 TCK  
6 NC  
7 TDO  
8 BCLK#  
9 BCLK  
10 GND  
11 FBO  
12 RESET#  
13 BPM5#  
14 GND  
15 BPM4#  
16 GND  
17 BPM3#  
18 GND  
19 BPM2#  
20 GND  
21 BPM1#  
22 GND  
23 BPM0#  
24 DBA#  
25 DBR#  
26 VTAP  
27 VTT  
28 VTT

MLX-CON28-U  
DY

(\*1) TCK SIGNAL IS BRANCHED AT CPU's PIN

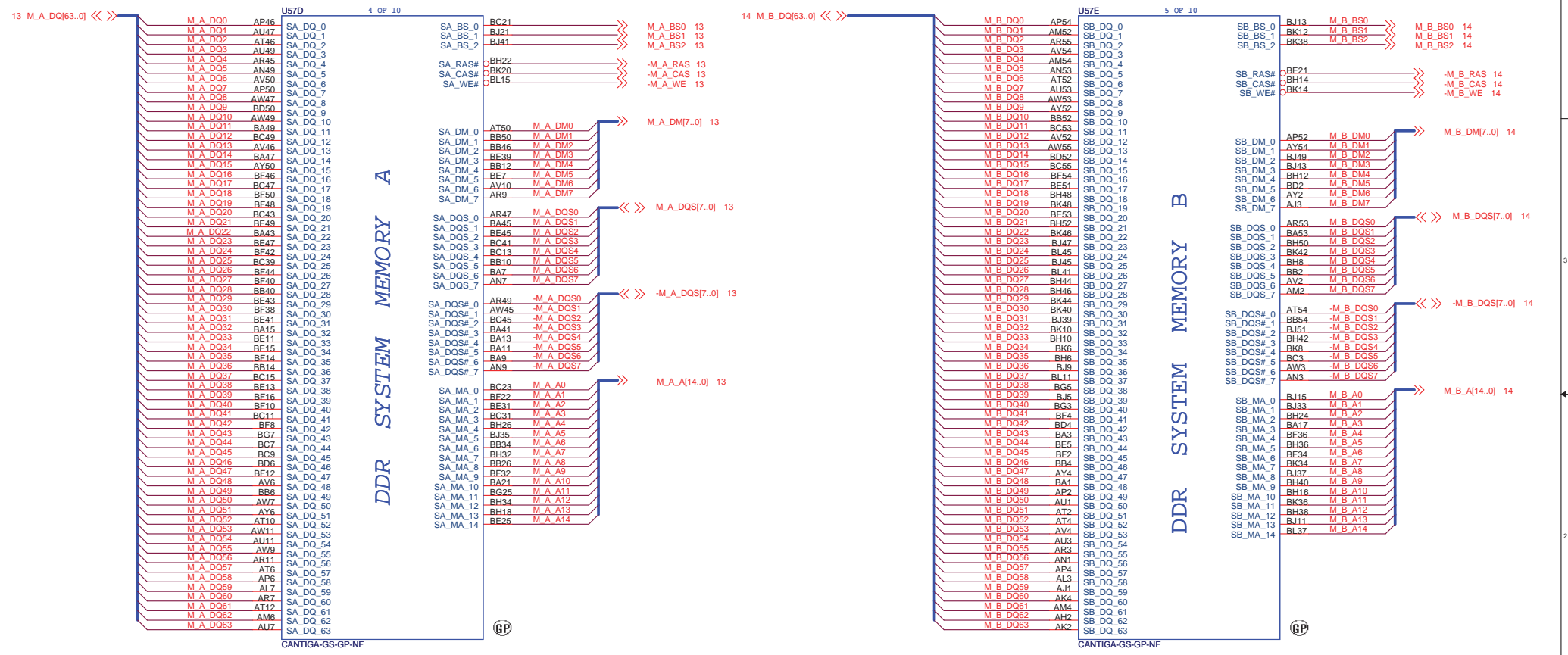
(\*2) CPURST# SIGNAL IS BRANCHED AT GMCH's PIN

Ref Des	For ITP-XDP
J1	NO_ASM-->ASM
C67	NO_ASM-->ASM
R88	ASM (NO Change)
R105	ASM (No Change)
R86	NO_ASM (NO Change)
R97	NO_ASM-->ASM
R91	ASM (No Change)
R108	ASM (No Change)

<Variant Name>

<b>緯創資通</b>		<b>Wistron Corporation</b>	
		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title <b>ITP_XDP CONN</b>			
Size Custom	Document Number <b>Pecan-1</b>		Rev <b>-2</b>
Date:	Wednesday, August 06, 2008	Sheet 6 of	61





<Variant Name>

**緯創資通** **Wistron Corporation**  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title  
**Cantiga-GS (2/6):DDR3**

Size A3	Document Number <b>Pecan-1</b>	Rev <b>-2</b>
------------	-----------------------------------	------------------

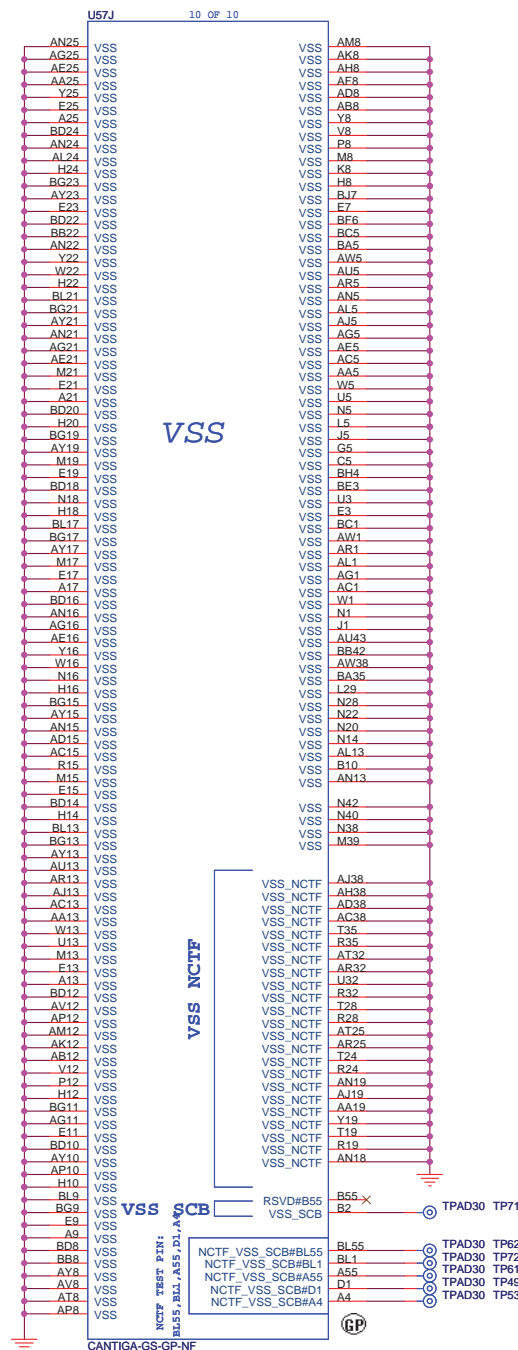
Date: Wednesday, August 06, 2008 Sheet 8 of 61



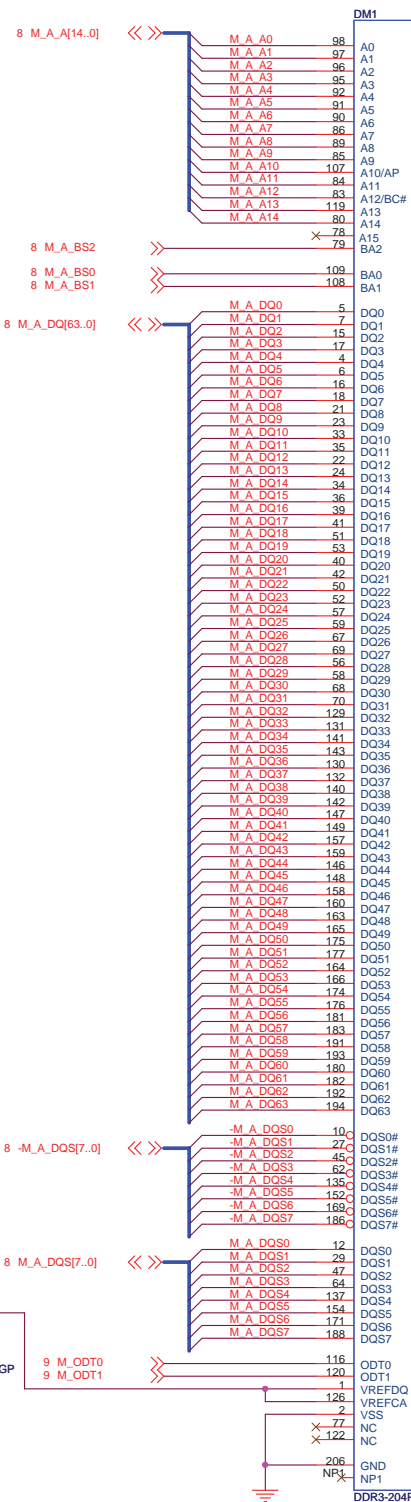




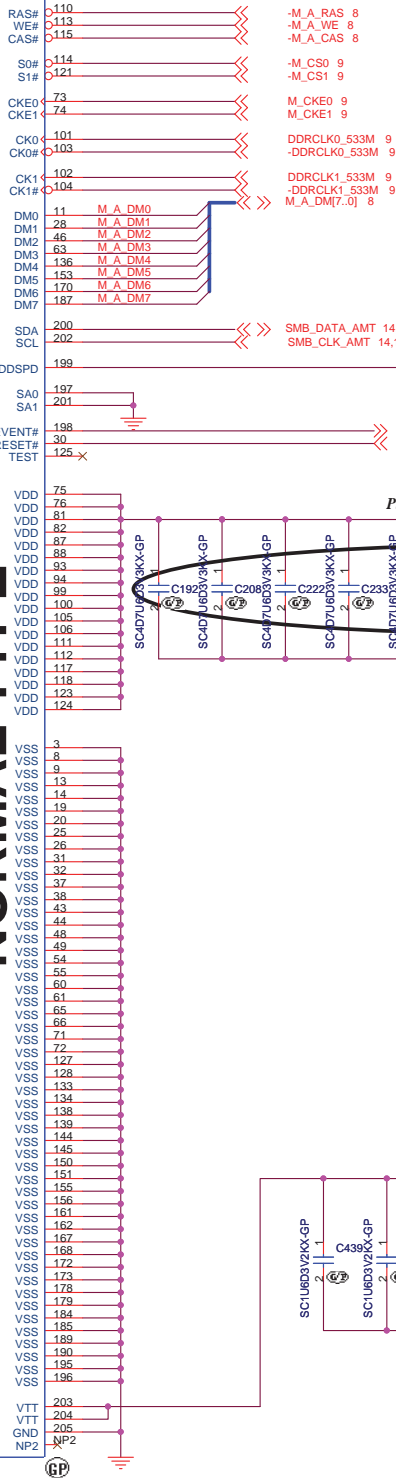




Title			
<b><i>Cantiga-GS (6/6):GND</i></b>			
Size A3	Document Number		Rev
	<b>Pecan-1</b>		<b>-2</b>
Date:	Wednesday, August 06, 2008	Sheet 12 of	61



NORMAL TYPE

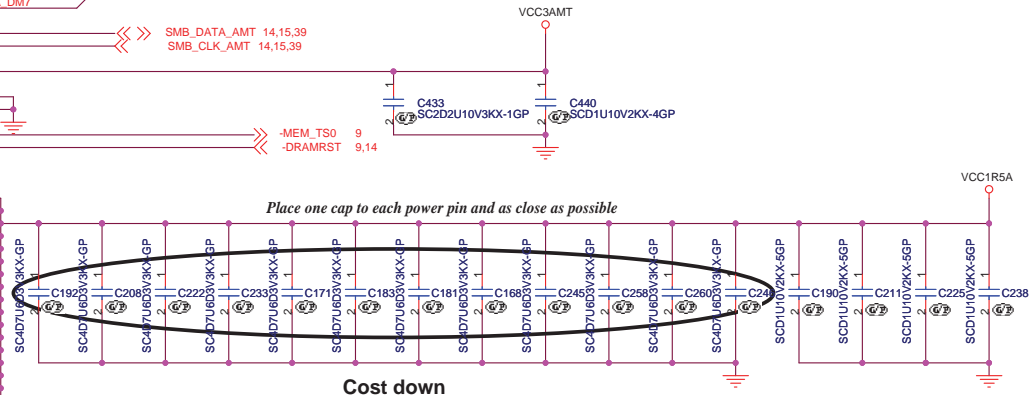
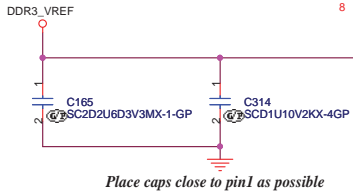


-M\_A\_RAS 8  
-M\_A\_WE 8  
-M\_A\_CAS 8  
-M\_CS0 9  
-M\_CS1 9  
M\_CKE0 9  
M\_CKE1 9  
DDRCLK0\_533M 9  
-DDRCLK0\_533M 9  
DDRCLK1\_533M 9  
-DDRCLK1\_533M 9  
M\_A\_DM[7.0] 8

SMB\_DATA\_AMT 14,15,39  
SMB\_CLK\_AMT 14,15,39  
-MEM\_TS0 9  
-DRAMRST 9,14

Place one cap to each power pin and as close as possible  
Cost down

VCCOR75AMT

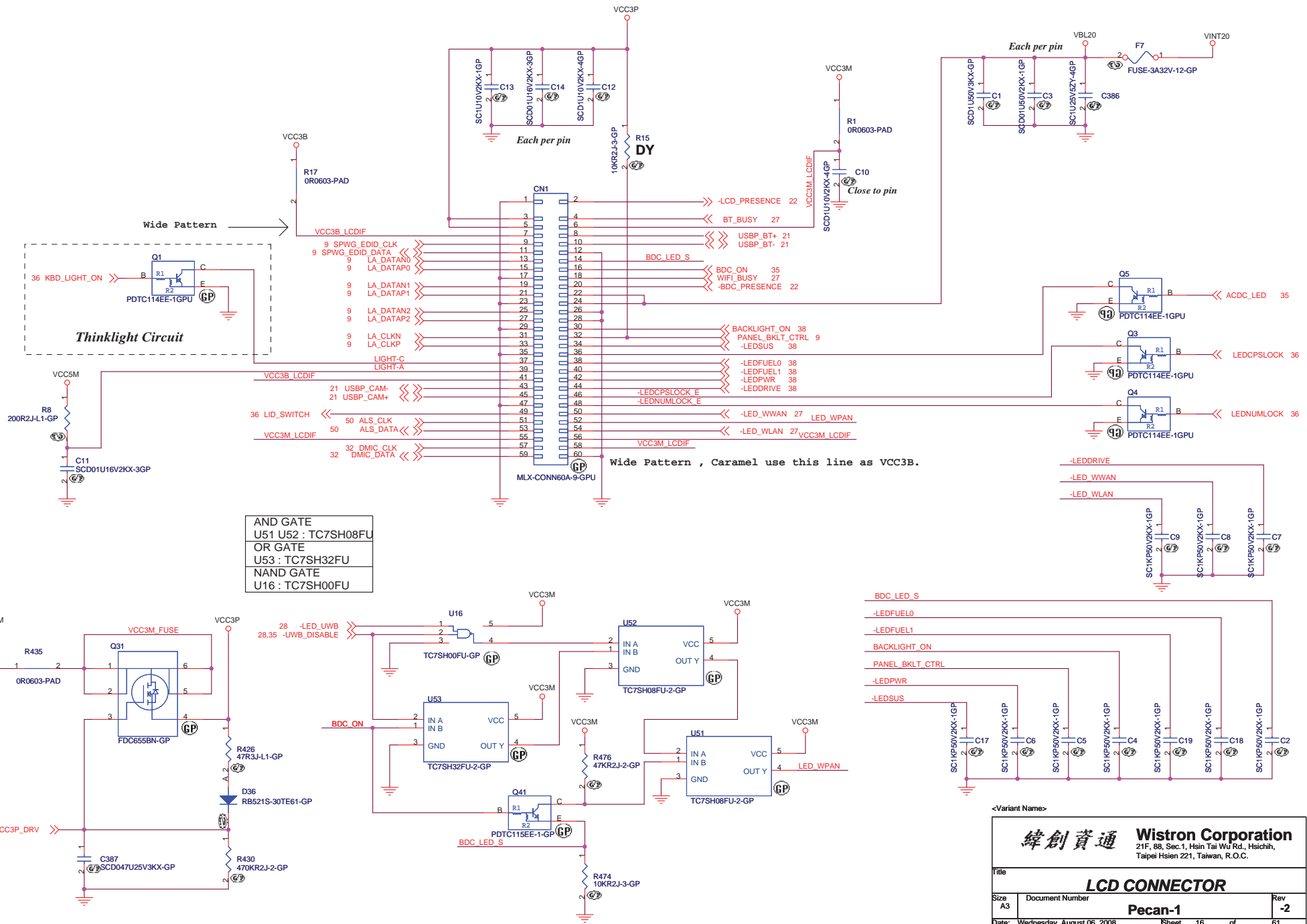








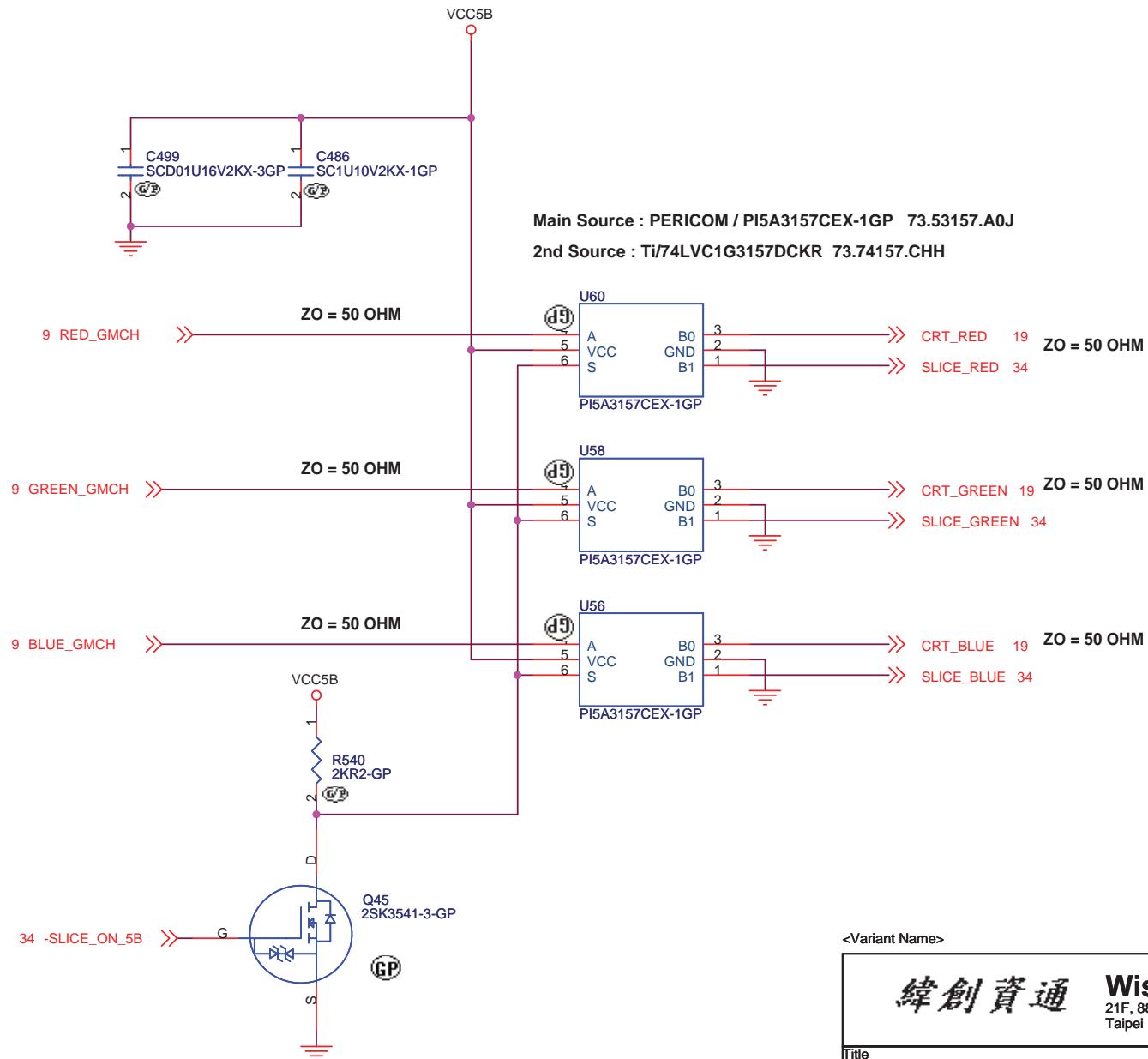
# LCD / Inverter Connector




**緯創資通 Wistron Corporation**  
21F, 88, Sec. 1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.

**LCD CONNECTOR**  
Pecan-1  
Date: Wednesday, August 06, 2008 Sheet 16 of 61

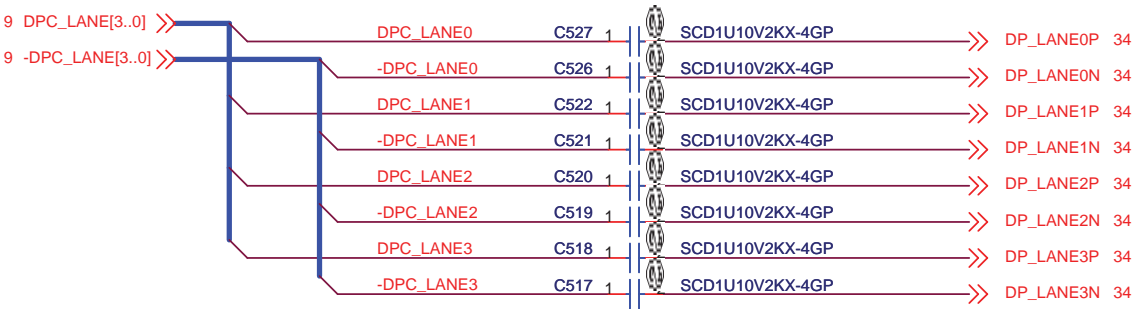




<Variant Name>

		<b>Wistron Corporation</b> 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
<b>CRT SELECTOR</b>			
Size A4	Document Number <b>Pecan-1</b>		Rev <b>-2</b>
Date:	Wednesday, August 06, 2008	Sheet 17 of 61	

System DP: GMCH to SLICE Connector

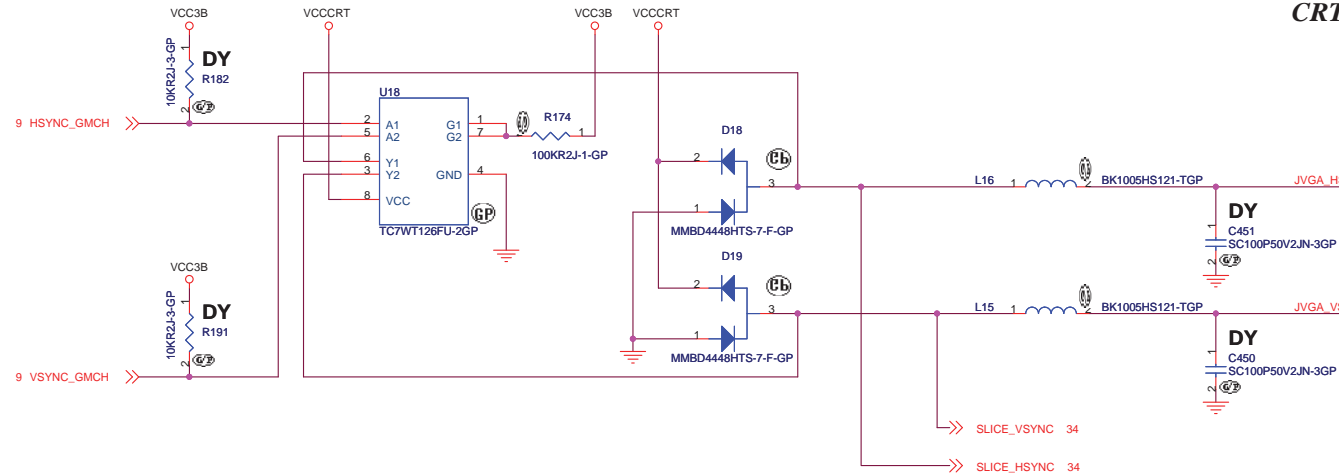
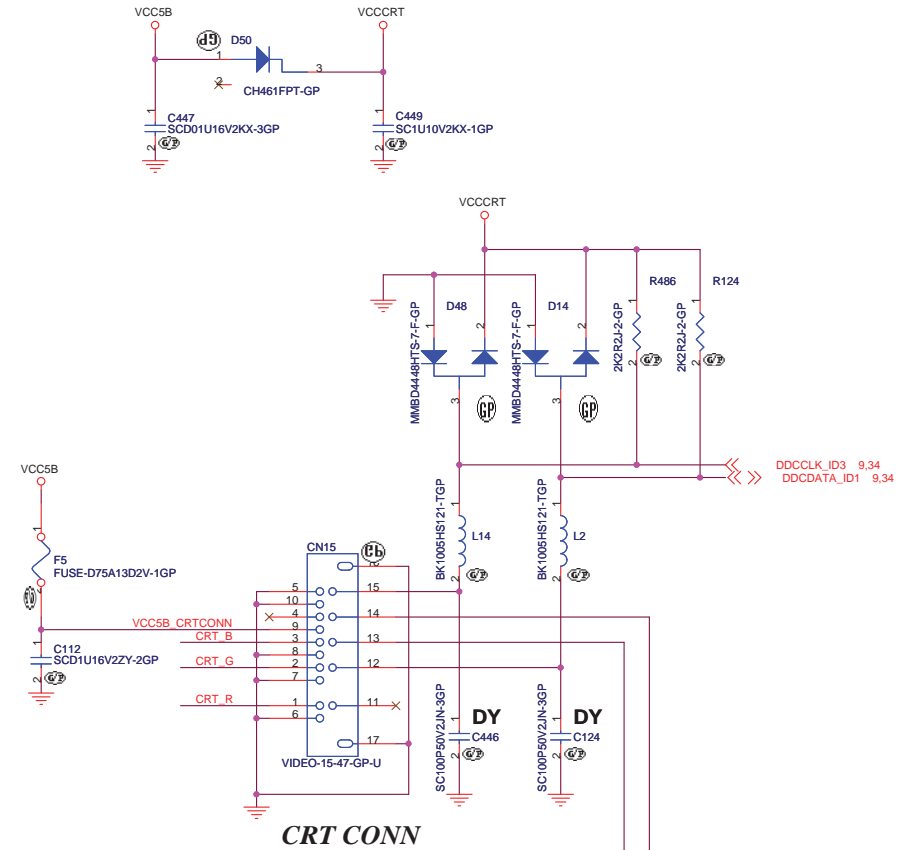
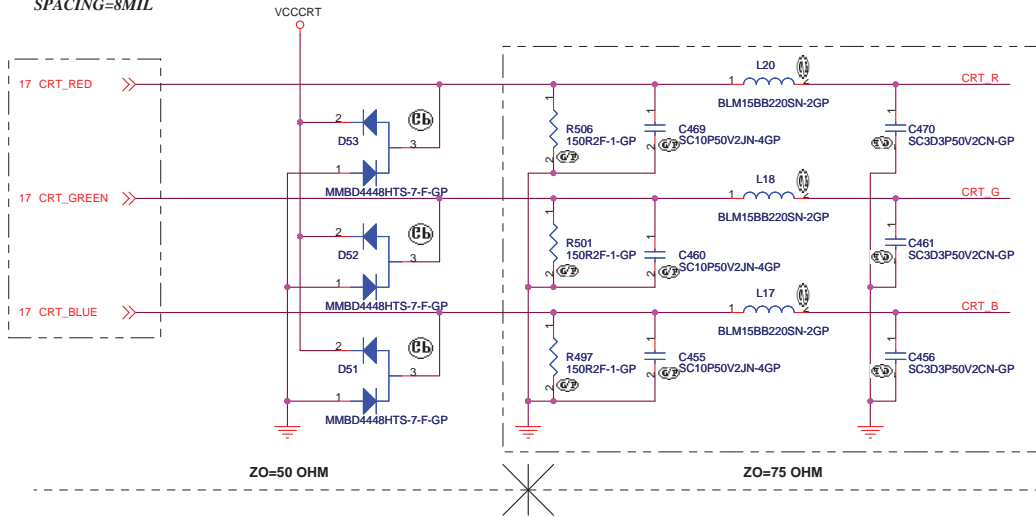


<Variant Name>

<div><div>緯創資通</div><div>Wistron Corporation</div><div>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</div></div>		
Title <div>Display Port AC Coupling</div>		
Size <div>A4</div>	Document Number <div>Pecan-1</div>	Rev <div>-2</div>
Date: Wednesday, August 06, 2008		

Sheet 18 of 61

**GND GUARDING**  
**EACH SIGNAL WIDTH DEPENDS ON ZO(TRACE IMPEDANCE)**  
**SPACING=8MIL**



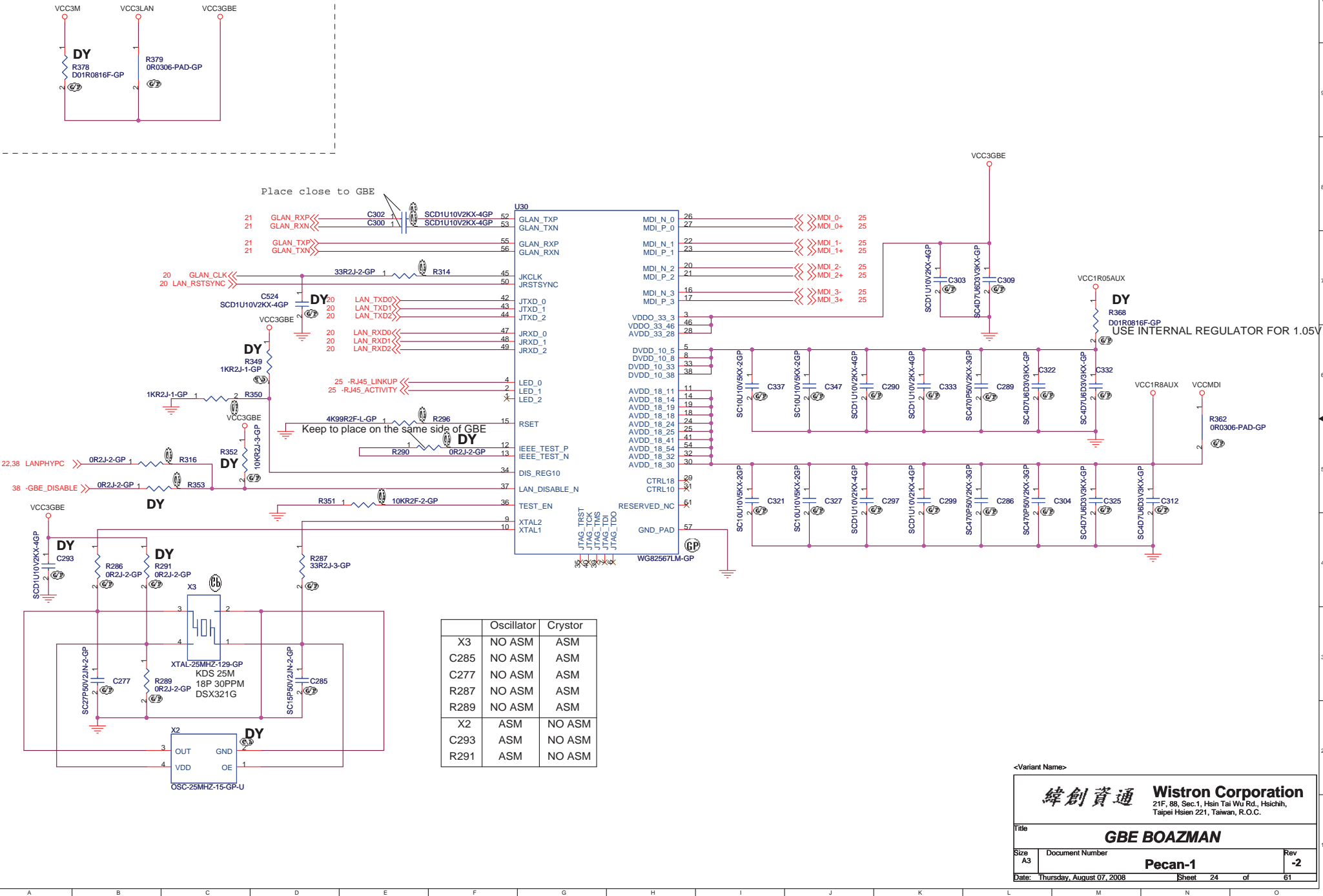
<Variant Name>			
緯創資通		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
EXT CRT INTERFACE			
Size	Document Number	Rev	
A3		-2	
Date: Wednesday, August 06, 2008		Sheet	61











<Variant Name>

**緯創資通 Wistron Corporation**  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.

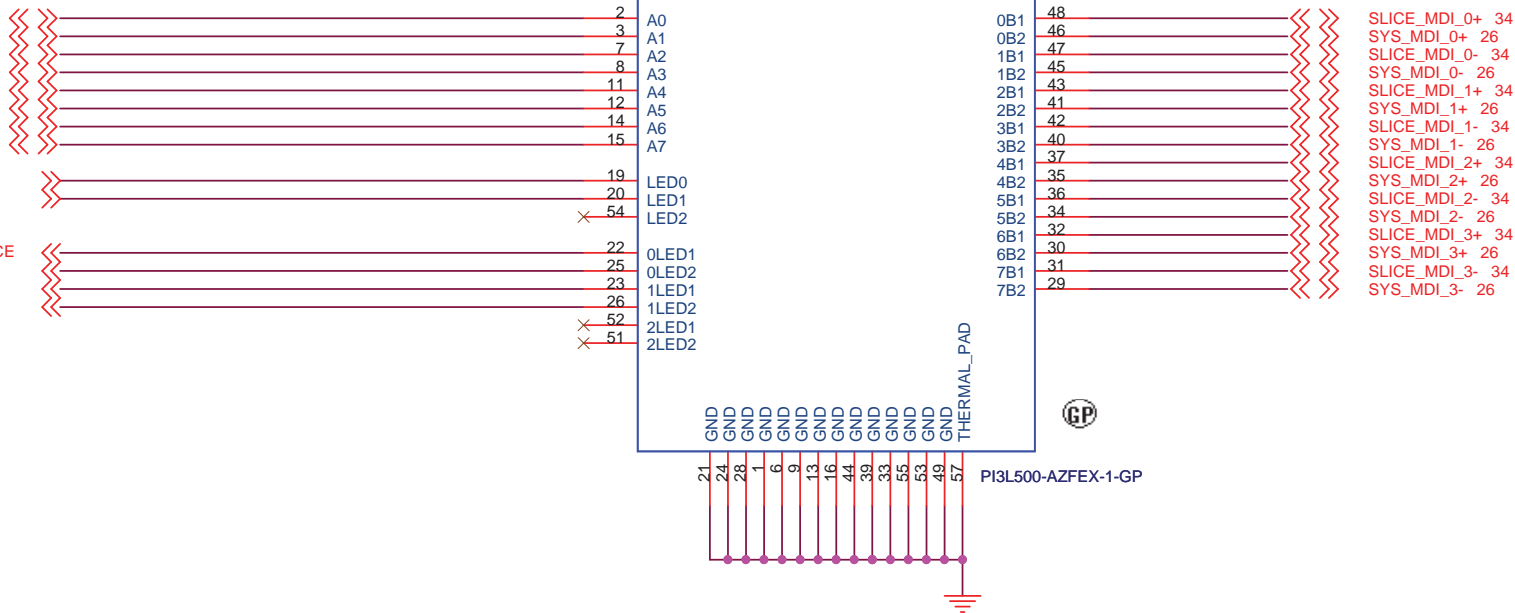
Title **GBE BOAZMAN**

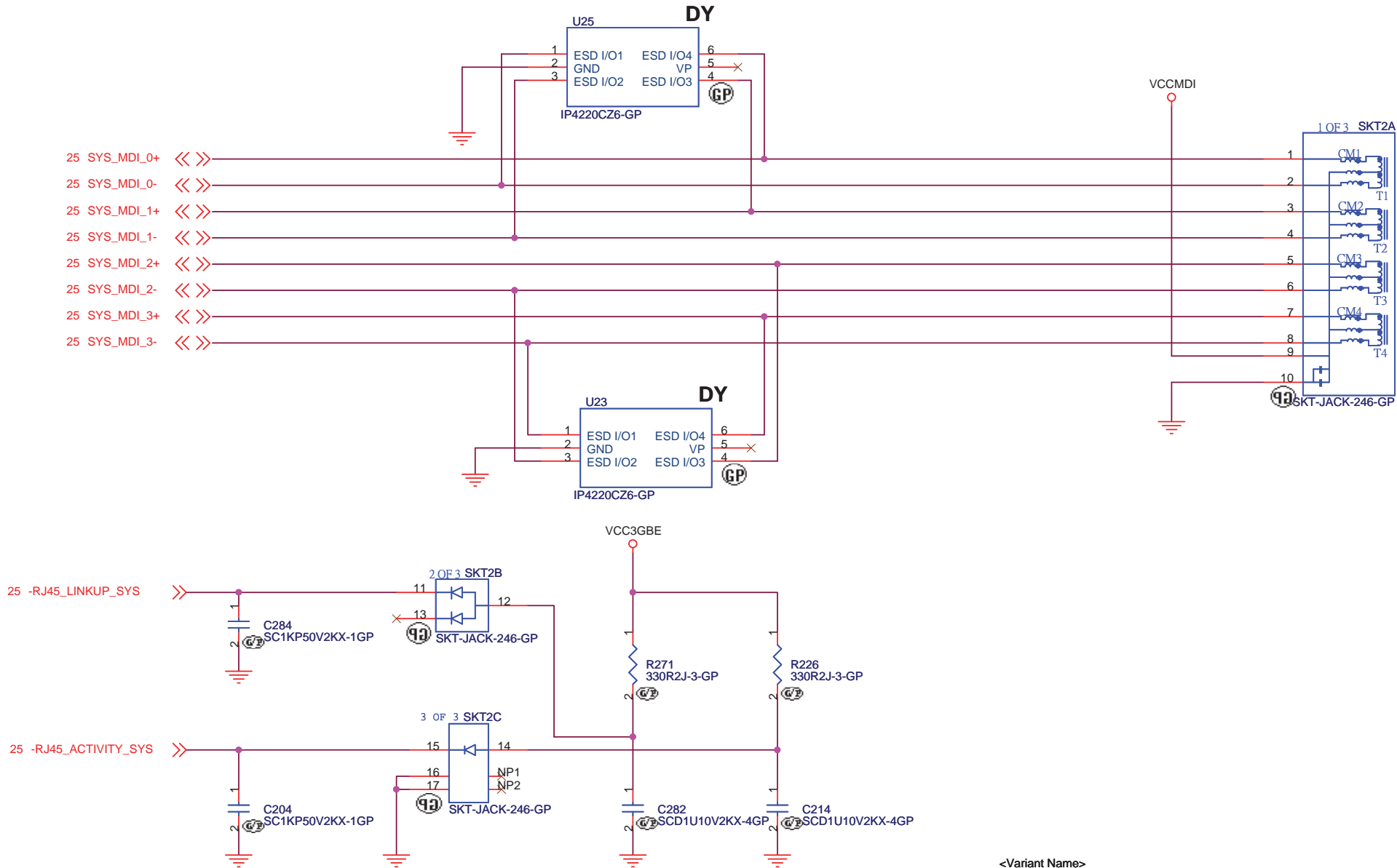
Size A3 Document Number **Pecan-1** Rev **-2**

Date: Thursday, August 07, 2008 Sheet 24 of 61



24 MDI\_0+  
24 MDI\_0-  
24 MDI\_1+  
24 MDI\_1-  
24 MDI\_2+  
24 MDI\_2-  
24 MDI\_3+  
24 MDI\_3-  
  
24 -RJ45\_ACTIVITY  
24 -RJ45\_LINKUP  
  
34 -RJ45\_ACTIVITY\_SLICE  
26 -RJ45\_ACTIVITY\_SYS  
34 -RJ45\_LINKUP\_SLICE  
26 -RJ45\_LINKUP\_SYS

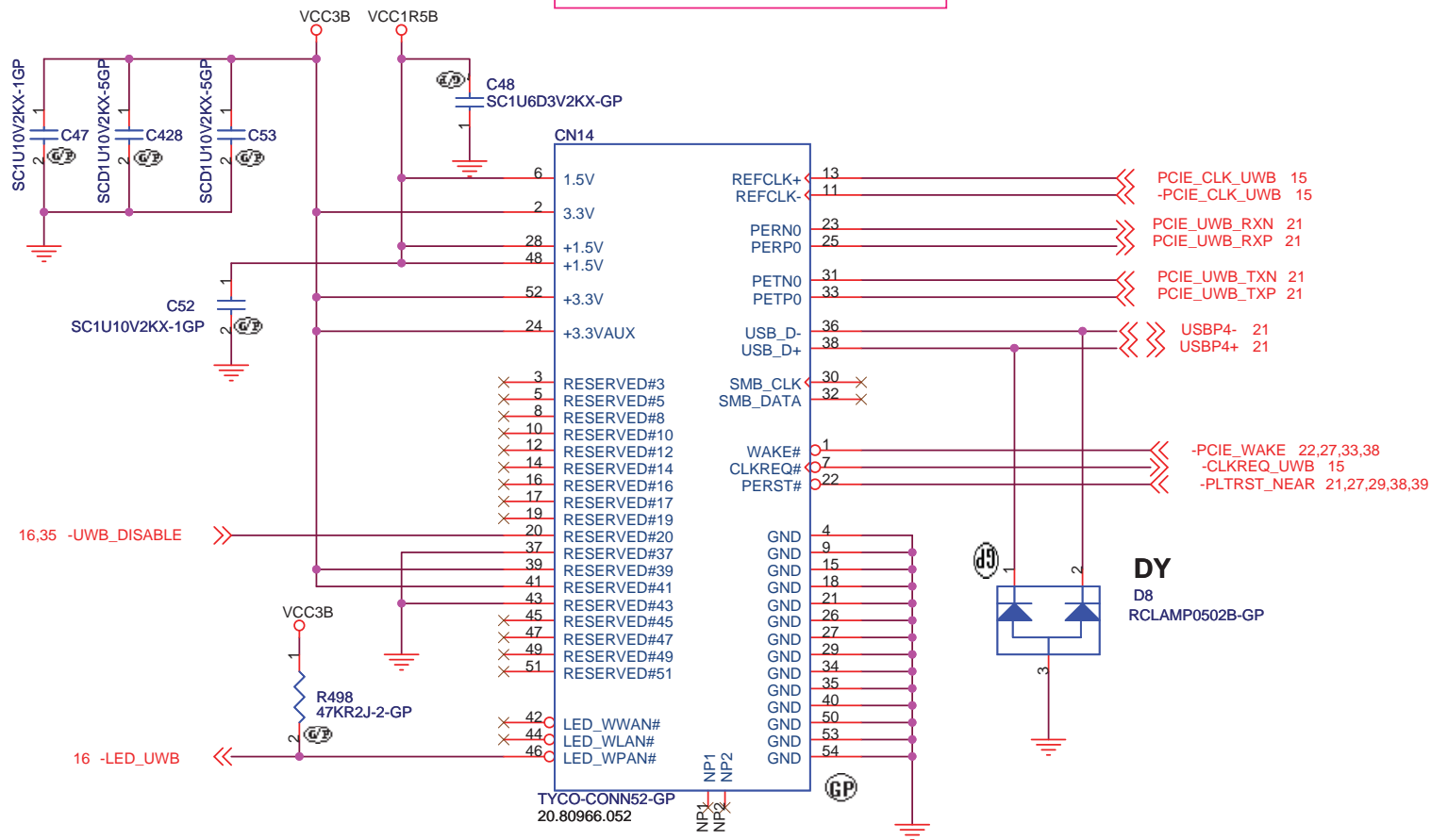




<Variant Name>

<b>緯創資通</b>		<b>Wistron Corporation</b> 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
<b>RJ45 CONN</b>			
Size A4	Document Number <b>Pecan-1</b>		Rev <b>-2</b>
Date:	Wednesday, August 06, 2008	Sheet 26 of	61





<Variant Name>

緯創資通

**Wistron Corporation**  
 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
 Taipei Hsien 221, Taiwan, R.O.C.

Title

**MINI CARD SLOT 2**

Size  
A4

Document Number

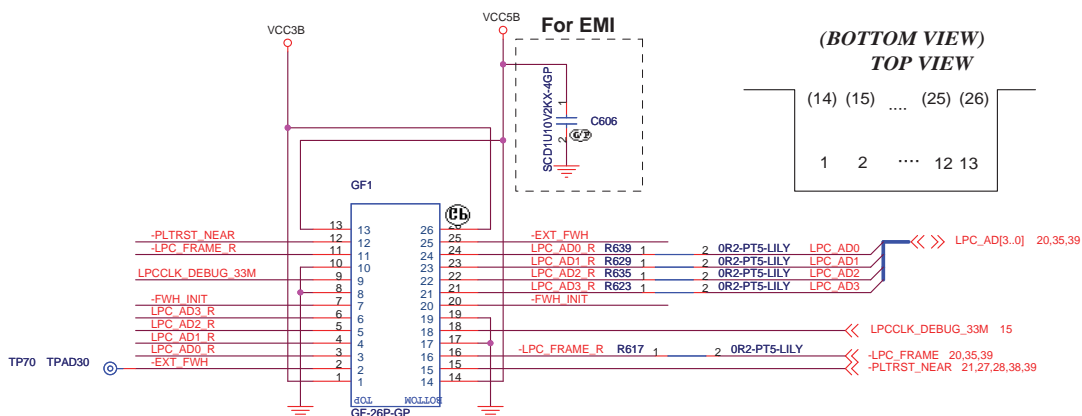
**Pecan-1**

Rev  
-2

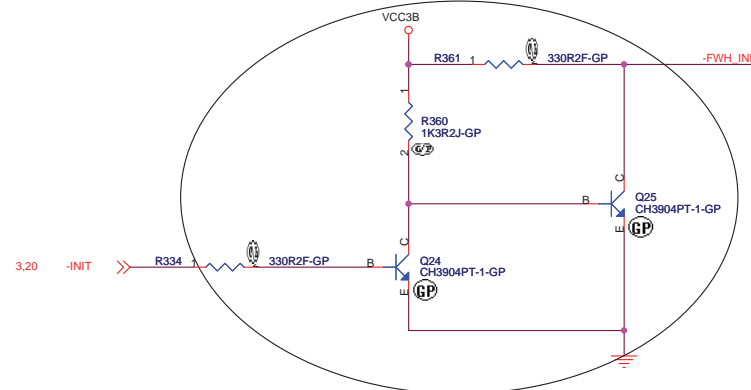
Date: Wednesday, August 06, 2008

Sheet 28 of 61

## Golden Finger for Debug Board



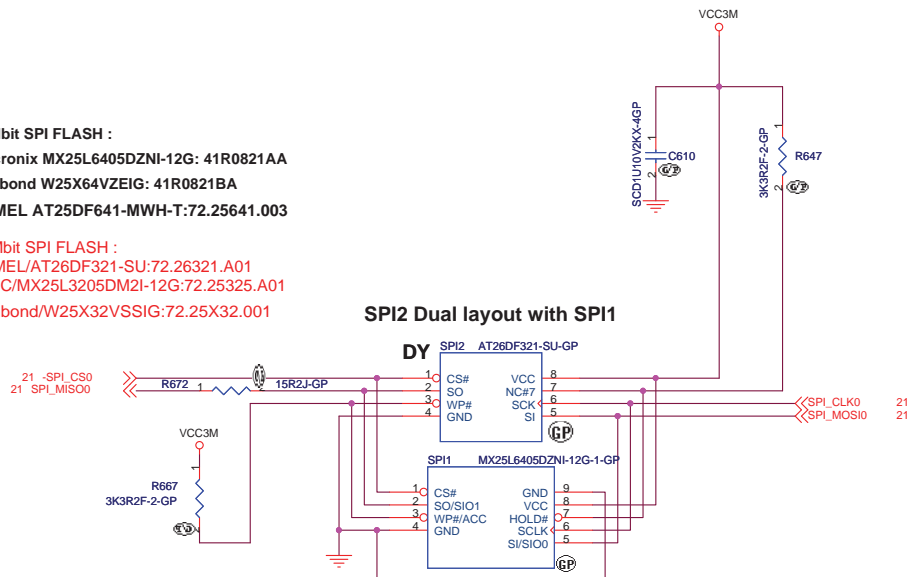
Check need to ALL NoASM the same KS-3



64Mbit SPI FLASH :  
 Macronix MX25L6405DZNI-12G: 41R0821AA  
 Winbond W25X64VZEIG: 41R0821BA  
 ATMEL AT25DF641-MWH-T:72.25641.003

32Mbit SPI FLASH :  
 ATMEL/AT26DF321-SU:72.26321.A01  
 MXIC/MX25L3205DM2I-12G:72.25325.A01  
 Winbond/W25X32VSSIG:72.25X32.001

### SPI2 Dual layout with SPI1



SO8 and WSON8 are both supported!

<Variant Name>

**緯創資通** **Wistron Corporation**  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title	Author	Date	Page	Page	Page	Page	Page	Page	Page	Page	Page
Title	Author	Date	Page	Page	Page	Page	Page	Page	Page	Page	

**SPI**Size  
A3

Document Number
-----------------

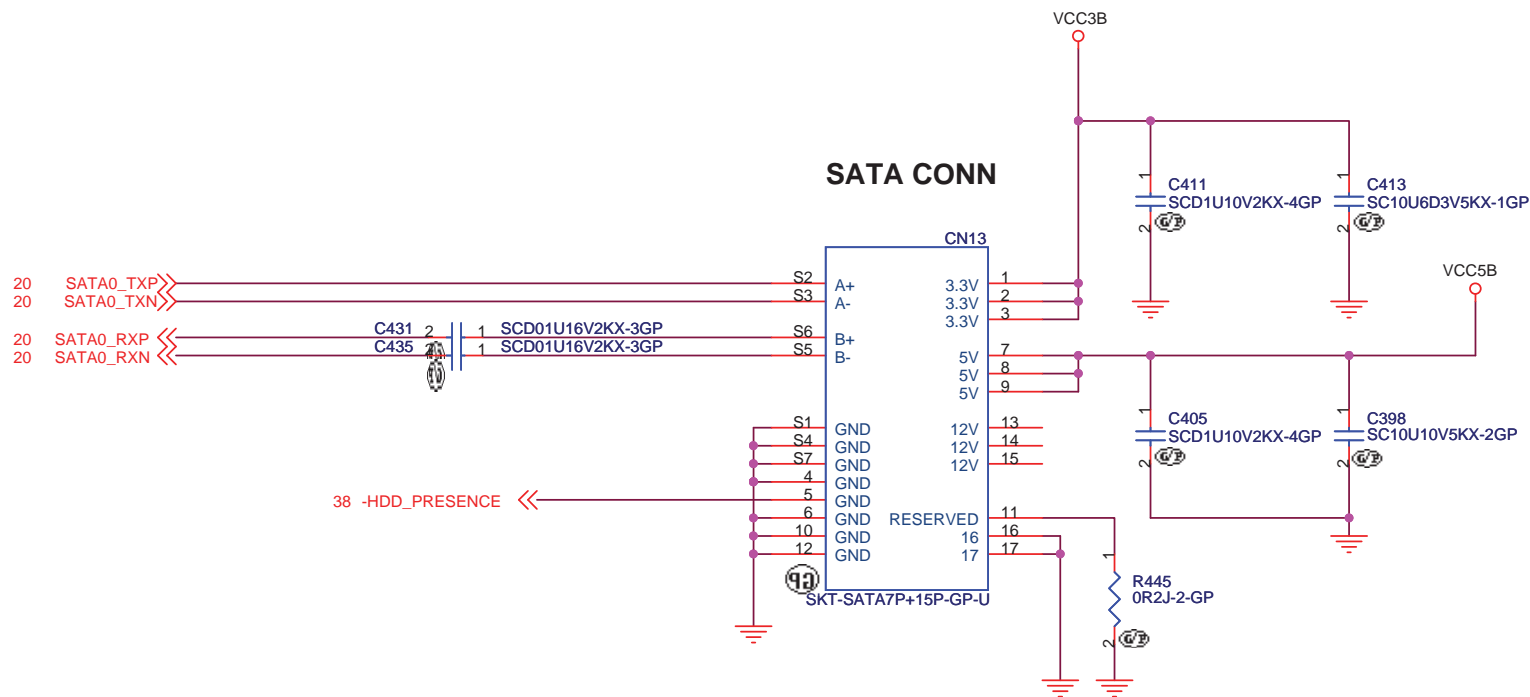
## Pecan-1

Rev	-2
-----	----

Date: Wednesday, August 06, 2008

Sheet

61



<Core Design>

緯創資通

**Wistron Corporation**  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title

**2.5 inch SATA Connector**

Size  
A4

Document Number

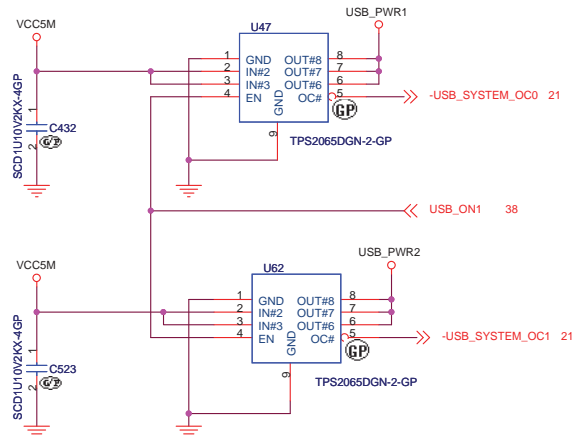
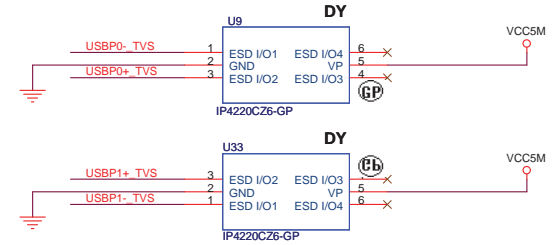
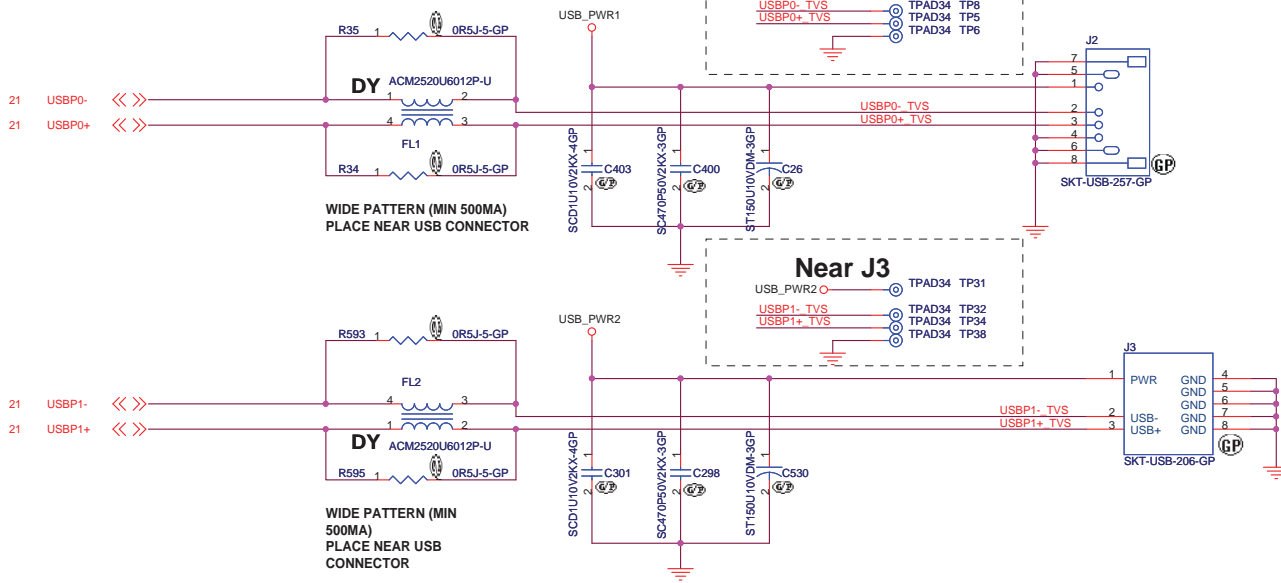
**Pecan-1**

Rev  
-2

Date: Wednesday, August 06, 2008

Sheet 30 of 61

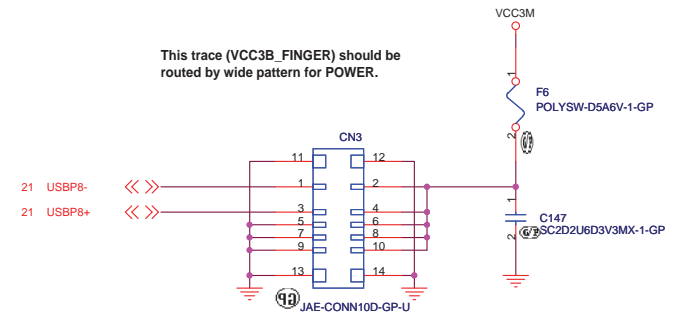
COMMON MODE CHOKE COIL: TDK ACM2520U  
PLACE THESE RESISTORS AS THE CHOKE COIL ARE ALSO  
PLACED ON THE SMA PADS OF THESE TWO 0-OHM  
RESISTORS



	TI P/N	WISTRON P/N
1	TPS2065DGN-GP	41A1229AA
2	TPS2065DGN-1-GP	74.02065.A79
3	BD8010FVJ-GP	74.08010.07G

## Fingerprint Reader

This trace (VCC3B\_FINGER) should be routed by wide pattern for POWER.



<Variant Name>

<b>緯創資通 Wistron Corporation</b> 21F, 88, Sec 1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title	
Size A3 Document Number	
Date: Wednesday, August 06, 2008	
Sheet 31 of 61	
<b>USB POWER / CONNECTOR</b> <b>Pecan-1</b> Rev -2	

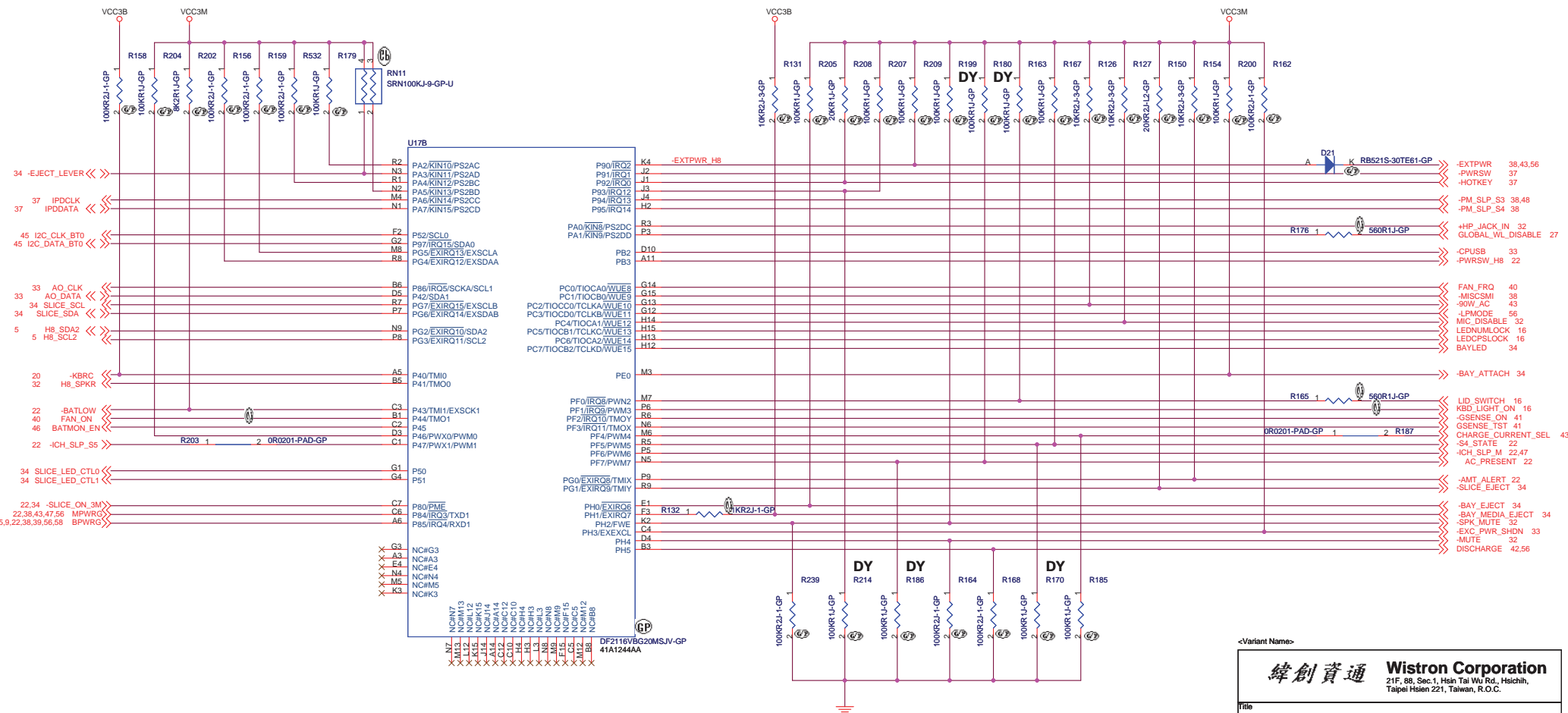
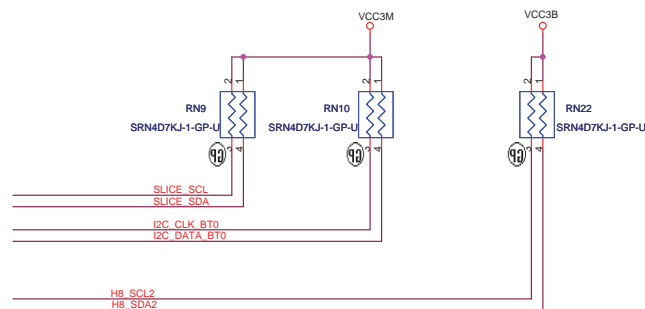




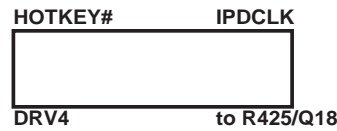
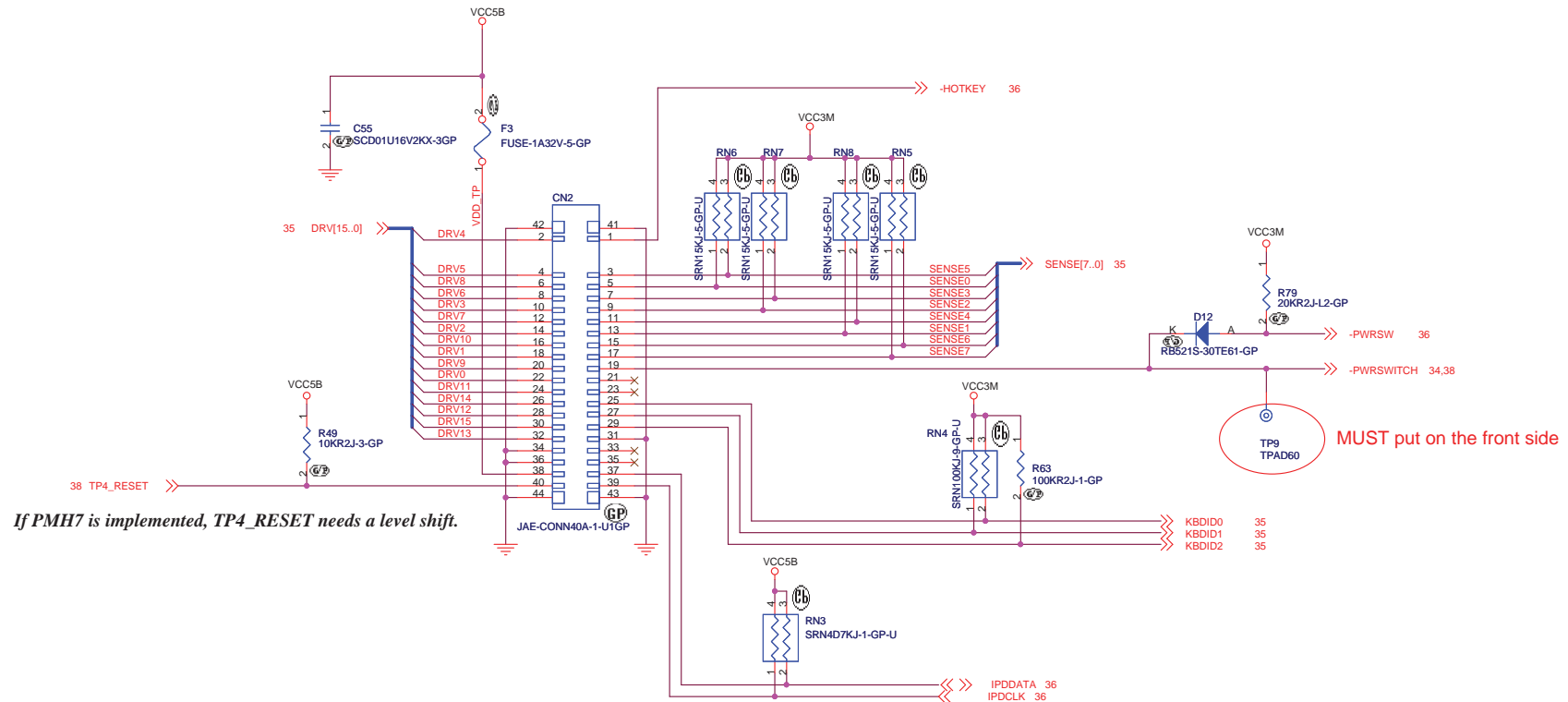








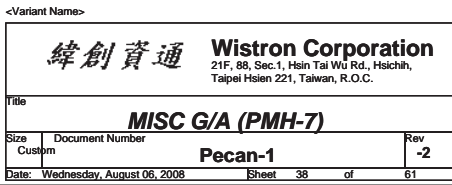
# Keyboard Connector



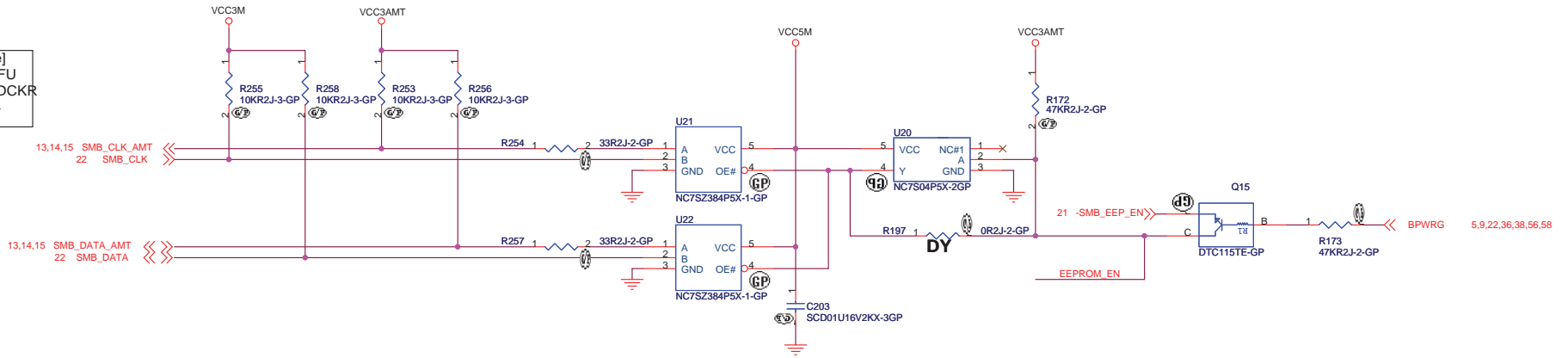
Keyboard Connector Top View

<Variant Name>

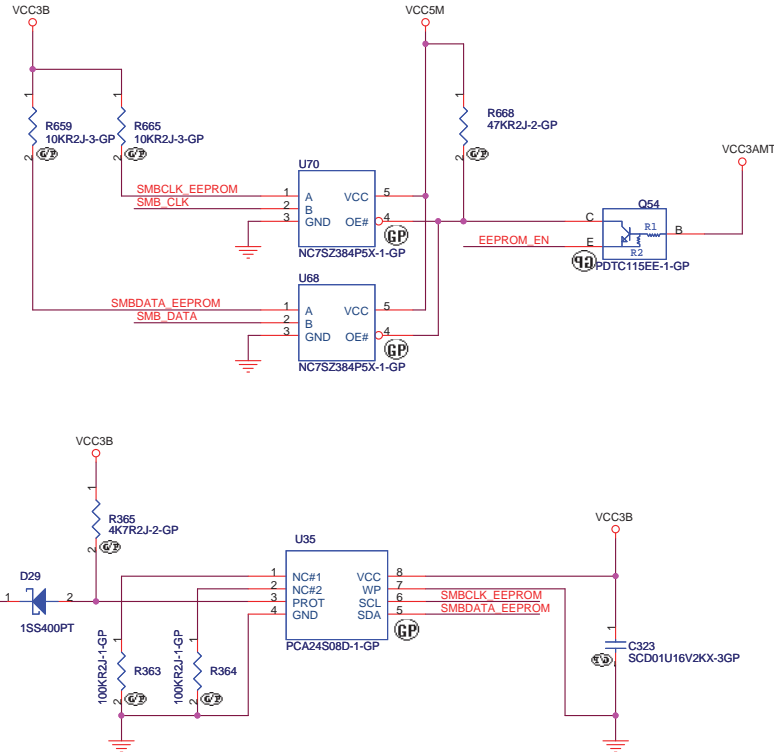
<b>緯創資通</b>		<b>Wistron Corporation</b> 21F, 88, Sec. 1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
<b>KEYBOARD CONN</b>			
Size A3	Document Number	<b>Pecan-1</b>	Rev -2
Date:	Wednesday, August 06, 2008	Sheet 37 of	61



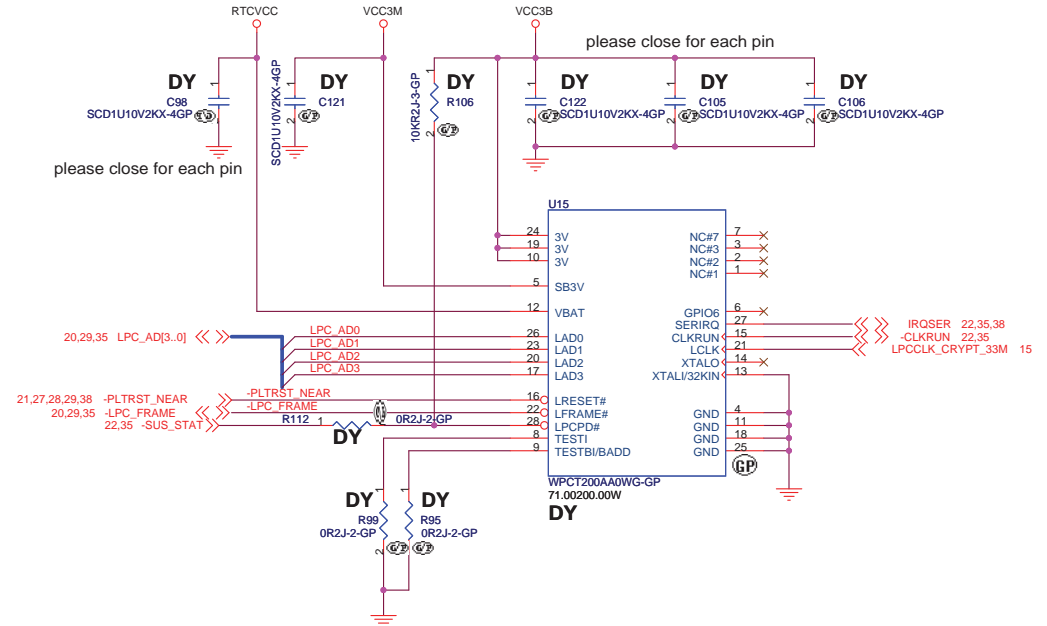
[Sourcing Candidate]  
Toshiba TC7SB384FU  
TI SN74CBT1G384DCKR  
Fairchild NC7SZ384



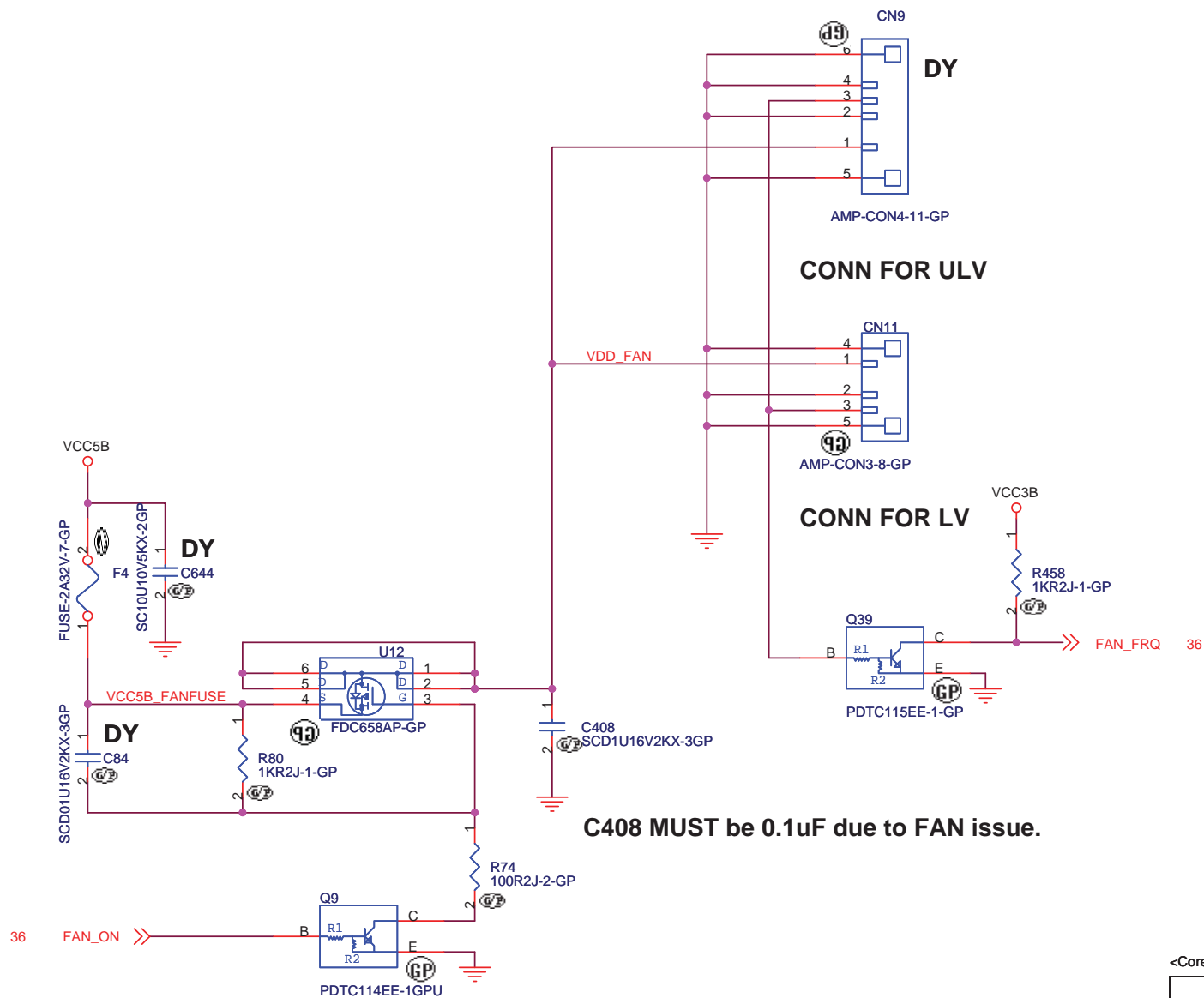
## EEPROM



## T CPA



<Core Design>			
緯創資通		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
EEPROM/TCPA			
Size	Document Number		Rev
A3			-2
Date: Wednesday, August 06, 2008		Sheet 39 of 61	Pecan-1



<Core Design>

緯創資通

**Wistron Corporation**  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title

**FAN CONTROL**

Size  
A4

Document Number

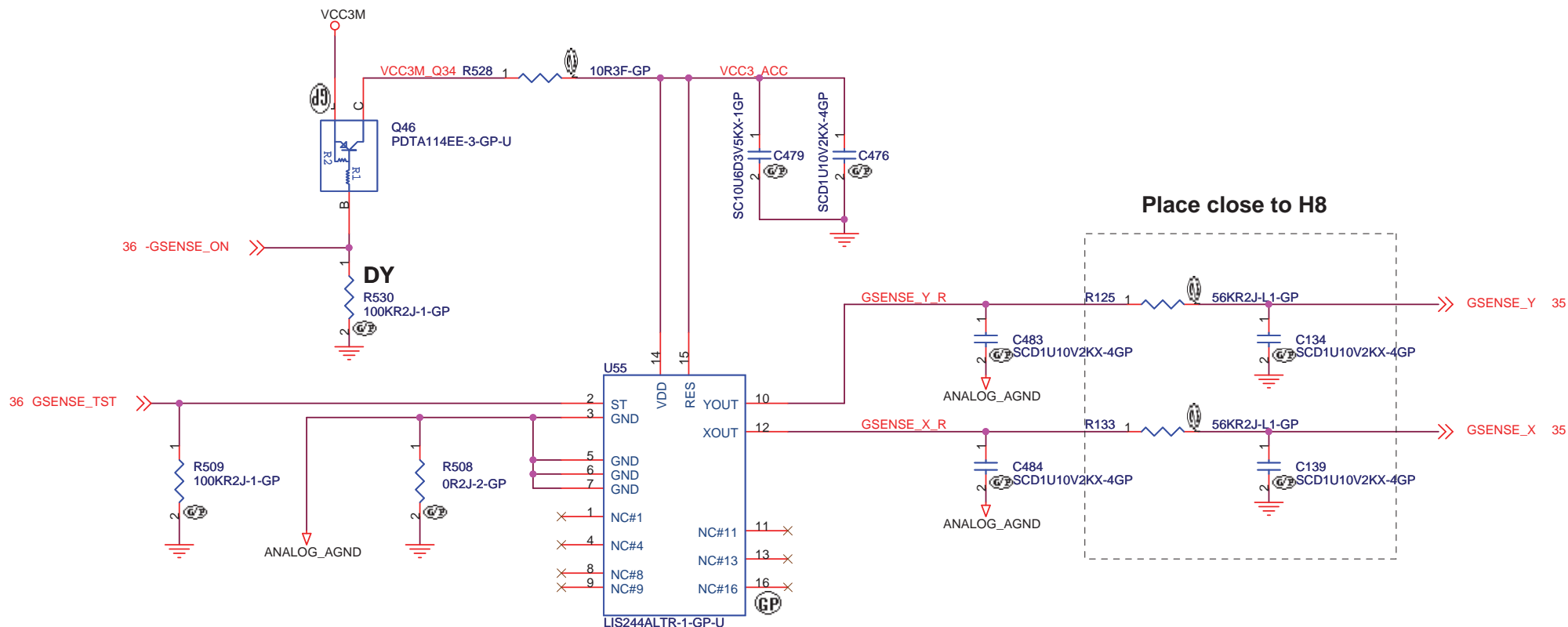
**Pecan-1**

Rev  
-2

Date: Wednesday, August 06, 2008

Sheet 40 of 61






2-axis	ADI	ADXL322	41R0828BA
	ST	LIS244AL	41R0525AA
3-axis	ST	LIS34AL	45R0828AA

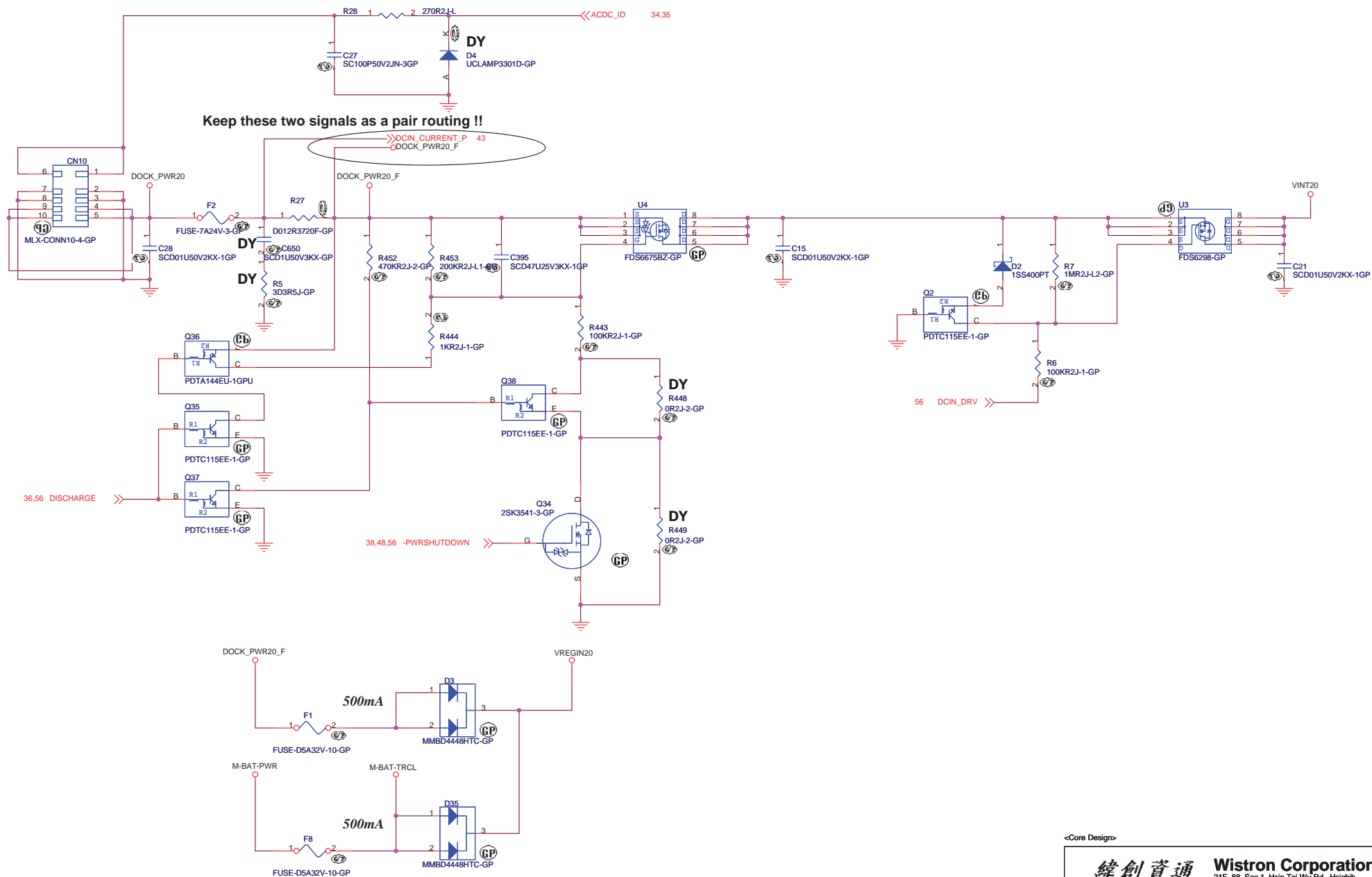
Width = 6 mil & Spacing = 10 mil  
for three Output traces

	ADXL322 LIS244AL LIS34AL	No Accel
R530	NO_ASM	ASM
R509	ASM	ASM
All other	ASM	NO_ASM

Layout Comment :  
(1) Place C483, C484, Q46, R528, R530, C479, C476, R509, R508 close to U55.  
(2) Avoid routing under DCDC switching area.

<Core Design>

		<b>Wistron Corporation</b> 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
<b>G-SENSOR</b>			
Size A4	Document Number		Rev -2
<b>Pecan-1</b>			
Date:	Thursday, August 07, 2008	Sheet 41 of	61



<Core Design>

緯創資通

**Wistron Corporation**  
21F, 88, Sec. 1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title

**DC-IN AND CHARGER (1/2)**

Size  
A3

Document Number

**Pecan-1**

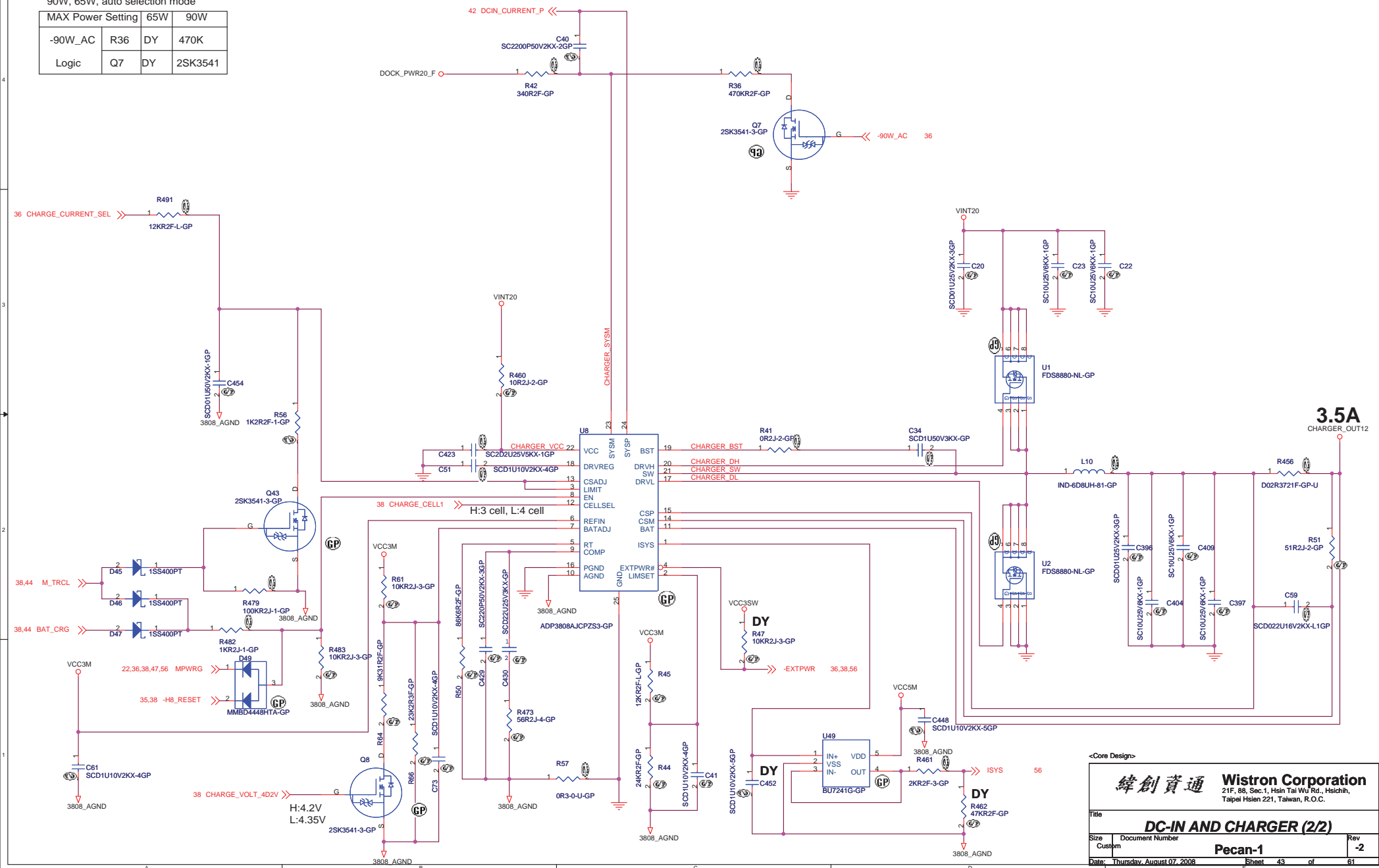
Rev  
-2

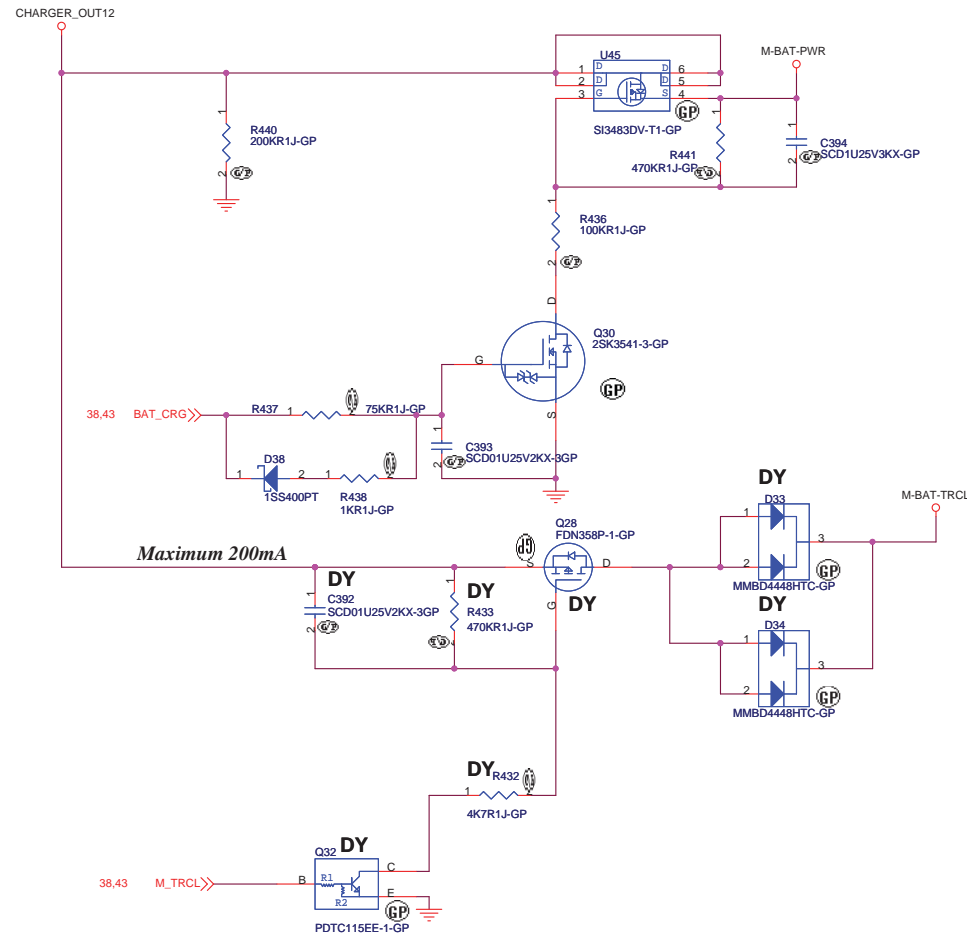
Date: Wednesday, August 06, 2008

Sheet 42 of 61

Keep these two signals as a pair routing !!

90W, 65W, auto selection mode				
MAX Power Setting	65W	90W		
-90W_AC	R36	DY	470K	
Logic	Q7	DY	2SK3541	



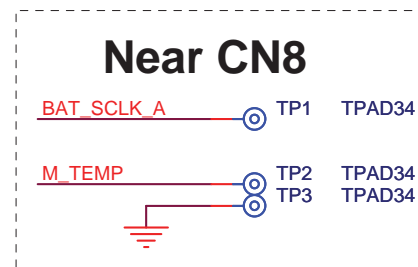
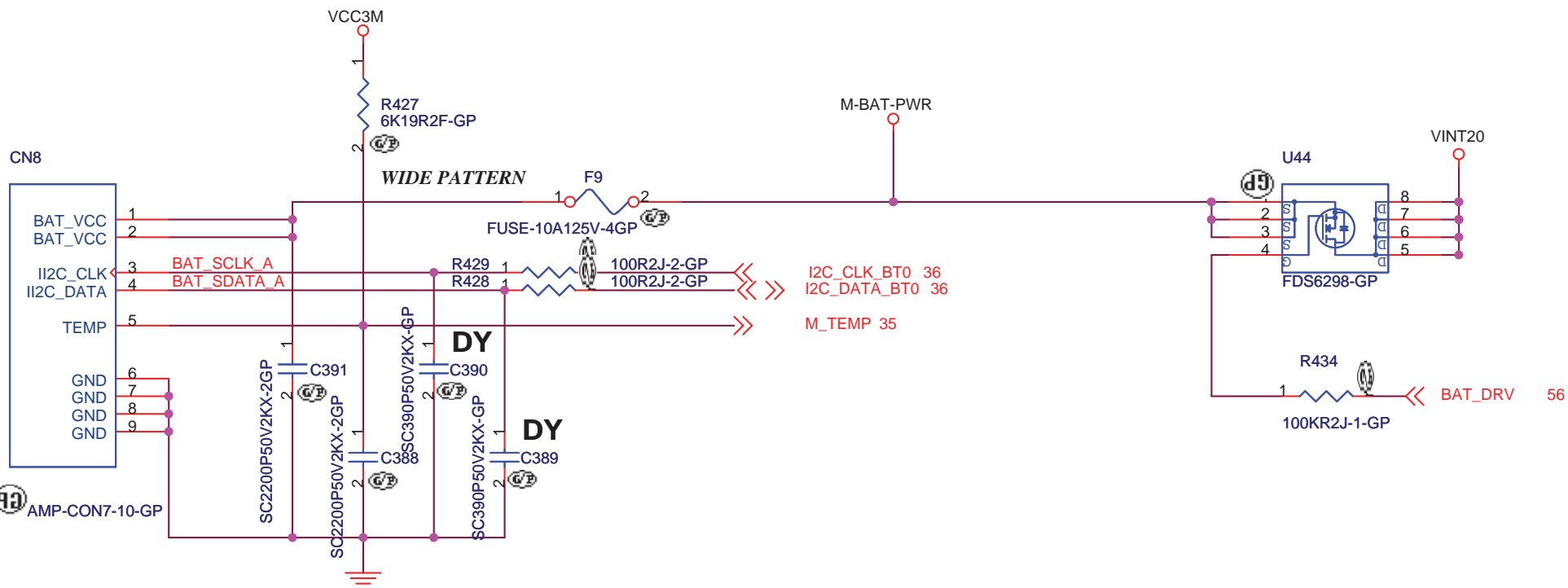


<Core Design>

緯創資通

**Wistron Corporation**  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title			
<b>CHARGER SELECT</b>			
Size	Document Number	Rev	
A3	<b>Pecan-1</b>	<b>-2</b>	
Date: Wednesday, August 06, 2008		Sheet 44	of 61



<Core Design>

緯創資通

**Wistron Corporation**

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title

**BATTERY INPUT**

Size  
A

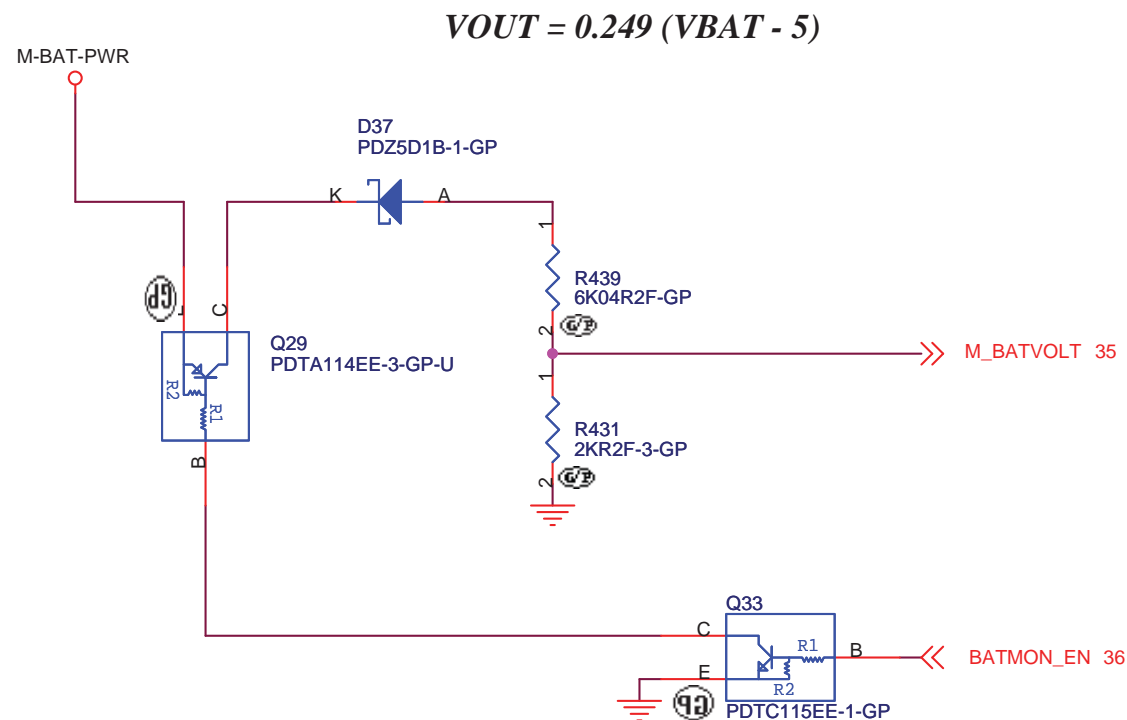
Document Number

**Pecan-1**

Rev  
-2

Date: Wednesday, August 06, 2008

Sheet 45 of 61



<Core Design>

緯創資通

**Wistron Corporation**

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title

**BATTERY MONITOR**

Size  
A

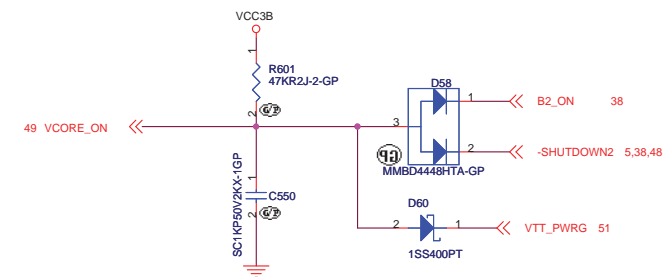
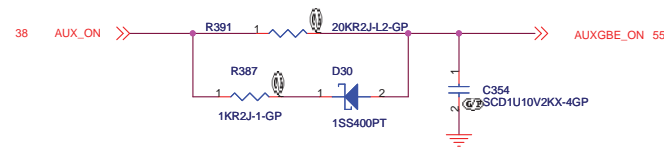
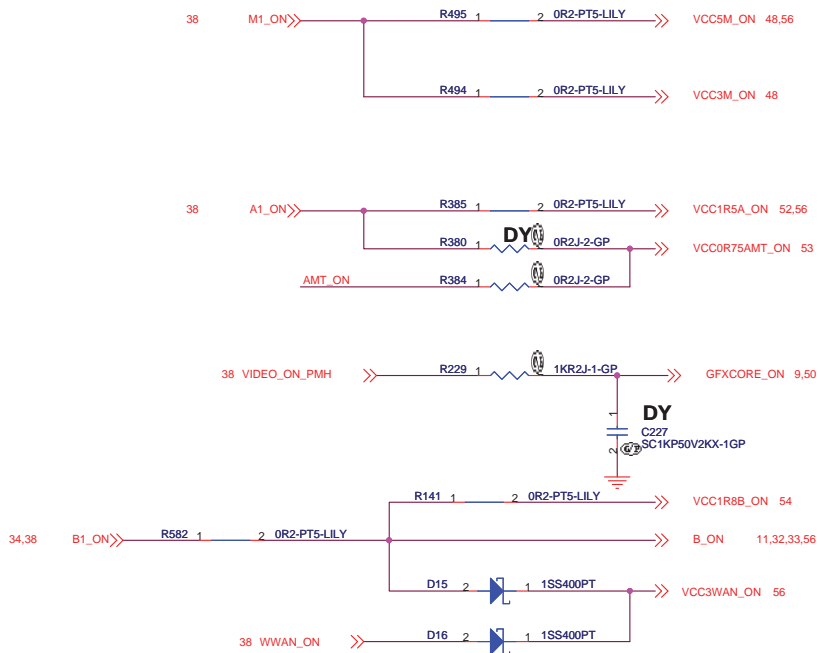
Document Number

**Pecan-1**

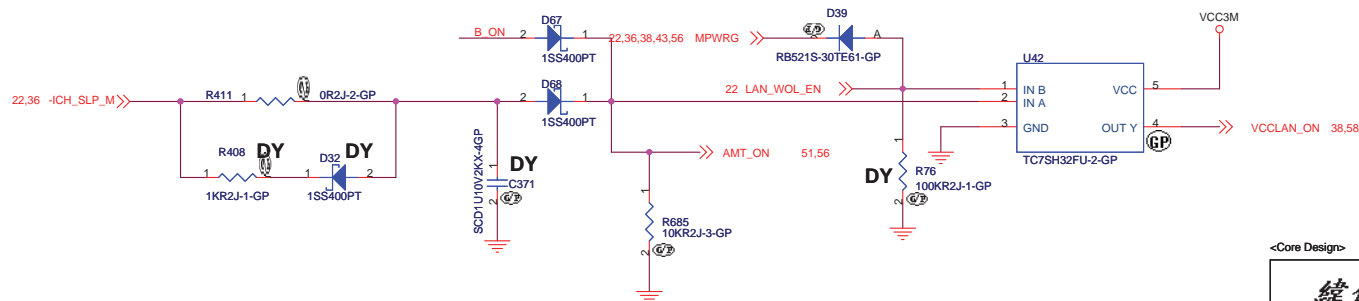
Rev  
-2

Date: Wednesday, August 06, 2008

Sheet 46 of 61



Always ON	Enable	Disable
D15	ASM	No_ASM
D16	ASM	No_ASM



<Core Design>

**緯創資通 Wistron Corporation**  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title: **POWER SEQUENCE**

Size: A3 Document Number: **Pecan-1** Rev: **-2**

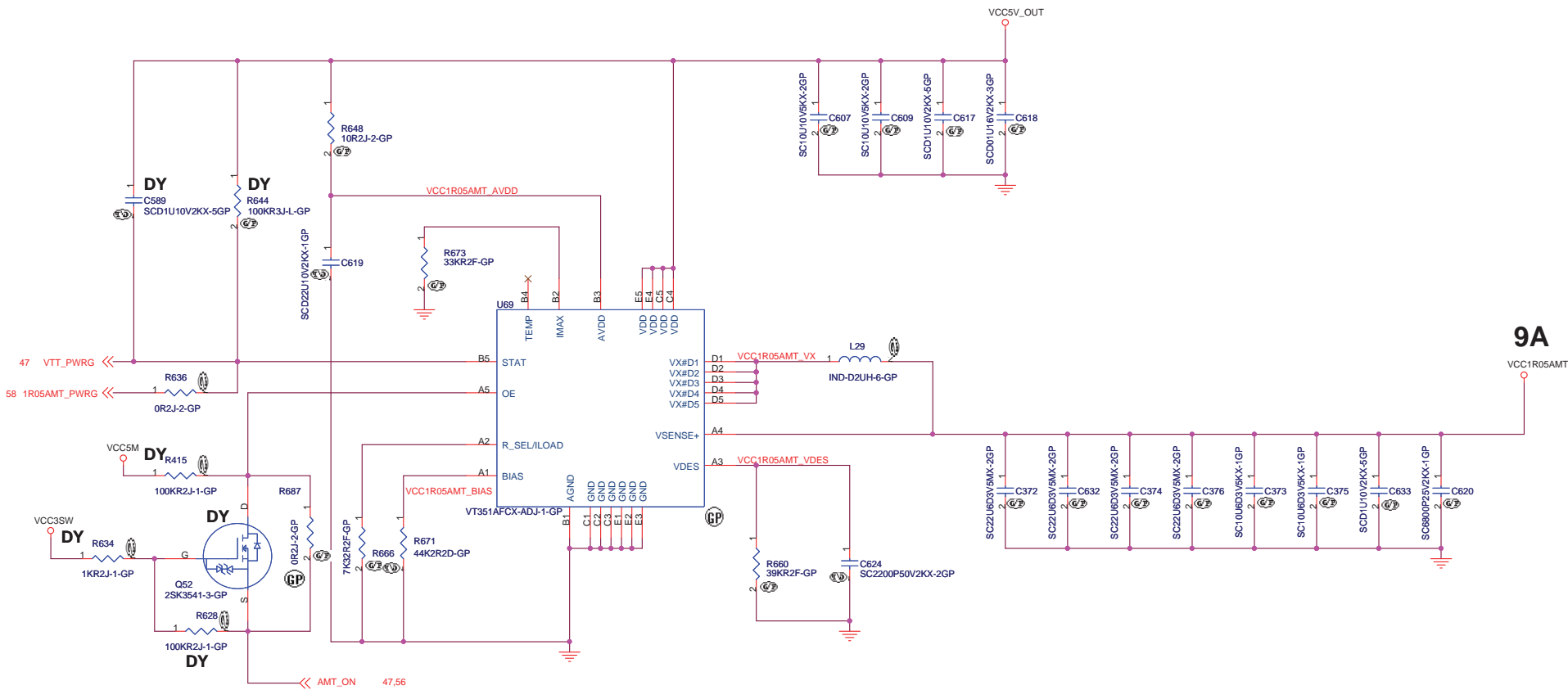
Date: Wednesday, August 06, 2008 Sheet: 47 of 61



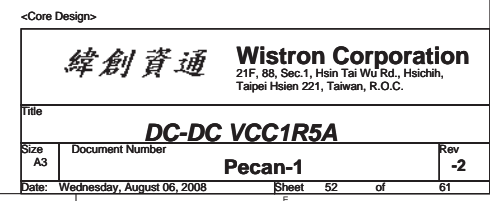


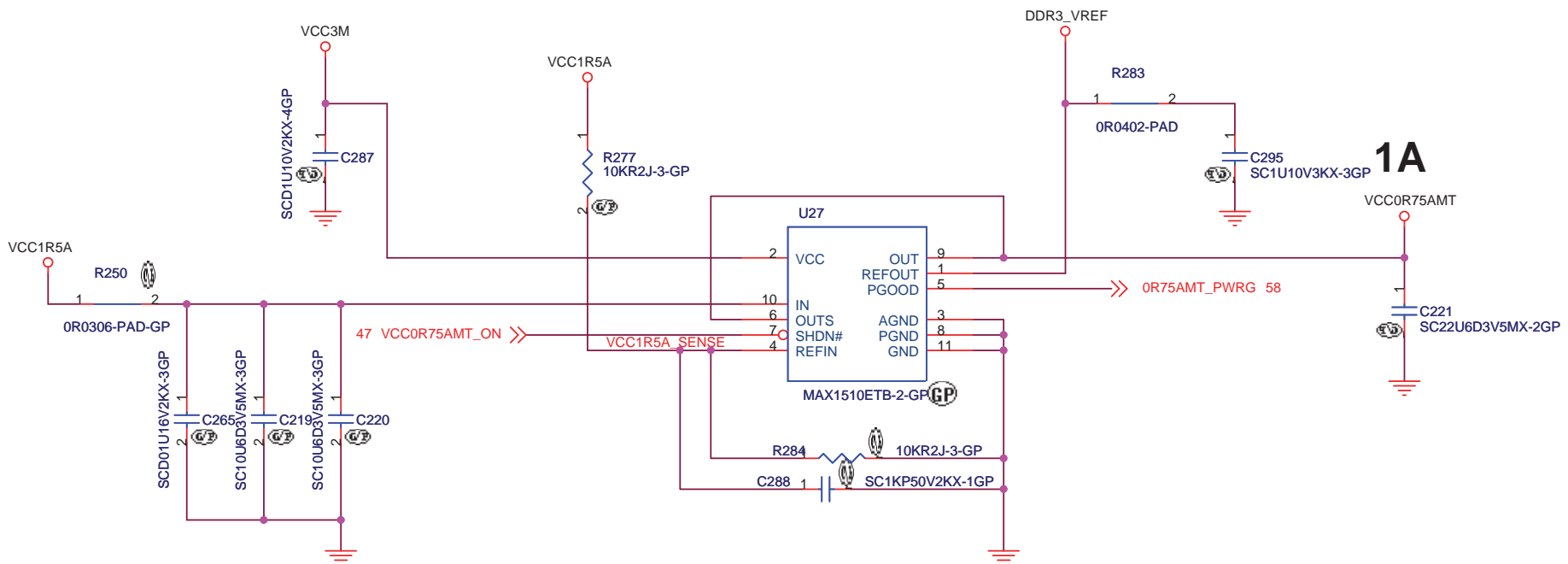






<Core Design>			
緯創資通		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
DC-DC VCC1R05AMT			
Size	Document Number		Rev
A3			-2
Date: Wednesday, August 06, 2008		Sheet 51 of 61	
Pecan-1			

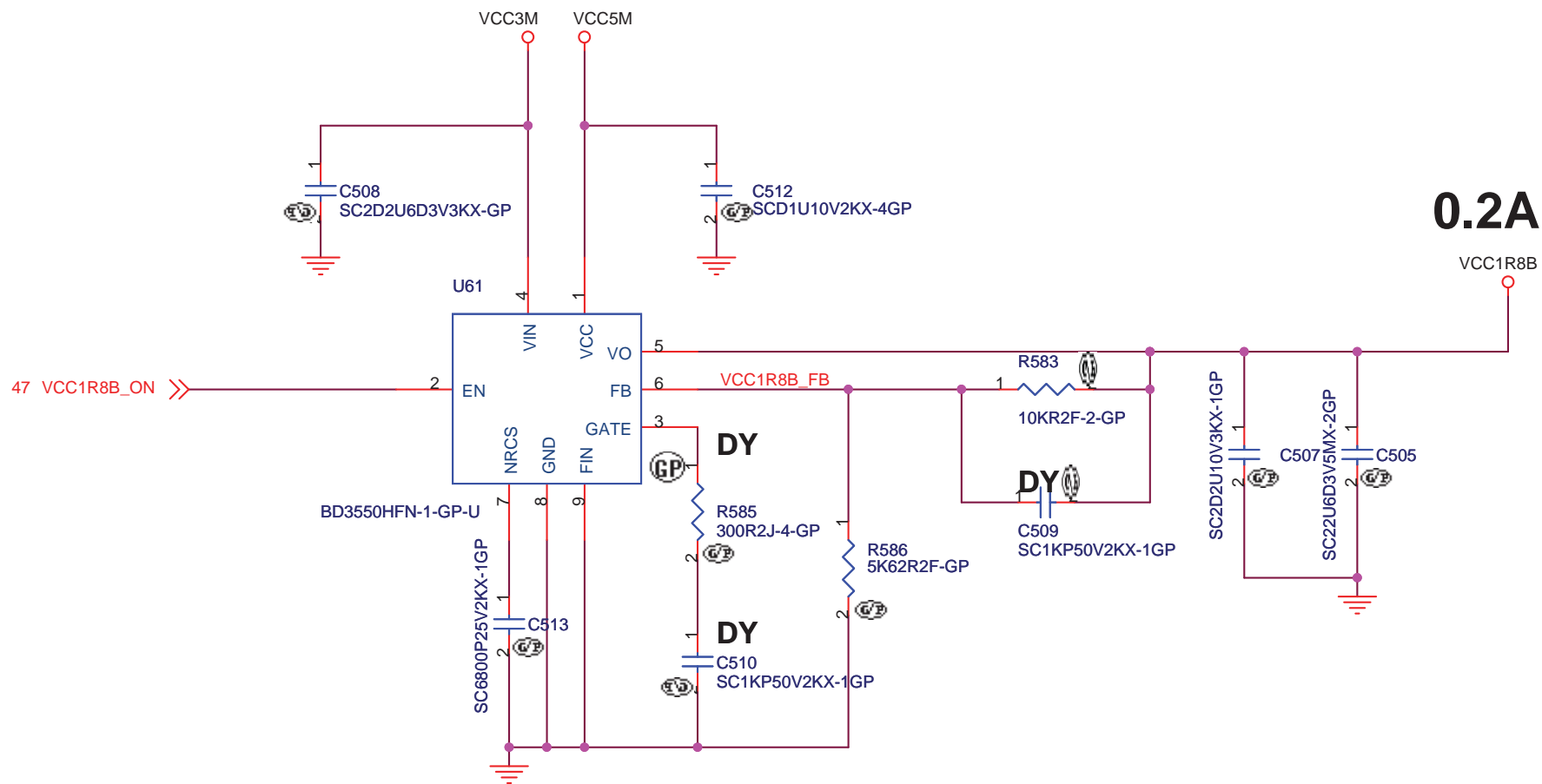




<Core Design>

緯創資通 **Wistron Corporation**  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title			<b>DC-DC VCC0R75AMT</b>	
Size A4	Document Number			Rev -2
Date: Wednesday, August 06, 2008			<b>Pecan-1</b>	
Sheet		53	of	61



<Core Design>

緯創資通

**Wistron Corporation**

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title

**DC-DC VCC1R8B**

Size  
A

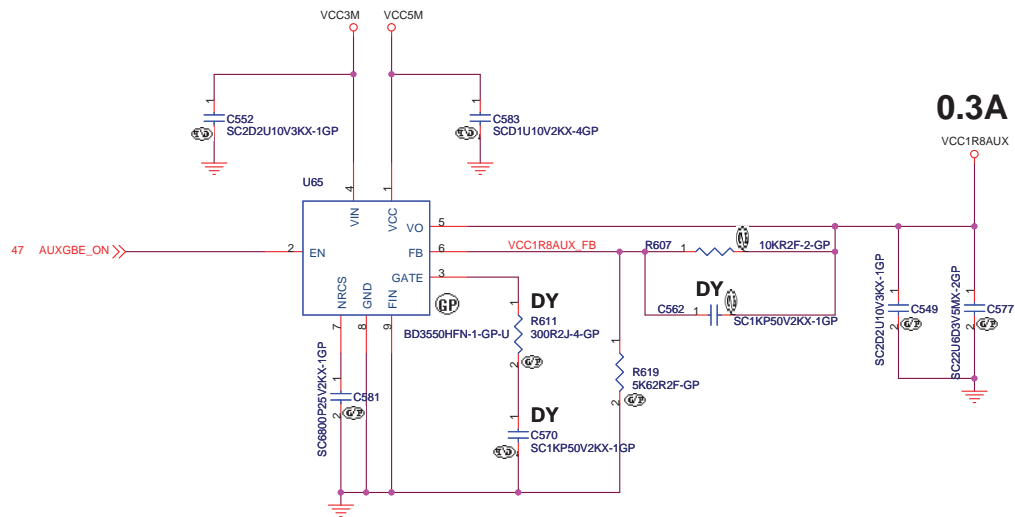
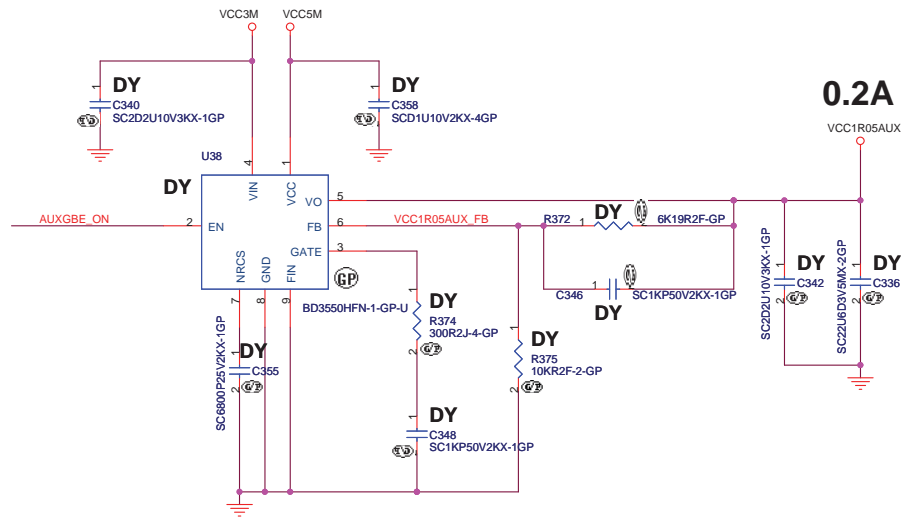
Document Number

**Pecan-1**

Rev  
-2

Date: Wednesday, August 06, 2008

Sheet 54 of 61



<Core Design>

緯創資通

**Wistron Corporation**  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title

**DC-DC VCC1R05AUX/VCC1R8AUX**

Size

Document Number

**Pecan-1**

Rev

**-2**

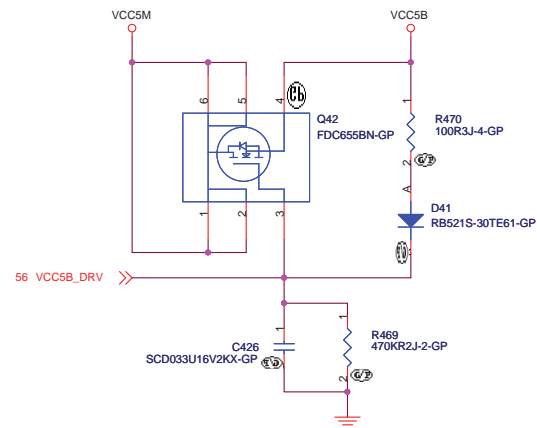
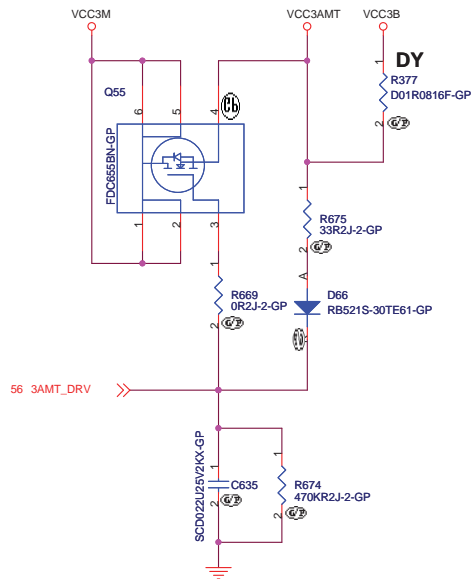
Date: Wednesday, August 06, 2008

Sheet 55 of 61

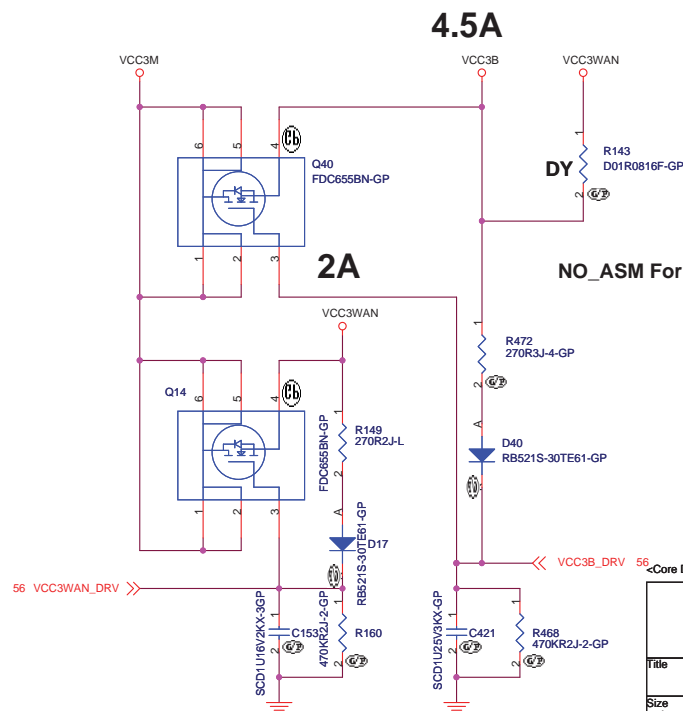
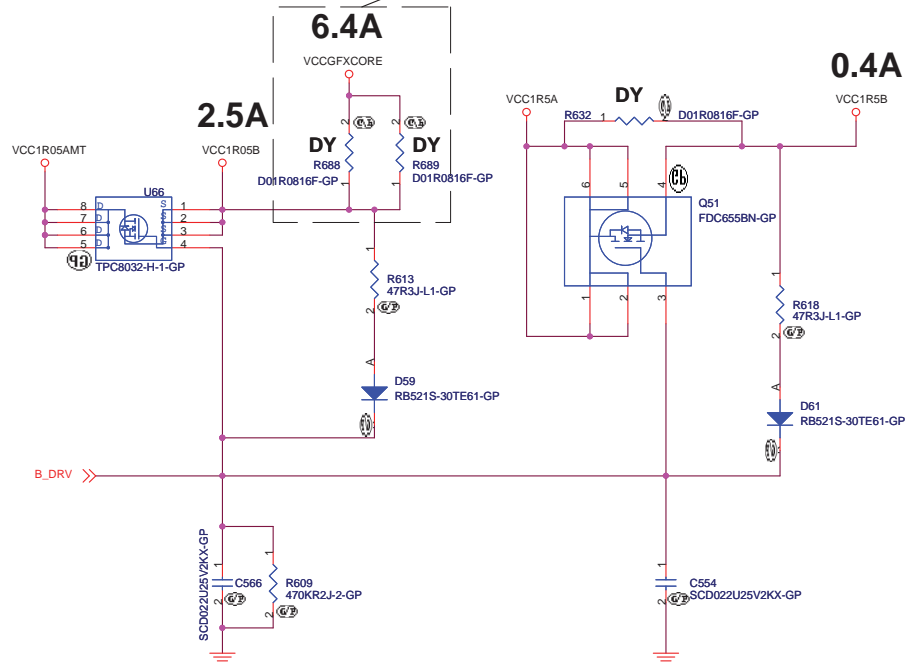




0.2A



Place near VCCGFXCORE logic

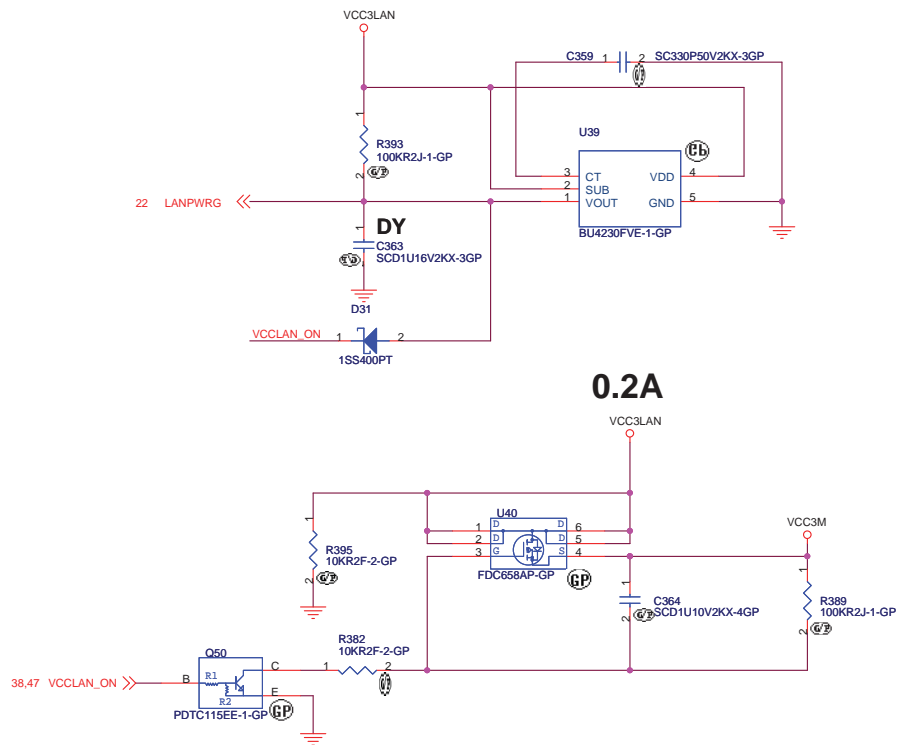


NO\_ASM For No WWAN CONN. MODEL

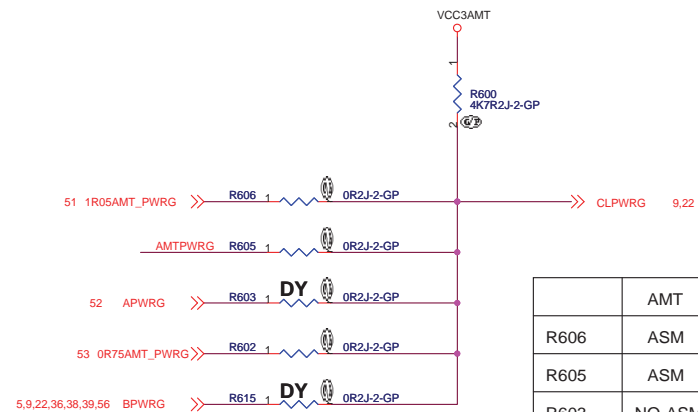
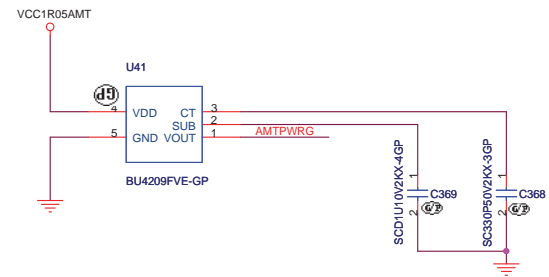
Always ON	Enable	Disable
Q14	ASM	No_ASM
R149	ASM	No_ASM
D17	ASM	No_ASM
R160	ASM	No_ASM
C153	ASM	No_ASM
R143	No_ASM	ASM

緯創資通 Wistron Corporation  
21F, 88, Sec. 1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title		
LOAD SW 1		
Size A3	Document Number Pecan-1	Rev -2
Date: Wednesday, August 06, 2008	Sheet	57 of 61



## NOASM for NON-AMT

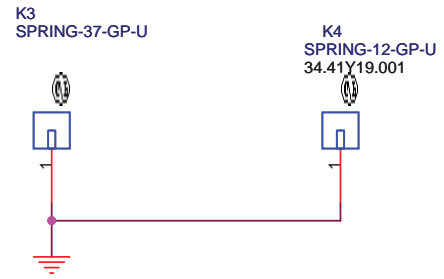
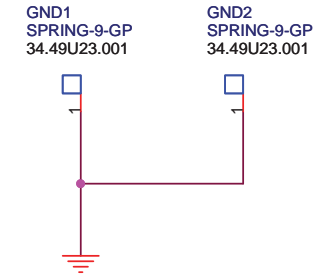
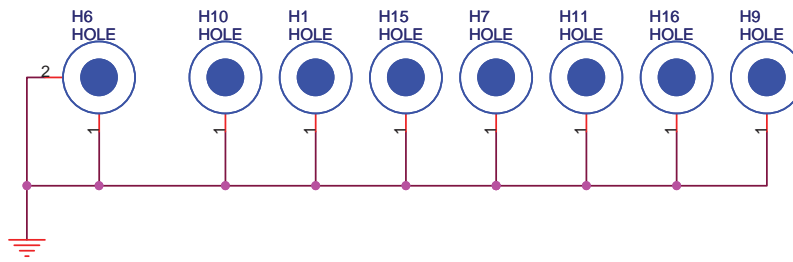
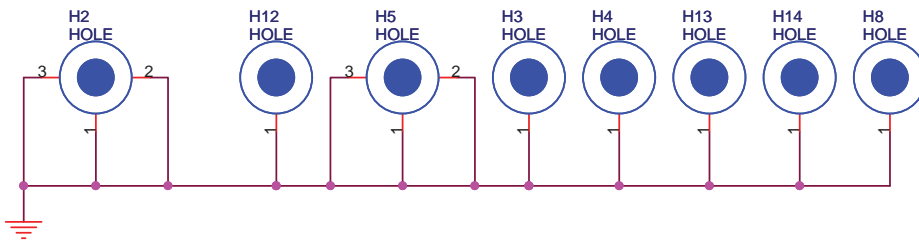


	AMT	NON-AMT
R606	ASM	NO-ASM
R605	ASM	NO-ASM
R603	NO-ASM	NO-ASM
R602	ASM	NO-ASM
R600	ASM	NO-ASM
R615	NO-ASM	ASM

<Core Design>

緯創資通 Wistron Corporation  
21F, 88, Sec. 1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title			
LOAD SW 2			
Size A3	Document Number		Rev -2
Date: Wednesday, August 06, 2008		Sheet 58 of 61	



<Core Design>

<b>緯創資通</b>		<b>Wistron Corporation</b> 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
<b>HOLES/GND/PADS</b>			
Size A4	Document Number <b>Pecan-1</b>		Rev <b>-2</b>
Date: Wednesday, August 06, 2008		Sheet 59 of	61

Comparison chart for AMT function

Item	Page	Location Netname	AMT	NON_AMT	Modify	Note
1	22	U37	ICH9-M Enhanced	ICH9-M		
	22	R424	NO_ASM	ASM		
	24	U30	BOAZMAN 82567LM			
	29	SPI1	64Mbit			
	48	R411	ASM	NO_ASM		
	51	R636	ASM	NO_ASM		
	58	R603	ASM	NO_ASM		
		R605	ASM	NO_ASM		
		R606	ASM	NO_ASM		
	57	R377	NO_ASM	ASM		
		Q55	ASM	NO_ASM		
		R669	ASM	NO_ASM		
		R675	ASM	NO_ASM		
		D66	ASM	NO_ASM		
		C635	ASM	NO_ASM		
		R674	ASM	NO_ASM		
		U66	ASM	NO_ASM		
		R613	ASM	NO_ASM		
		D59	ASM	NO_ASM		
		C566	ASM	NO_ASM		
		R609	ASM	NO_ASM		

<Core Design>

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title

AMT FUNCTION CONTROL TABLE

Size

A3

Document Number

Pecan-1

Rev

-2

Date: Wednesday, August 06, 2008

Sheet 60 of 61

**WWW.AliSaler.Com**

