

Project code: 91.4HD01.001
PCB P/N : 48.4GX01.0SA
PCB 版號 : 09919 SA
REVISION : PCB STACKUP



緯創資通

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

BLOCK DIAGRAM

30

Document Number

A:

Document Number

39	
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Sheet 1 of 6

5

4

3

2

1

D

C

B

A

Discrete Madison Hynix

緯創資通

Wistron Corporation

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Title

History

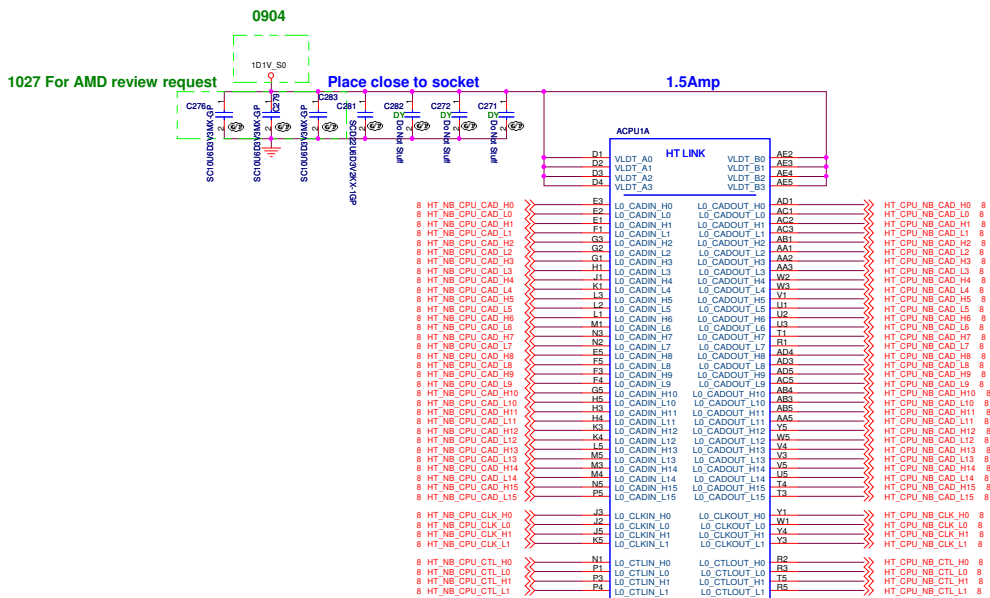
Size
A3

Document Number
JE40-DN

Rev
-3

Date: Friday, March 28, 2019

Sheet 2 of 63



SKT-CPU63AP-GP-U2
62.10055.111
2ND = 62.10055.181(ENG 未上)

SKT-BGA638H176

Discrete Madison Hynix

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Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsinchu,
Taipai Hsien 321, Taiwan, R.O.C.

File

CPU HT LINK I/F (1/4)

Rev

A3

Document Number

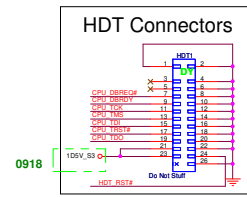
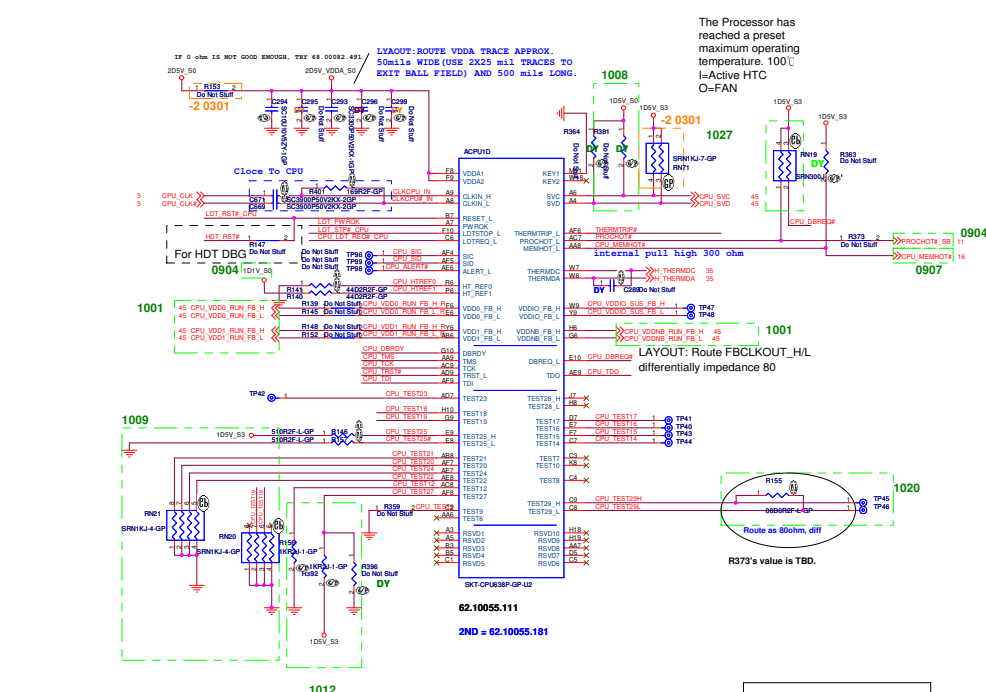
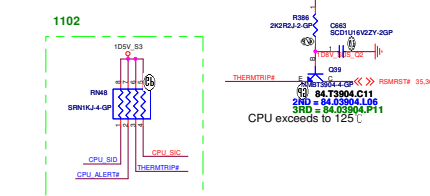
JE40-DN


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-3

Date: Friday, March 26, 2010

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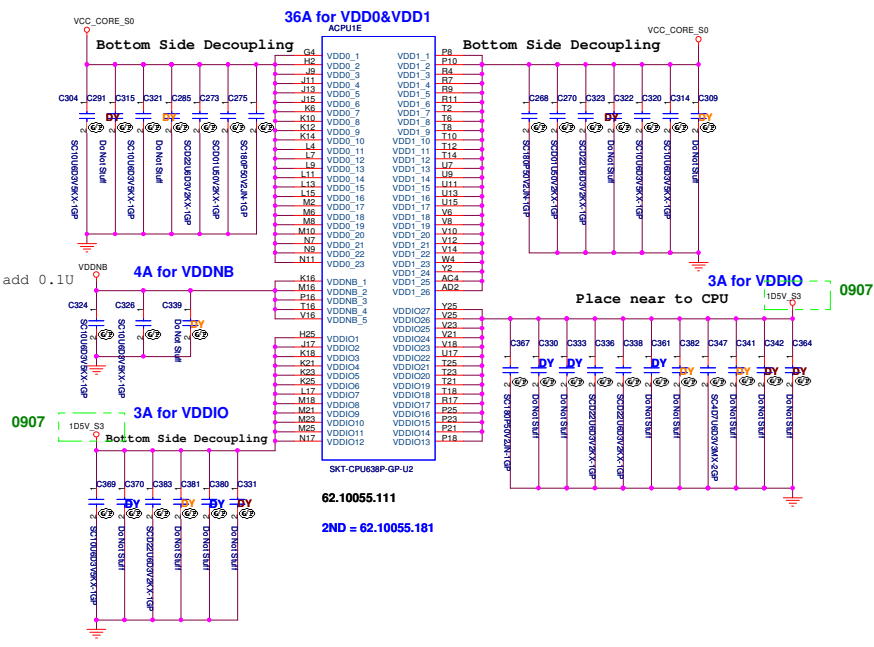


Discrete Madison Hynix			
		Wistron Corporation 21F, Hsien-1, Hsien Tai Wu Rd., Hsuehshui, Taipei Hsien 221, Taiwan, R.O.C.	
Title CPU_Control&Debug_(3/4)			
Size	Document Number		Rev
Custom	JE40-DN		-3
Date: March, 28th, 2010		Sheet 6	of 63

ACPU1F		
AA4	VSS1	VSS66
AA11	VSS2	VSS67
AA13	VSS3	VSS68
AA15	VSS4	VSS69
AA17	VSS5	VSS70
AA19	VSS6	VSS71
AB2	VSS7	VSS72
AB7	VSS8	VSS73
AB9	VSS9	VSS74
AB23	VSS10	VSS75
AB25	VSS11	VSS76
AC11	VSS12	VSS77
AC13	VSS13	VSS78
AC17	VSS14	VSS79
AC19	VSS15	VSS80
AC21	VSS16	VSS81
AC23	VSS17	VSS82
AD6	VSS18	VSS83
AD8	VSS19	VSS84
AD25	VSS20	VSS85
AE11	VSS21	VSS86
AE13	VSS22	VSS87
AE15	VSS23	VSS88
AE17	VSS24	VSS89
AE19	VSS25	VSS90
AE21	VSS26	VSS91
AE23	VSS27	VSS92
B4	VSS28	VSS93
B6	VSS29	VSS94
B8	VSS30	VSS95
B9	VSS31	VSS96
B11	VSS32	VSS97
B13	VSS33	VSS98
B15	VSS34	VSS99
B17	VSS35	VSS100
B19	VSS36	VSS101
B21	VSS37	VSS102
B23	VSS38	VSS103
B25	VSS39	VSS104
D6	VSS40	VSS105
D8	VSS41	VSS106
D9	VSS42	VSS107
D11	VSS43	VSS108
D13	VSS44	VSS109
D15	VSS45	VSS110
D17	VSS46	VSS111
D19	VSS47	VSS112
D21	VSS48	VSS113
D23	VSS49	VSS114
D25	VSS50	VSS115
E4	VSS51	VSS116
F2	VSS52	VSS117
F11	VSS53	VSS118
F13	VSS54	VSS119
F15	VSS55	VSS120
F17	VSS56	VSS121
F19	VSS57	VSS122
F21	VSS58	VSS123
F23	VSS59	VSS124
F25	VSS60	VSS125
H7	VSS61	VSS126
H9	VSS62	VSS127
H21	VSS63	VSS128
H23	VSS64	VSS129
JA	VSS65	

SKT-CPU638P-GP-U2

62.10055.111
2ND = 62.10055.181



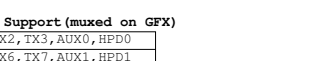
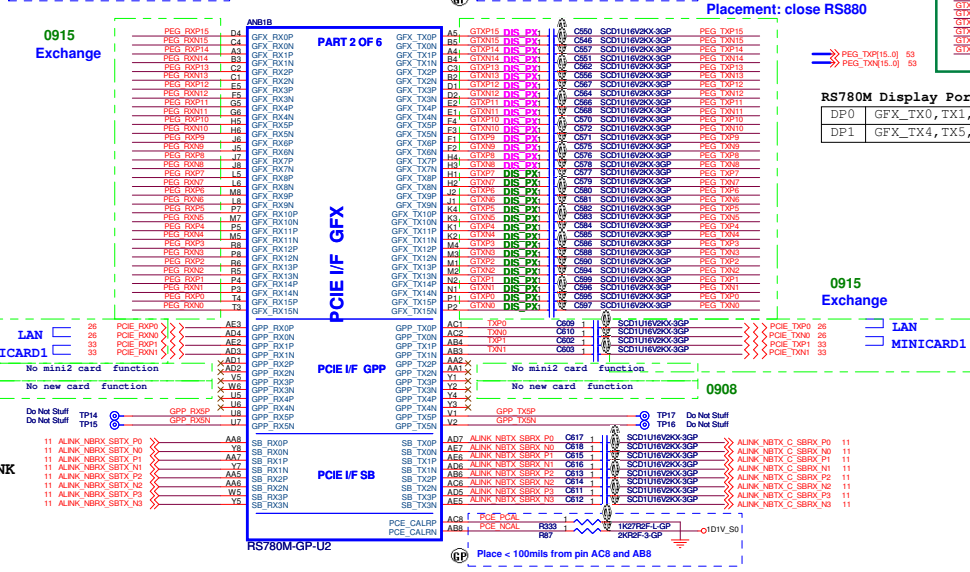
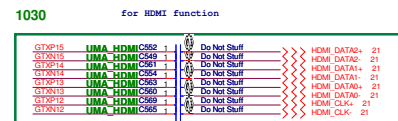
36A for VDD0&VDD1

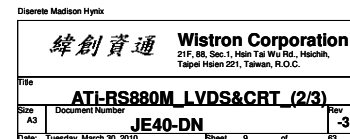
3A for VDDIO

62.10055.111
2ND = 62.10055.181

Discrete Madison Hynix

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File			
CPU_Power_(4/4)			
Size A3	Document Number JE40-DN	Rev -3	
Date: Friday, March 26, 2010		Sheet 7 of 8	





1027 For AMD review request

0.6A per ANT Rev1.1, Page3

0.45A per ANT Rev1.1, Page3

1027 For AMD review request

MEM_COMP_P and MEM_COMP_N trace width >=10mils and 10mils spacing from other Signals in X,Y,Z directions

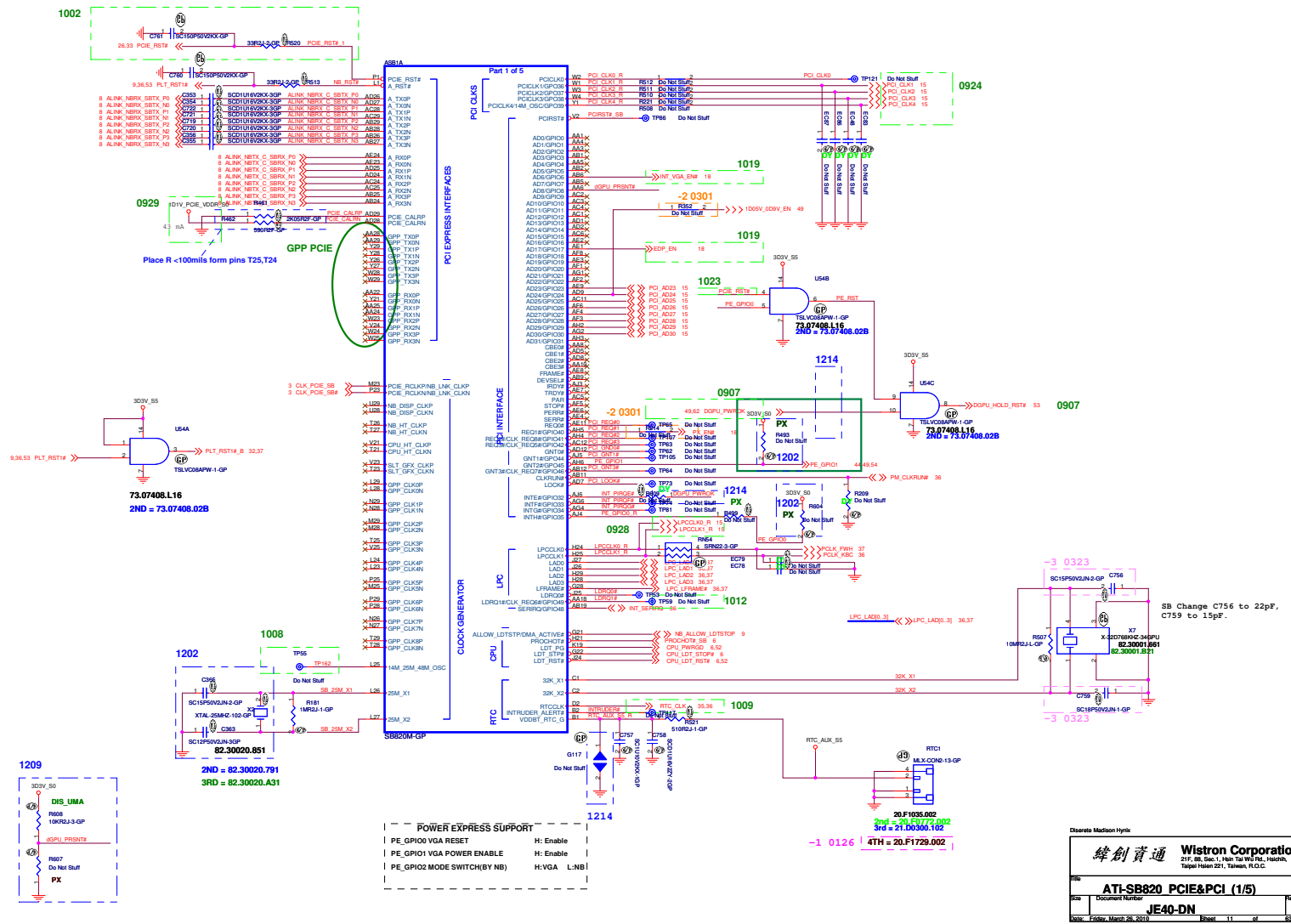
ANB1F

GROUND

Discrete Madison Hynix

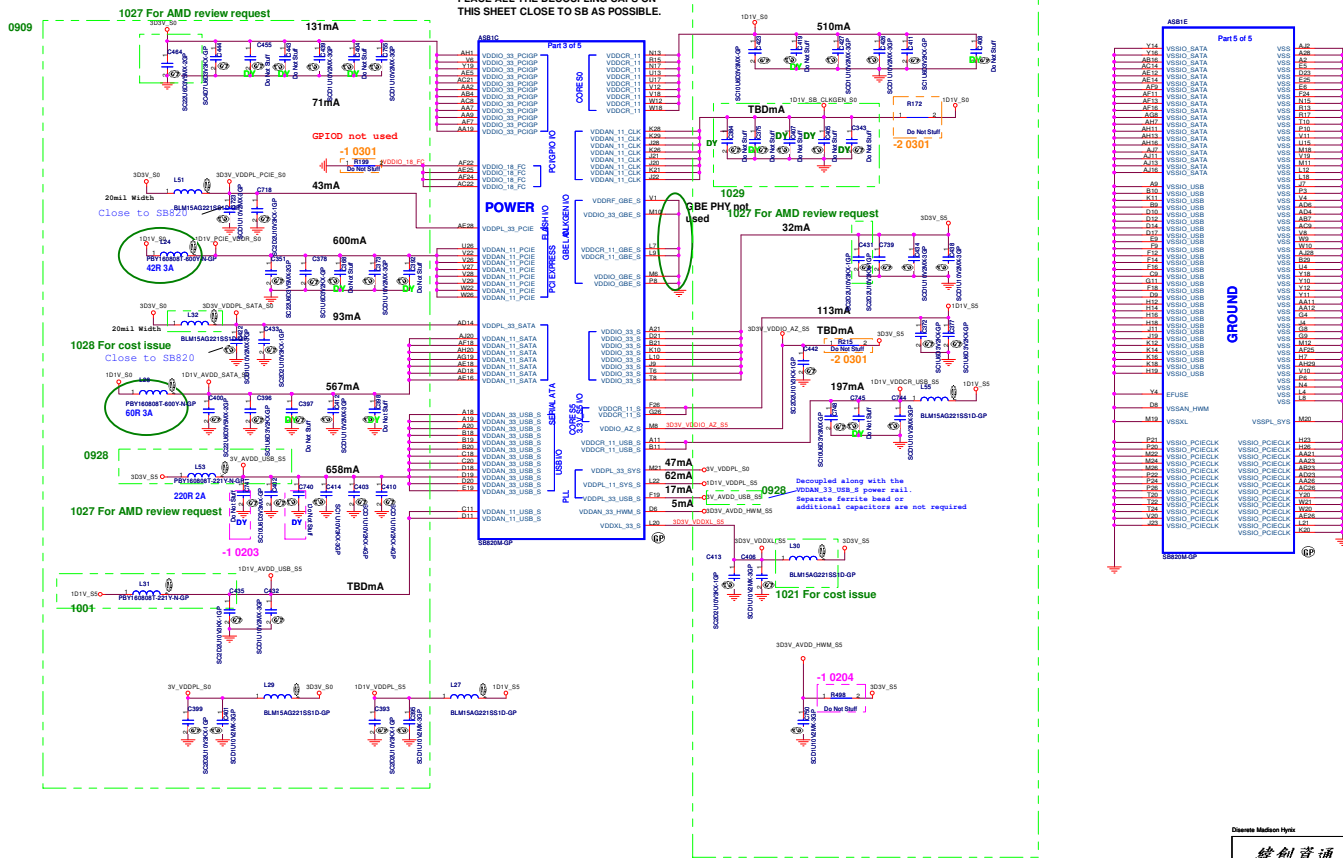
緯創資通 Wistron Corporation
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Taipai Hsien 221, Taiwan, R.O.C.

File
AT1-RS880M_Side Port&PWR&GND(3/3)
Size A3 Document Number
Date: Friday, March 26, 2010 JE40-DN Rev -3
Sheet 10 of 30

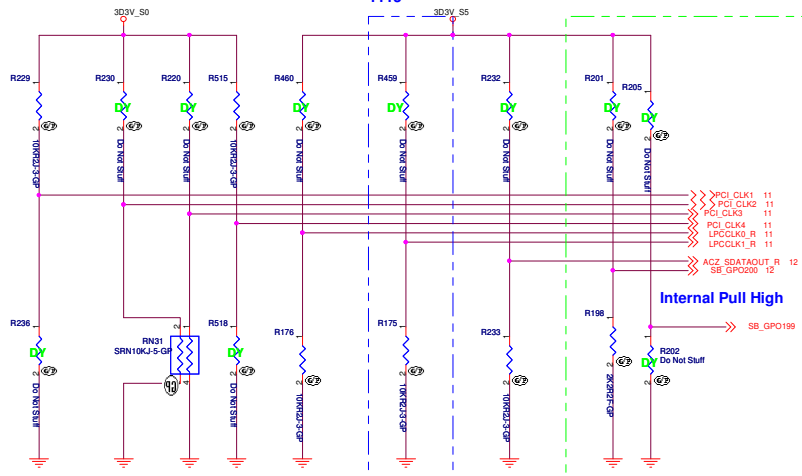


Title			
ATI-SB820 SATA-IDE (3/5)			
Size	Document Number	Rev	
A3	JE40-DN	-3	
Date:	Friday, March 26, 2010	Sheet	13 of 63









PLACE ALL THE DECOUPLING CAPS ON
THIS SHEET CLOSE TO SB AS POSSIBLE.



1115




DEBUG STRAPS

Do Not Stuff	TP67		PCI_AD23	11
Do Not Stuff	TP70		PCI_AD24	11
Do Not Stuff	TP66		PCI_AD25	11
Do Not Stuff	TP77		PCI_AD26	11
Do Not Stuff	TP80		PCI_AD27	11
Do Not Stuff	TP110		PCI_AD28	11
Do Not Stuff	TP108		PCI_AD29	11
Do Not Stuff	TP111		PCI_AD30	11

	PCI_CLK1	PCI_CLK2	PCI_CLK3	PCI_CLK4	LPC_CLK0	LPC_CLK1	AZ_SDOUT	GPIO200	GPIO199
PULL HIGH	ALLOW PCIe Gen2 DEFAULT	Watchdog Timer Enabled	USE DEBUG STRAP	non_Fusion CLOCK MODE DEFAULT	EC ENABLED DEFAULT	CLKGEN ENABLED DEFAULT	LOW POWER MODE	H,H = Reserved H,L = SPI ROM	
PULL LOW	FORCE PCIe Gen1	Watchdog Timer Disabled DEFAULT	IGNORE DEBUG STRAP DEFAULT	FUSION CLOCK MODE	EC DISABLED	CLKGEN DISABLED	PERFORMANCE MODE DEFAULT	L,H = LPC ROM (Default) L,L = FWH ROM	

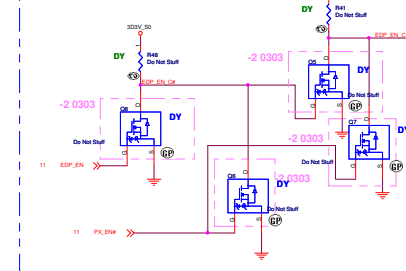
	PCI_AD27	PCI_AD26	PCI_AD25	PCI_AD24	PCI_AD23
PULL HIGH	USE PCI PLL DEFAULT	DISABLE ILA AUTORUN DEFAULT	USE FC PLL DEFAULT	USE DEFAULT PCIE STRAPS DEFAULT	DISABLE PCI MEM BOOT DEFAULT
PULL LOW	BYPASS PCI PLL	ENABLE ILA AUTORUN	BYPASS FC PLL	USE EEPROM PCIE STRAPS	ENABLE PCI MEM BOOT

Discrete Madison Hynix

 緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsinshih, Taipei Hsien 221, Taiwan, R.O.C.			
Title			
ATI-SB820 STRAPPING (5/5)			
Size	Document Number		Rev
A3	JE40-DN		-3
Date:	Friday, March 28, 2010	Sheet	15 of 63



 緯創資通		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
File			
		DDRIII SO-DIMM SKT 2	
Size	Document Number		Rev
Custom	JE40-DN		-3
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Truth Table

Function	SEL
A _N to B ₁	L
A _N to B ₂	H

CRT

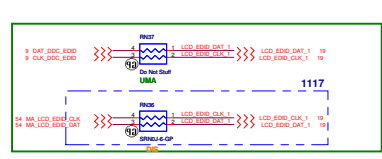
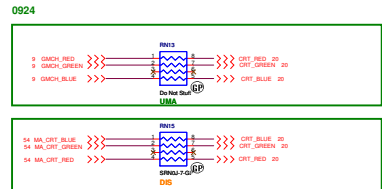
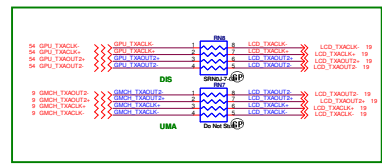
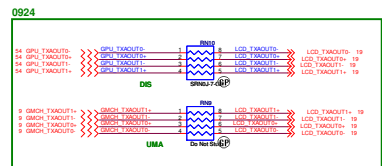
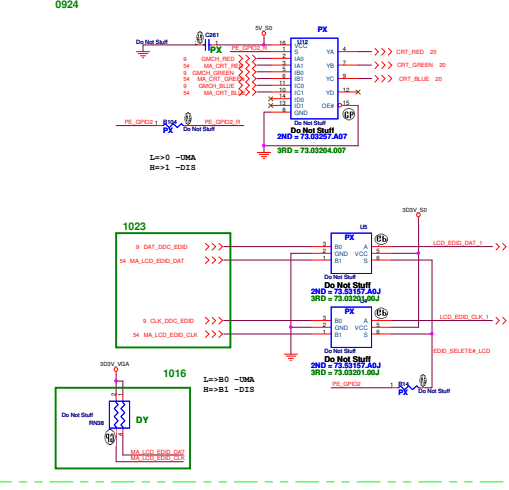
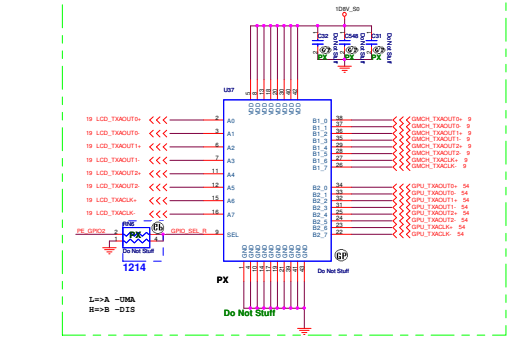
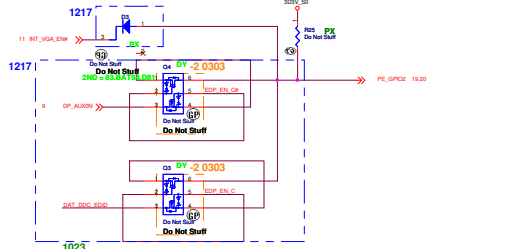
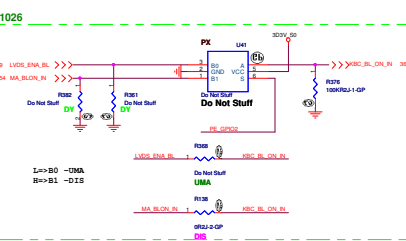
E	S	YA	YB	YC	YD	Function
H	X	Hb-Z	Hb-Z	Hb-Z	Hb-Z	Disable
L	L	IA0	IB0	IC0	ID0	S=0
L	H	IA1	IB1	IC1	ID1	S=1

Function Table

Input (B)	Function
L	B ₁ Connected to A
H	B ₂ Connected to A

Function Table

Input (B)	Function
L	B ₁ Connected to A
H	B ₂ Connected to A



[illegible]

LCD/INVERTER/CCD CONN

1028 for EMC request

9923

1005

Change PX

0922

Inverter Pin

Pin	Symbol
1	Vin
2	Vin
3	Brightness
4	BLON
5	GND
6	GND

CCD Pin

Pin	Symbol
1	CCD_PWR
2	USB-
3	USB+
4	GND
5	GND

1026

Reserve direct connector to KBC

-1 0125

UMA_PX

PX U04

DIS_PX

UMA

BLON_OUT 1

7405285.07F

2ND = 74.05724.09F

JV42_DN_SA

JV42_DN_PX_SA

UMA

DIS

PX U6

U2

Layout 40 mil

Wistron Corporation

21F, 8B, Sec. 1, Hsin Tai Wu Rd., Hsiehshih, Taipei Hsien 221, Taiwan, R.O.C.

Date: Friday, March 26, 2010 Sheet 19 of 63

LCD/INVERTER/CCD CONN

1028 for EMC request

9923

1005

Change PX

0922

Inverter Pin

Pin	Symbol
1	Vin
2	Vin
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CCD Pin

Pin	Symbol
1	CCD_PWR
2	USB-
3	USB+
4	GND
5	GND

1026

Reserve direct connector to KBC

-1 0125

UMA_PX

PX U04

DIS_PX

UMA

BLON_OUT 1

7405285.07F

2ND = 74.05724.09F

JV42_DN_SA

JV42_DN_PX_SA

Layout 40 mil

Wistron Corporation

21F, 8B, Sec. 1, Hsin Tai Wu Rd., Hsiehshih, Taipei Hsien 221, Taiwan, R.O.C.

Date: Friday, March 26, 2010 Sheet 19 of 63

[illegible][illegible]

LCD/INVERTER/CCD CONN

1028 for EMC request

9923

1005

Change PX

0922

Inverter Pin

Pin	Symbol
1	Vin
2	Vin
3	Brightness
4	BLON
5	GND
6	GND

CCD Pin

Pin	Symbol
1	CCD_PWR
2	USB-
3	USB+
4	GND
5	GND

1026

Reserve direct connector to KBC

-1 0125

UMA_PX

PX U04

DIS_PX

UMA

BLON_OUT 1

7405285.07F

2ND = 74.05724.09F

JV42_DN_SA

JV42_DN_PX_SA

UMA

DIS

PX U6

U2

Layout 40 mil

Wistron Corporation

21F, 8B, Sec. 1, Hsin Tai Wu Rd., Hsiehshih, Taipei Hsien 221, Taiwan, R.O.C.

Date: Friday, March 26, 2010 Sheet 19 of 63

LCD/INVERTER/CCD CONN

1028 for EMC request

9923

1005

Change PX

0922

Inverter Pin

Pin	Symbol
1	Vin
2	Vin
3	Brightness
4	BLON
5	GND
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CCD Pin

Pin	Symbol
1	CCD_PWR
2	USB-
3	USB+
4	GND
5	GND

1026

Reserve direct connector to KBC

-1 0125

UMA_PX

PX U04

DIS_PX

UMA

BLON_OUT 1

7405285.07F

2ND = 74.05724.09F

JV42_DN_SA

JV42_DN_PX_SA

UMA

DIS

PX U6

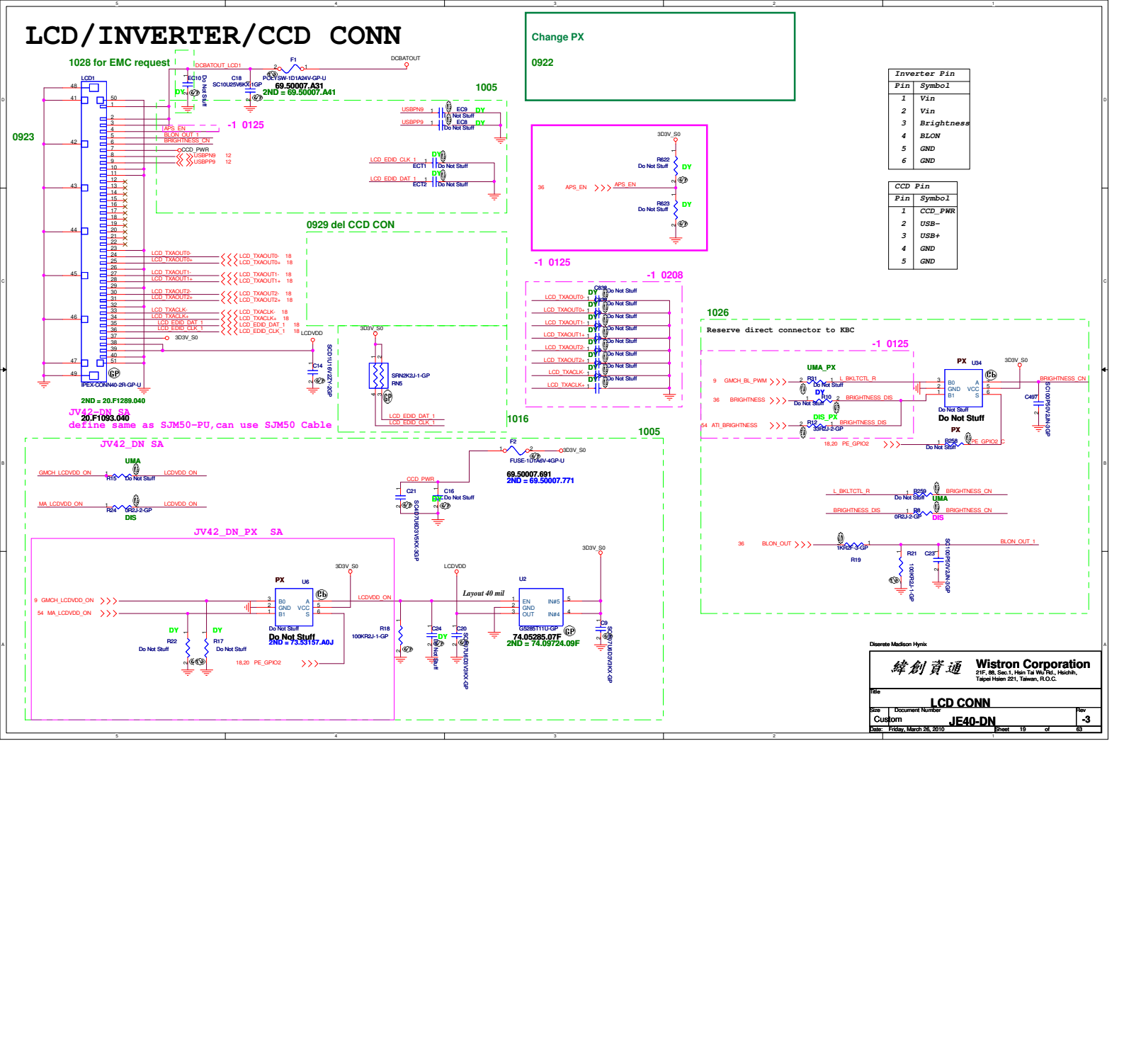
U2

Layout 40 mil

Wistron Corporation

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Date: Friday, March 26, 2010 Sheet 19 of 63

[illegible]

LCD/INVERTER/CCD CONN

1028 for EMC request

9923

1005

Change PX

0922

Inverter Pin

Pin	Symbol
1	Vin
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3	Brightness
4	BLON
5	GND
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CCD Pin

Pin	Symbol
1	CCD_PWR
2	USB-
3	USB+
4	GND
5	GND

1026

Reserve direct connector to KBC

-1 0125

UMA_PX

PX U04

DIS_PX

UMA

BLON_OUT 1

7405285.07F

2ND = 74.05724.09F

JV42_DN_SA

JV42_DN_PX_SA

Layout 40 mil

Wistron Corporation

21F, 8B, Sec. 1, Hsin Tai Wu Rd., Hsiehshih, Taipei Hsien 221, Taiwan, R.O.C.

Date: Friday, March 26, 2010 Sheet 19 of 63

LCD/INVERTER/CCD CONN

1028 for EMC request

9923

1005

Change PX

0922

Inverter Pin

Pin	Symbol
1	Vin
2	Vin
3	Brightness
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5	GND
6	GND

CCD Pin

Pin	Symbol
1	CCD_PWR
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3	USB+
4	GND
5	GND

1026

Reserve direct connector to KBC

-1 0125

UMA_PX

PX U04

DIS_PX

UMA

BLON_OUT 1

7405285.07F

2ND = 74.05724.09F

JV42_DN_SA

JV42_DN_PX_SA

UMA

DIS

PX U6

U2

Layout 40 mil

Wistron Corporation

21F, 8B, Sec. 1, Hsin Tai Wu Rd., Hsiehshih, Taipei Hsien 221, Taiwan, R.O.C.

Date: Friday, March 26, 2010 Sheet 19 of 63

LCD/INVERTER/CCD CONN

1028 for EMC request

9923

1005

Change PX

0922

Inverter Pin

Pin	Symbol
1	Vin
2	Vin
3	Brightness
4	BLON
5	GND
6	GND

CCD Pin

Pin	Symbol
1	CCD_PWR
2	USB-
3	USB+
4	GND
5	GND

1026

Reserve direct connector to KBC

-1 0125

UMA_PX

PX U04

DIS_PX

UMA

BLON_OUT 1

7405285.07F

2ND = 74.05724.09F

JV42_DN_SA

JV42_DN_PX_SA

UMA

DIS

PX U6

U2

Layout 40 mil

Wistron Corporation

21F, 8B, Sec. 1, Hsin Tai Wu Rd., Hsiehshih, Taipei Hsien 221, Taiwan, R.O.C.

Date: Friday, March 26, 2010 Sheet 19 of 63

LCD/INVERTER/CCD CONN

1028 for EMC request

9923

1005

Change PX

0922

Inverter Pin

Pin	Symbol
1	Vin
2	Vin
3	Brightness
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CCD Pin

Pin	Symbol
1	CCD_PWR
2	USB-
3	USB+
4	GND
5	GND

1026

Reserve direct connector to KBC

-1 0125

UMA_PX

PX U04

DIS_PX

UMA

BLON_OUT 1

7405285.07F

2ND = 74.05724.09F

JV42_DN_SA

JV42_DN_PX_SA

UMA

DIS

PX U6

U2

Layout 40 mil

Wistron Corporation

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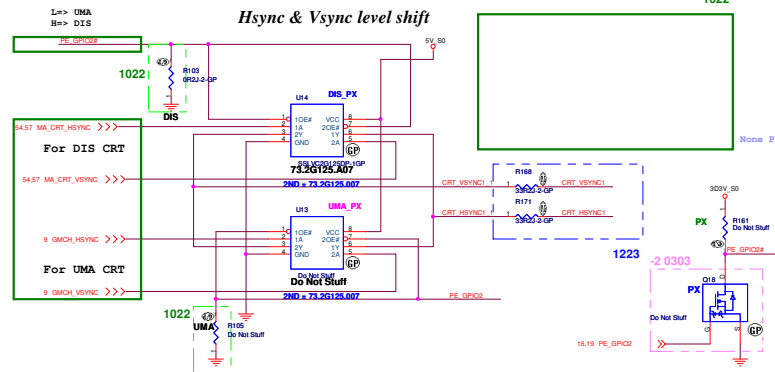
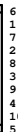
Date: Friday, March 26, 2010 Sheet 19 of 63



** Must be a ground return path between this ground and the ground on the VGA connector.*

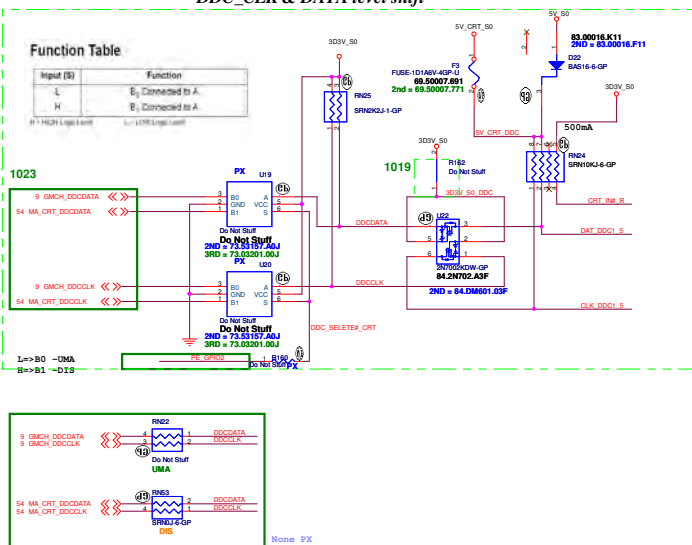
Pi-filter & 150 Ohm pull-down resistors should be as close as to CRT CONN. RGB will hit 75 Ohm first, pi-filter, then CRT CONN.

0918



0924

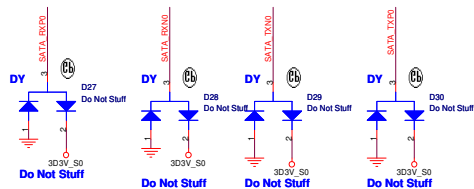
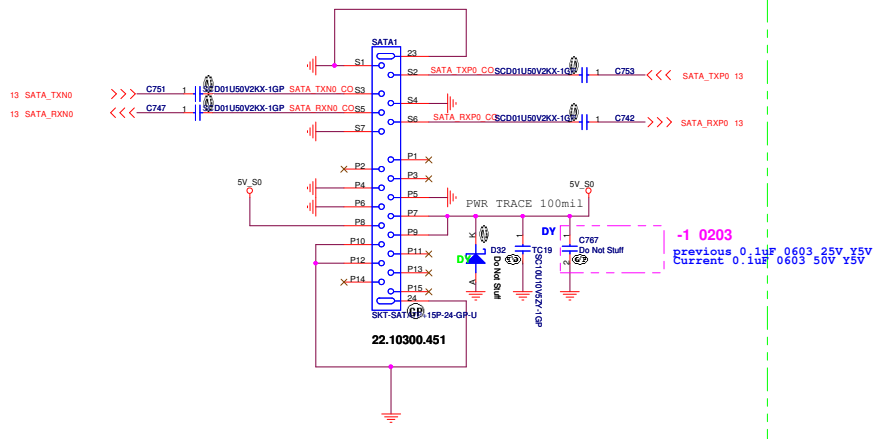
Input (B)	Function
L	E_0 Connected to A
H	E_1 Connected to A





SATA Connector

0923



Discrete Madison Hynix

緯創資通

Wistron Corporation
 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsinchu,
 Taipei Hsin 221, Taiwan, R.O.C.

File

HDD

Size

Document Number

JE40-DN

Rev

-3

Date: Friday, March 26, 2010

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1009

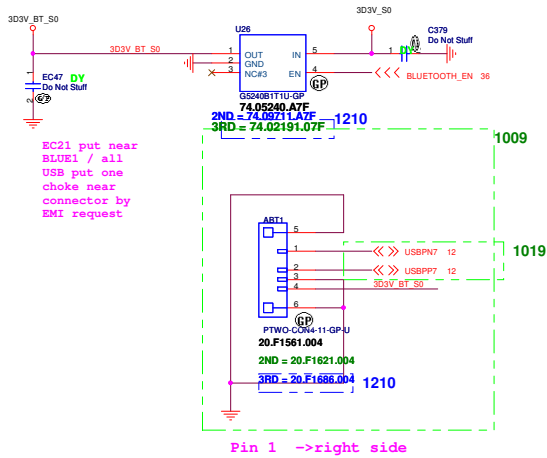


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3

BLUETOOTH MODULE

1.5A / High Active Voltage 2V

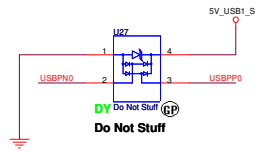


Discrete Madison Hynix

緯創資通

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Taipei Hsien 221, Taiwan, R.O.C.

Title		
BLUETOOTH		
Size	Document Number	Rev
	JE40-DN	-3
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The schematic diagram illustrates the USB connector for the 20.K0276.024 module. It shows the connection of a USB Type-C plug to a 28-pin USB connector. Key components and connections include:

- Power and Ground:**
 - VDD:** Connected to pin 1 (VDD) and pin 25 (VDD).
 - GND:** Connected to pin 4 (GND), pin 6 (GND), pin 8 (GND), pin 9 (GND), pin 10 (GND), pin 11 (GND), pin 12 (GND), pin 13 (GND), pin 14 (GND), pin 15 (GND), pin 16 (GND), pin 17 (GND), pin 18 (GND), pin 19 (GND), pin 20 (GND), pin 21 (GND), pin 22 (GND), pin 23 (GND), pin 24 (GND), pin 25 (GND), pin 26 (GND), pin 27 (GND), and pin 28 (GND).
- Data and Control:**
 - D+** (Red): Connected to pin 19 (D+).
 - D-** (Red): Connected to pin 20 (D-).
 - NCS** (Green): Connected to pin 21 (NCS).
 - NCS#** (Green): Connected to pin 22 (NCS#).
- Other Pins:**
 - Pin 1:** VDD
 - Pin 2:** GND
 - Pin 3:** GND
 - Pin 4:** GND
 - Pin 5:** GND
 - Pin 6:** GND
 - Pin 7:** GND
 - Pin 8:** GND
 - Pin 9:** GND
 - Pin 10:** GND
 - Pin 11:** GND
 - Pin 12:** GND
 - Pin 13:** GND
 - Pin 14:** GND
 - Pin 15:** GND
 - Pin 16:** GND
 - Pin 17:** GND
 - Pin 18:** GND
 - Pin 19:** D+
 - Pin 20:** D-
 - Pin 21:** NCS
 - Pin 22:** NCS#
 - Pin 23:** GND
 - Pin 24:** GND
 - Pin 25:** VDD
 - Pin 26:** GND
 - Pin 27:** GND
 - Pin 28:** GND

Annotations and labels in the diagram include:

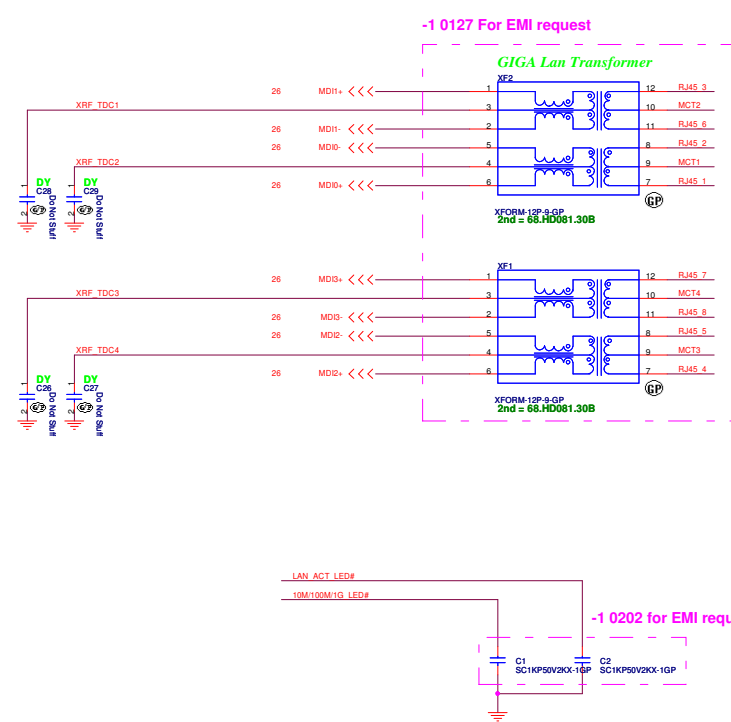
- 2 0301** (Blue)
- 1 0129** (Red)
- 1123 -SB** (Blue)
- AUD_AGND_2** (Blue)
- AUD_AGND_1** (Blue)
- 5V_S5** (Blue)
- 20.K0276.024** (Blue)
- 1 0125** (Red)

Discrete Madison Hynix			
緯創資通		Wistron Corporation 21F, 6B, Sec.1, Hsien Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
USB			
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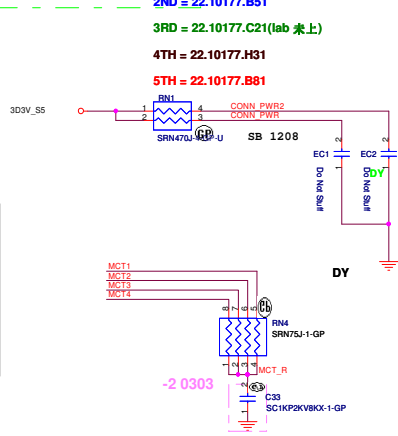
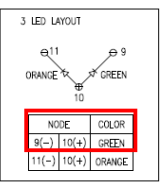
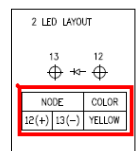
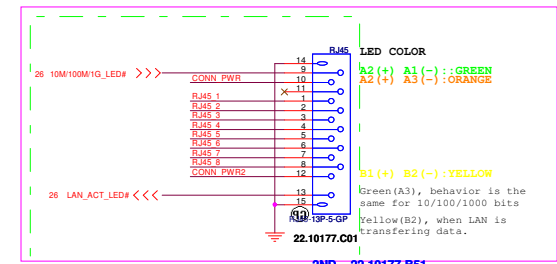
- 1.route on bottom as differential pairs.
- 2.Tx+/Tx- are pairs. Rx+/Rx- are pairs.
- 3.No vias, No 90 degree bends.
- 4.pairs must be equal lengths.
- 5.6mil trace width, 12mil separation.
- 6.36mil between pairs and any other trace.
- 7.Must not cross ground moat,except RJ-45 moat.

LAN Connector

LAN Connector



0923



Discrete Madison Hynix

緯創資通 Wistron Corporation

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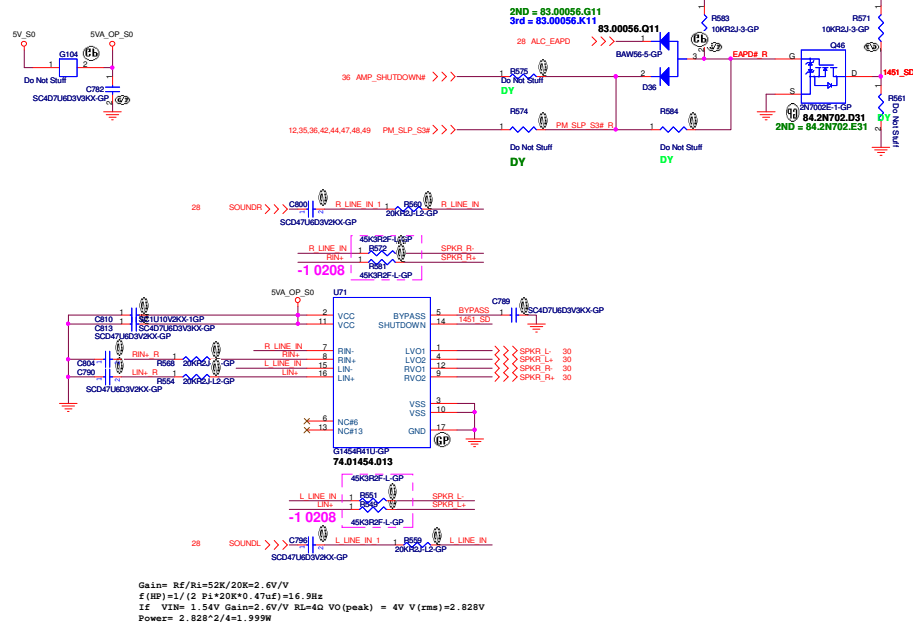
File

LAN CONN

Size A3 Document Number JE40-DN Rev -3

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Discrete Madison Hynix

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Wistron Corporation
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Taipei Hsien 221, Taiwan, R.O.C.

File

AUDIO AMP

JE40-DN

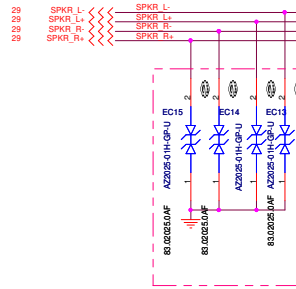
Date: Friday, March 26, 2010

Sheet 29 of 63

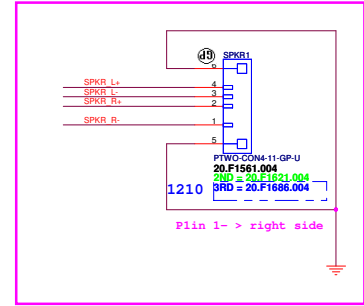
Internal Speaker

0914

0910



-1 0125 for EMI request



Pin 1 -> right side

JV42-DN SA

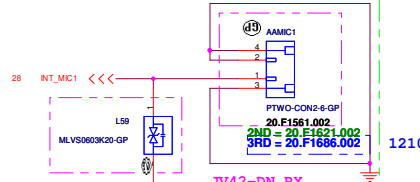
0910

LINE OUT

0911

1016

Internal Mic



-1 0209 For MIC issue

JV42-DN PX

Pin 1 ->right side

20.F1240.002 Pin1->left side

same as JV70-CP

Discrete Madison Hynd

緯創資通

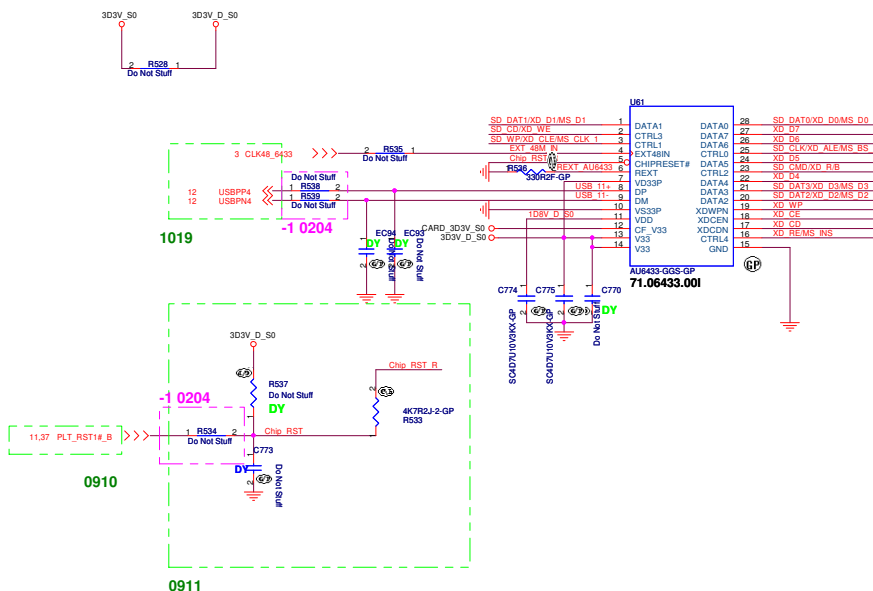
Wistron Corporation
21F, 88, Sec 1, Hsin Tai Wu Rd., Hsinchu,
Taipei Hsien 321, Taiwan, R.O.C.

File		
Size	Document Number	Rev
AUDIO JACK		-3
JE40-DN		
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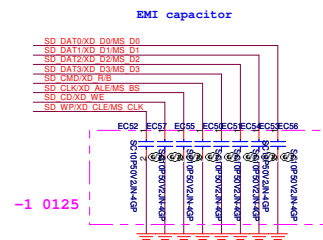
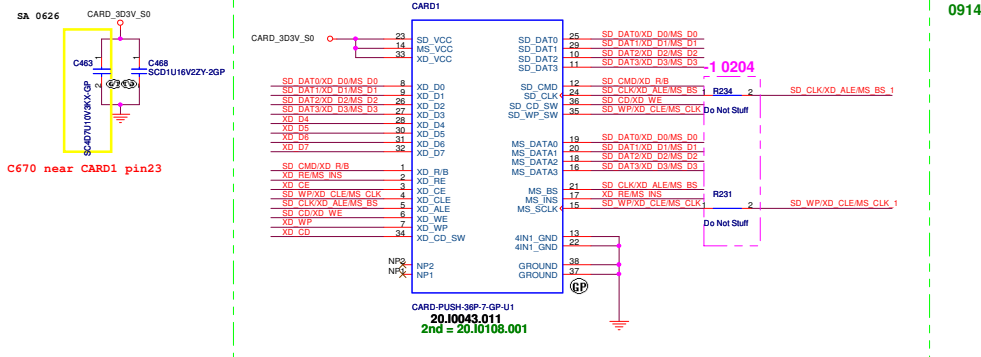
No Modem Function

Discrete Madison Hynix


緯創資通		Wistron Corporation	
		21F., 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title		MDC	
Size	Document Number		Rev
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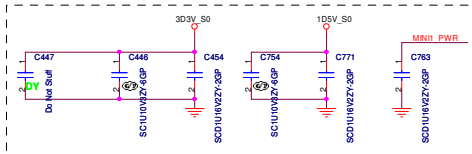
5 IN1 CARD-READER (SD/MMC/MS/MS PRO/XD)



Discrete Madison Hynix

 緯創資通		Wistron Corporation 21F, 88, Sec. 1, Hsin Tai Wu Rd., Hsinchu, Taipei Hsien 321, Taiwan, R.O.C.	
Title			
CARDREADER			
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0923

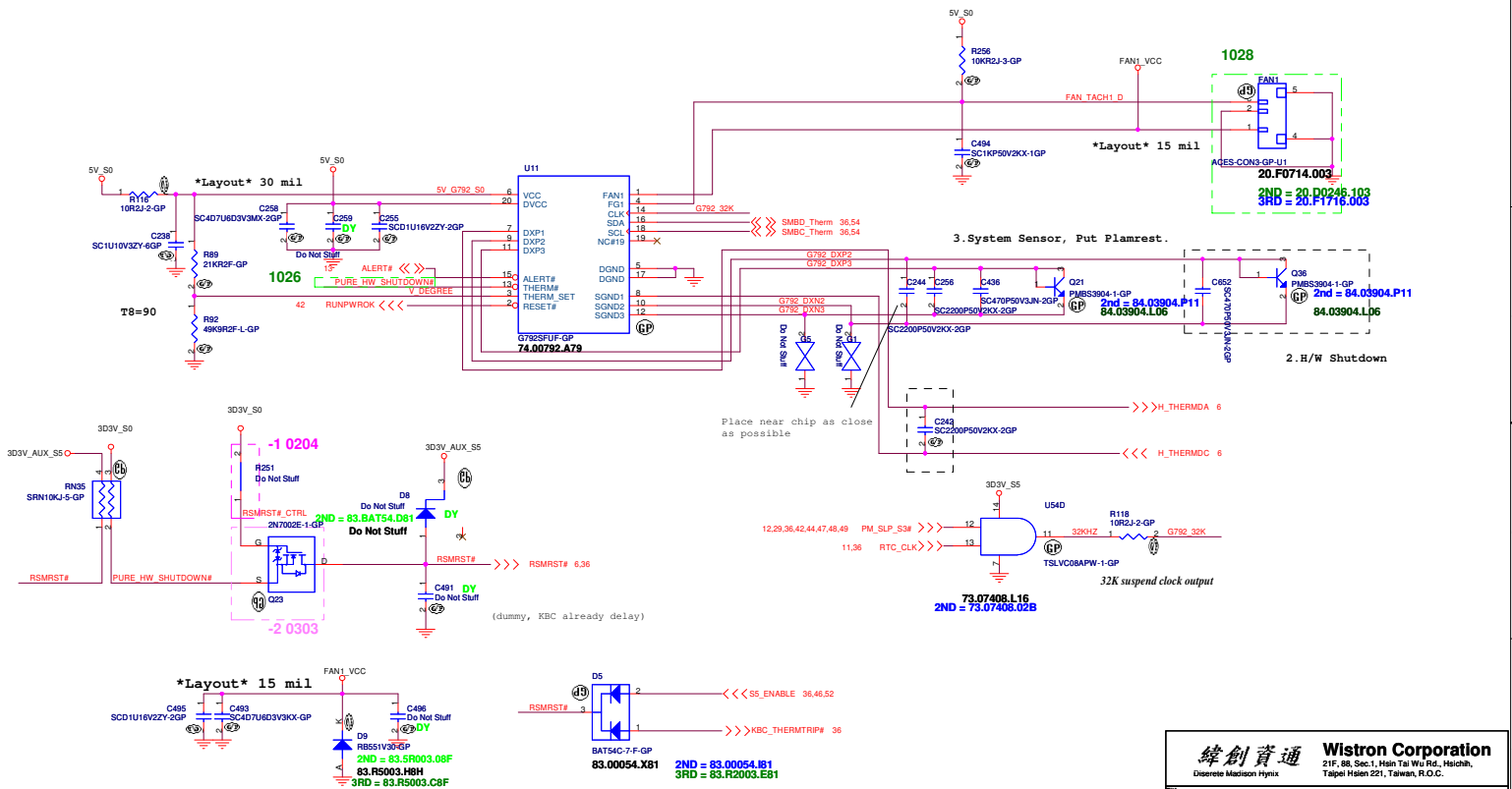


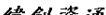
Title			
MINI CARD			
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No NEWCARD Function

Discrete Madison Hynix

緯創資通		Wistron Corporation	
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File			
NEW CARD			
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 緯創資通 Discrete Madison Hynix		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
		G792	
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for ENE FAE suggest, SPICS# is push-pull pin,
don't need to pull high

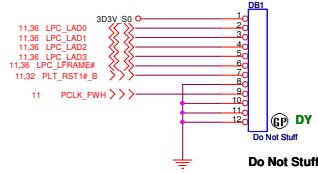
base on FAE Kevin discuss with KBC

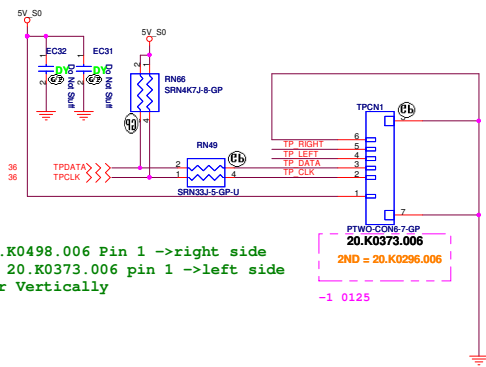
3rd 4th source LAB ENG未上

16M Bits
SPI FLASH ROM

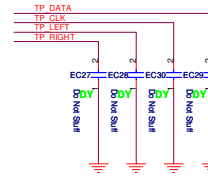
GOLDEN FINGER FOR DEBUG BOARD

11.36 LPC_LAD[0..3] <<> LPC_LAD[0..3]

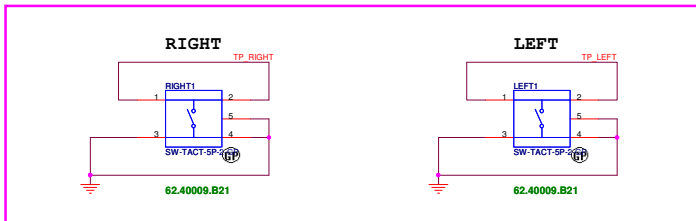




Eng stuff 20.K0498.006 Pin 1 -->right side
 PD change to 20.K0373.006 pin 1 -->left side
 so net mirror Vertically



-1 0205



No Finger printer Function

Discrete Madison Hynix

緯創資通

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Title

Touch PAD

Size
 A3

Document Number

JE40-DN

Rev
 -3

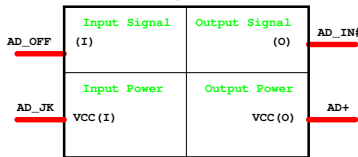
Date: Friday, March 28, 2019

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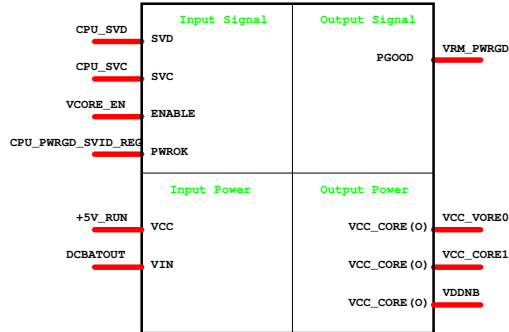
NONE BOARD

緯創資通		Wistron Corporation	
		21F, 88, Sec. 1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
NONE BOARD			
Size	Document Number		Rev
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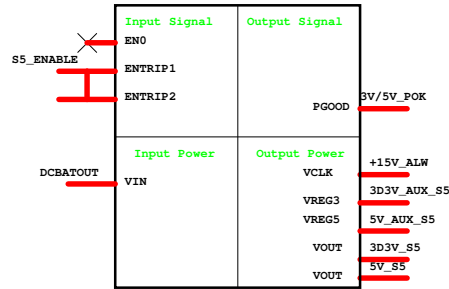
Adapter



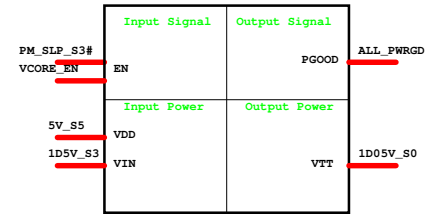
CPU_CORE ISL6265HRTZ



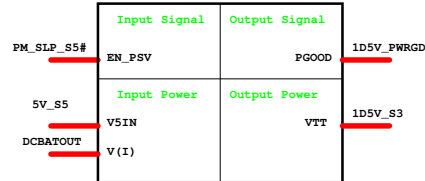
DCDC 5V/3D3V(RT8223)



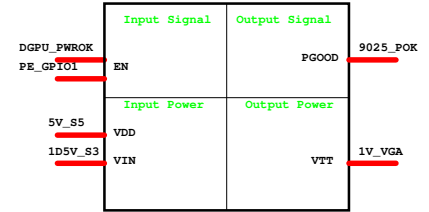
DCDC 1D05V_S0(RT9025)



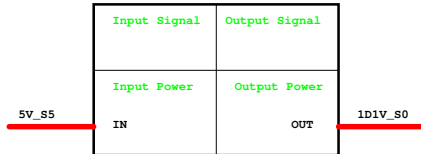
DCDC 1D5V(RT8209E)



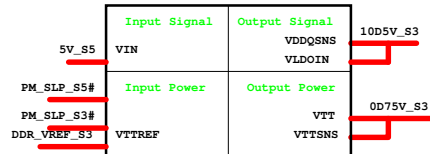
DCDC 1V_VGA(RT9025)



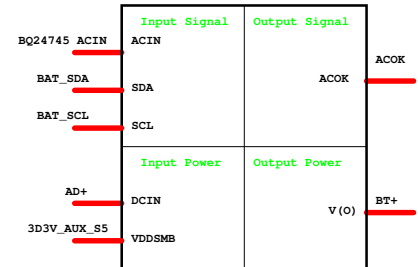
1D1V LDO RT8209E



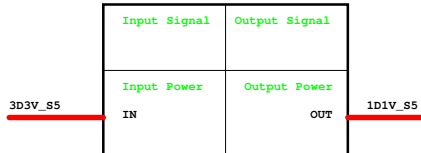
0D75V LDO RT9026



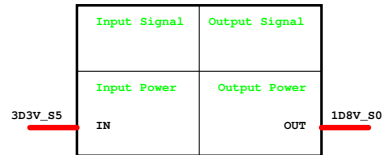
CHARGER BQ24745



1D1V LDO RT9025



1D8V LDO RT8015A

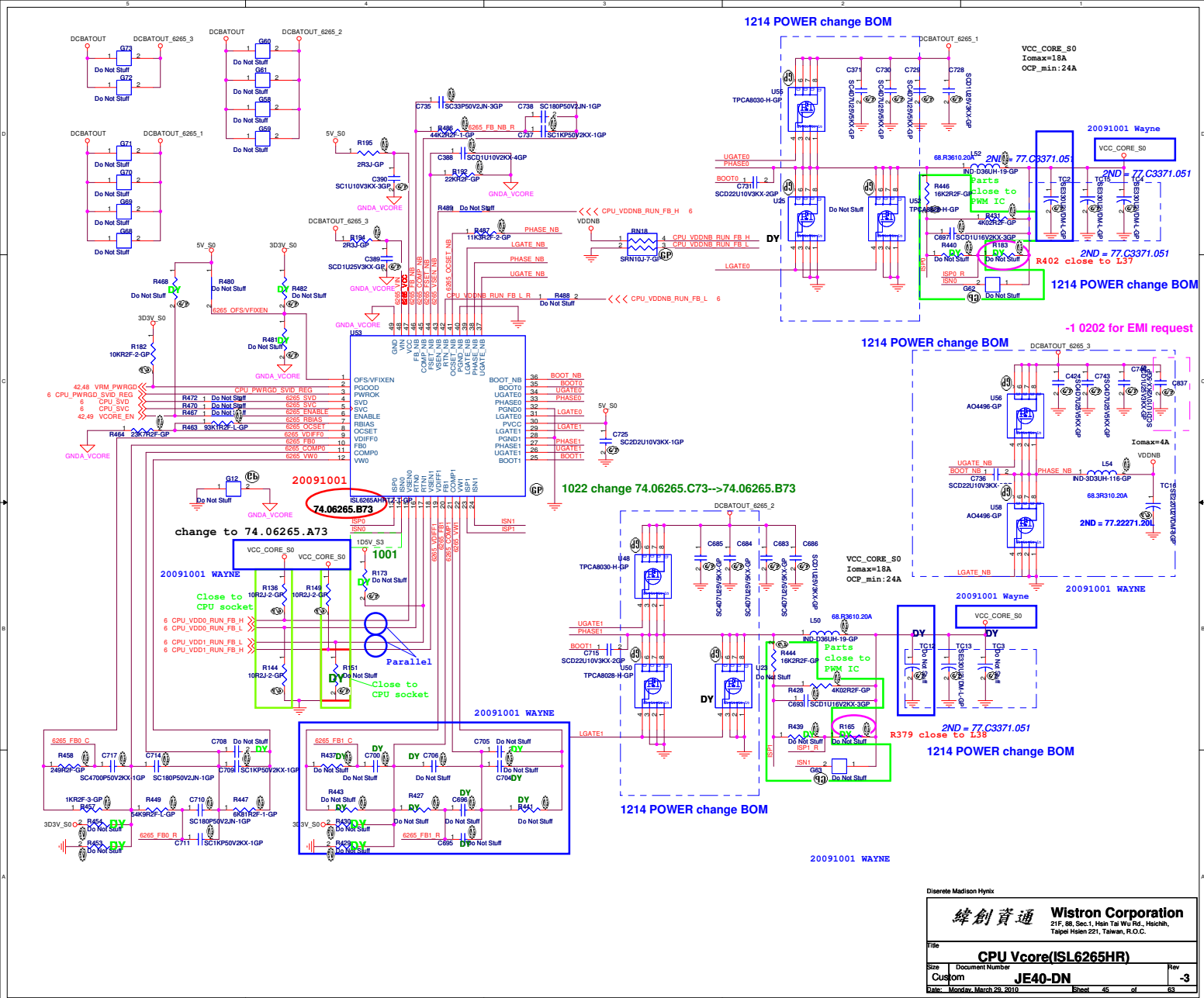


Discrete Madison Hynix

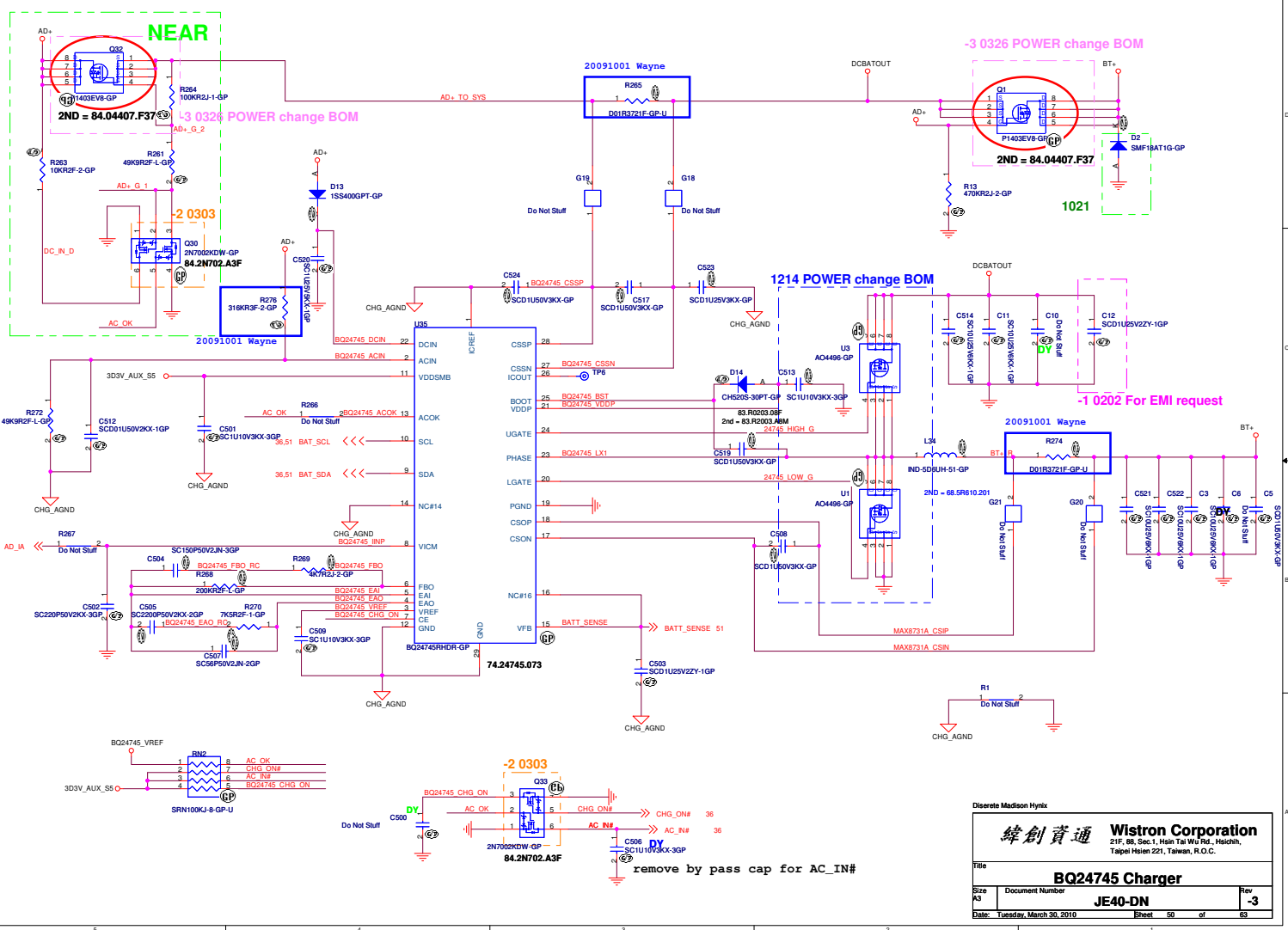
緯創資通

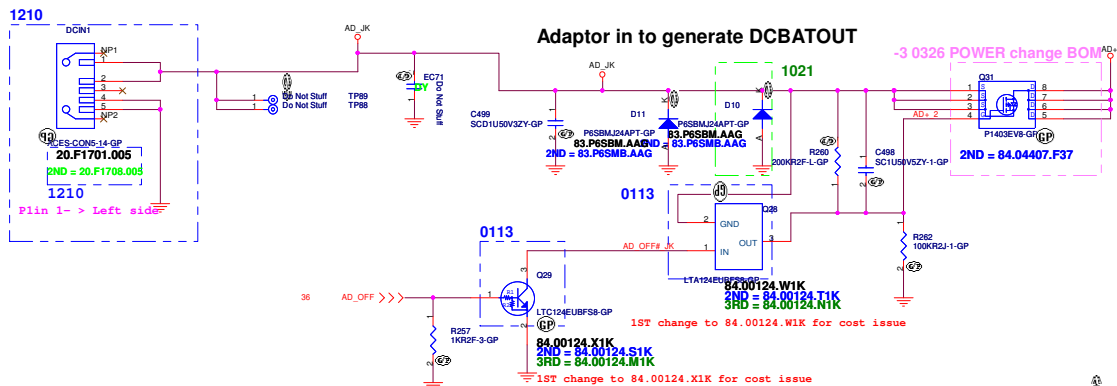
Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsinchu,
Taippei Hsien 221, Taiwan, R.O.C.

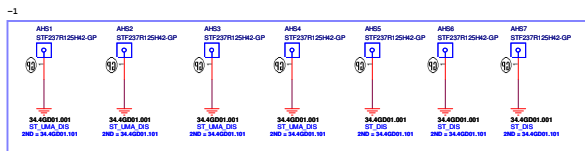
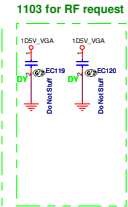
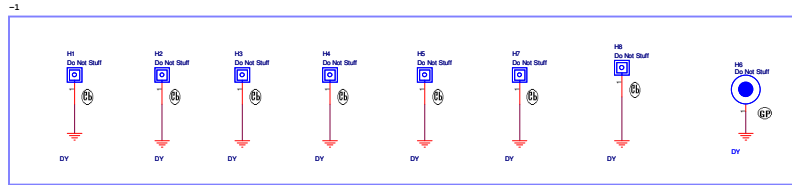
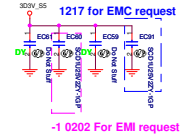
Power Block Diagram		
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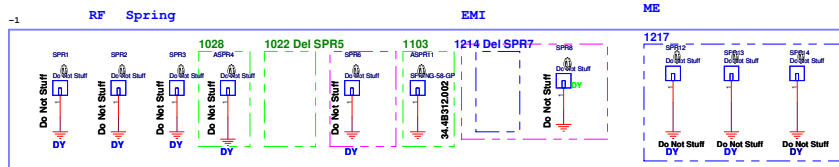


SA SB -1

Check test point



Test Point放在Dimm Door打開可量測處



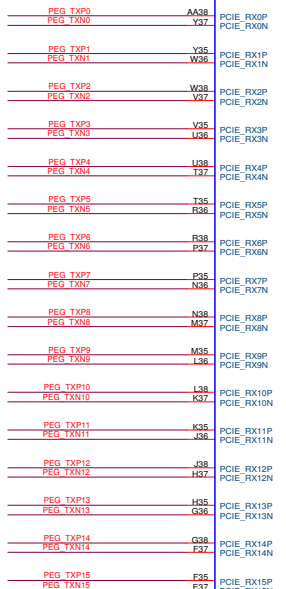
Discrete Madson Hyrax

緯創資通

Wistron Corporation
21F, 88, Sec. 1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

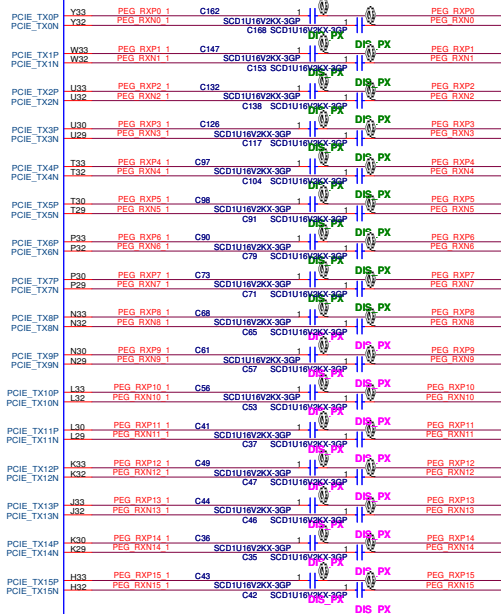
File				EMI/Spring/Boss			
Size	Document Number						Rev
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8 PEG_TXP[15:0] << PEG_TXP[15:0]
8 PEG_TXN[15:0] << PEG_TXN[15:0]

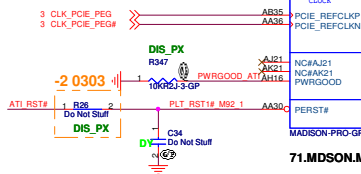


PCI EXPRESS INTERFACE

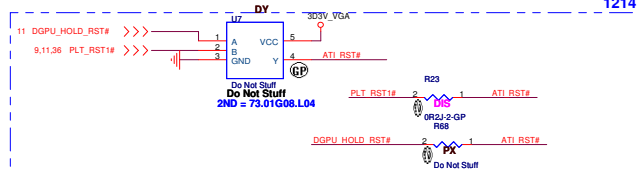
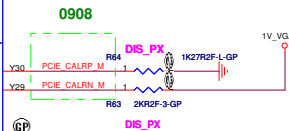
1 OF 8



8 PEG_RXP[15:0] << PEG_RXP[15:0]
8 PEG_RXN[15:0] << PEG_RXN[15:0]



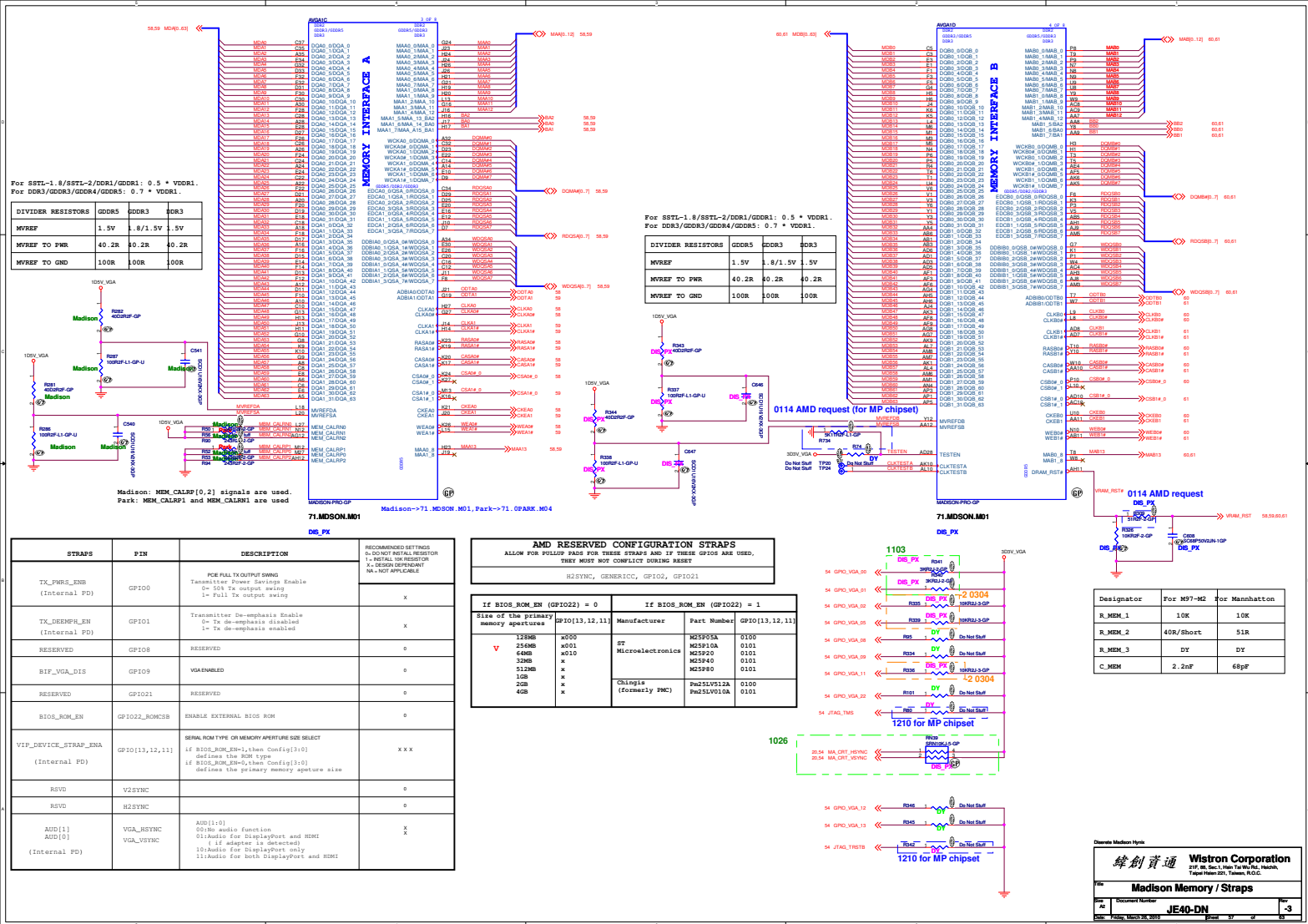
CALIBRATION
PCIE_CALRP
PCIE_CALRN



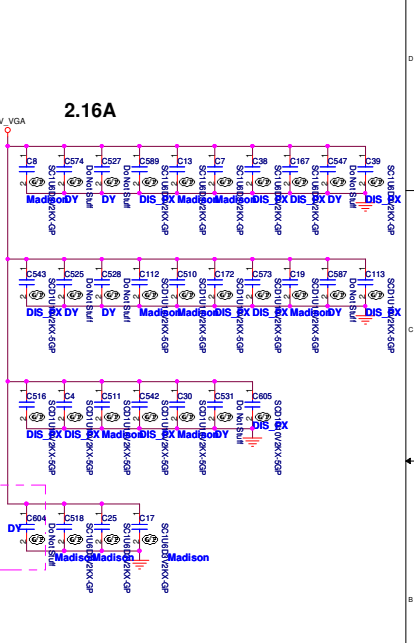
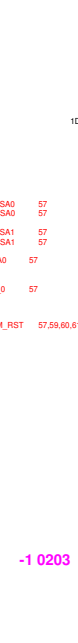
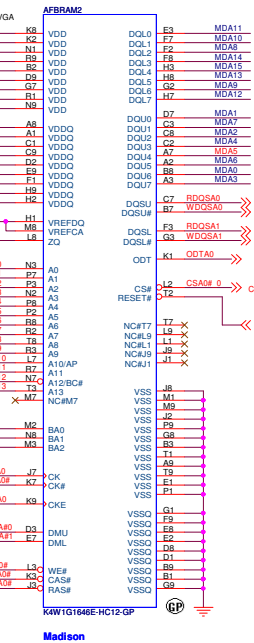
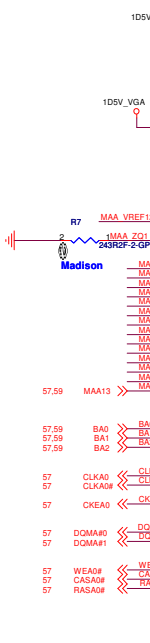
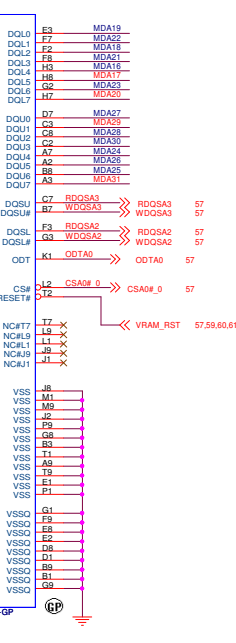
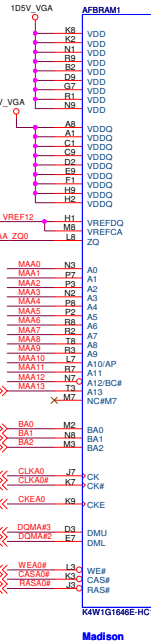
Discrete Madison Hyntx

緯創資通 Wistron Corporation
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Taipei Hsien 221, Taiwan, R.O.C.

Title		Madison PCIE	
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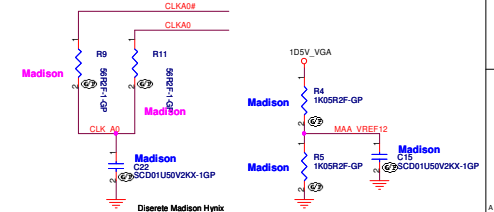


DDR3



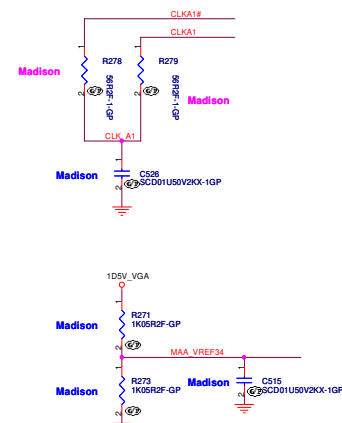
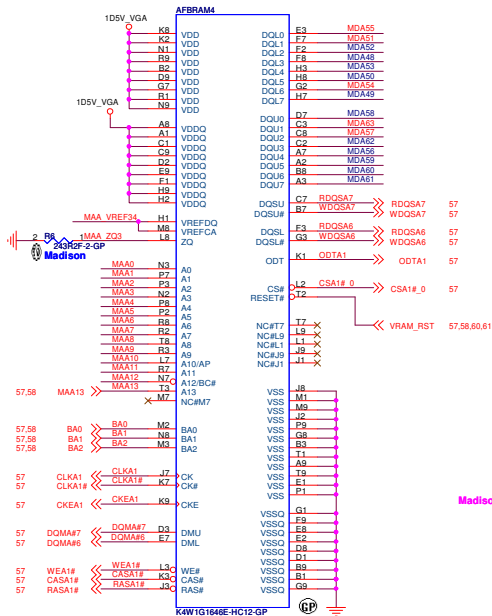
SAMSUNG: 72.41164.H0U
HYNIX: 72.51G63.C0U

57.59 DQMA#(0..7) <<>
57.59 RDQSA(0..7) <<>
57.59 WDQSA(0..7) <<>
57.59 MAA(0..12) <<>
57.59 MDA(0..63) <<>



File		Wistron Corporation	
Size		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsinchi, Taipei Hsien 221, Taiwan, R.O.C.	
Date		Friday, March 28, 2010	
Document Number		JE40-DN	
Rev		3	

2.41164.H0U
2.51G63.C0U

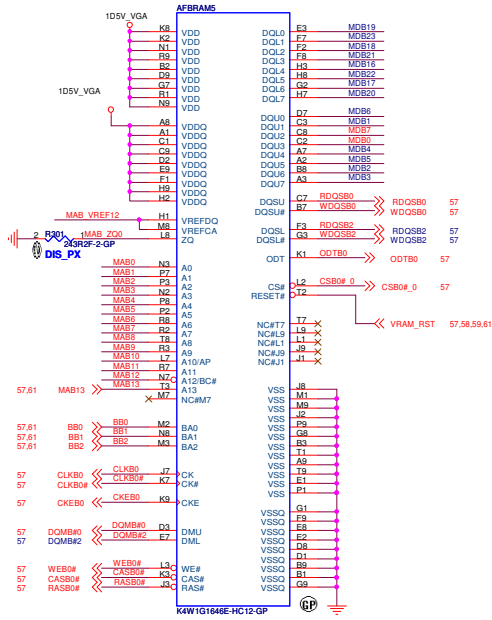


Discrete Madison Hynix

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

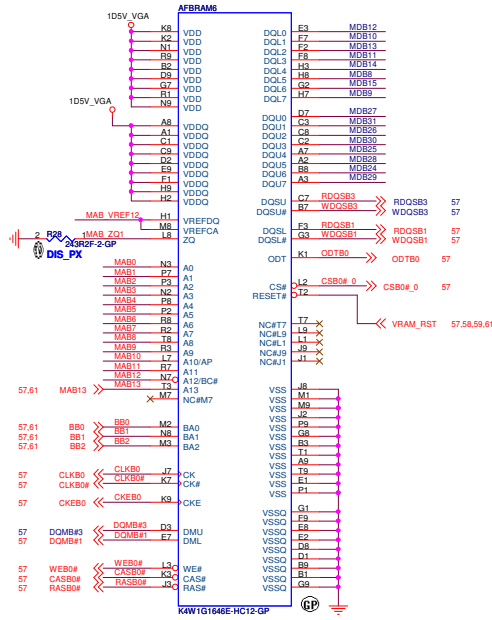
Title			
VRAM(2/4)			
Size A3	Document Number		Rev
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DDR3



DIS_PX

72.41164.H0U



DIS_PX

72.41164.H0U

SAMSUNG: 72.41164.H0U

HYNIX: 72.51G63.C0U

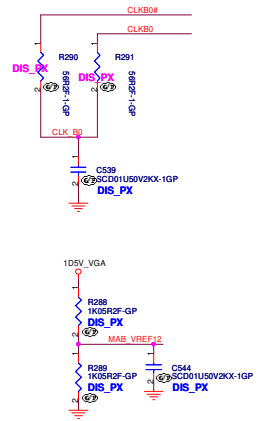
57.61 DOMB0[0..7] <<>

57.61 RDQS8[0..7] <<>

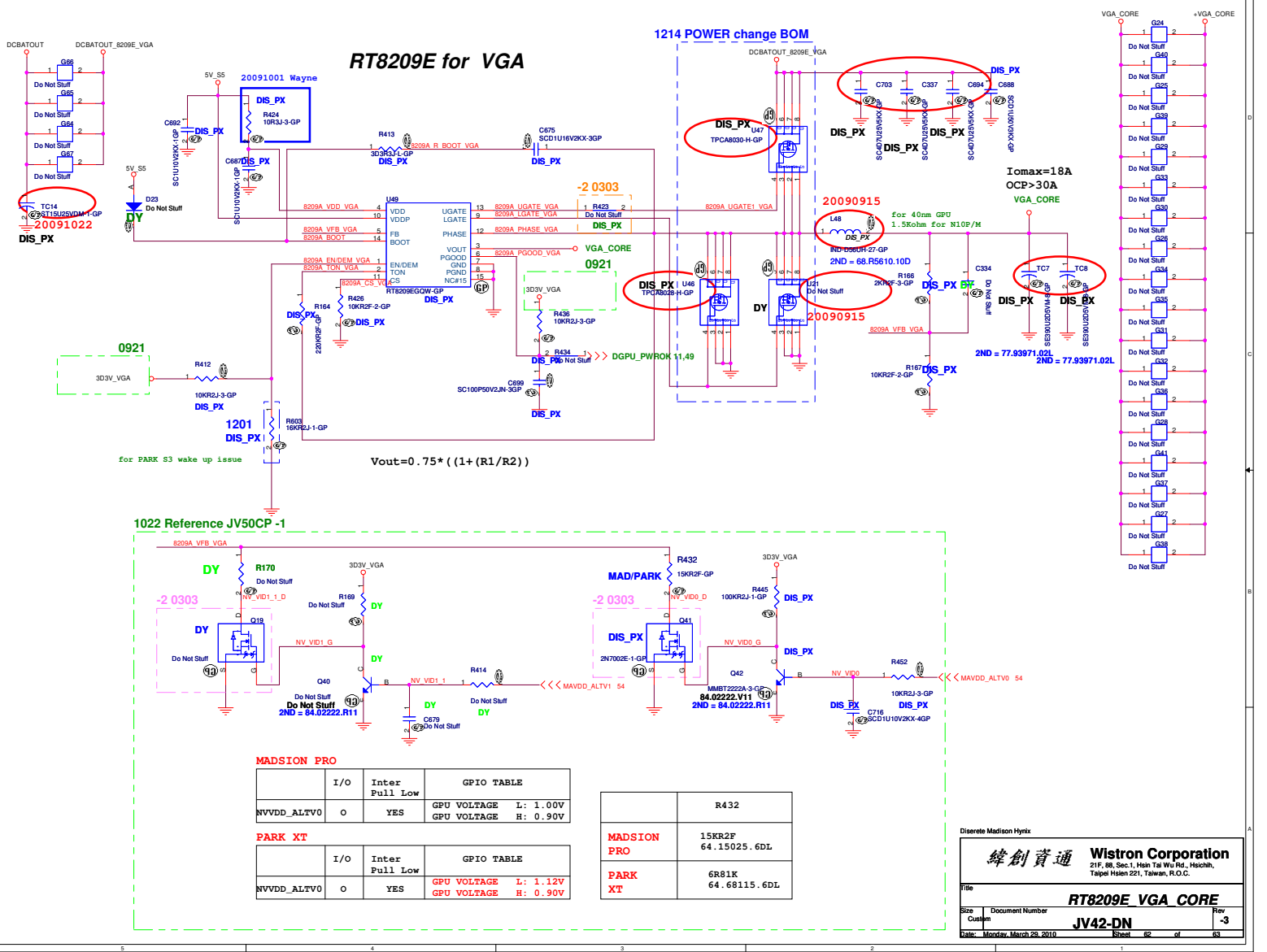
57.61 WDQS8[0..7] <<>

57.61 MAB0..12] <<>

57.61 MAB0..63] <<>



Discrete Madison Hynix	
緯創資通 Wistron Corporation	
21F, 8B, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
File	
VRAM(3/4)	
Size	Document Number
A0	JE40-DN
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Rev	-3



4	<div>NB</div> <div>RS780-->RS880M</div> <div>71.RS780.M02-->71.RS880.M02</div>		<div>BOM</div> <div>1st -> All SKU</div>		
	<div>SB</div> <div>SB820M-1-GP</div> <div>71.SB820.00U-->71.SB820.M03</div> <div>SATA_CALRP</div> <div>A11: 80歐姆 1% resistor to GND</div> <div>A12: TBD歐姆 1% resistor to GND</div> <div>SATA_CALRN</div> <div>A11:A11: 931歐姆 1% resistor to VDDAN_11_SATA</div> <div>A12: TBD歐姆 1% resistor to VDDAN_11_SATA.</div>		<div>R250</div> <div>Madison-->R250 64.32425.6DL R402H16 32K4R2F</div> <div>Park -->R250 64.32425.6DL R402H16 32K4R2F</div> <div>PX -->R250 64.32425.6DL R402H16 32K4R2F</div>		
	<div>VGA</div> <div>Madison--> PN:71.MDSON.M01</div> <div>Park--> PN:71.0PARK.M04</div>				
	<div>VRAM</div> <div>Samsung-->VRAM FBRAM1~8 PN:VR.1GB0B.006</div> <div>Hynix--> VRAM FBRAM1~8 PN:VR.1GB0G.004</div> <div>ATI--> VRAM FBRAM1~8 PN: VR.1GB0T.002</div>				
2	<div>CRT</div> <div>UMA-->L1-->2R 0603</div> <div>C62-->47U/6.3V</div> <div>DIS-->L1-->Bead(L1 68.00217.711 L1608-UH38 SBK160808T-221Y-N-GP)</div> <div>C62-->DY</div> <div>PX--> 133 歐姆 (R58 63.10334.1DL R402H16 133R2F-GP)</div> <div>R605(MAD/PARK/PX)</div> <div>Madison-->R605 64.15005.6DL R402H16150R2F-L-GP</div> <div>Park -->R605 64.15005.6DL R402H16150R2F-L-GP</div> <div>PX -->R605 64.15005.6DL R402H16150R2F-L-GP</div>				
	<div>HDMI</div> <div>UMA -->R612 R613 R618 R619 R620 R621 R624 R625 64.71505.6DL R402H16715R2F-2-GP</div>				
	<div>R432(MAD/PARK)</div> <div>Madison-->15KR2F 64.15025.6DL</div> <div>Park-->R432 64.68115.6DL R402H16 6.81KR2F</div>				

Discrete Madison Hynix

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsinshih, Taipei Hsien 221, Taiwan, R.O.C.

Title

NOTE

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