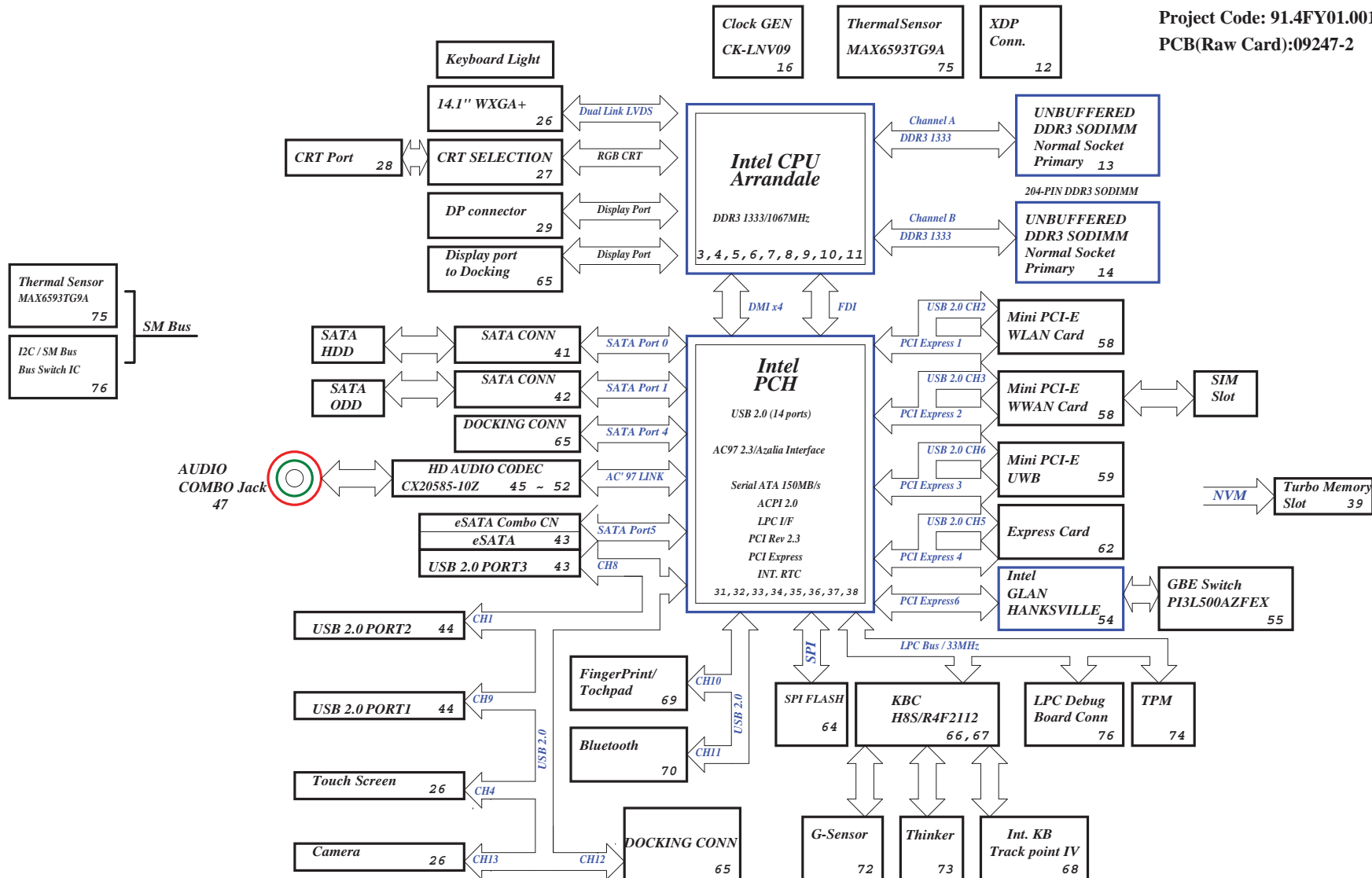


# SHINAI-2 Block Diagram

2009/12/28

Project Code: 91.4FY01.001  
PCB(Raw Card):09247-2



PCB Layer Stackup	
L1:TOP	
L2:Signal-GND	
L3:Signal	
L4:GND	
L5:Signal	
L6:GND	
L7:VCC	
L8:Signal	
L9:Signal-GND	
L10:BOTTOM	
Battery Charger/Selector BQ24741 79	
INPUTS	OUTPUTS
DCIN_PWR20_F	M-BAT-PWR
TPS51222	S-BAT-PWR
System DC/DC 83	
VINT20	VCC5M
	VCC3M
CPU DC/DC 84	
ADP3212	
VINT20	VCCCPUCORE
GFX CORE 87	
ADP3211	
VINT20	VCCGFXCORE
VCC1R5A 90	
VT356FCX	
VCC5M_OUT	VCC1R5A
VCC0R75B 91	
MAX1510	
VCC1R5A	VCC0R75B
VCC1R8B 93	
BD3551HFN	
VCC3M	VCC1R8B
VCC1R05LAN 89	
VT356FCX	
VCC5M_OUT	VCC1R05LAN
VCC1R05B_VTT 88	
VT358FCX	
VCC5M_OUT	VCC1R05B_VTT

<Variant Name>	
<b>緯創資通 Wistron Corporation</b> 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title	
Block Diagram	
Size	Document Number
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## RESISTOR

Symbol name	Value	Tolerance (J: 5%, F: 1%, D: 0.5%, B: 0.1 %)	Rating 0402=> 1/16W, 25V 0603 => 1/16W, 75V 0805 => 1/10W, 100V	Size 2=>0402, 3=>0603, 5=>0805, 6=>1206, 0=>1210
10KR3	10K Ohm	If no letter, it means J: 5%	1/16W, 75V	0603
33D3R5	33.3 Ohm	If no letter, it means J: 5%	1/10W, 100V	0805
1KR3F	1K Ohm	F: 1%	1/16W, 75V	0603

The naming rule is value + R + size + tolerance  
 For the value, it can be read by the number before R. (R means resistor)  
 For the tolerance, it can be read from the last letter.  
 For the rating, we don't show on the symbol name.  
 For the size, R2=>0402, R3=>0603, R5=>0805,....

## CAPACITOR

Symbol name	Value	Tolerance (M: +/-20, K: +/-10, Z: +80/-20)	Rating	Size 2=>0402, 3=>0603, 5=>0805, 6=>1206, 0=>1210
SCD1U10V2MX-1	0.1uF	M/X5R	10V	0402
SC10U6D3V5MX	10uF	M/X5R	6.3V	0805
SC2D2U16V5ZY	2.2uF	Z/Y5V	16V	0805

The naming rule is  
Capacitor type + value + rating + size + tolerance + material  
SCD1U10V2MX-1  
SC=> SMT Ceramic, TC=> POS cap or SP cap  
D1U => 0.1uF  
10V => the voltage rating is 10V  
2=> 0402, 3=>0603, 5=>0805  
M=>tolerance M, K, Z  
X=> X7R/X5R, Y=> Y5V  
-1 => symbol version, nonsense to EE characteristic

## PLANAR\_ID[3..0]

ICH9-M GPIO <sub>n</sub>	39	38	37	36	Planar ID Version	Planar PCB Version
PLANAR_ID <sub>n</sub>	3	2	1	0		
	0	0	0	0	SHINAI-2 SDV	SA
	0	0	0	1	SHINAI-2 SIV	SC
	0	0	1	0	SHINAI-2 SIT	SD
	0	0	1	1	SHINAI-2 SVT	-1
	0	1	0	0		
	0	1	0	1	SHINAI-2 SVT	-2
	0	1	1	0		
	0	1	1	1		

## EC HISTORY

[illegible]

## PCI TABLE

DEVICE	IDSEL	IRQ (Default)	REQ# / GNT#
MINIPCI SLOT	AD18	F, G	REQ# 3/ GNT#
CARDBUS R5C811	AD16	SERIRQ	REQ#0 / GNT#
USB UHCI	AD29	A, C, D	
USB 2.0 EHCI	AD29	H	
DMI-to-PCI/ AC97 Modem/ AC97 Audio	AD30	B B	
LPC Bridge IDE SATA SMBus	AD31	C C B	
PCI Express	AD28	A, B, C, D	

<Variant Name>

緯創資通

**Wistron Corporation**  
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Taipei Hsien 221, Taiwan, R.O.C.

Title
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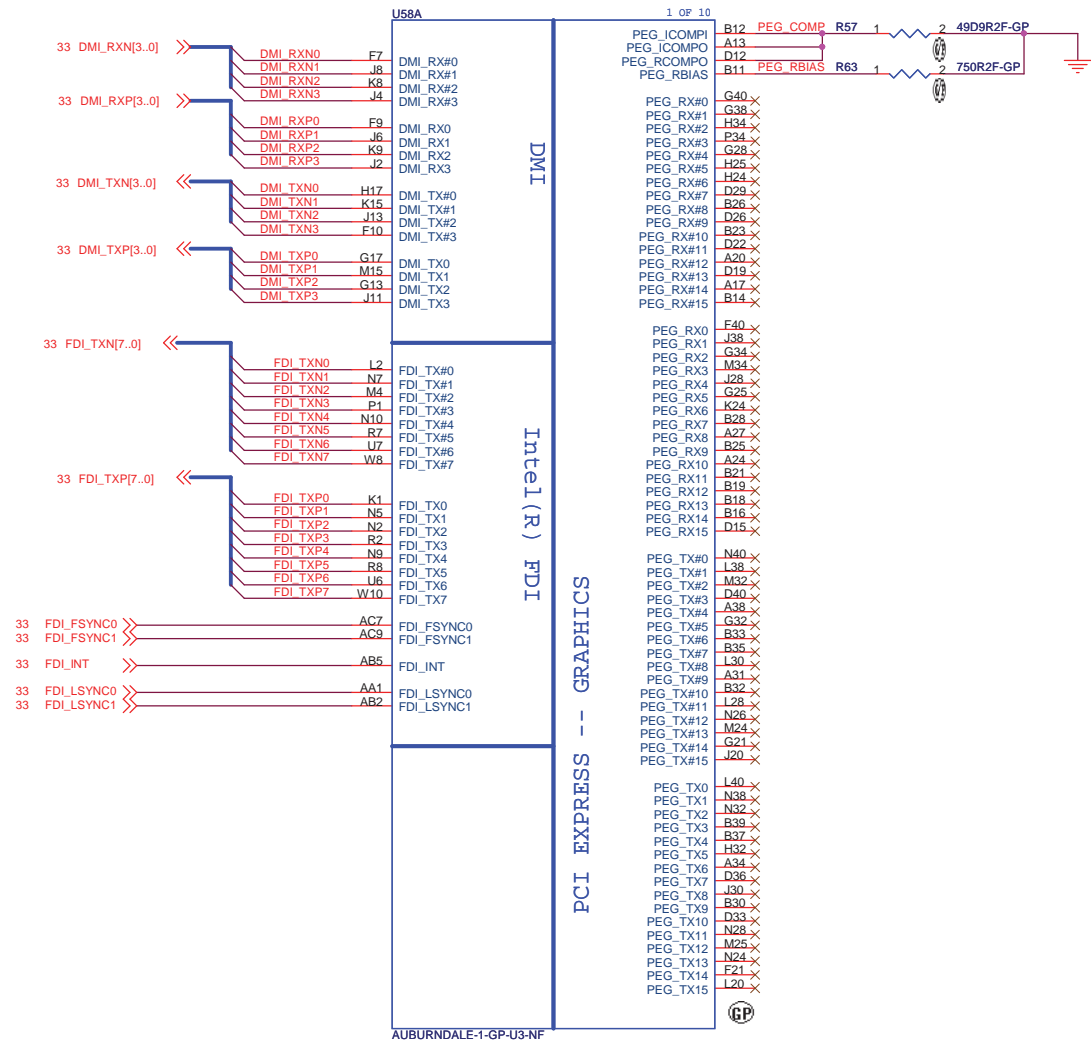
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Rev	-2
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Date: Monday, December 28, 2009

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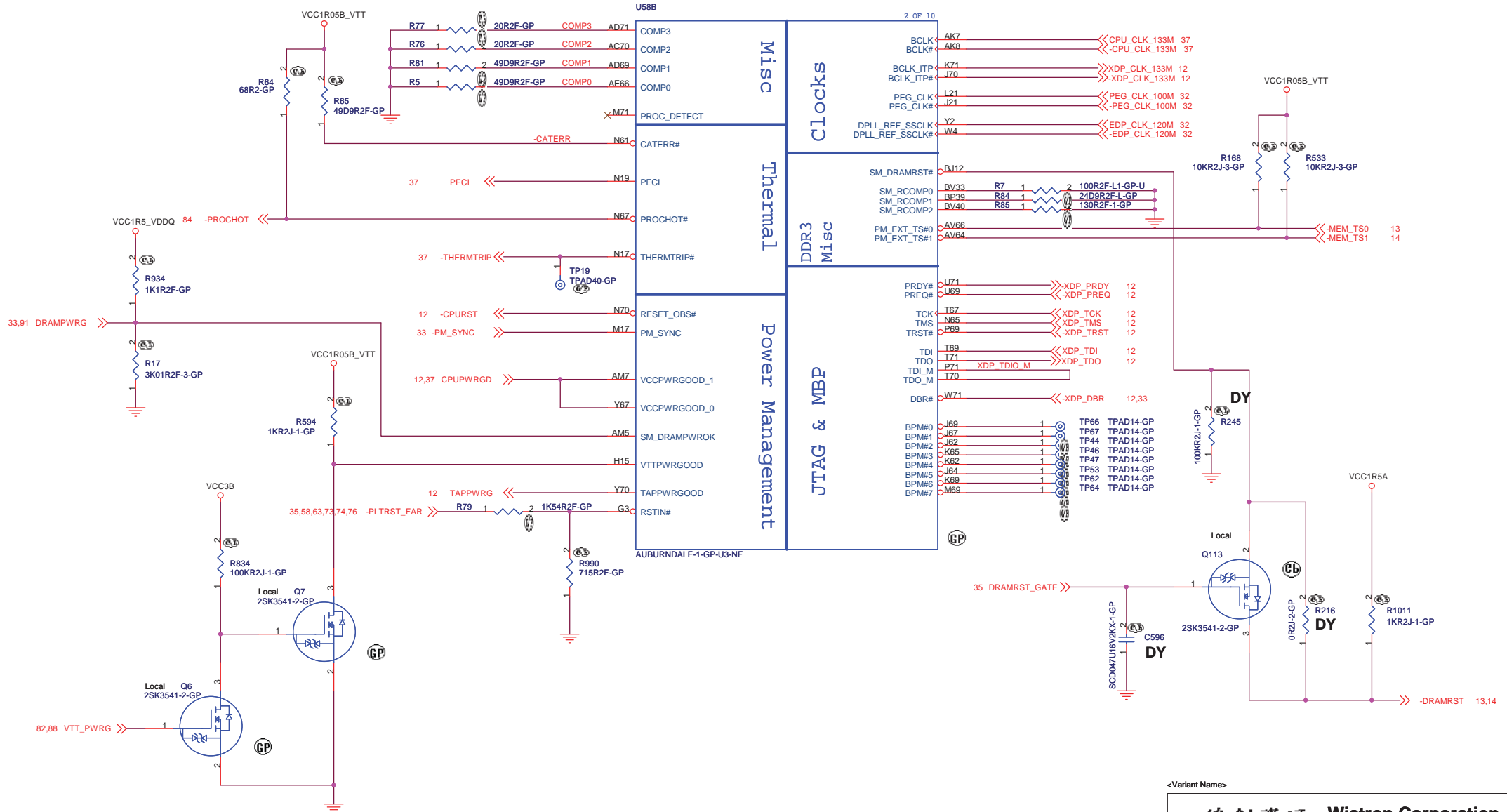
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21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

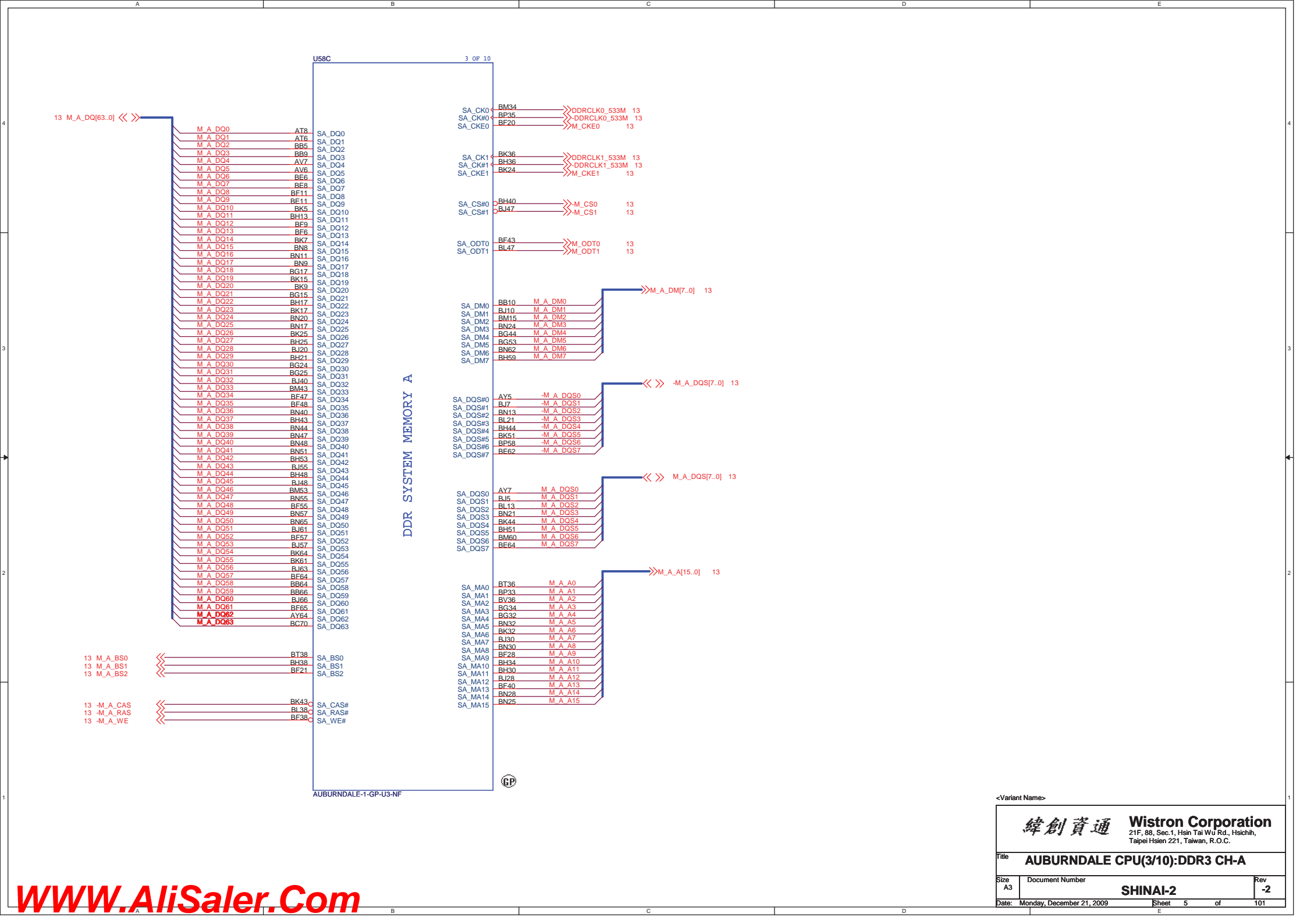
Title **AUBURNDALE CPU(1/10): DMI /PEG/FDI**

Size A3 Document Number **SHINAI-2** Rev **-2**

Date: Monday, December 21, 2009 Sheet 3 of 101

COMP0/1/2/3 : TRACE Length <0.5"  
COMP0/1/2/3 : 10mil width traces, 20mil spacing





<Variant Name>

緯創資通

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Title **AUBURNDALE CPU(3/10):DDR3 CH-A**

Size A3 Document Number

**SHINAI-2**

Rev

**-2**

Date: Monday, December 21, 2009

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D

D

C

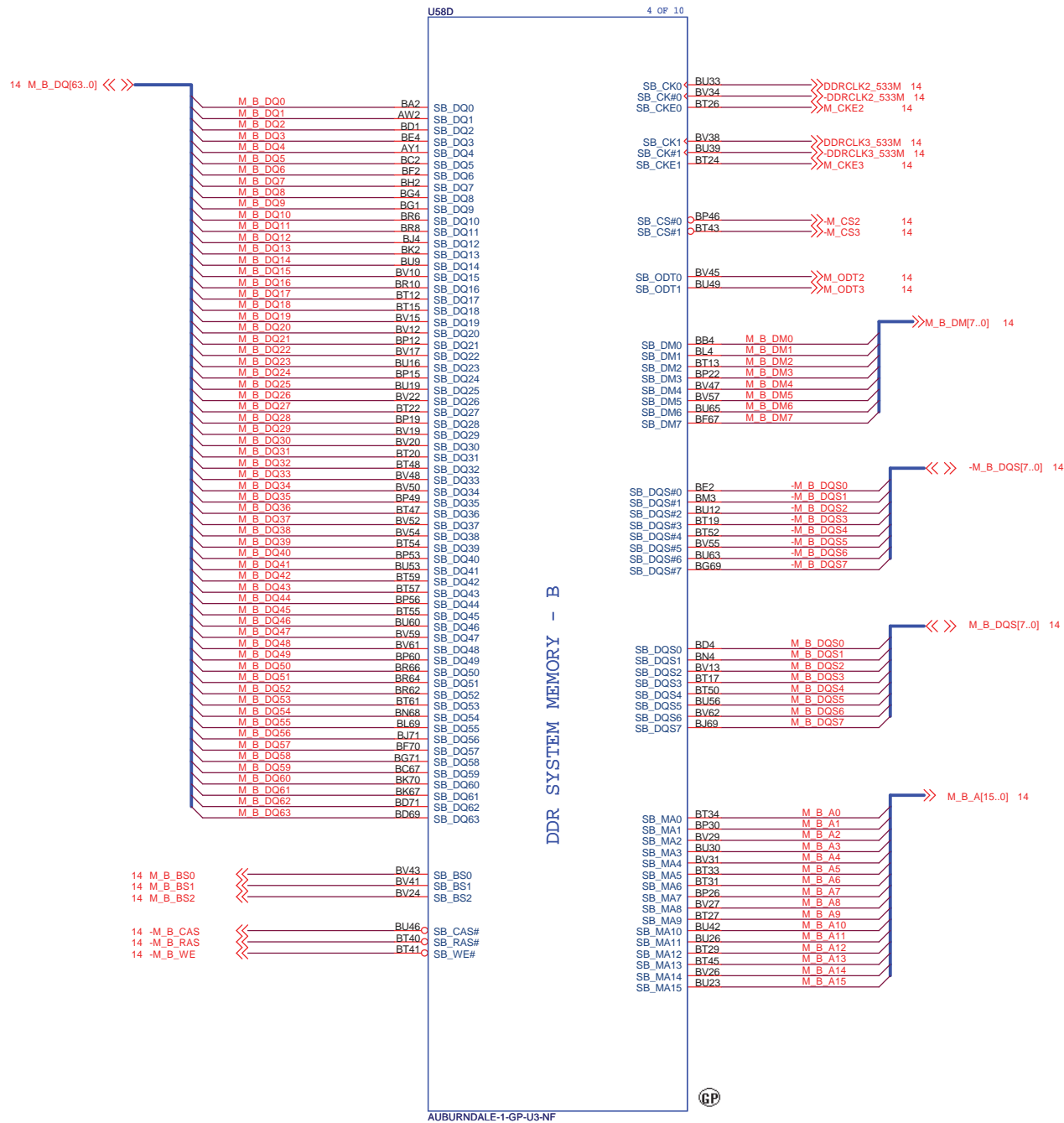
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A

A



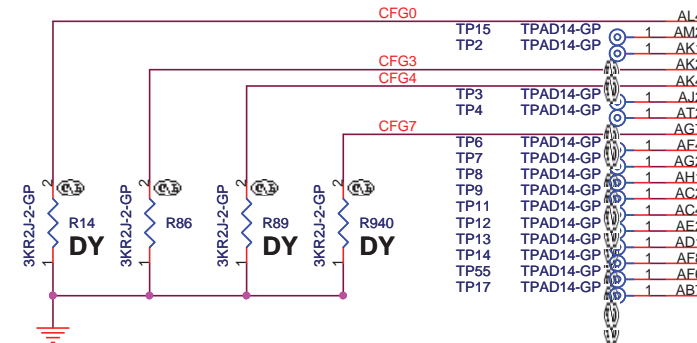
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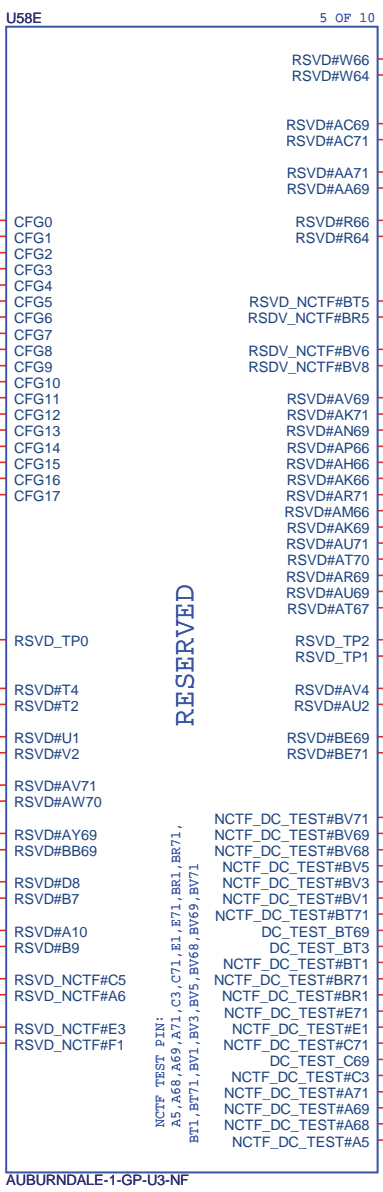
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Taipei Hsien 221, Taiwan, R.O.C.

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Size A3	Document Number SHINAI-2	Rev -2
Date: Monday, December 21, 2009	Sheet 6 of	101

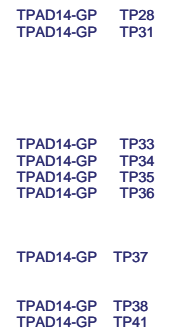


		ASM	DY
CFG[0]	PEG CONFIG	BIFURCATION	SINGLE
CFG[3]	PEG LANE REVERSAL	REVERSE	NORMAL
CFG[4]	DISPLAY PORT PRESENCE	ENABLE	DISABLE
CFG[7]	CLARKSFIELD JITTER ISSUE TEMPORARY WORKAROUND	ENABLE	DISABLE



RESERVED

NCTF TEST PIN:  
A5, A68, A69, A71, C3, C71, E1, E71, BR1, BR71, BT1, BT71, BV1, BV3, BV5, BV68, BV69, BV71

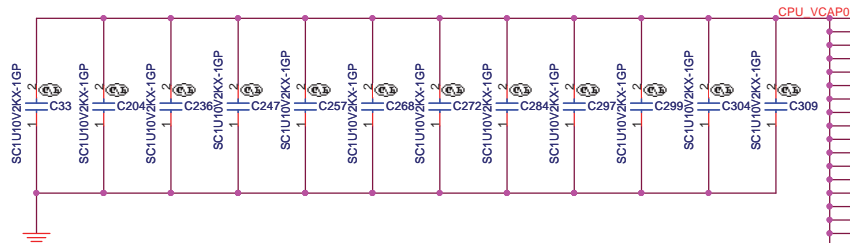


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Size Custom	Document Number SHINAI-2
Date: Monday, December 21, 2009	Sheet 7 of 101



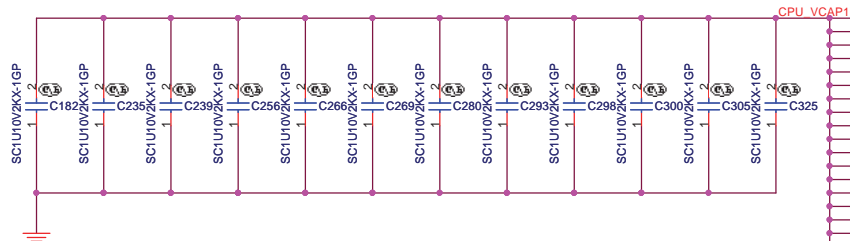




- BD55 VCAP0
- BD51 VCAP0
- BD48 VCAP0
- BB55 VCAP0
- BB51 VCAP0
- BB48 VCAP0
- AV57 VCAP0
- AY53 VCAP0
- AY50 VCAP0
- AW57 VCAP0
- AW53 VCAP0
- AW50 VCAP0
- AU55 VCAP0
- AU51 VCAP0
- AU48 VCAP0
- AR55 VCAP0
- AR51 VCAP0
- AR48 VCAP0
- AN57 VCAP0
- AN53 VCAP0
- AN50 VCAP0
- AL57 VCAP0
- AL53 VCAP0
- AL50 VCAP0
- AK57 VCAP0
- AK53 VCAP0
- AK50 VCAP0

## POWER

CPU CORE SUPPLY



- BD44 VCAP1
- BD41 VCAP1
- BD37 VCAP1
- BB44 VCAP1
- BB41 VCAP1
- BB37 VCAP1
- AY46 VCAP1
- AY42 VCAP1
- AY39 VCAP1
- AW46 VCAP1
- AW42 VCAP1
- AW39 VCAP1
- AU44 VCAP1
- AU41 VCAP1
- AU37 VCAP1
- AR44 VCAP1
- AR41 VCAP1
- AR37 VCAP1
- AN46 VCAP1
- AN42 VCAP1
- AN39 VCAP1
- AL46 VCAP1
- AL42 VCAP1
- AL39 VCAP1
- AK46 VCAP1
- AK42 VCAP1
- AK39 VCAP1

- VCC AF57
- VCC AF55
- VCC AF53
- VCC AF51
- VCC AF50
- VCC AF48
- VCC AF46
- VCC AF44
- VCC AF42
- VCC AF41
- VCC AD55
- VCC AD51
- VCC AD48
- VCC AD44
- VCC AD41
- VCC AB55
- VCC AB51
- VCC AB48
- VCC AB44
- VCC AB41
- VCC AA55
- VCC AA51
- VCC AA48
- VCC AA44
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- VCC U51
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- VCC R51
- VCC R48
- VCC R44
- VCC R41
- VCC P60
- VCC N55
- VCC N51
- VCC N48
- VCC N44
- VCC N42
- VCC M60
- VCC M51
- VCC M44
- VCC L55
- VCC K60
- VCC K51
- VCC K44
- VCC J55
- VCC H60
- VCC H51
- VCC H44
- VCC G60
- VCC G55
- VCC G51
- VCC G44
- VCC F55
- VCC F51
- VCC E60
- VCC E57
- VCC E53
- VCC E50
- VCC E46
- VCC E42
- VCC D59
- VCC D57
- VCC D55
- VCC D54
- VCC D52
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- VCC D47
- VCC D45
- VCC D43
- VCC B60
- VCC B56
- VCC B53
- VCC B49
- VCC B46
- VCC B42
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- VCC A43

VCCCPUCORE

AUBURNDAL-1-GP-U3-NF



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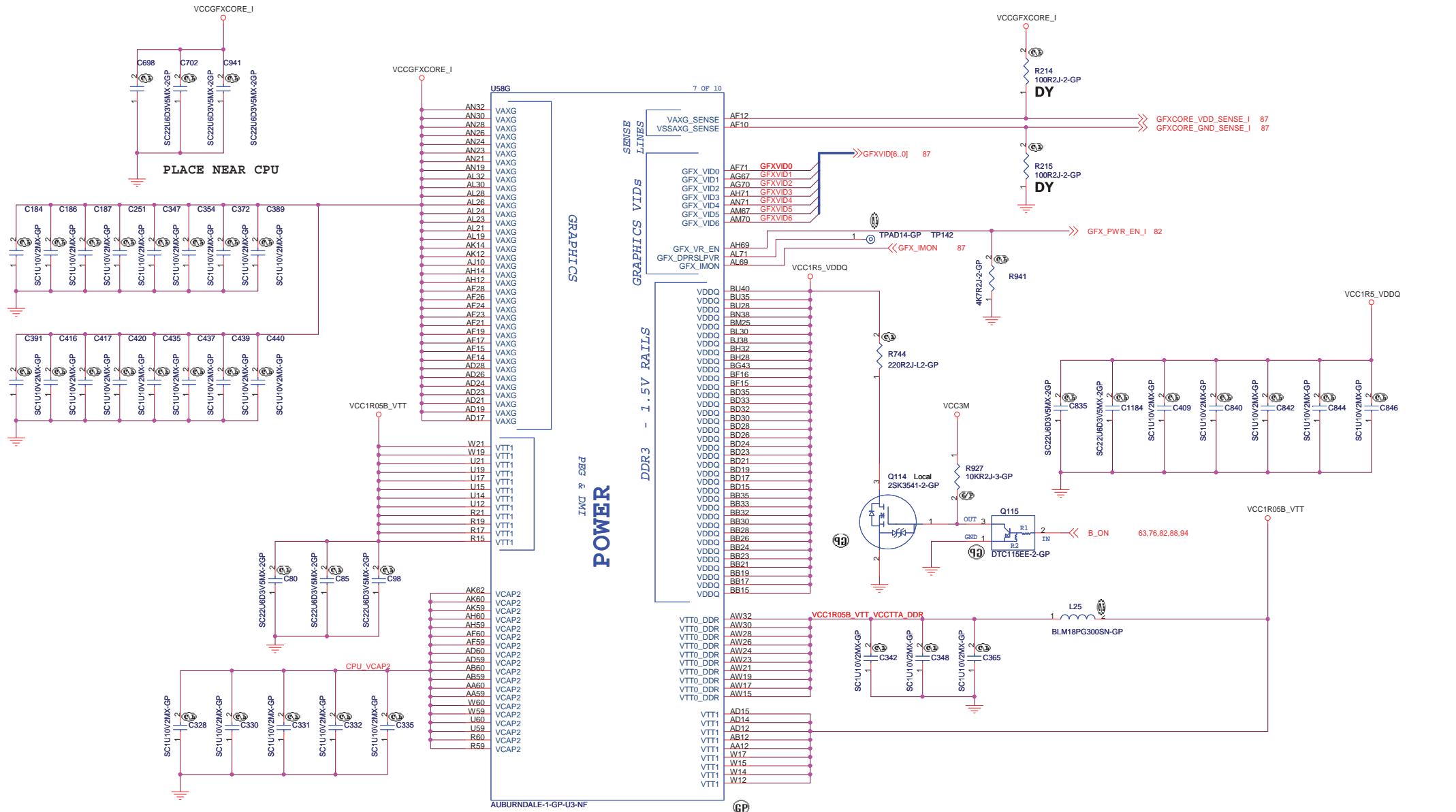
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**Wistron Corporation**  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title **AUBURNDAL CPU(8/10): VCC&VCAP**

Size A3 Document Number **SHINAI-2** Rev -2

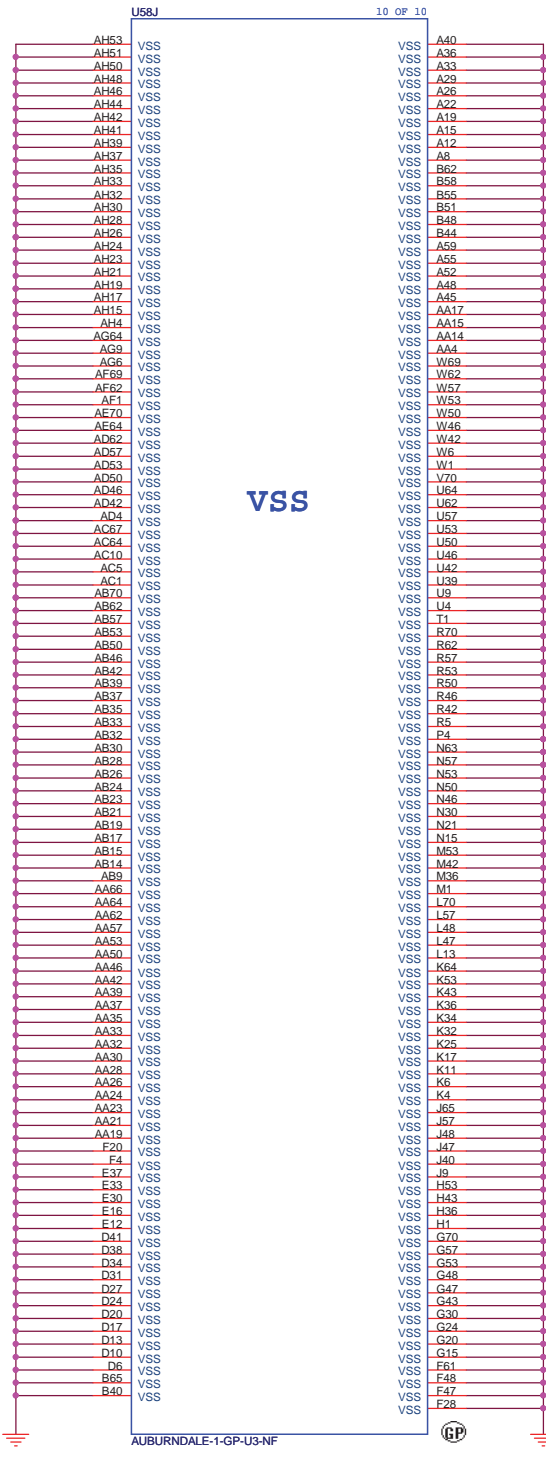
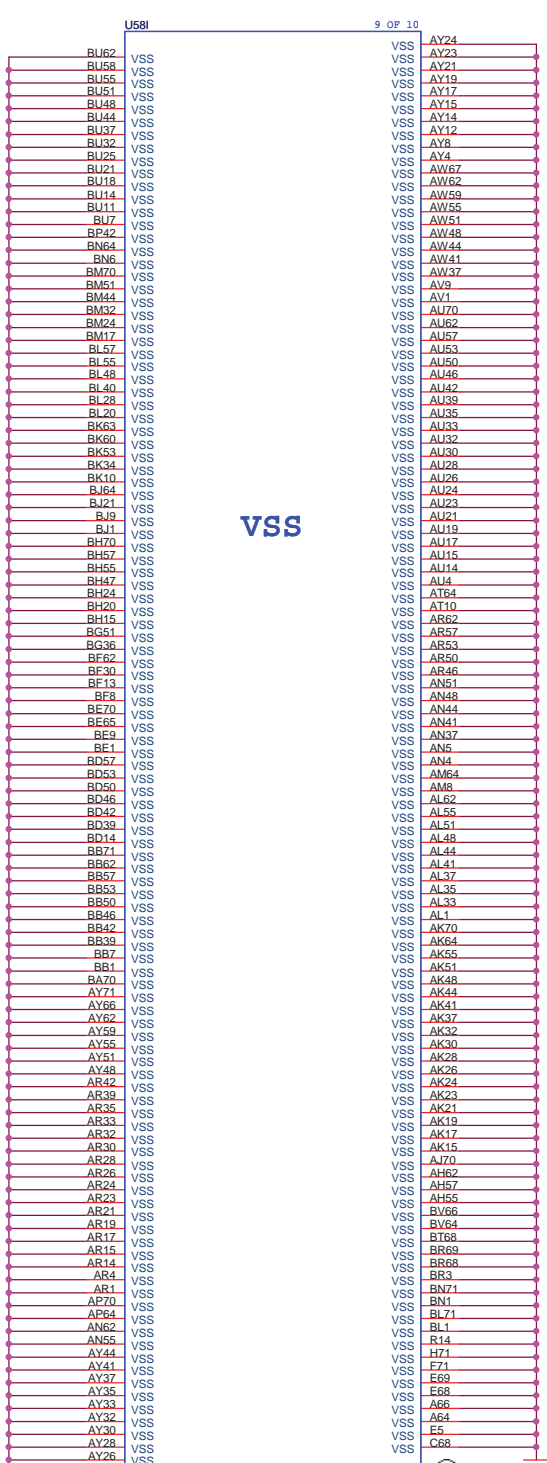
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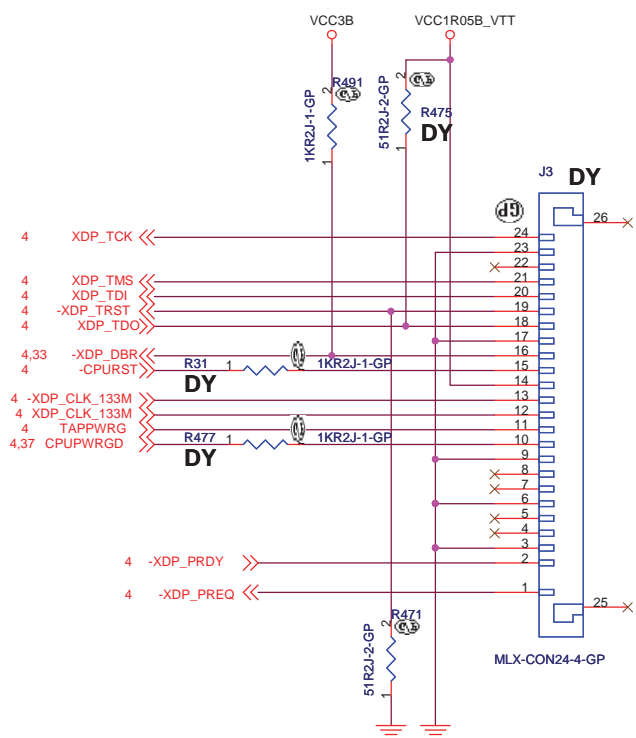


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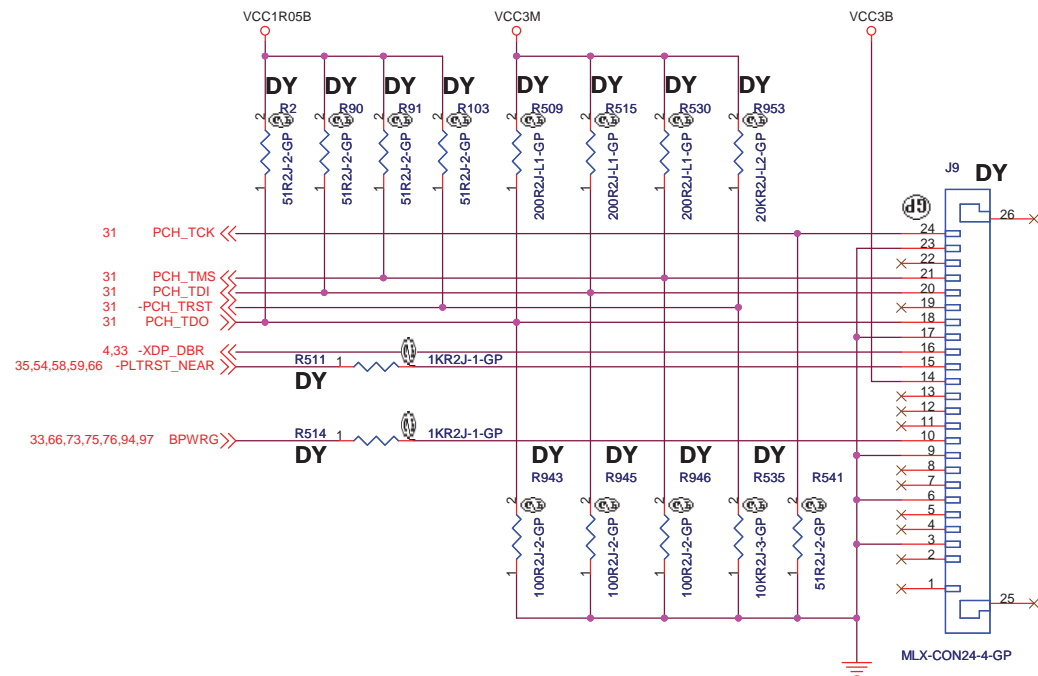
**緯創資通 Wistron Corporation**  
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Size	Document Number	<b>SHINAI-2</b>		Rev
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### DEBUG Interface for Processor.



### DEBUG Interface for PCH.

TABLE

PCH PIN	REF DES	PCH ES1 JTAG		PCH ES2 JTAG		PRODUCTION	
		Enable	Disable	Enable	Disable	Enable	Disable
TDO	R509	DY	DY	200 Ohms	DY	DY	DY
	R943	DY	DY	100 Ohms	DY	DY	DY
	R2	DY	DY	DY	DY	51 Ohms	DY
TMS	R530	200 Ohms	DY	200 Ohms	DY	DY	DY
	R946	100 Ohms	DY	100 Ohms	DY	DY	DY
	R91	DY	DY	DY	DY	51 Ohms	DY
TDI	R515	200 Ohms	20K Ohms	200 Ohms	DY	DY	DY
	R945	100 Ohms	10K Ohms	100 Ohms	DY	DY	DY
	R90	DY	DY	DY	DY	51 Ohms	DY
TCK	R541	51 Ohms	51 Ohms	51 Ohms	51 Ohms	51 Ohms	51 Ohms
TRST#	R953	20K Ohms	DY	DY	DY	DY	DY
	R535	10K Ohms	DY	DY	DY	DY	DY
	R103	DY	DY	DY	DY	DY	DY

↑  
**LOGIC**

<Variant Name>

緯創資通

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Title
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## XDP CONN

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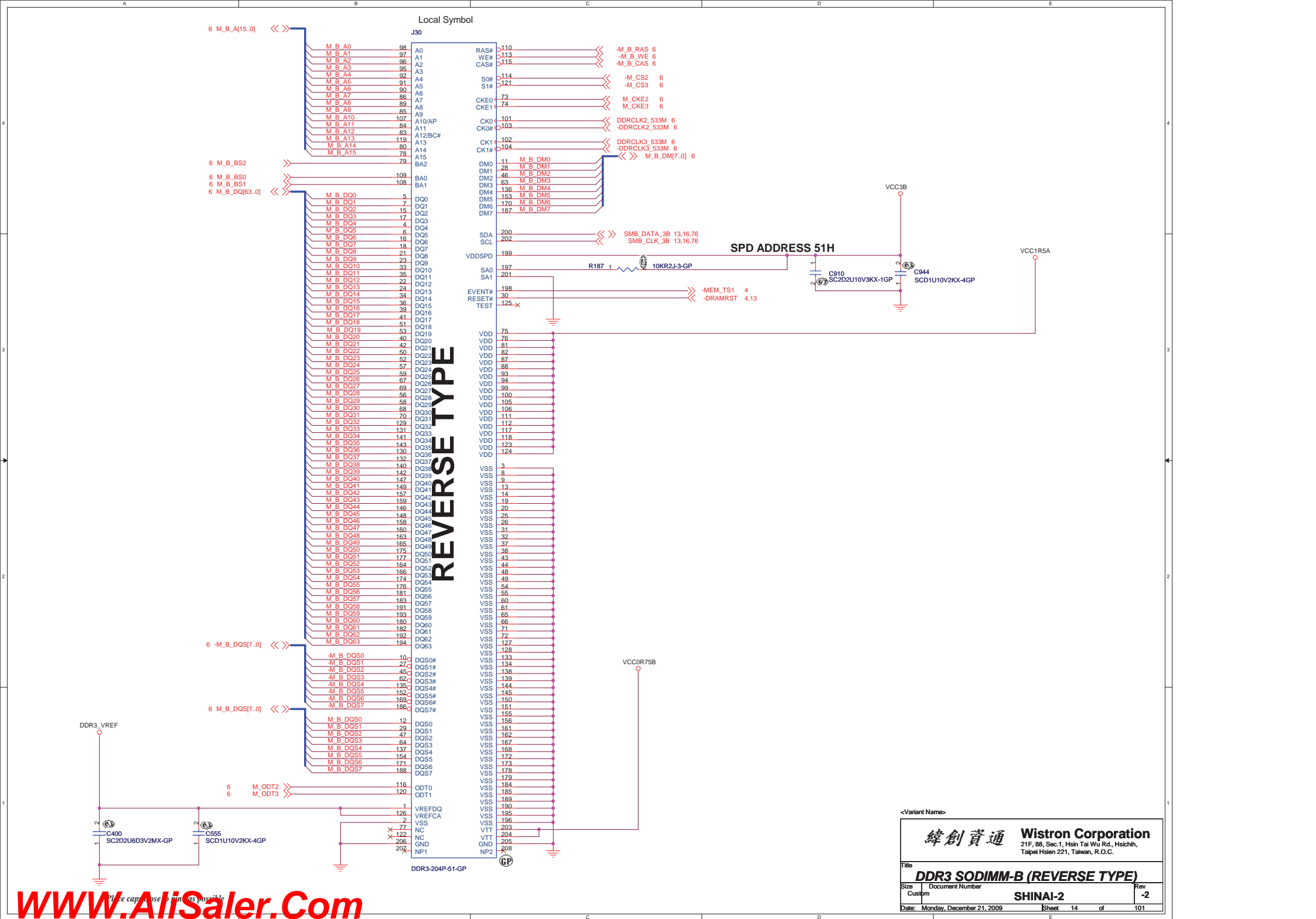
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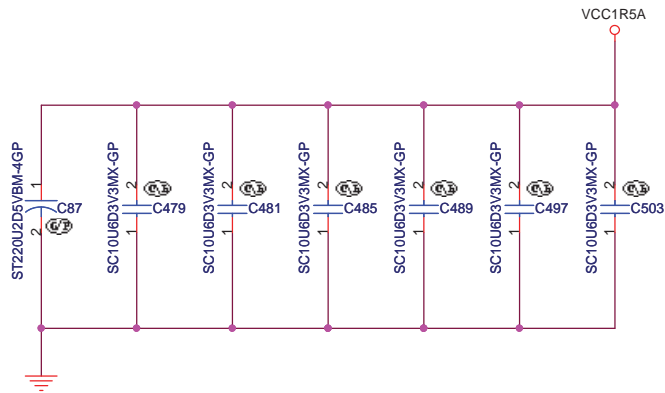
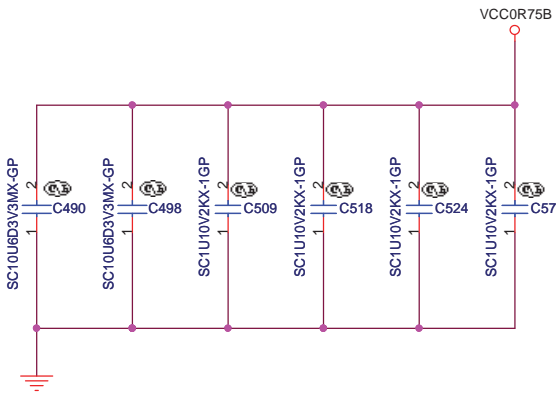
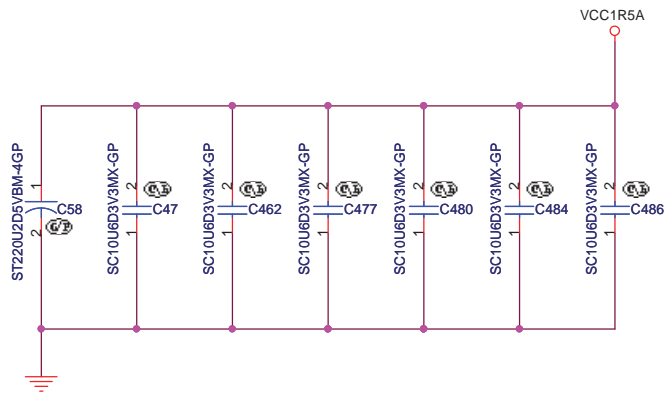
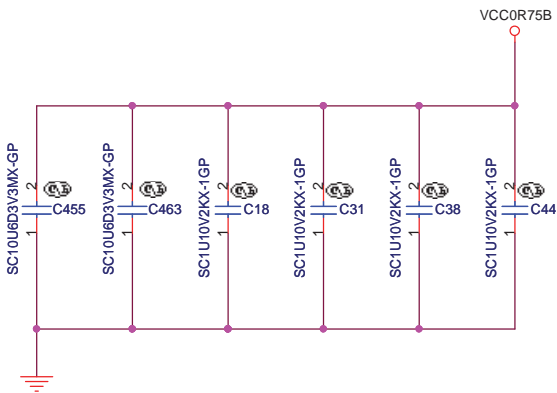
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<Variant Name>

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**Wistron Corporation**

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
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Title

**DDR3 DECOUPLING**

Size  
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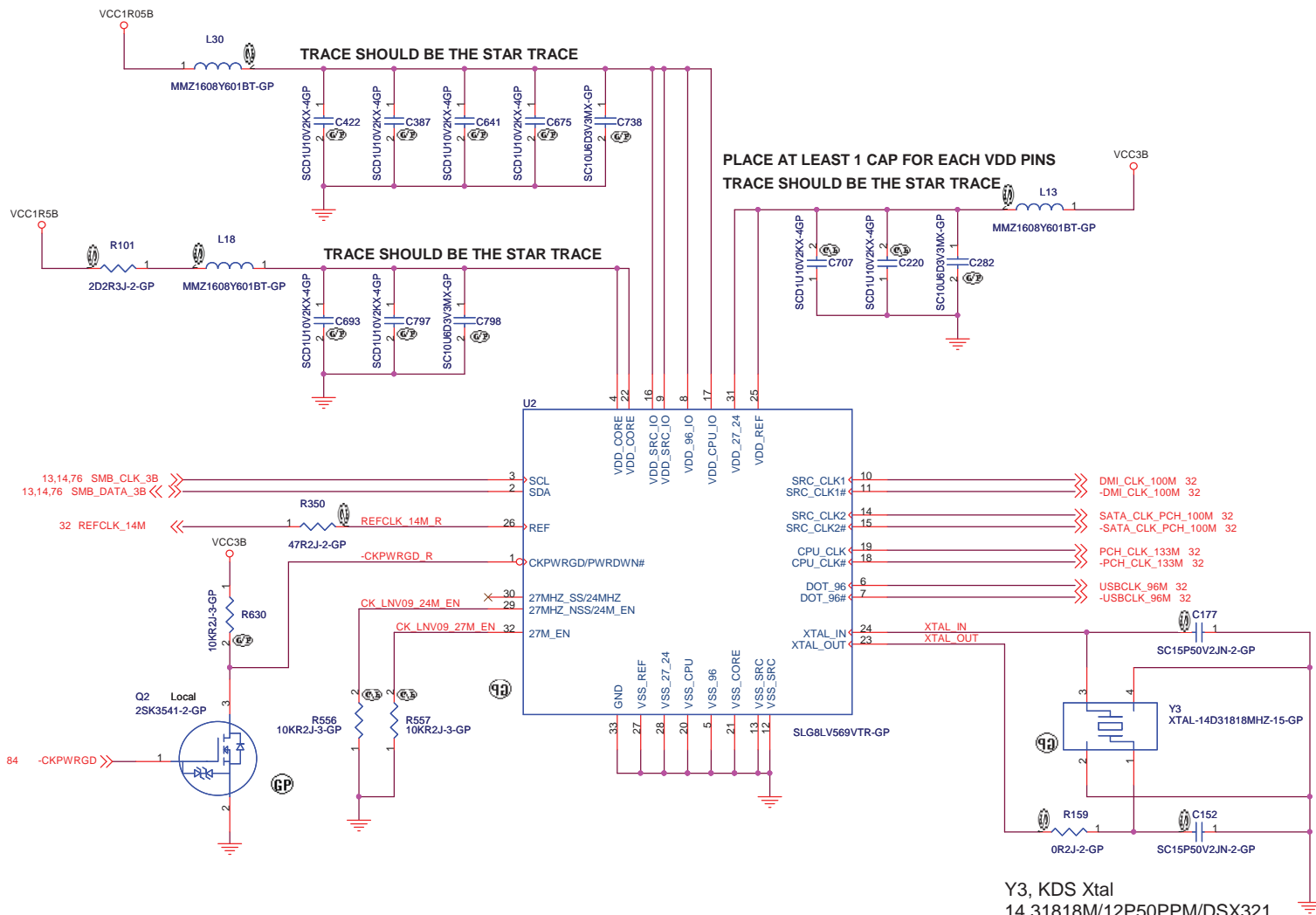
Document Number

**SHINAI-2**

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-2

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Y3, KDS Xtal  
14.31818M/12P50PPM/DSX321

TABLE

27M_EN	24M_EN	PIN 29	PIN 30
0	0	HI-Z	HI-Z
0	1	HI-Z	24.576M
1	0	27M_NSS	27M_SS
1	1	27M_NSS	24.576M

U2

Primary	Silego	SLG8LV569VTR	54Y9018AA
2nd	IDT	ICS9LVRS396BKLFT	54Y9018BA
3th	Realtek	RTM890N-634-VC-GRT	54Y9018CB

<Variant Name>

**緯創資通**

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Title

**CLOCK GEN(CK-LNV09)**

Size A3

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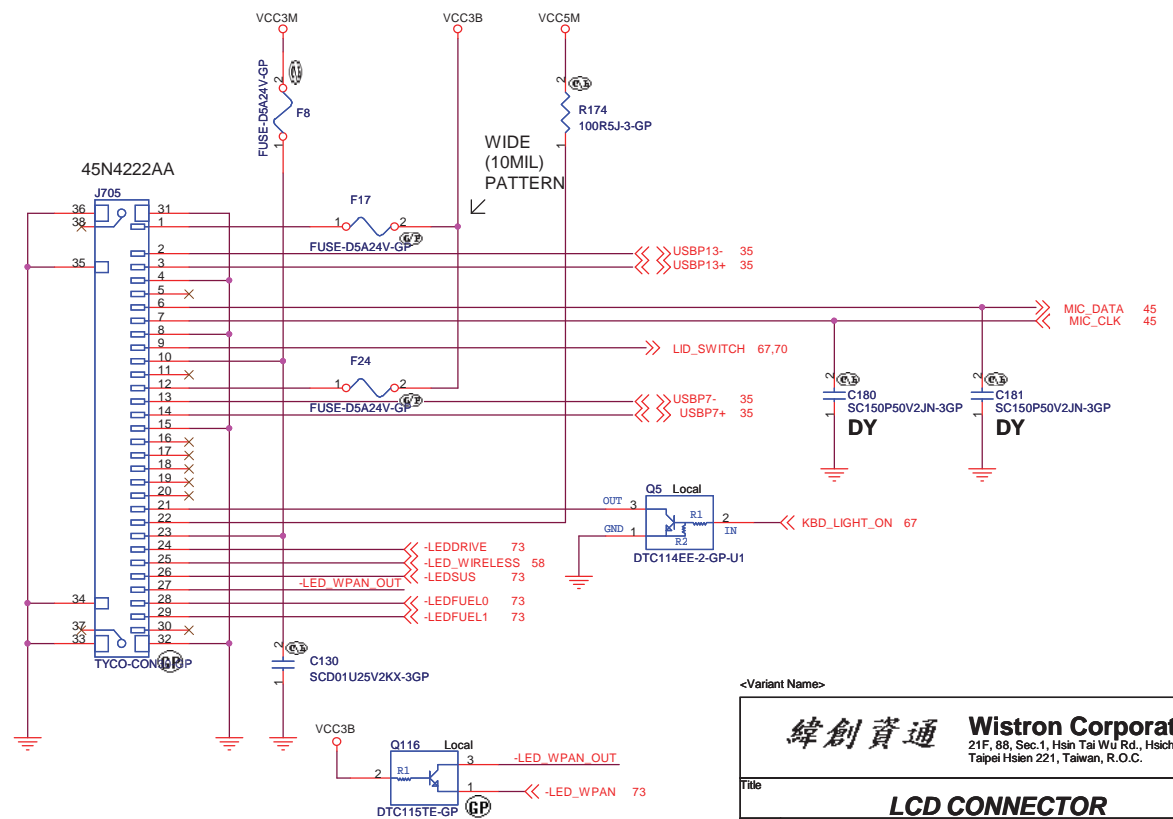
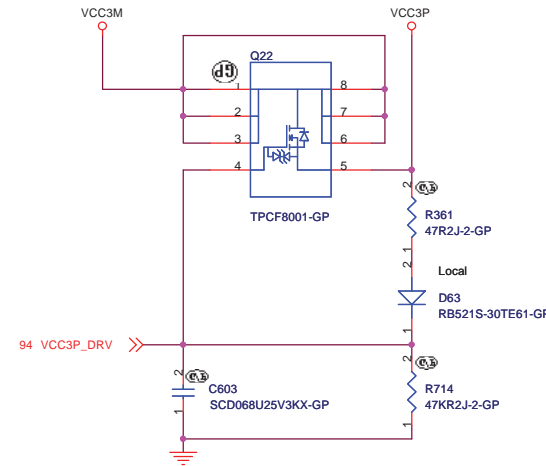
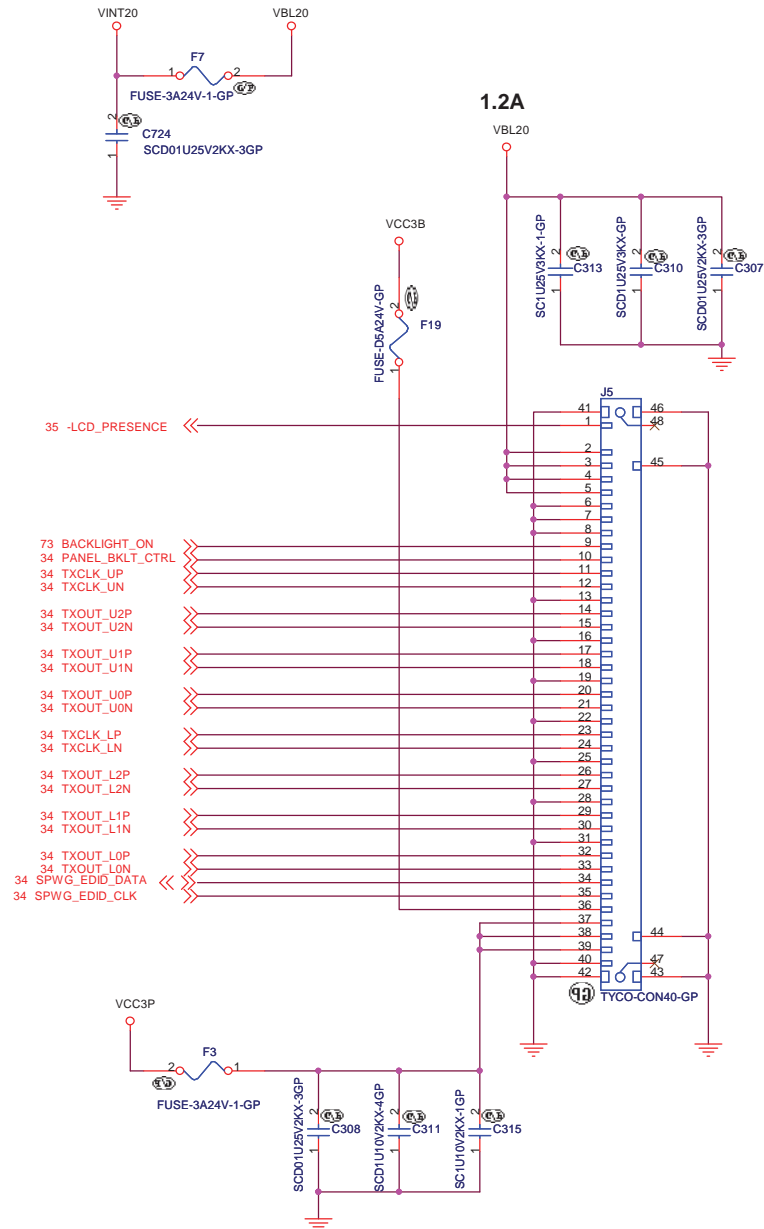


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Date:	Monday, December 21, 2009		Sheet 25 of 101

# LCD / Inverter Connector

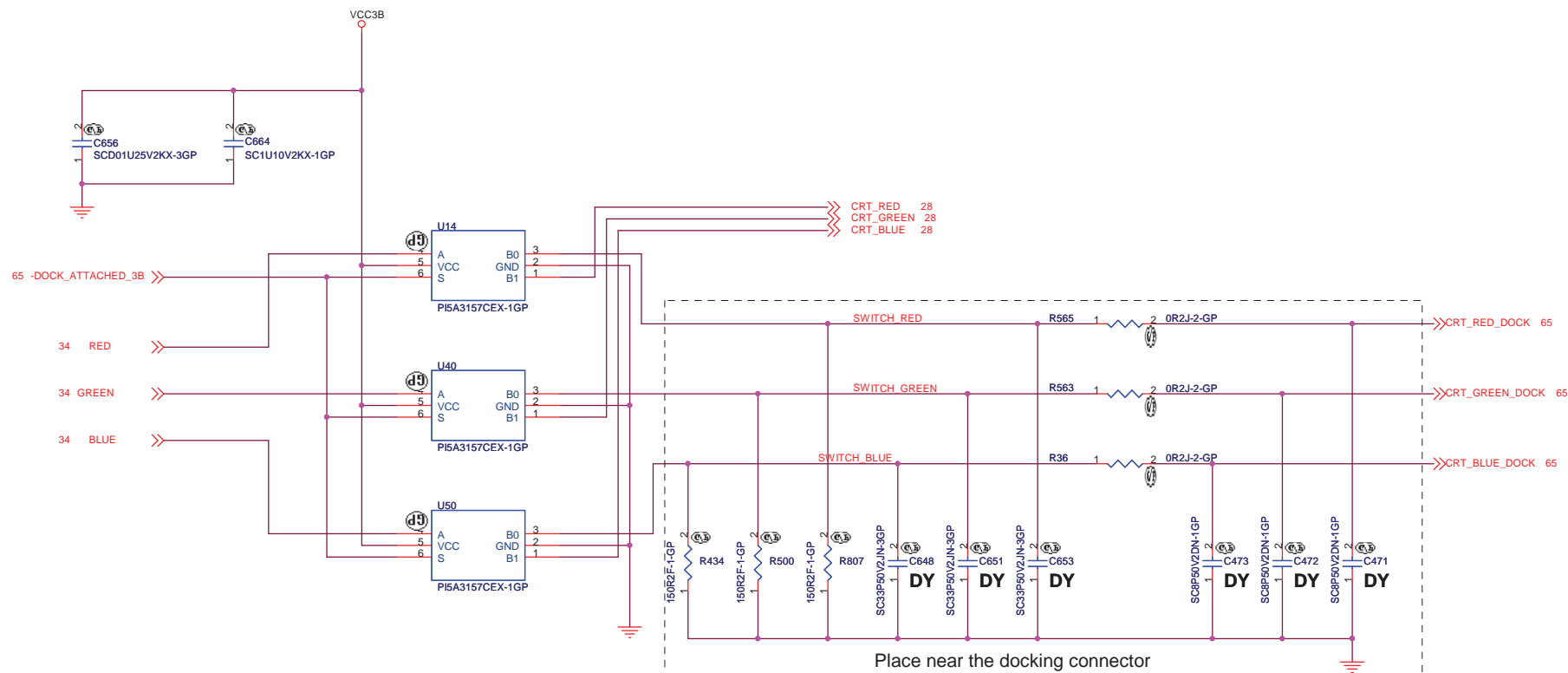


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**Wistron Corporation**  
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LCD CONNECTOR		
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<Variant Name>

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**Wistron Corporation**  
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Taipei Hsien 221, Taiwan, R.O.C.

Title

**RGB SWITCH**

Size

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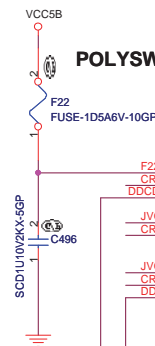
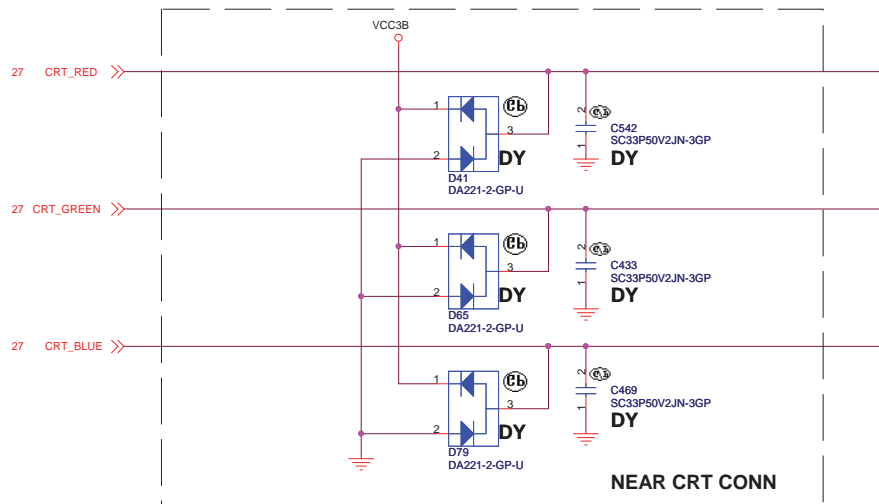
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Rev

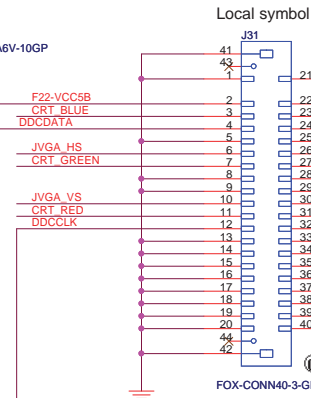
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Date: Monday, December 21, 2009

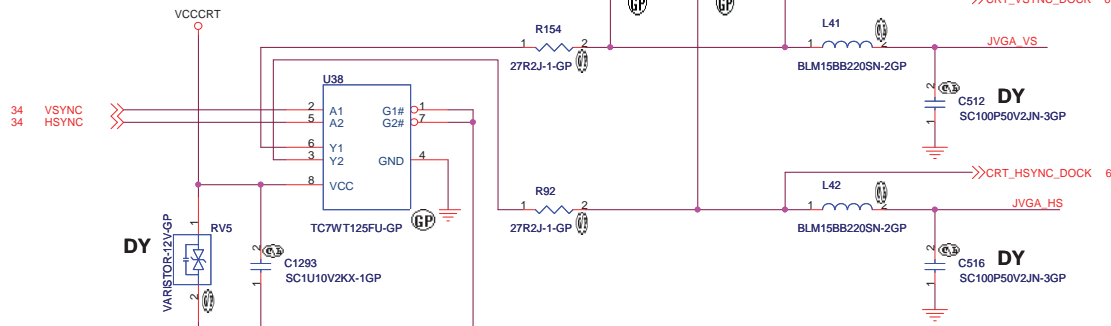
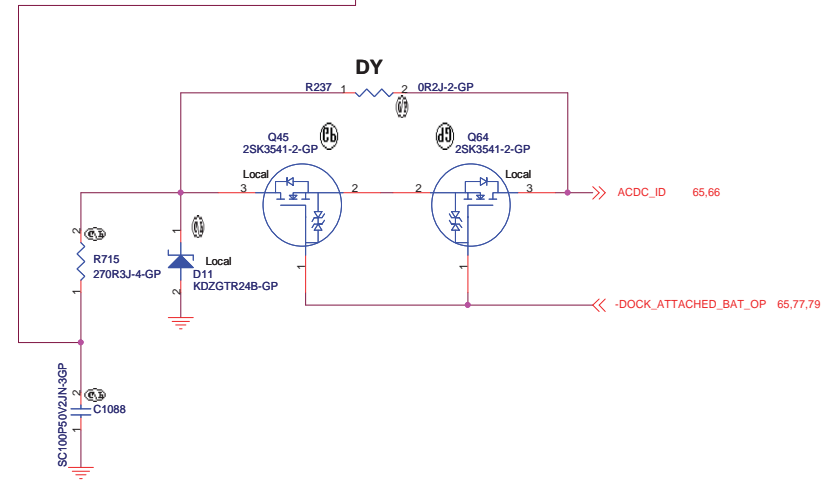
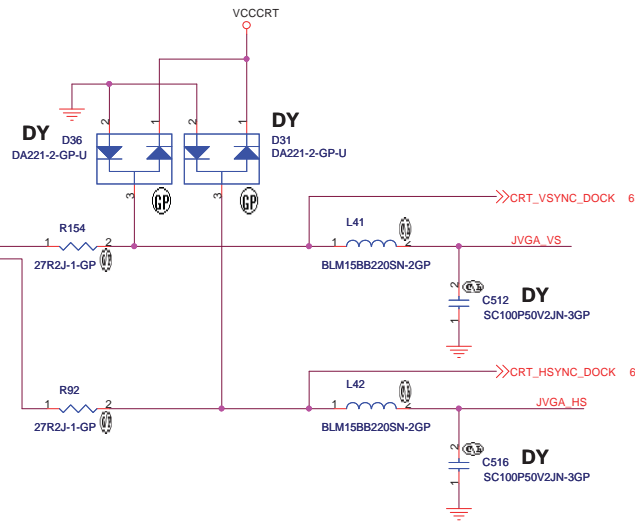
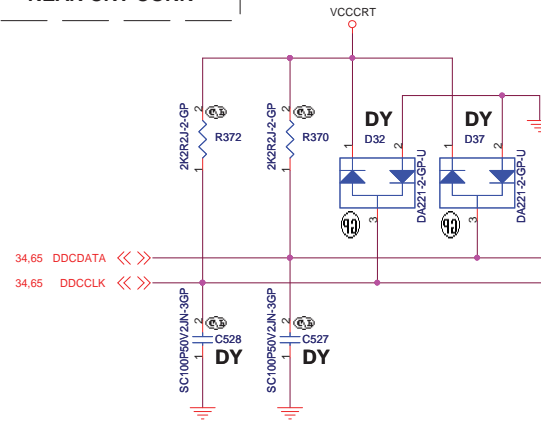
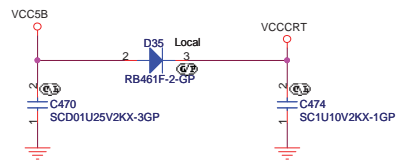
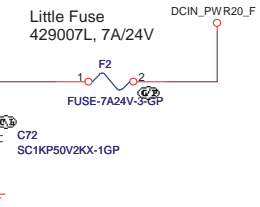
Sheet 27 of 101



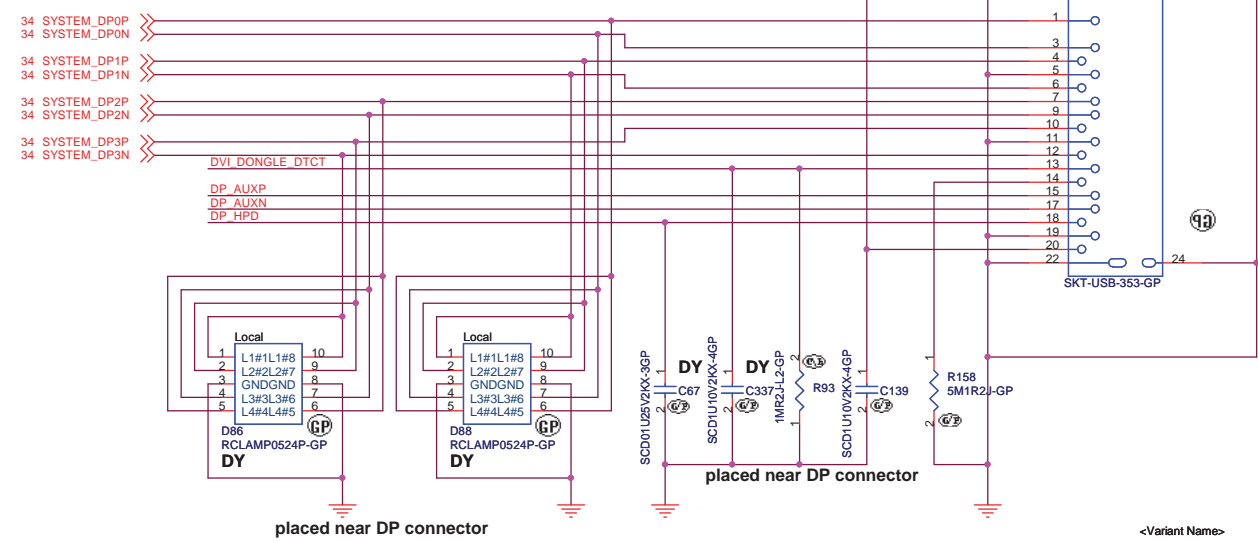
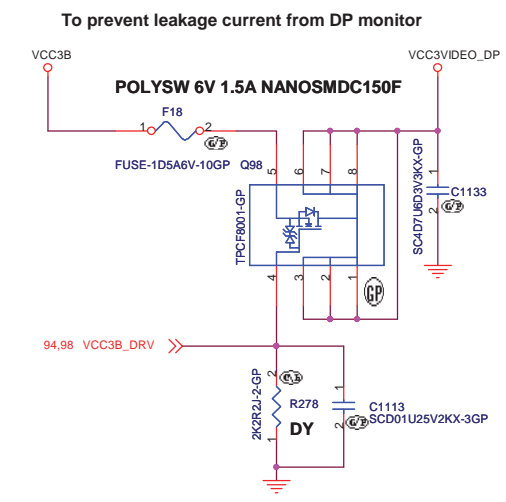
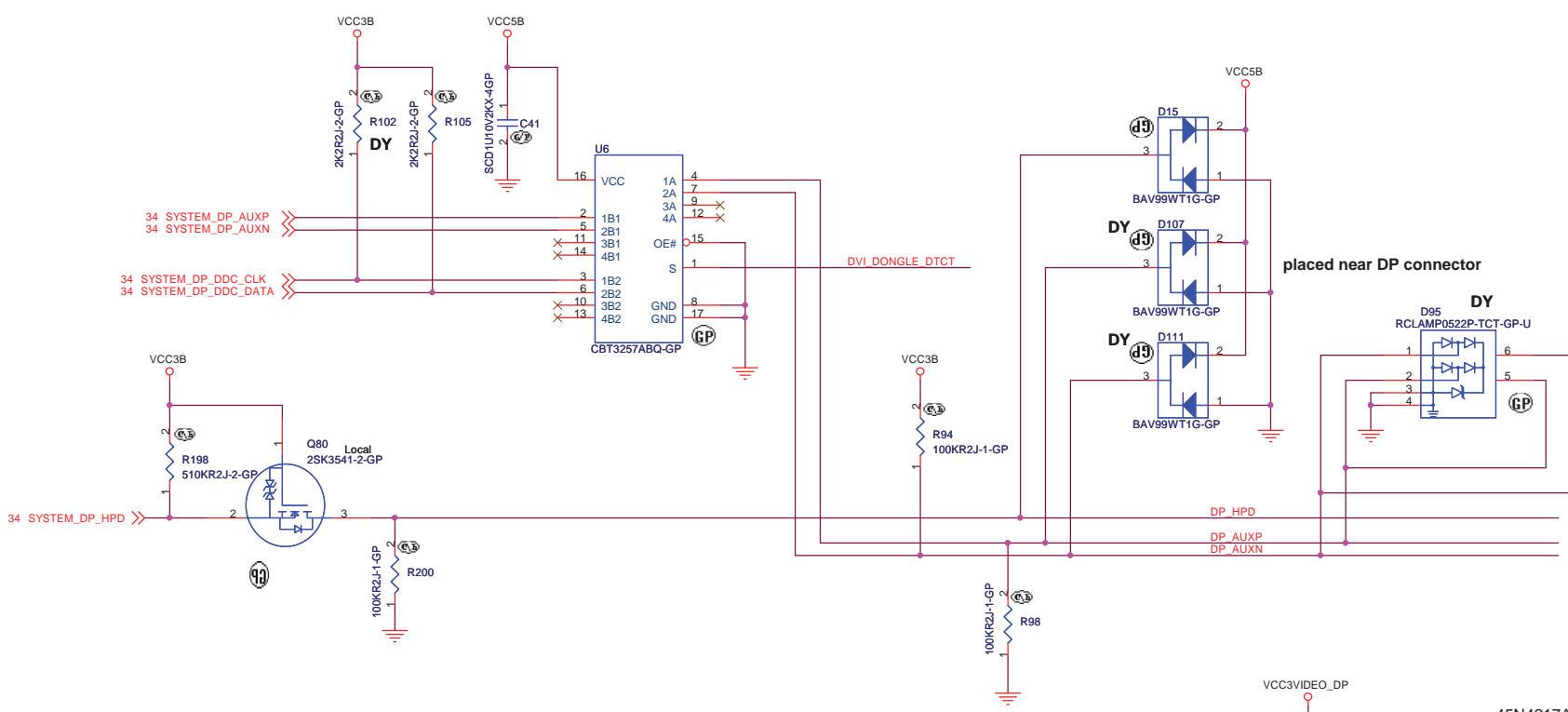
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PLACE NEAR CONNECTOR




<div> <div>緯創資通</div> <div>Wistron Corporation</div> <div>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</div> </div>			
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Size	Document Number	Rev	
Custom	SHINAI-2	-2	
Date:	Monday, December 21, 2009	Sheet	28 of 101



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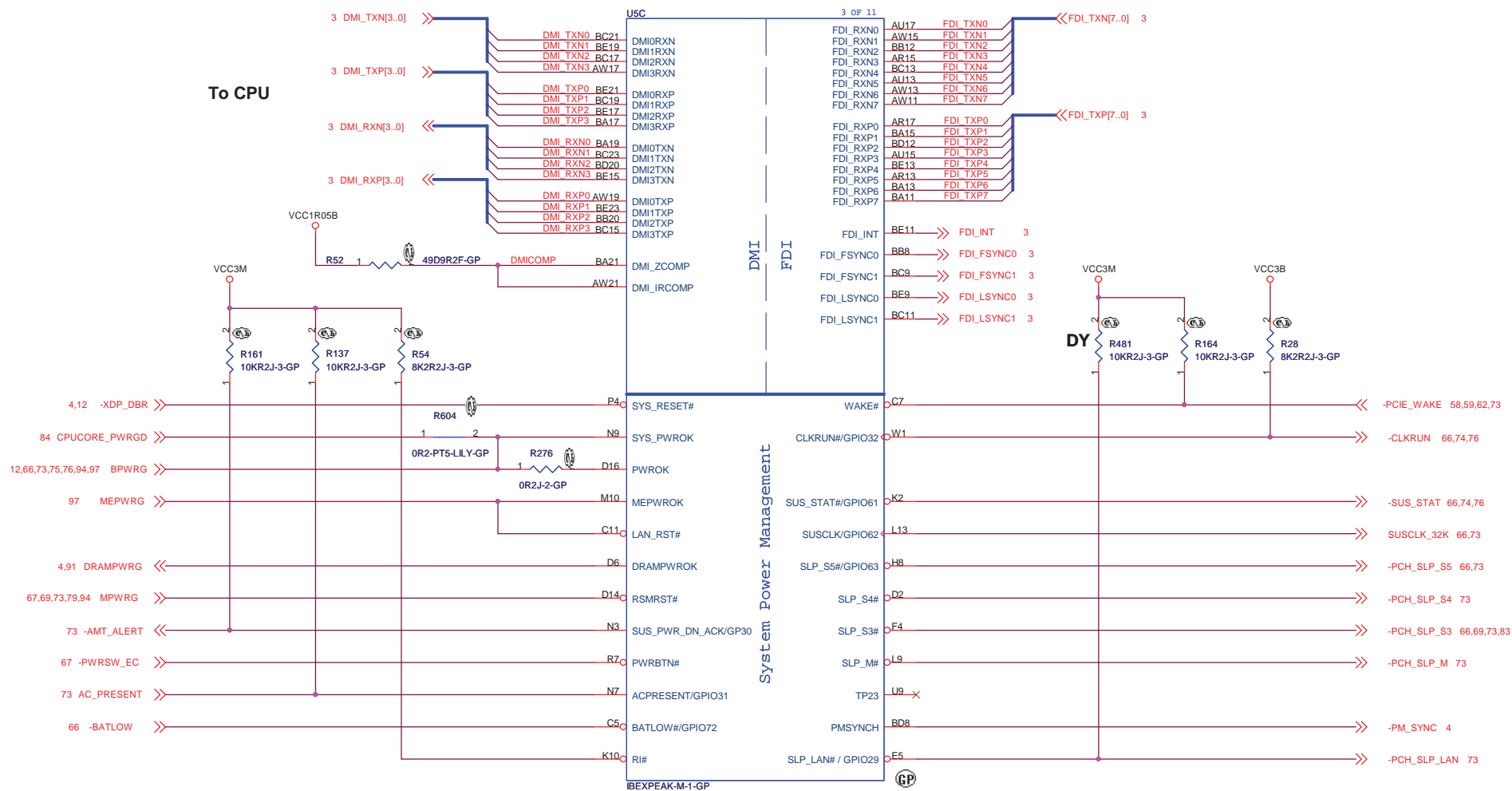
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Size A4	Document Number <b>SHINAI-2</b>		Rev <b>-2</b>
Date:	Monday, December 21, 2009	Sheet 30 of	101





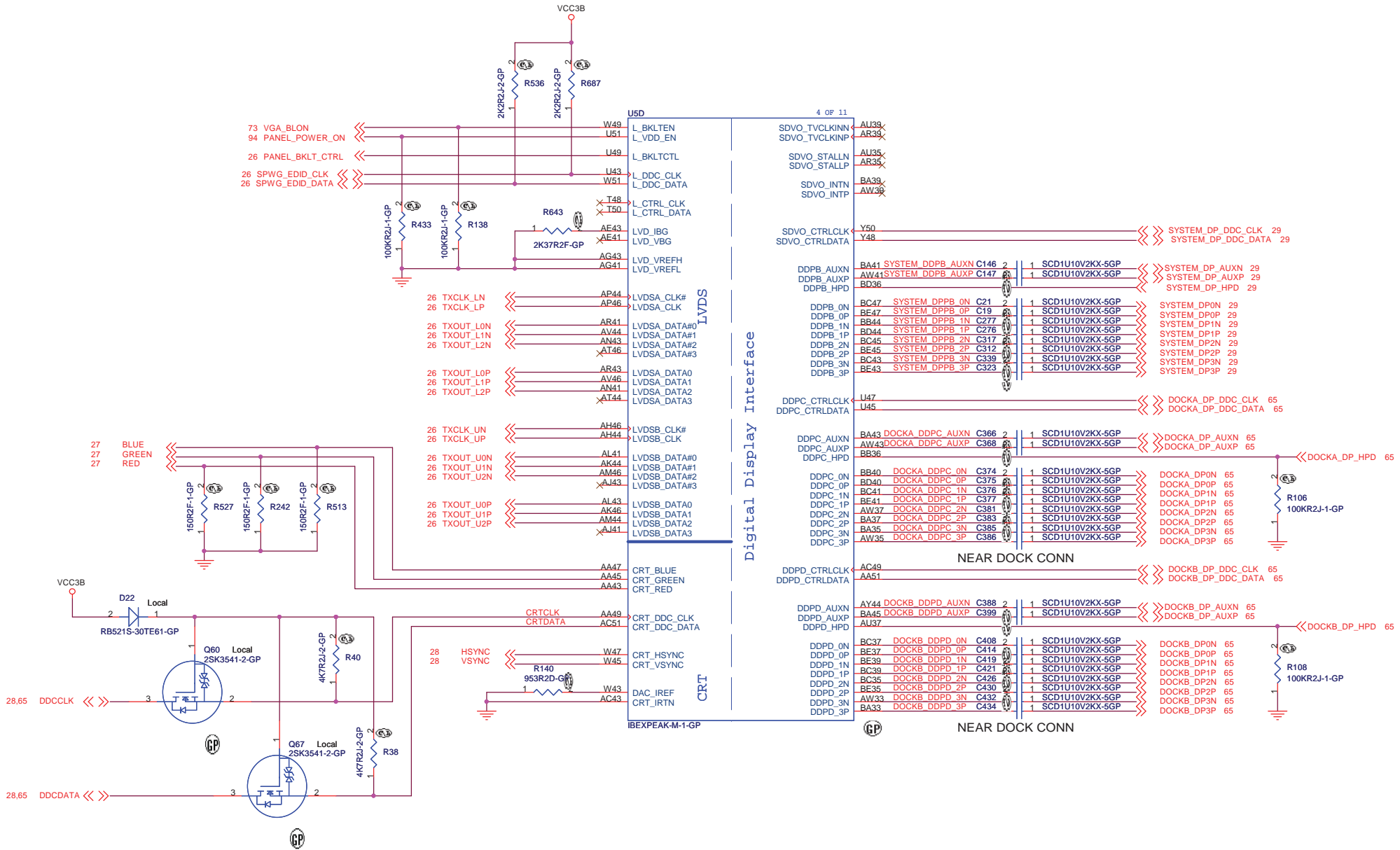


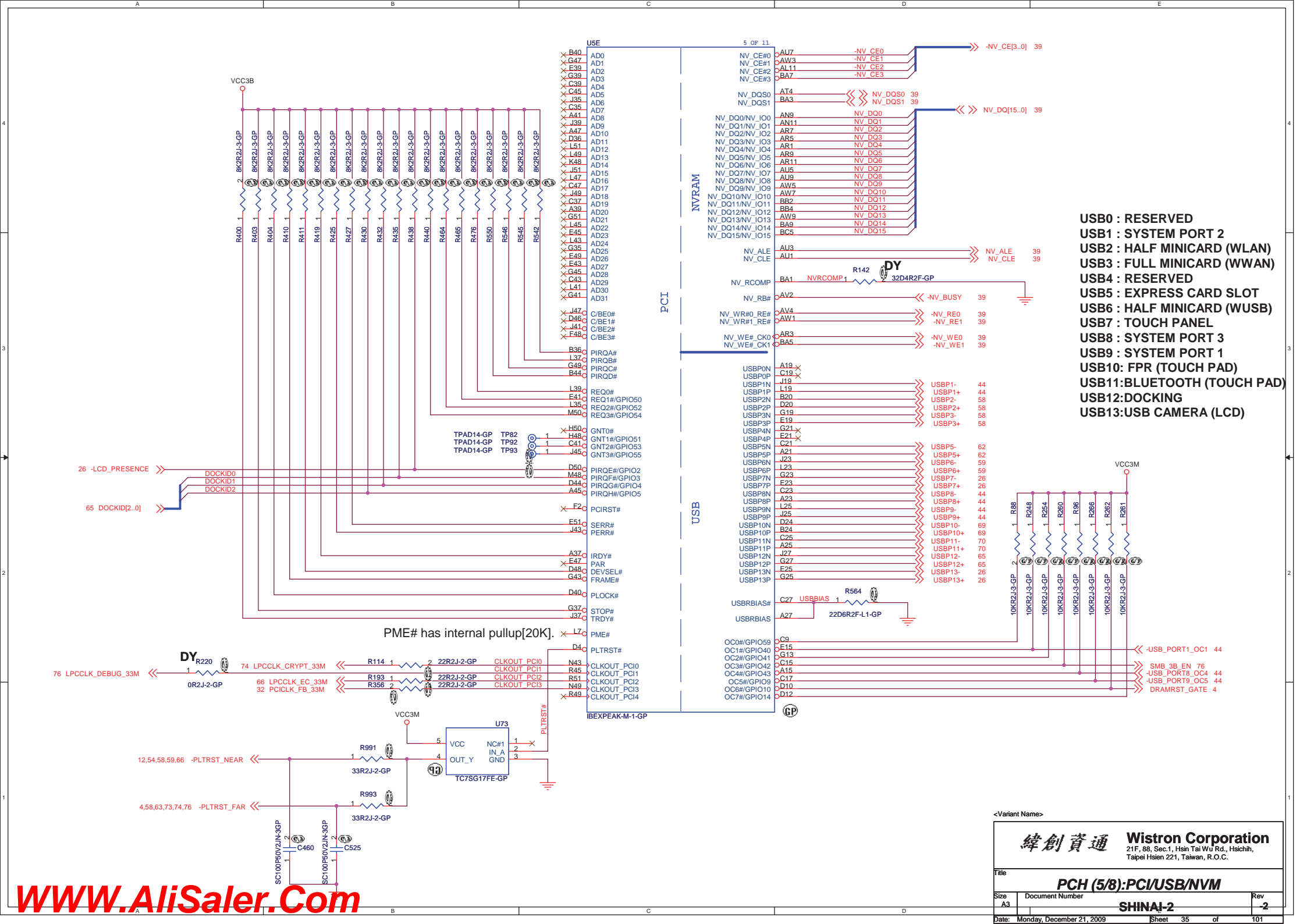
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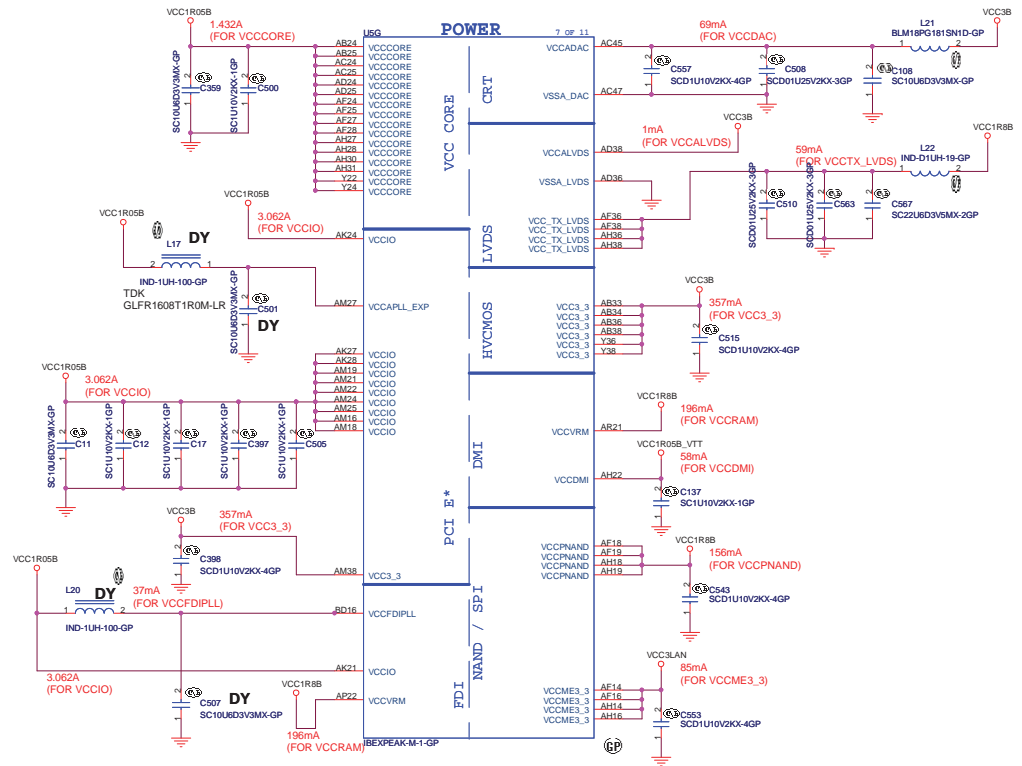
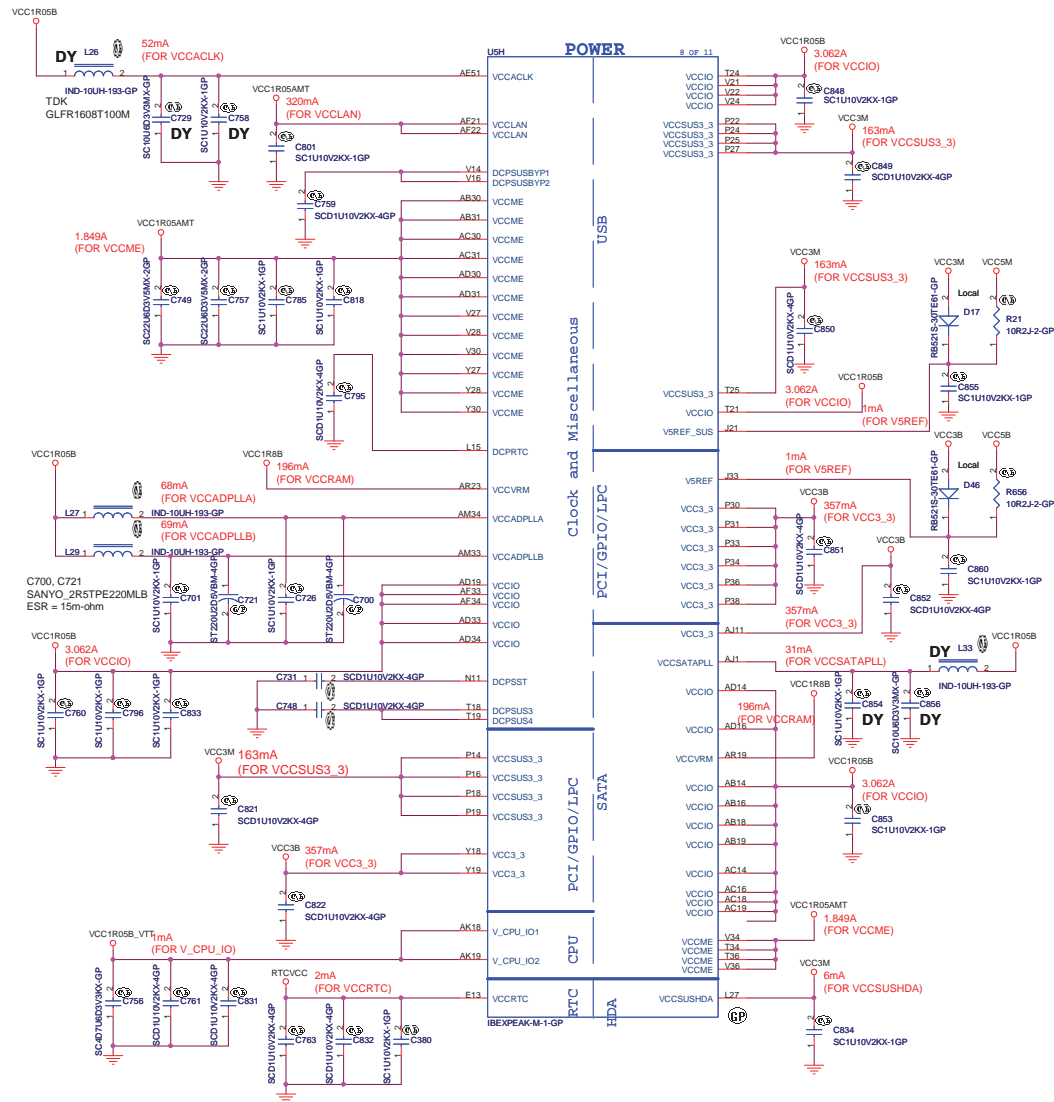
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21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.			
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Size	Document Number	Rev	
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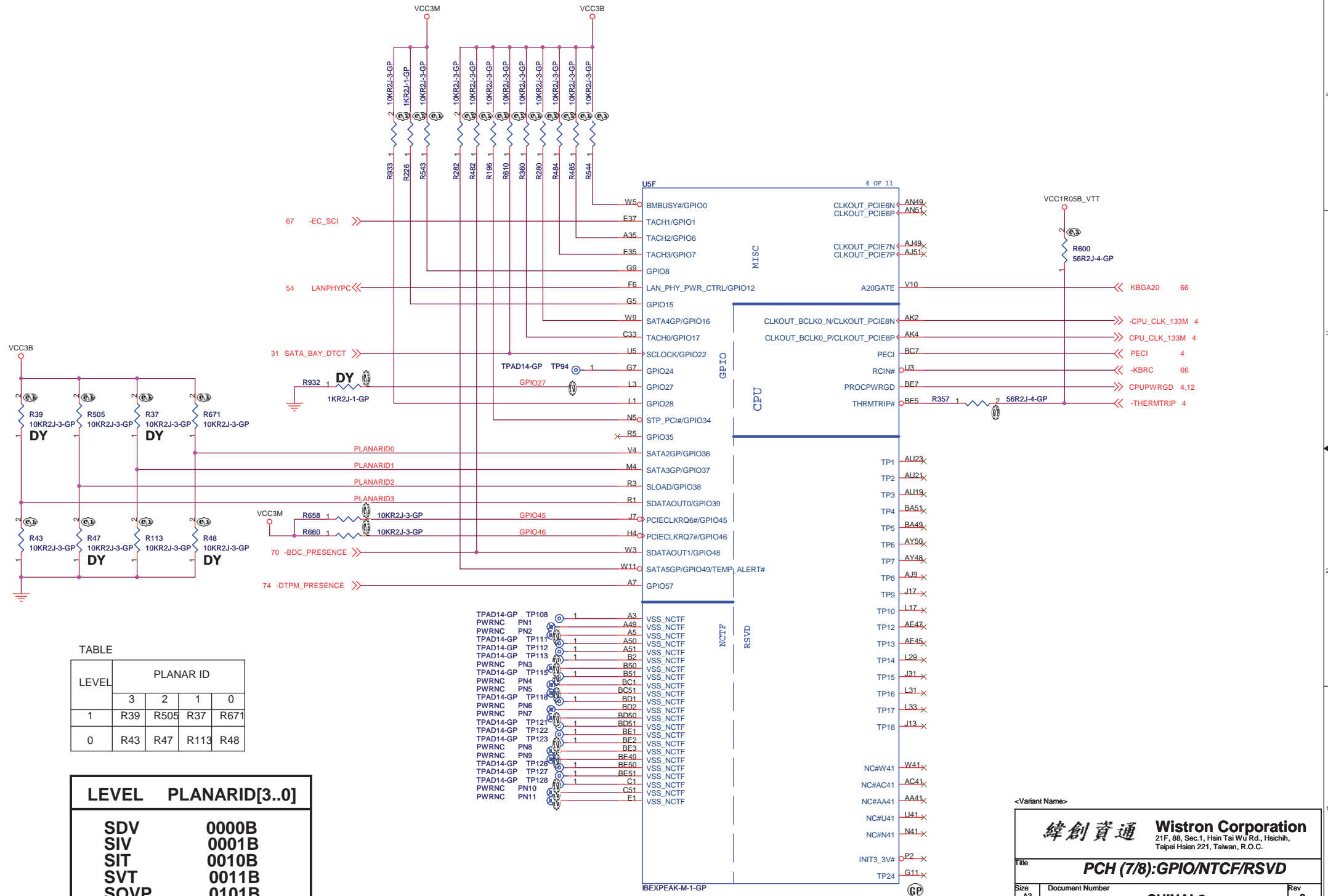


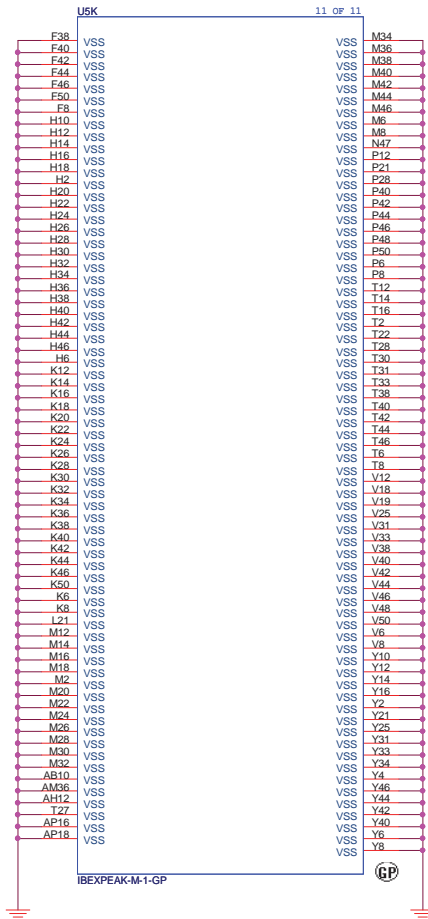
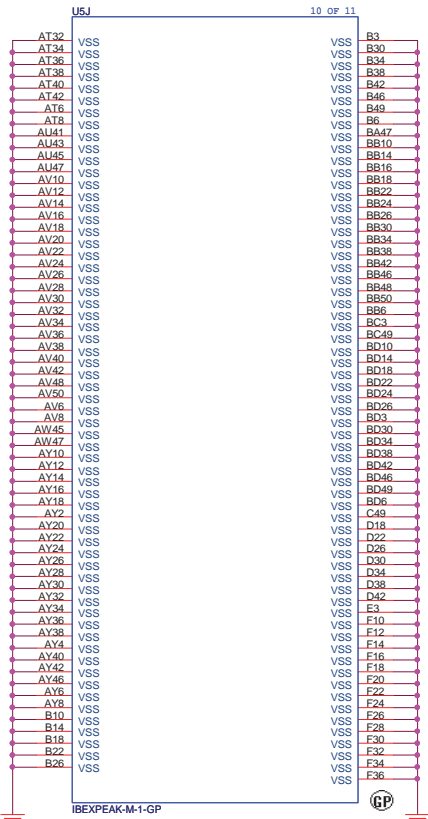
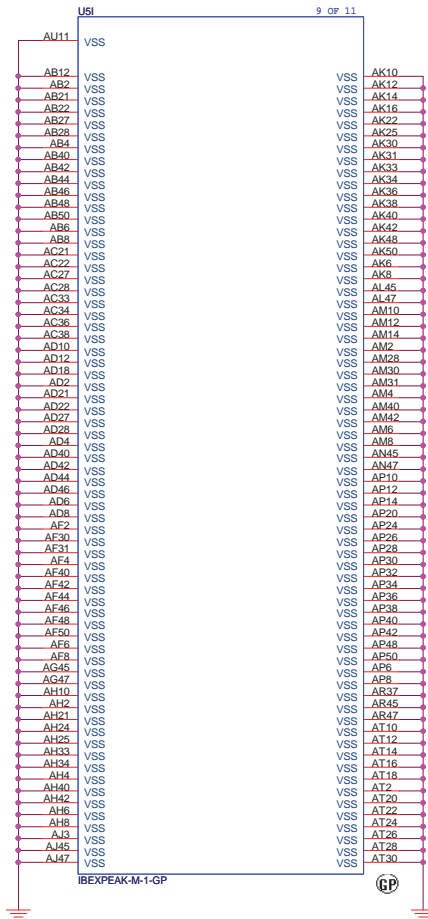
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- USB1 : SYSTEM PORT 2
- USB2 : HALF MINICARD (WLAN)
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- USB5 : EXPRESS CARD SLOT
- USB6 : HALF MINICARD (WUSB)
- USB7 : TOUCH PANEL
- USB8 : SYSTEM PORT 3
- USB9 : SYSTEM PORT 1
- USB10 : FPR (TOUCH PAD)
- USB11:BLUETOOTH (TOUCH PAD)
- USB12:DOCKING
- USB13:USB CAMERA (LCD)



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緯創資通 Wistron Corporation	
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsinchu, Taipei Hsein 221, Taiwan, R.O.C.	
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Size	Document Number
Custom	SHINAI-2
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Rev  
2





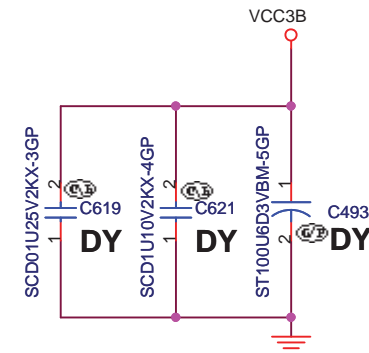
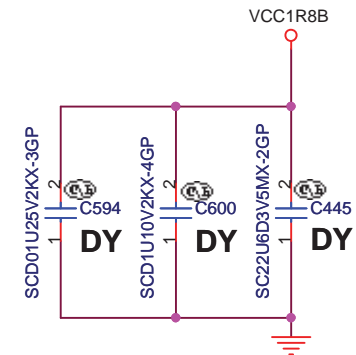
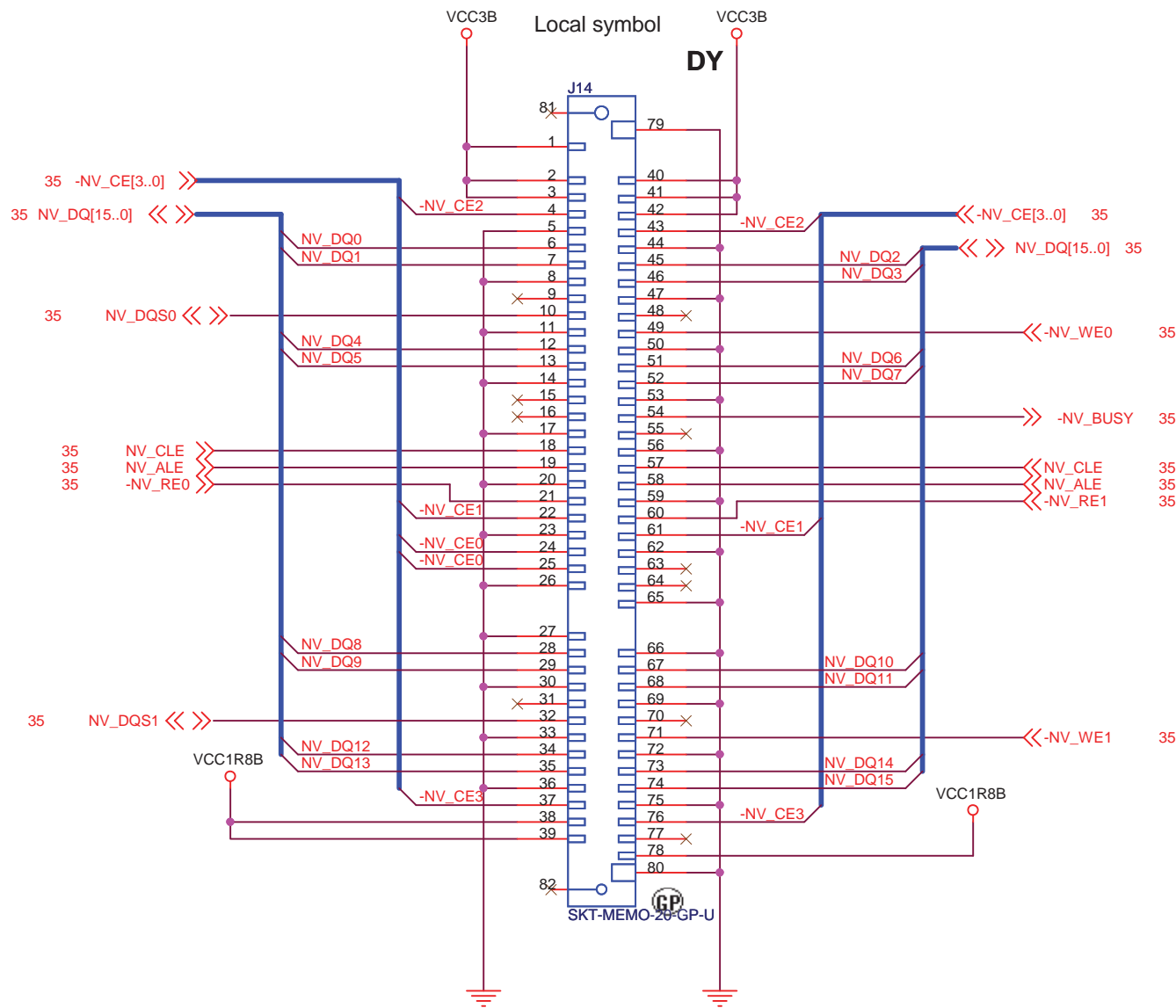
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21F, 88, Sec. 1, Hsin Tai Wu Rd., Hsichih,  
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Title

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Size Document Number Rev  
Custom SHINAI-2 -2

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<Variant Name>

緯創資通

**Wistron Corporation**

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title

**TURBO MEMORY SLOT**

Size  
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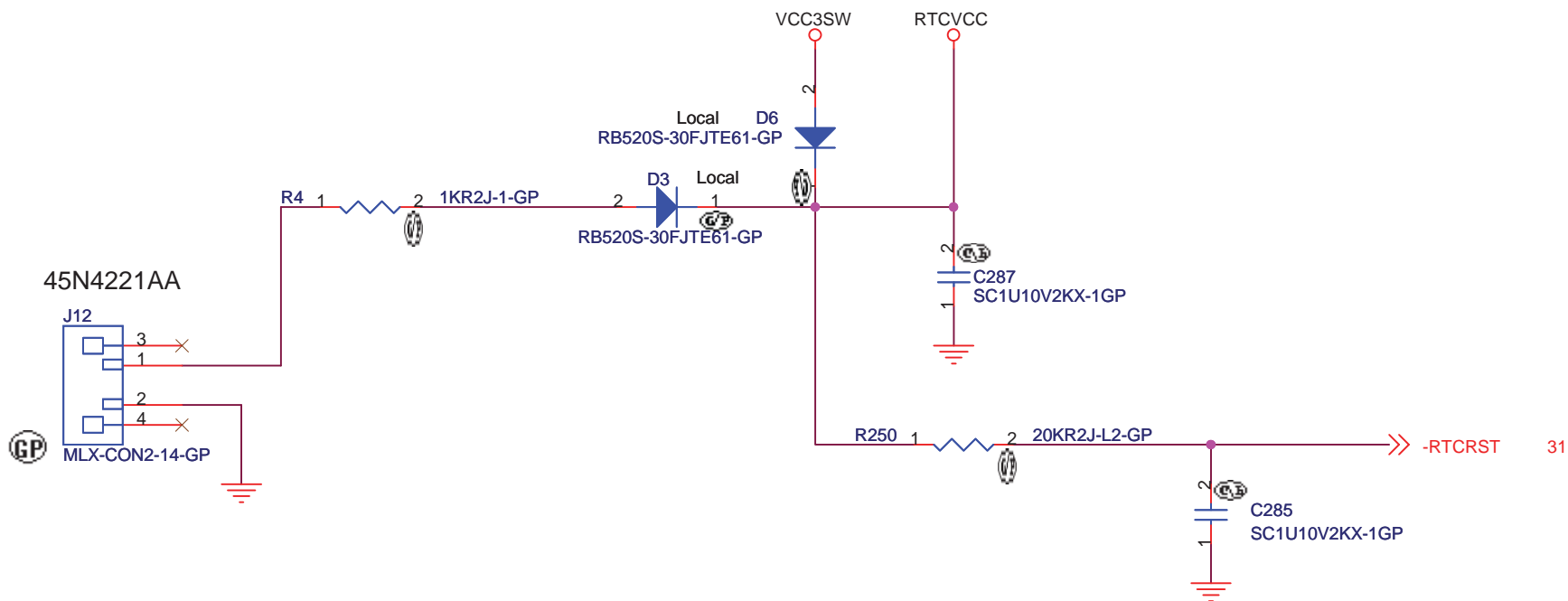
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Rev  
-2

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<Variant Name>

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**Wistron Corporation**

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title

**RTC BATTERY**

Size  
A

Document Number

**SHINAI-2**

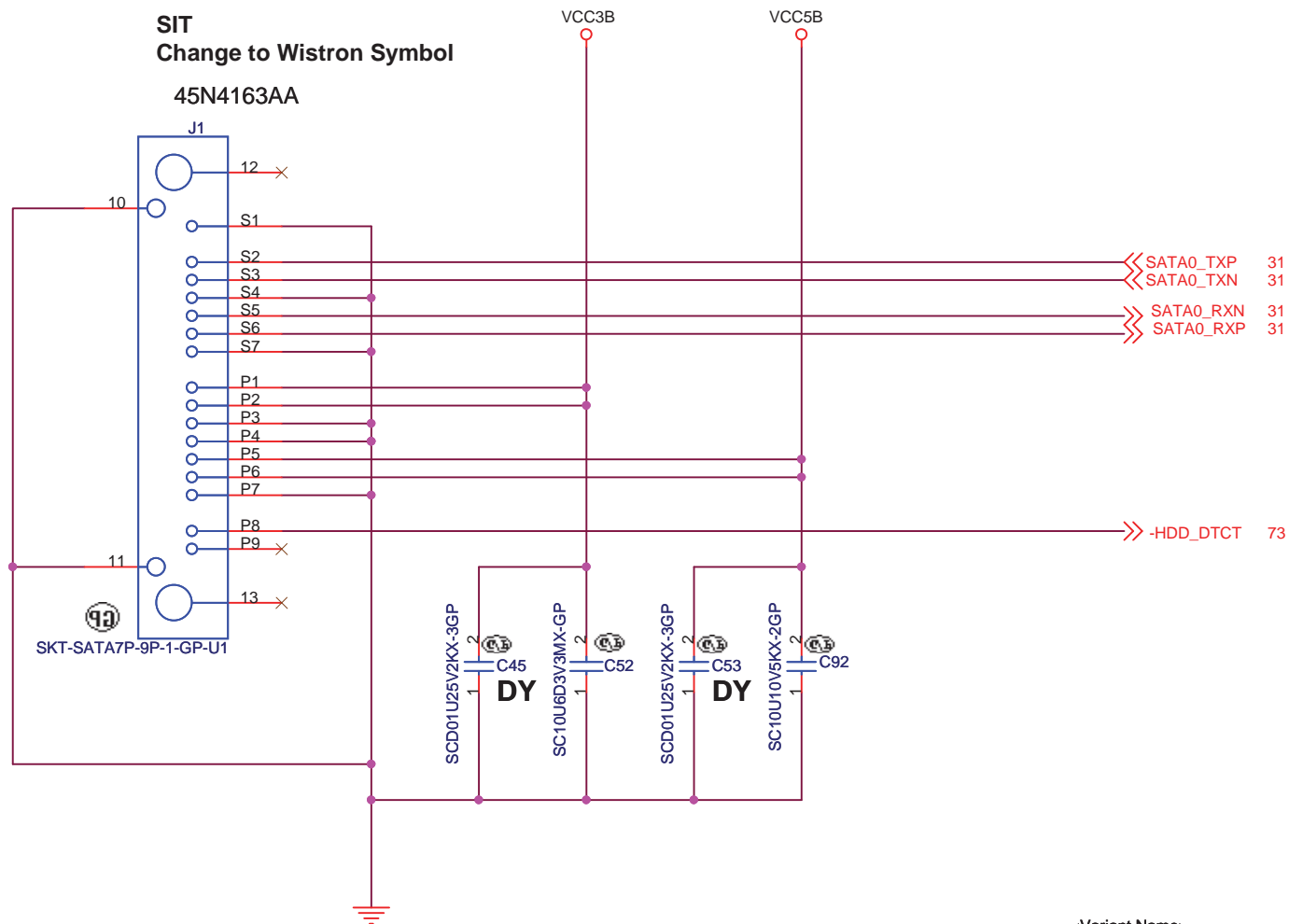
Rev  
-2

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**Wistron Corporation**21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

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Size

A4

Document Number

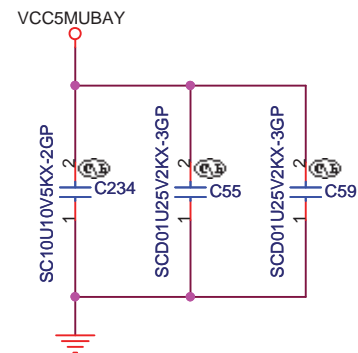
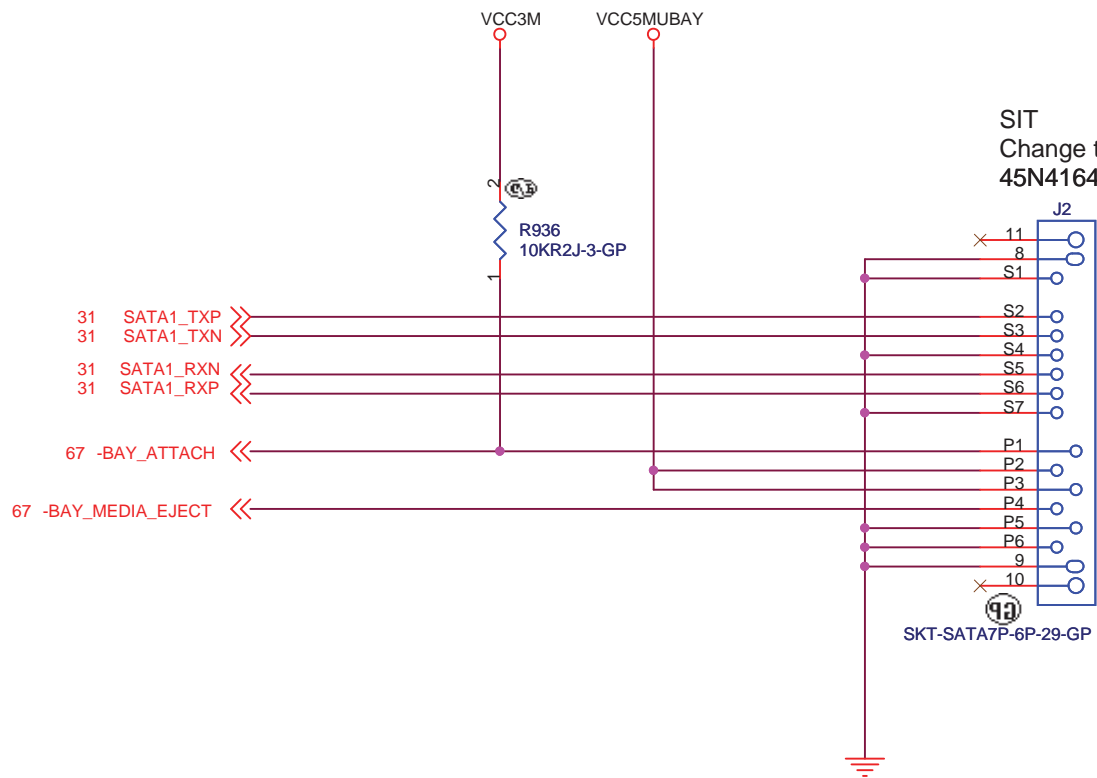
**SHINAI-2**

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**-2**

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<Variant Name>

緯創資通

**Wistron Corporation**

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title

**SATA ODD BAY**

Size

A4

Document Number

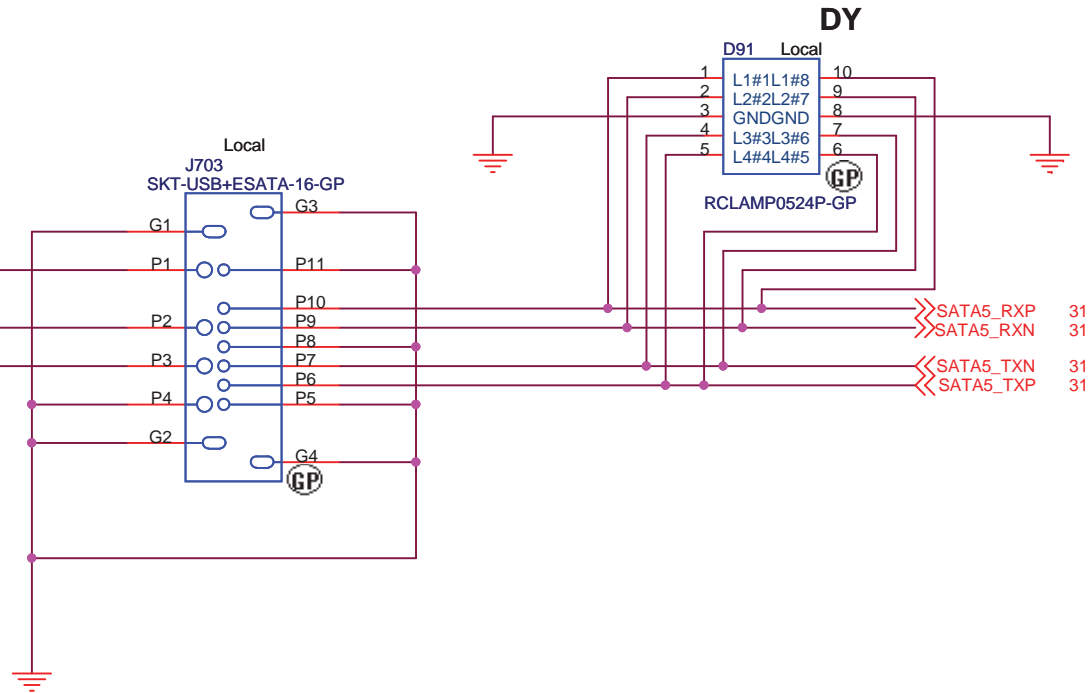
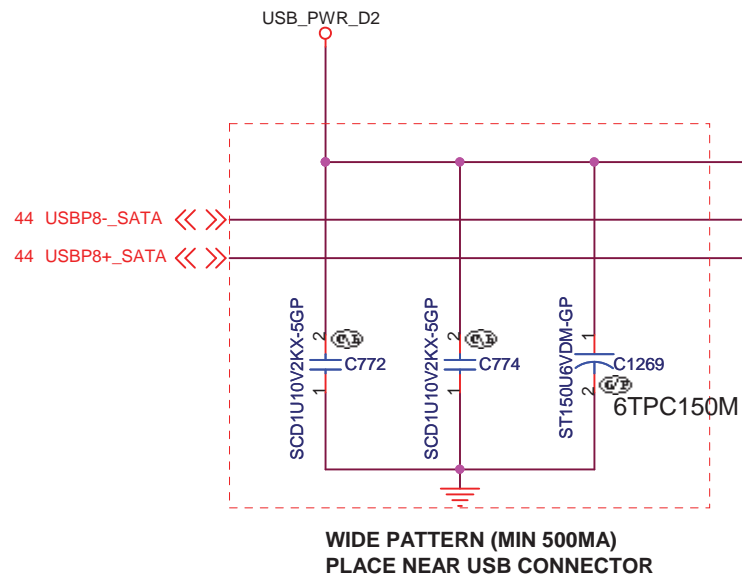
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Rev

**-2**

Date: Monday, December 21, 2009

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緯創資通

**Wistron Corporation**

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title

**ESATA CONNECTOR**

Size Custom Document Number

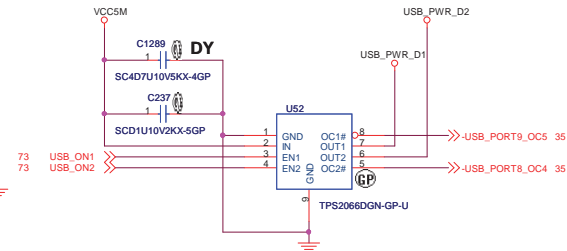
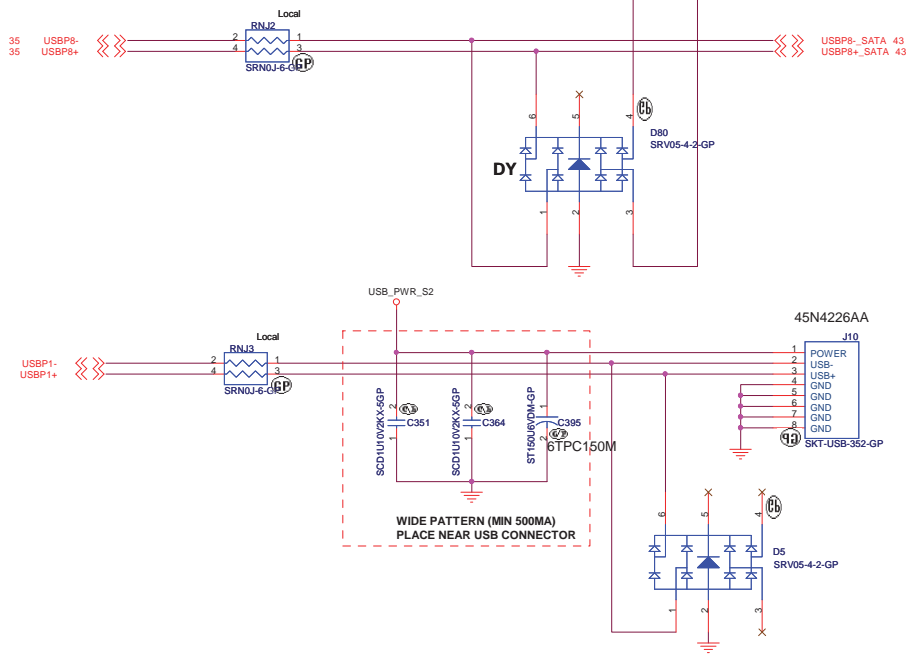
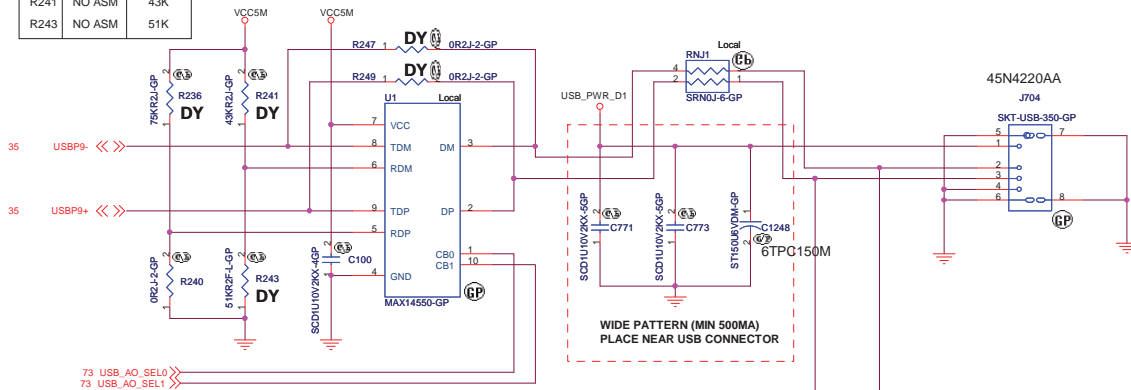
**SHINAI-2**

Rev -2

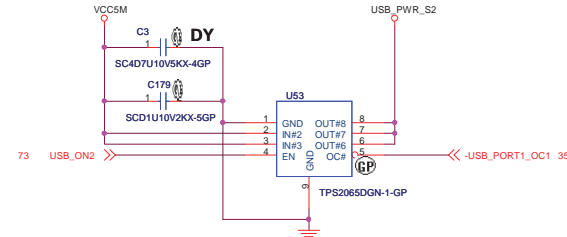
Date: Monday, December 21, 2009

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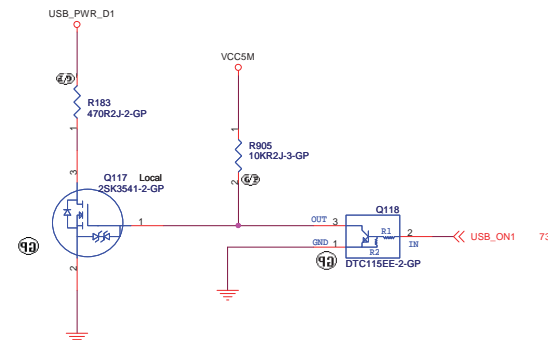
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R240	0	51K
R241	NO ASM	43K
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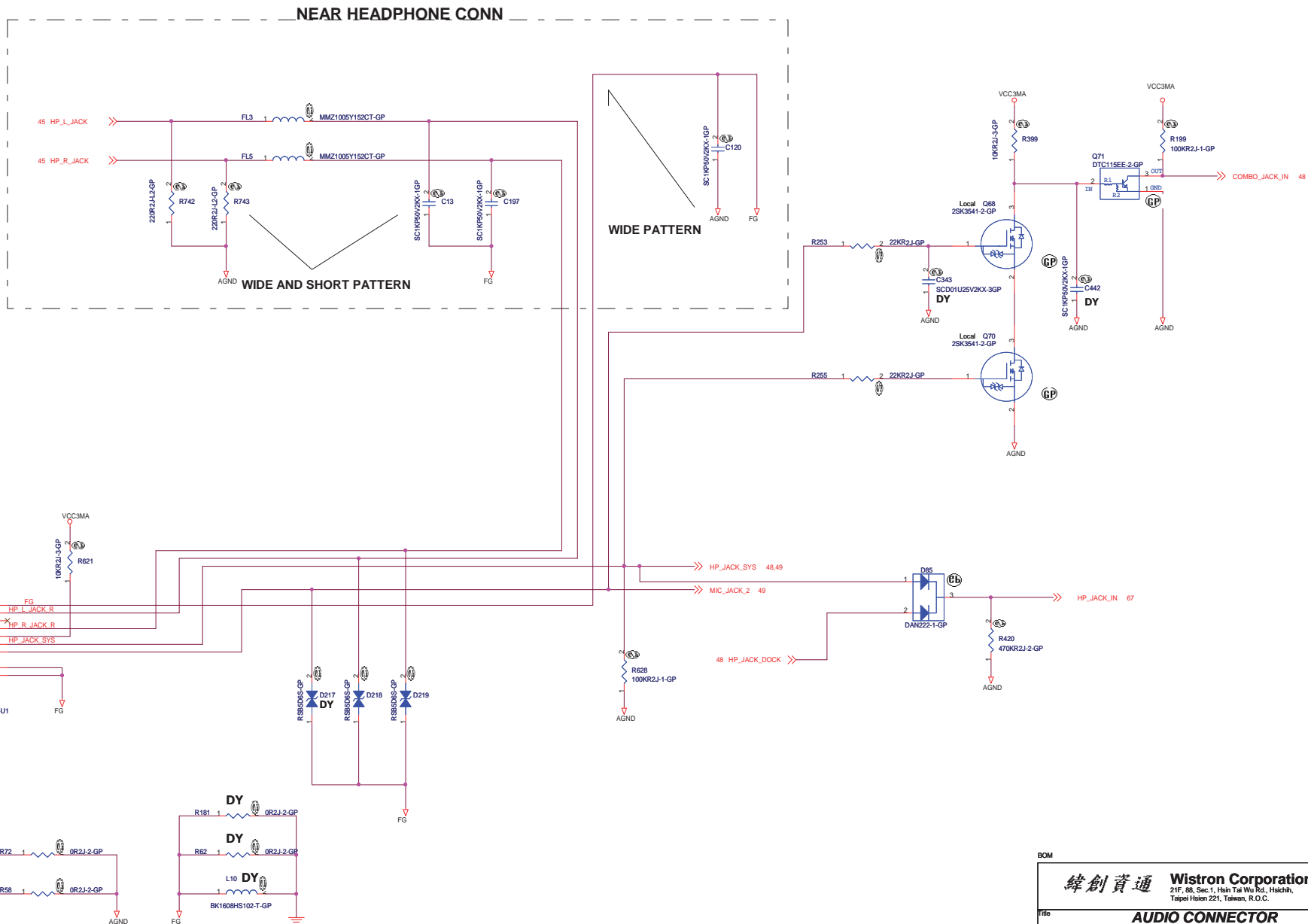
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Size	Document Number	SHINAI-2	Rev -2
Date:	Monday, December 21, 2009	Sheet 44 of	101



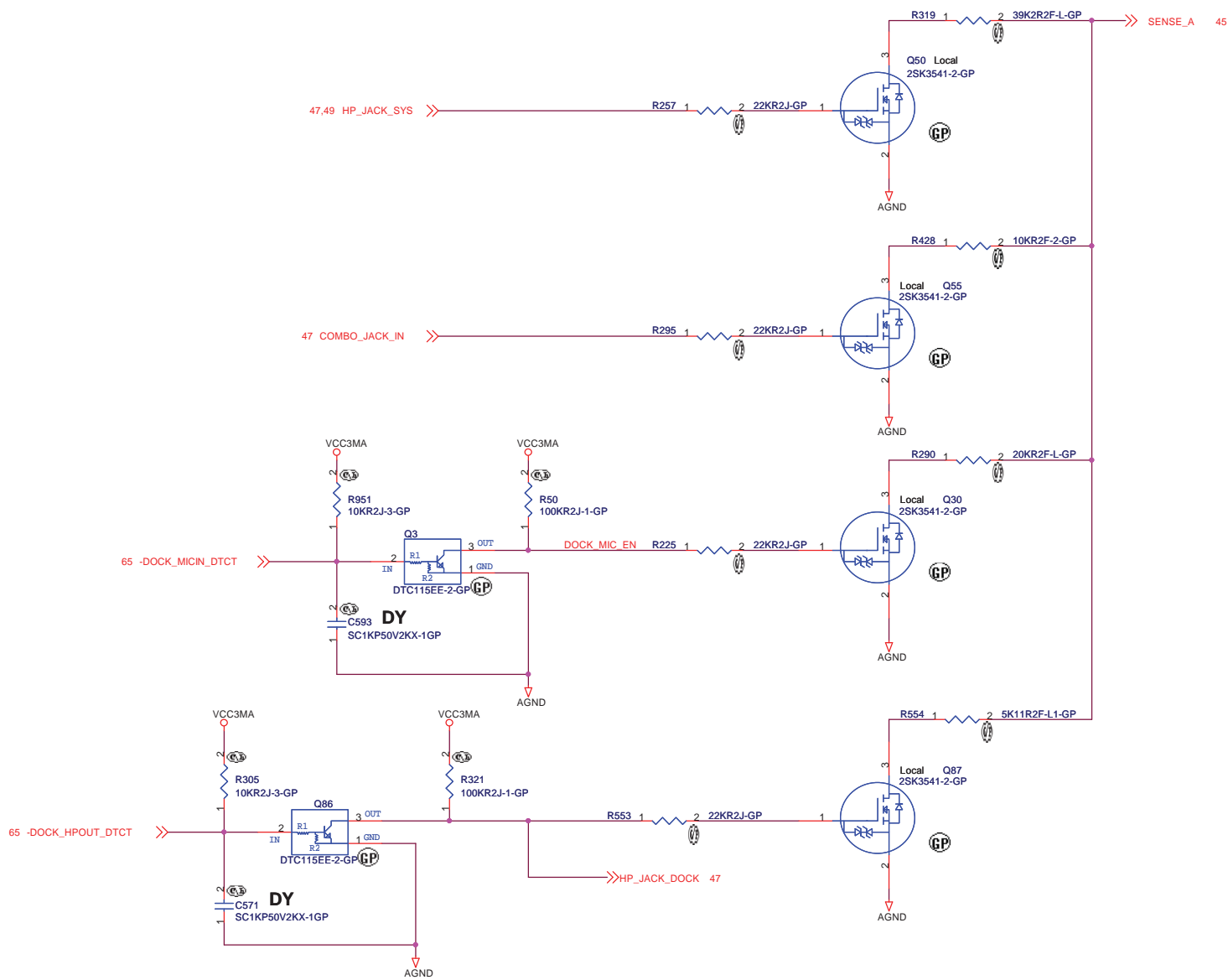
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A4	SHINAI-2		-2
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File	Document Number	Rev	
Size	C	SHINAI-2	-2
Date: Monday, December 21, 2009 Sheet 47 of 101			



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<b>緯創資通</b>		<b>Wistron Corporation</b> 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
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Size A3	Document Number <b>SHINAI-2</b>		Rev <b>-2</b>
Date: Monday, December 21, 2009		Sheet 48 of 101	





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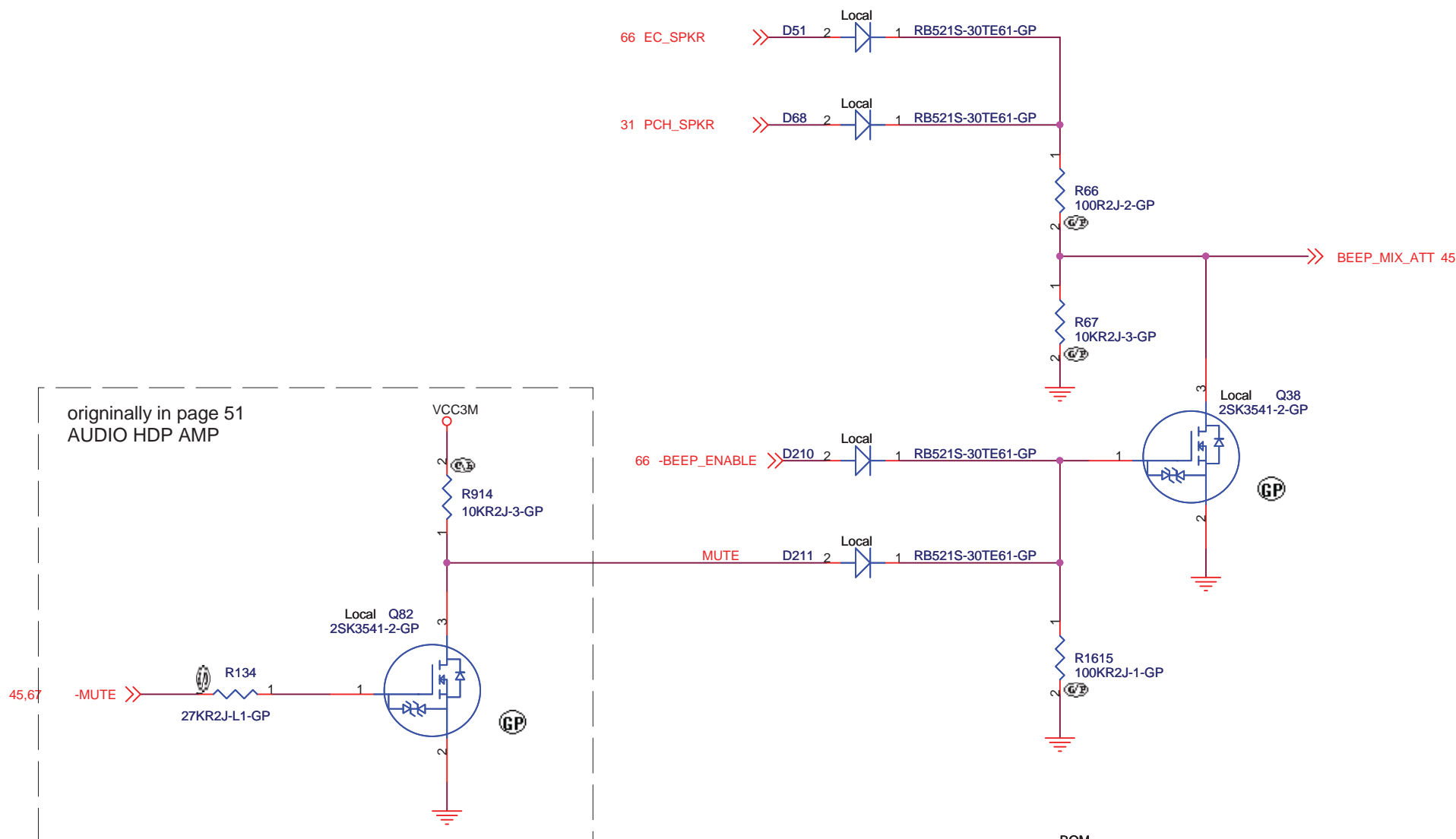
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<b>SHINAI-2</b>			
Date:	Monday, December 21, 2009	Sheet	50 of 101

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Date: Monday, December 21, 2009		Sheet 51 of	101



BOM

緯創資通

**Wistron Corporation**

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title

**AUDIO BEEP**

Size

A4

Document Number

**SHINAI-2**

Rev

-2

Date: Monday, December 21, 2009

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D

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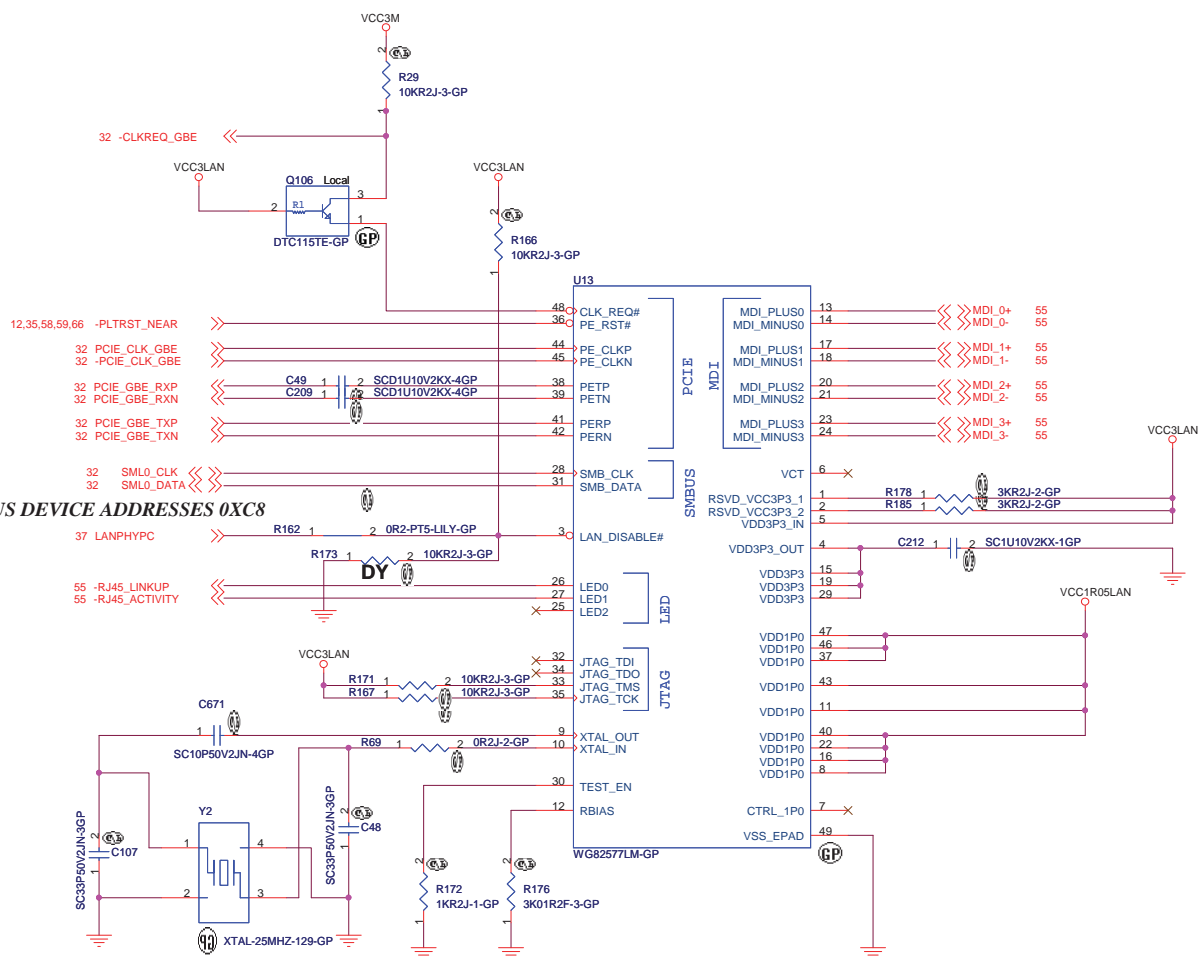
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Date: Monday, December 21, 2009	Sheet 53 of	101

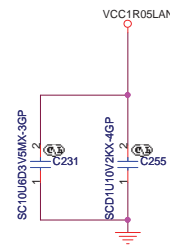
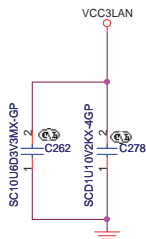
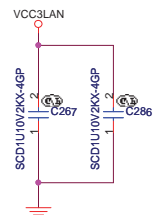


**SMBUS DEVICE ADDRESSES 0XC8**

KDS 25MHz  
18P 30PPM  
DSX321G

**NOTE: VCC1R05LAN WILL WORK AT 0.95V TO 1.15V**

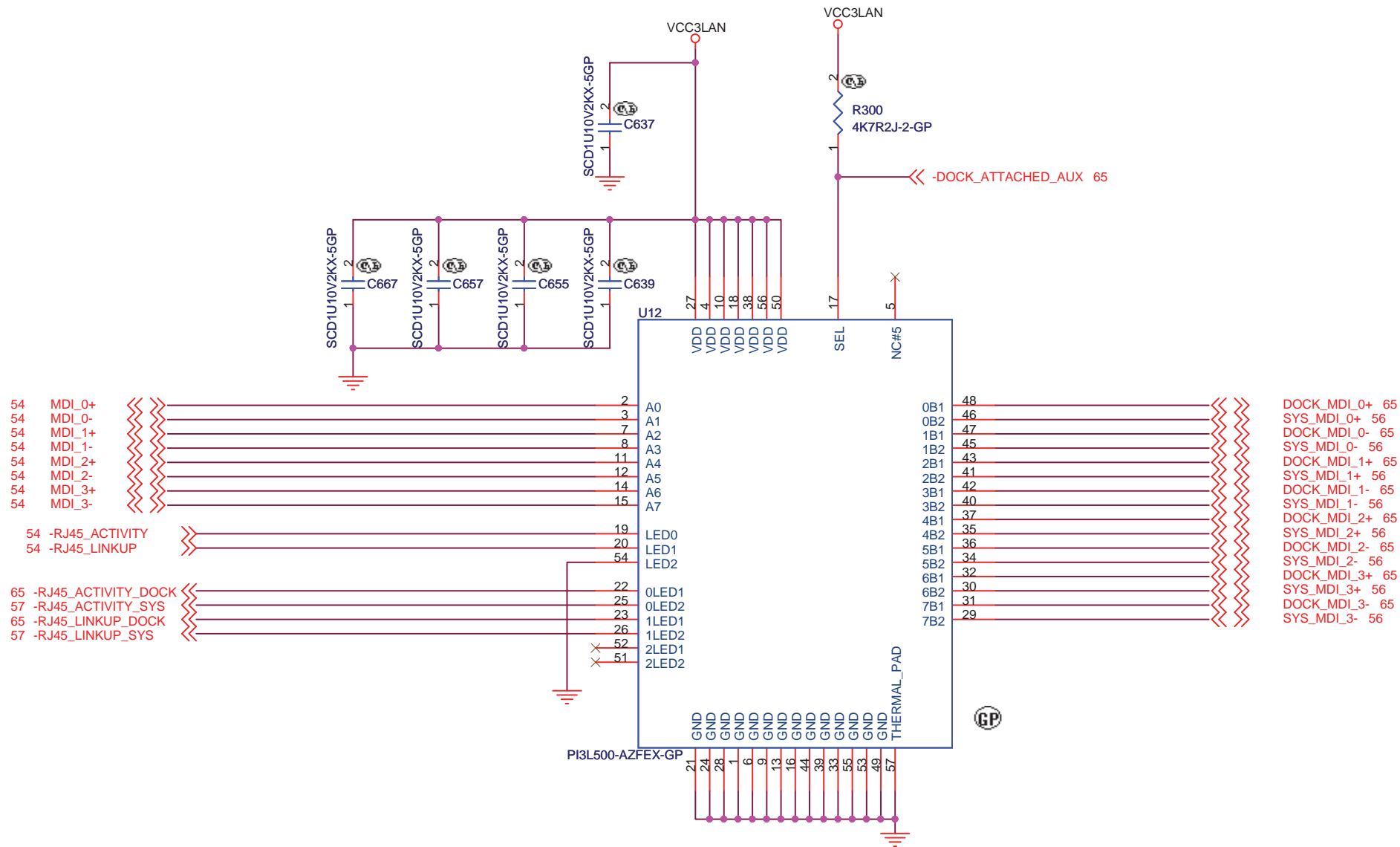
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2	H.ELE HXS321S 25M 18P 30PPM	82.30020.B21
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**DEFAULT SOLUTION: SHARDED WITH PCH VCC1R05LAN SVR**

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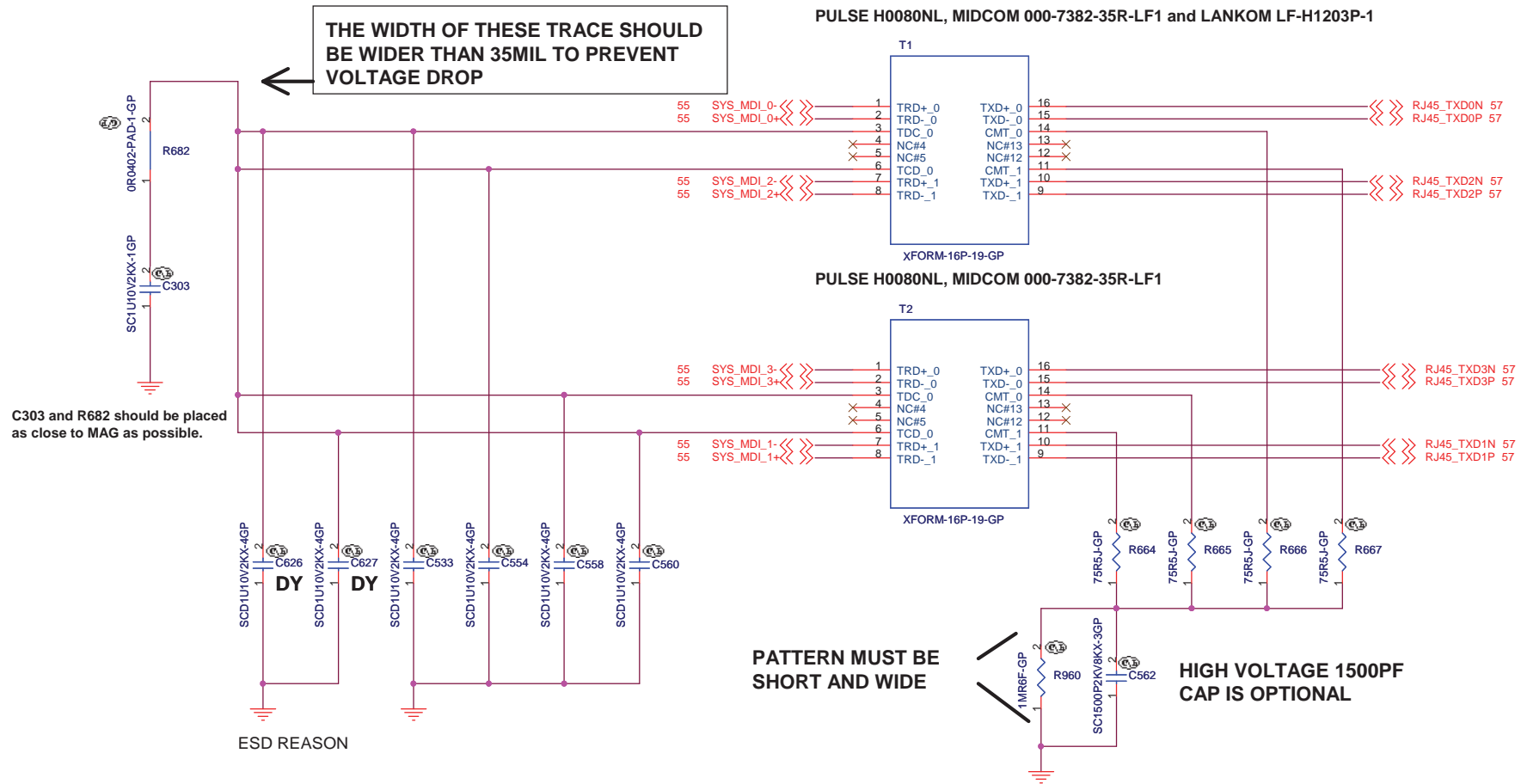
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<b>Title GBE HANKSVILLE</b>	
Size Custom	Document Number SHINAI-2
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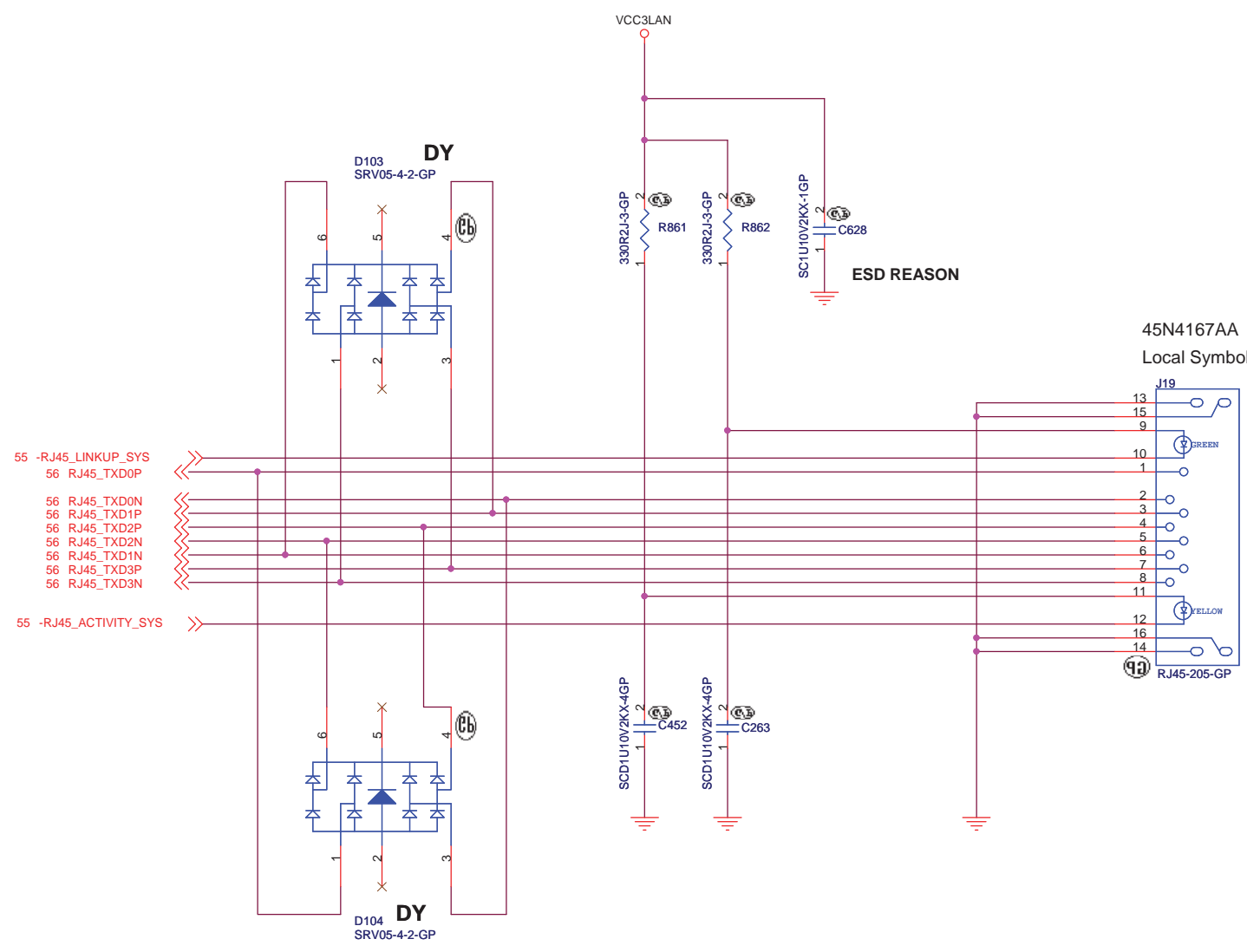
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Title			
GBE LAN SW			
Size A4	Document Number  SHINAI-2		Rev -2
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緯創資通 Wistron Corporation  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title			
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Size	Document Number	Rev	
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Date:	Monday, December 21, 2009	Sheet	56 of 101

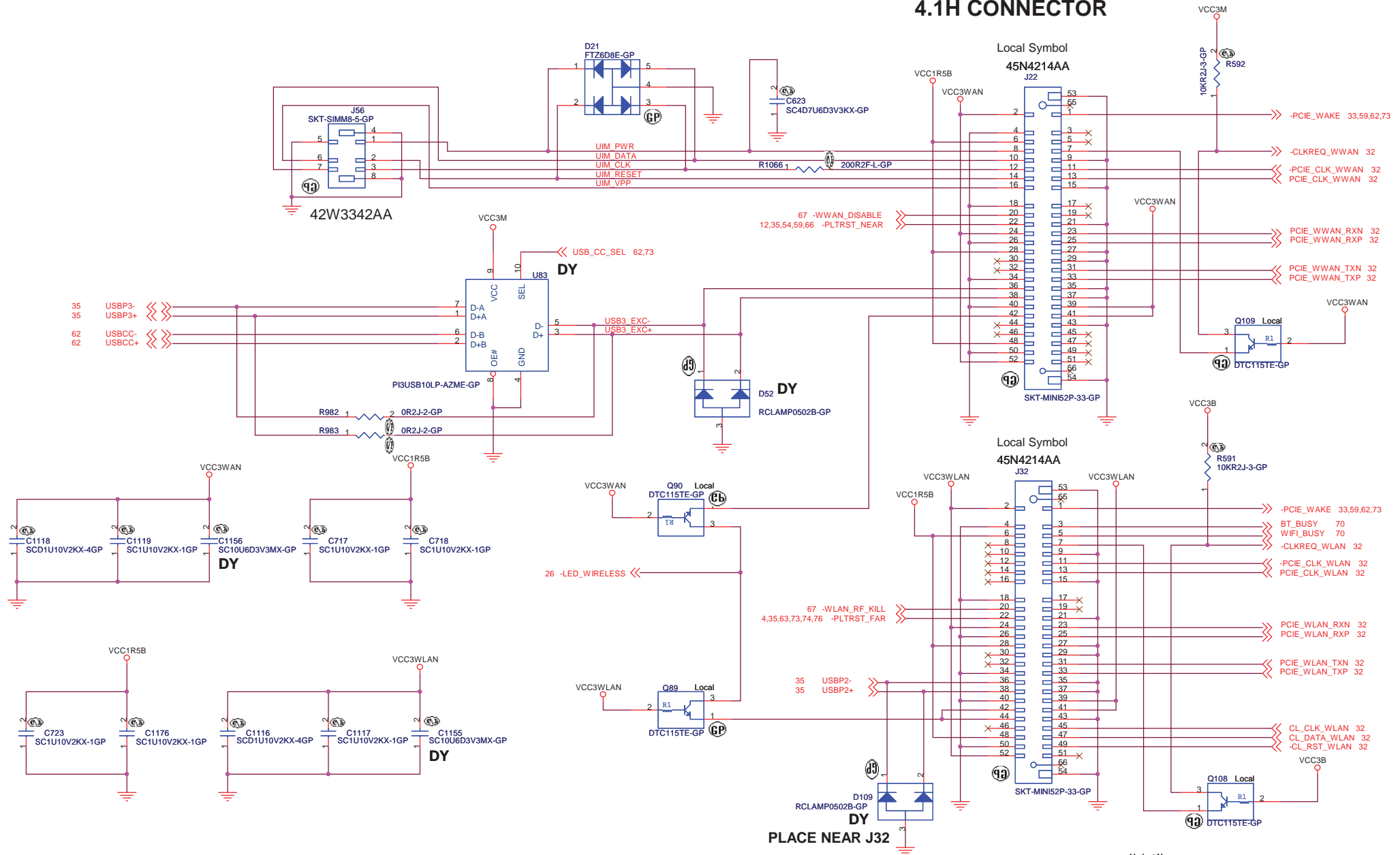




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Title RJ45 CONN		
Size A4	Document Number SHINAI-2	Rev -2
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# FULL MINI CARD FOR WWAN

## 4.1H CONNECTOR



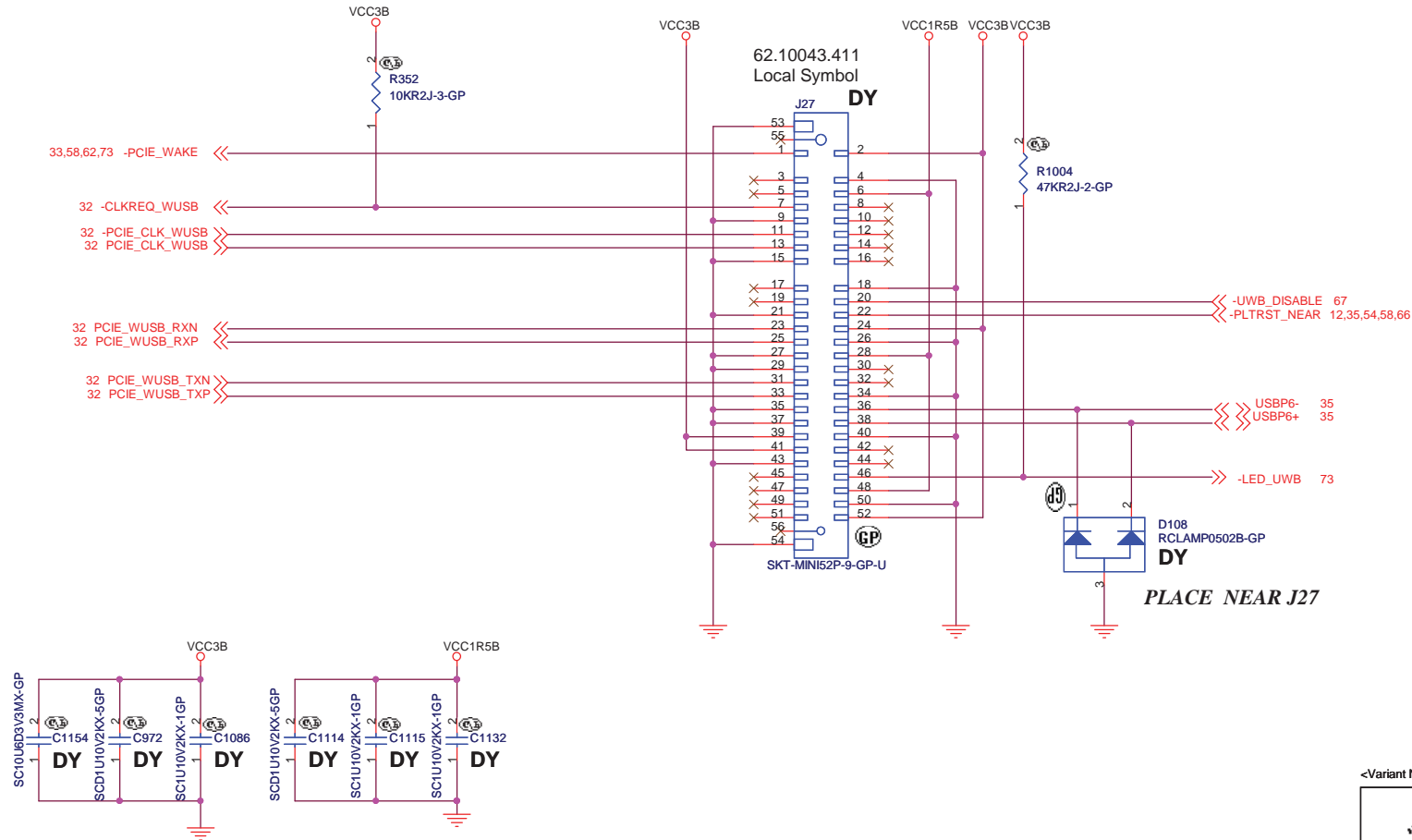
# HALF MINI CARD FOR WLAN

## 7.0H CONNECTOR

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<div> <div>MINI CARD SLOT 1</div> <div>Size A3 Document Number</div> <div>SHINAI-2</div> <div>Date: Monday, December 21, 2009</div> <div>Sheet 58 of 101</div> <div>Rev -2</div> </div>	

# HALF MINI CARD FOR WUSB

## 7.0H CONNECTOR



<Variant Name>

緯創資通

**Wistron Corporation**  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title

**MINI CARD SLOT 2**

Size  
B

Document Number

**SHINAI-2**

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-2

Date: Monday, December 21, 2009

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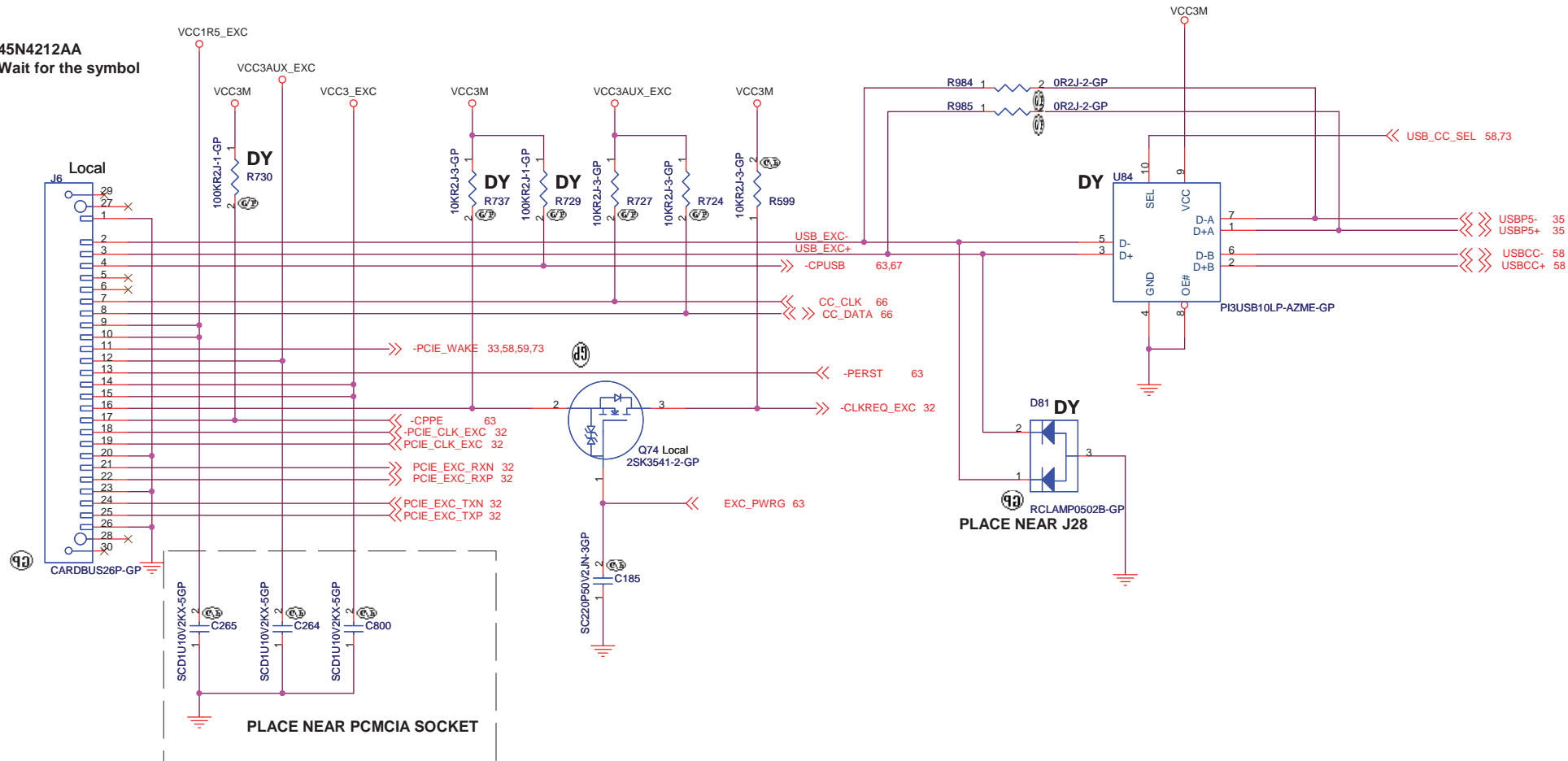
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Date:	Monday, December 21, 2009	Sheet	61 of 101

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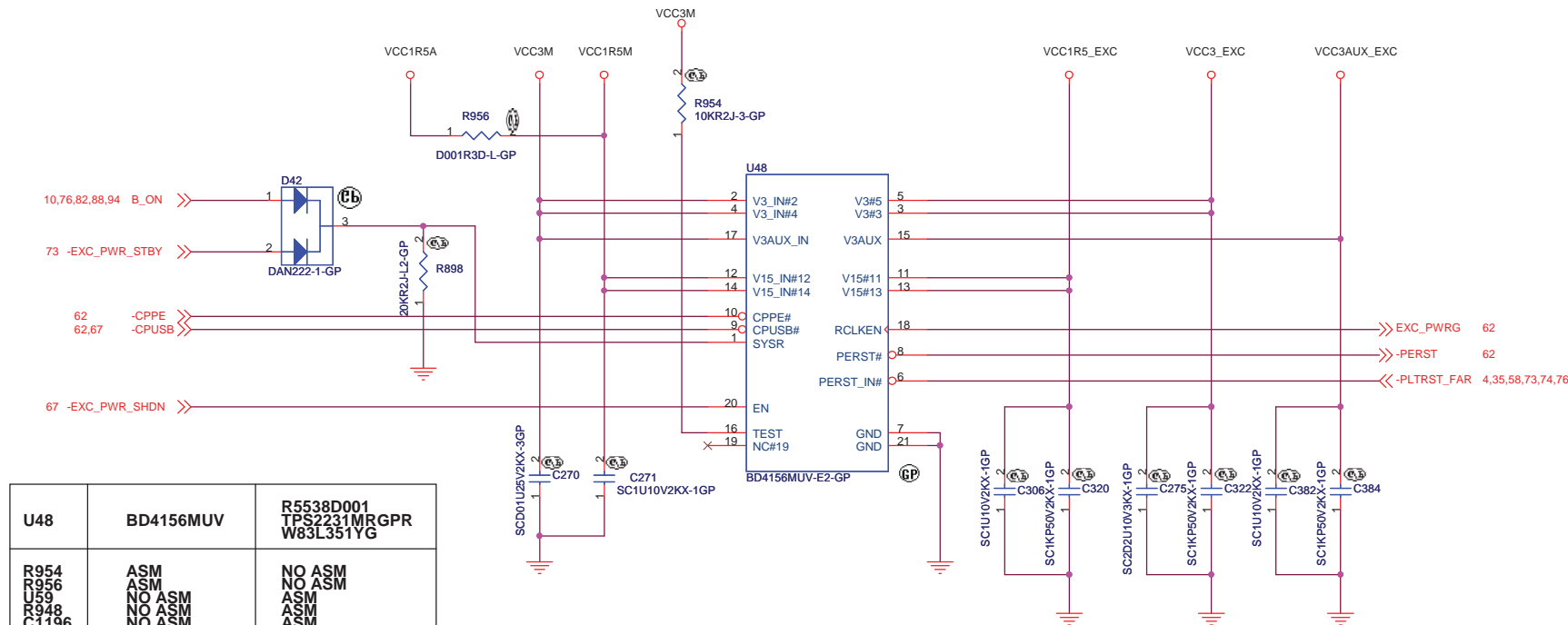
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**Wistron Corporation**  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title	<b><i>Express Card/SMART CARD IF</i></b>
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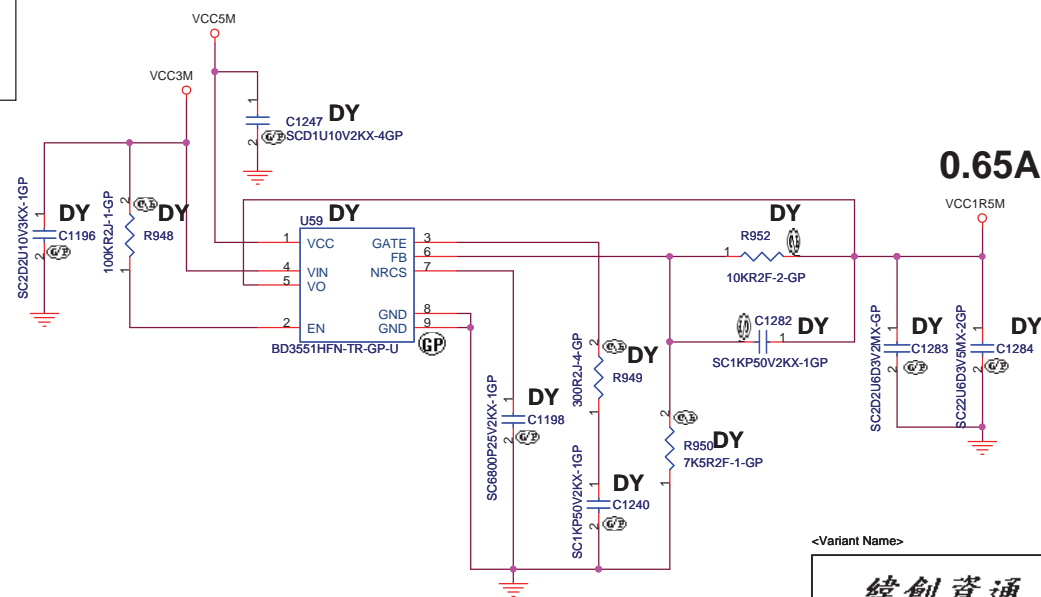
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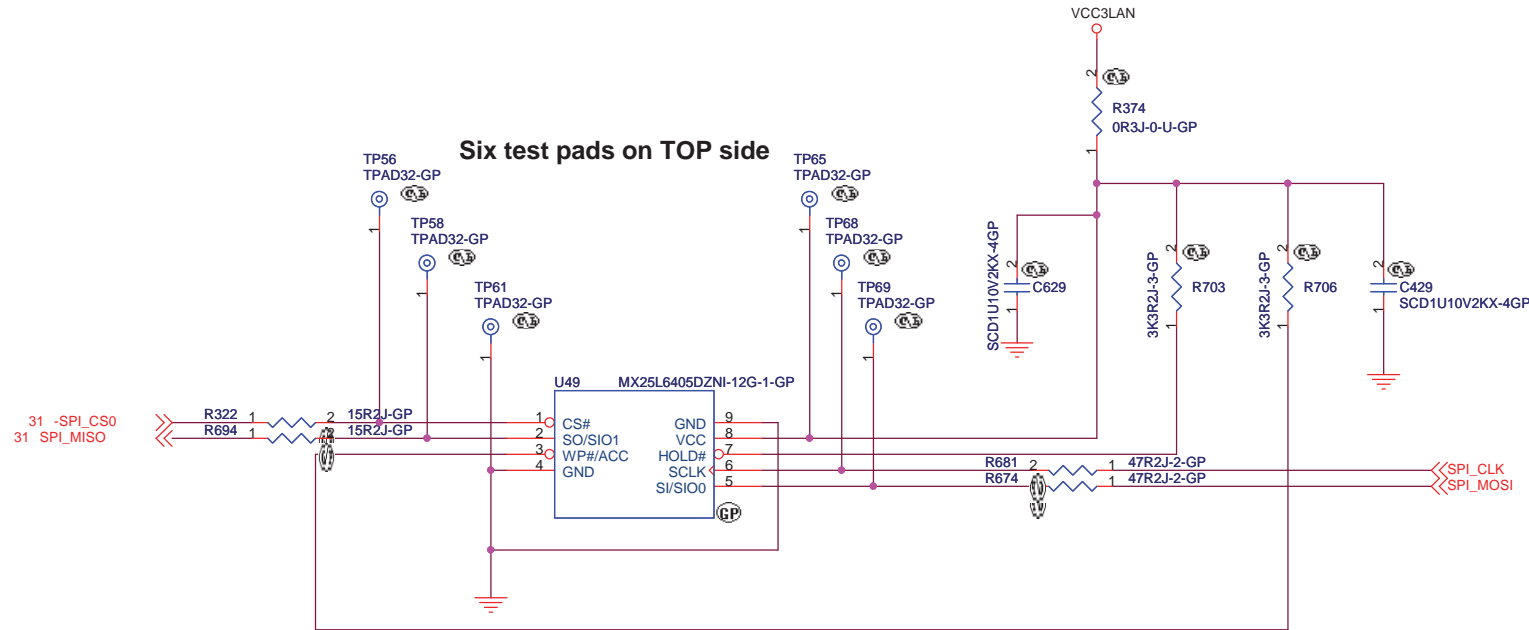
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U59	NO ASM	ASM
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C1196	NO ASM	ASM
C1247	NO ASM	ASM
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R952	NO ASM	ASM
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LOGIC



<Variant Name>

<b>緯創資通 Wistron Corporation</b> 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		
<b>Title</b> <b>SLOT POWER CONTROL</b>		
<b>Size</b> Custom	<b>Document Number</b> <b>SHINAI-2</b>	<b>Rev</b> <b>-2</b>
<b>Date:</b> Monday, December 21, 2009 <b>Sheet</b> 63 <b>of</b> 101		



**8MB WSON8**  
**MACRONIX MX25L6405DZNI-12G**  
**WINBOND W25X64BVZEIG**  
**NUMONYX M25PX64-VMD6TG**

**PHYSICAL PAD DESIGN TO SUPPORT  
 BOTH WSONB AND SOIC8**

**8MB SOIC8**  
**MACRONIX MX25L6445EM2I-10G**  
**WINBOND W25X64BVSSIG**

**TABLE**

	ICP	ENABLE
R374	RB520S-	30
R322	470	5%
R681	470	5%
R674	470	5%
R694	470	5%

**TABLE**

SF100 PIN HEADER IN TERFACE (TOP VIEW)					
1 VCC	R374.1	GND	GND	2	
3 CS#	R322.2	R681.2	CLK	4	
5 MISO	R694.2	R674.2	MOSI	6	
7 (KEY)	N/A	N/A	(RESET)	8	

**TABLE**

8MB WSON8 PIN	SIGNAL	8MB SOP16 PIN
1	-CS	7
2	SO	8
3	-WP	9
4	GND	10
5	SI	15
6	SCLK	16
7	-HOLD	1
8	VCC	2

<Variant Name>

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**Wistron Corporation**

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Title

**SPI FLASH**

Size  
 A4

Document Number

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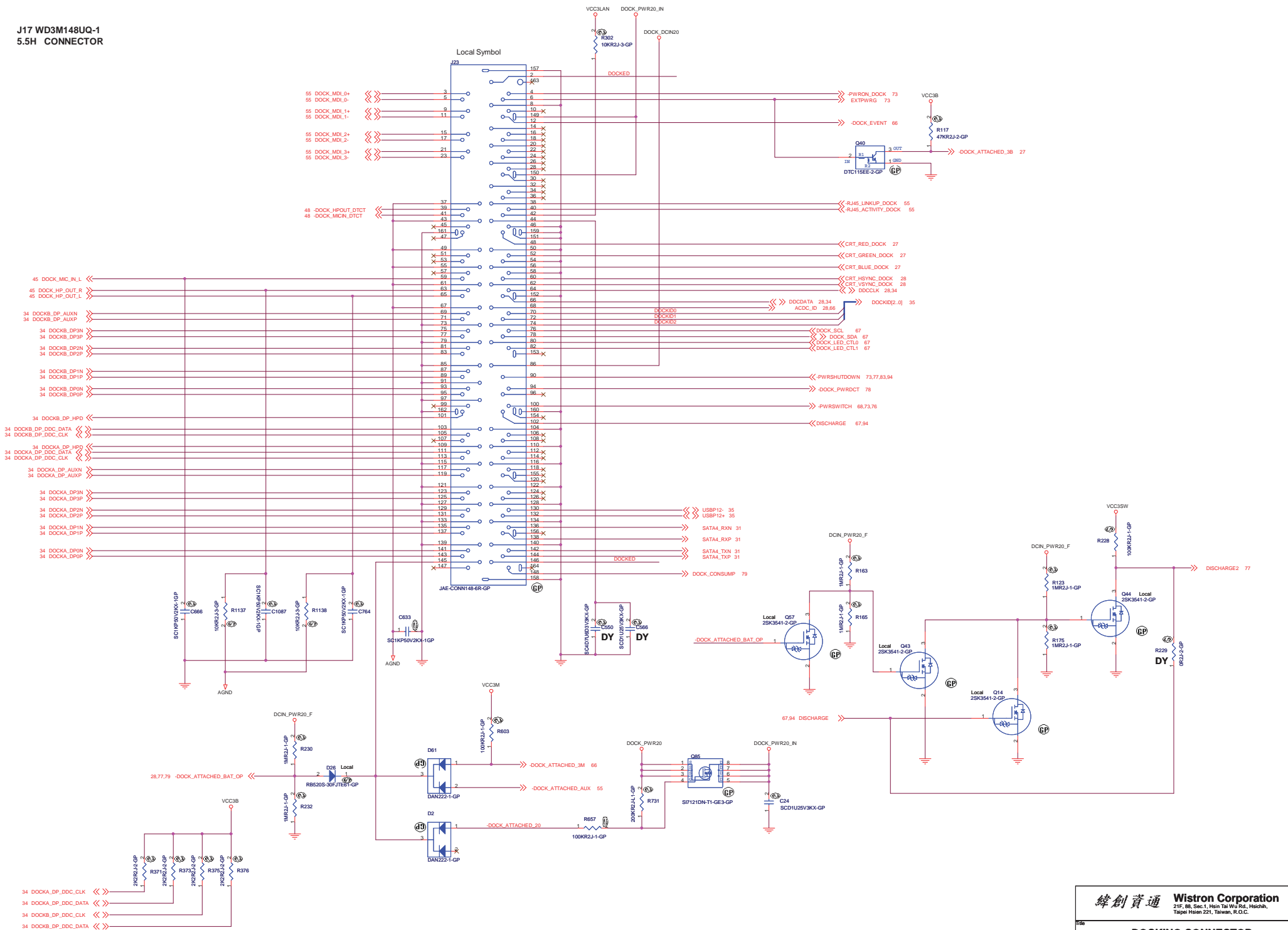
Rev  
 -2

Date: Monday, December 21, 2009

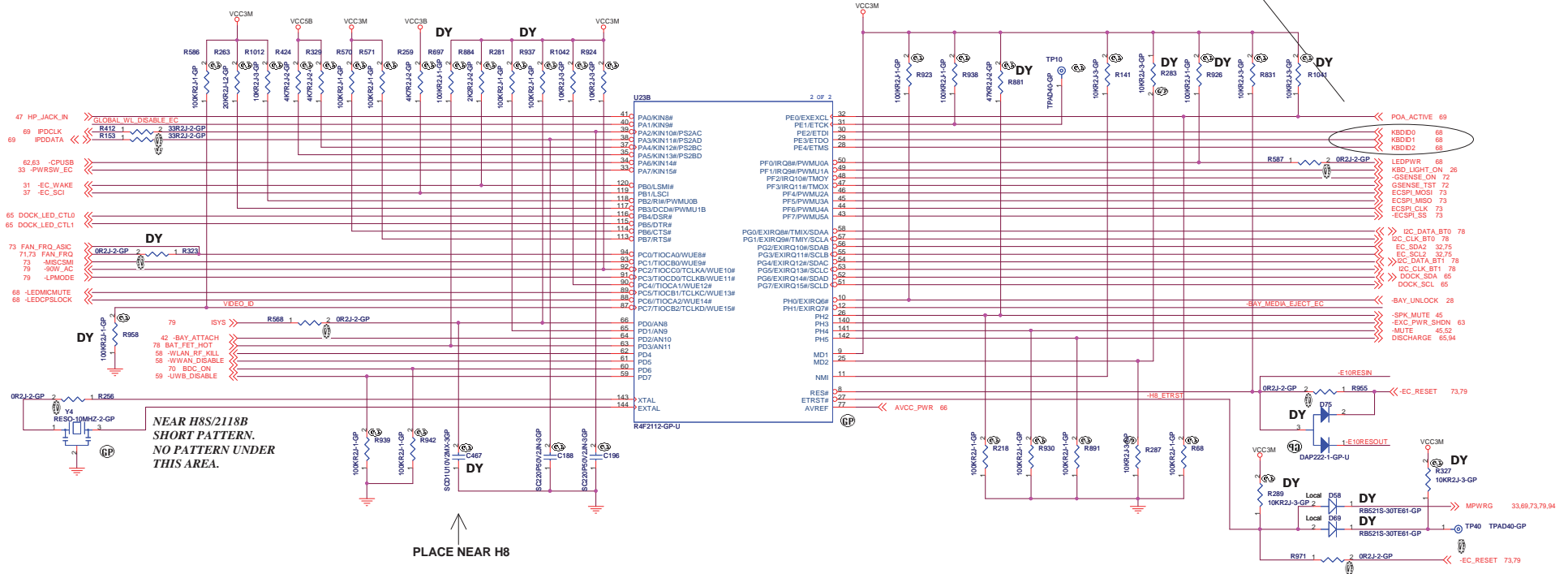
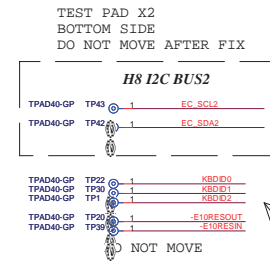
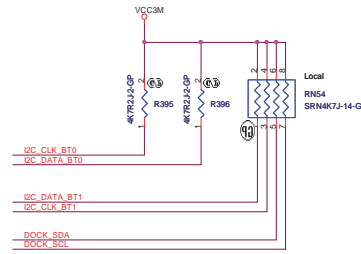
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**J17 WD3M148UQ-1  
5.5H CONNECTOR**



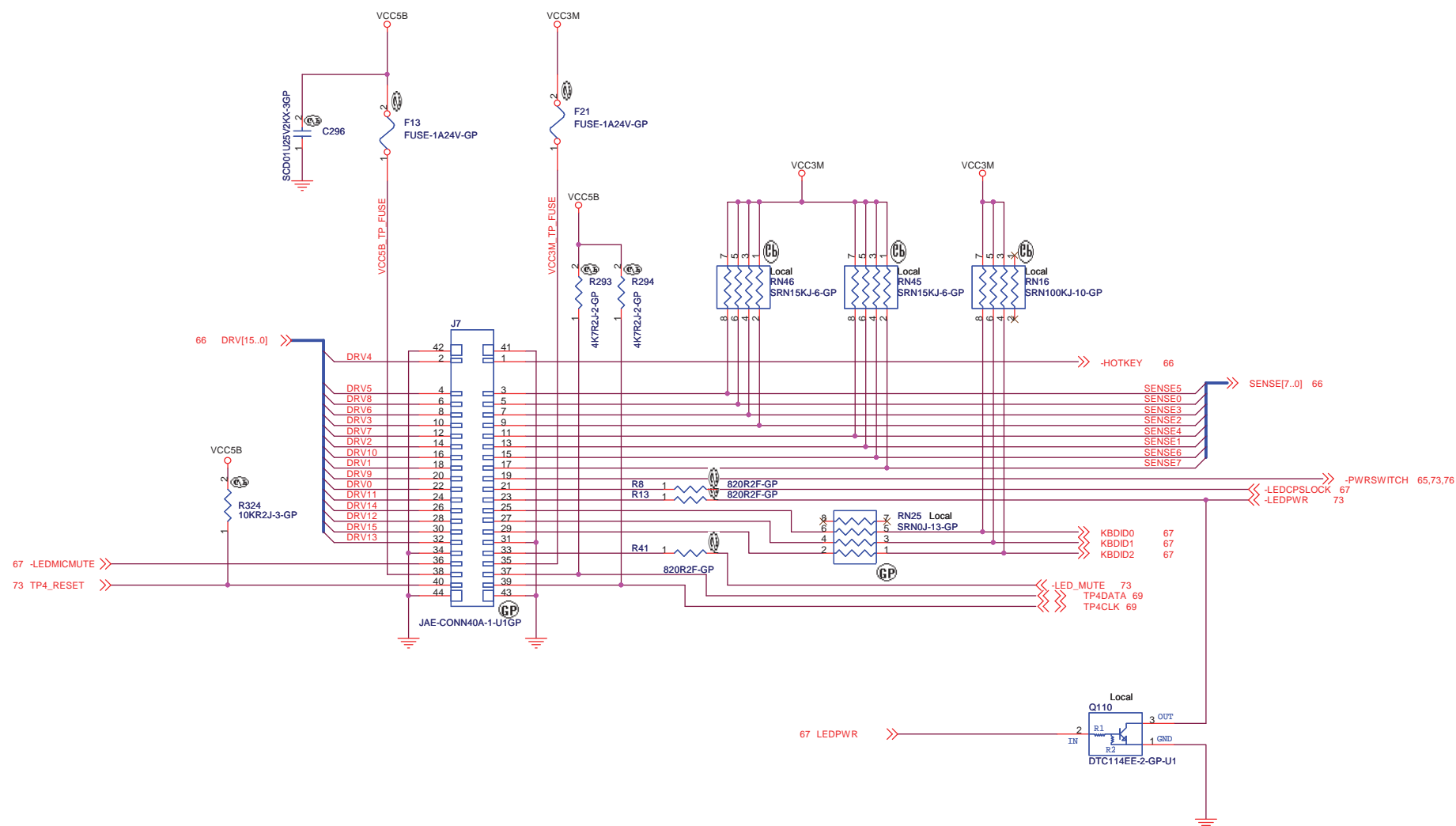




NEAR H8S/2118B  
SHORT PATTERN.  
NO PATTERN UNDER  
THIS AREA.

PLACE NEAR H8

## Keyboard Connector



<Variant Name>

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Title
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**KEYBOARD CONN**Size  
A3

Document Number	
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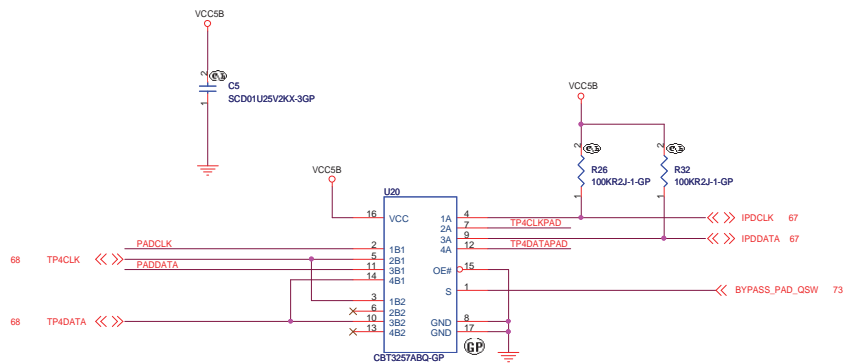
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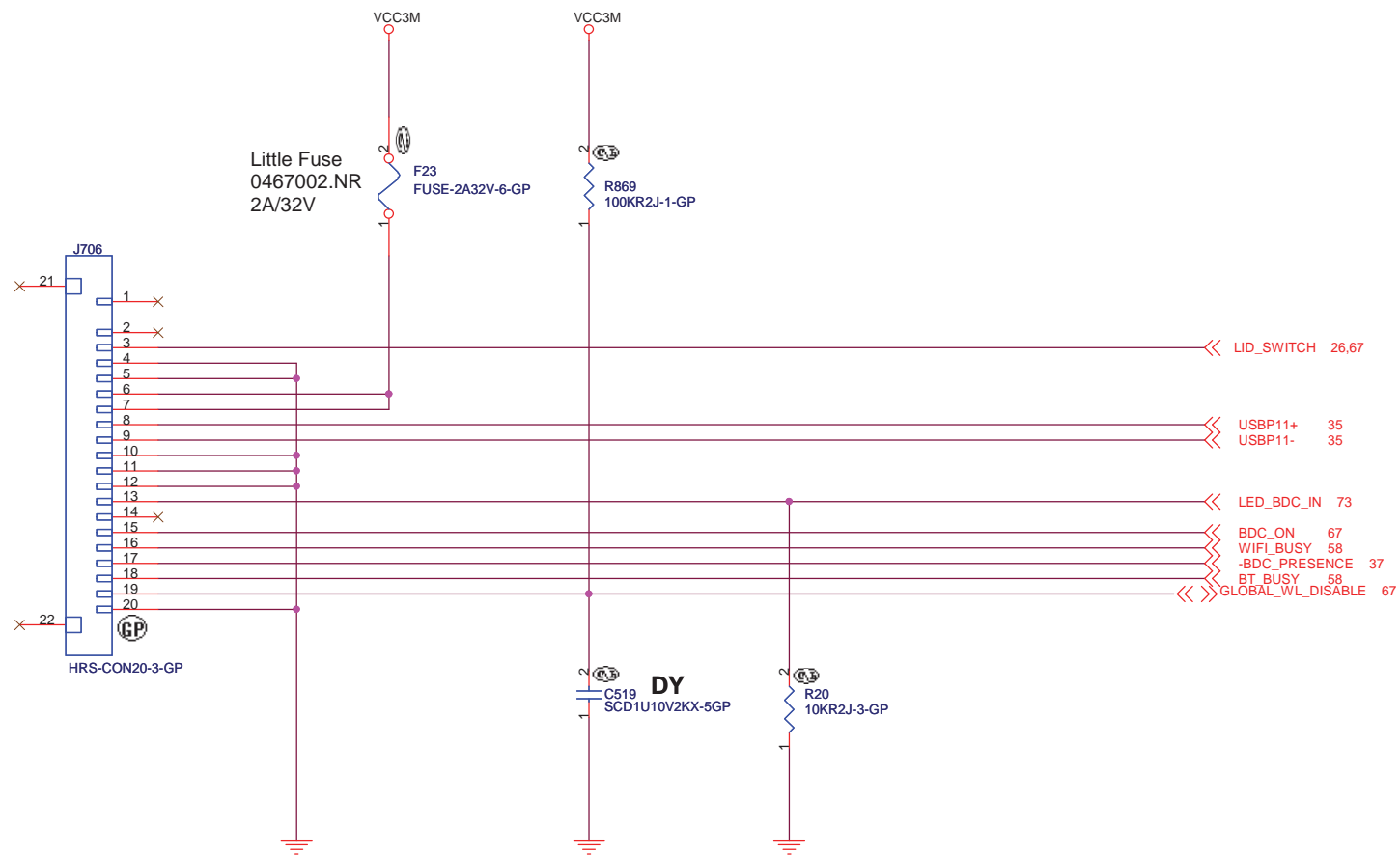
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-2

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101





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Title

**WIRELESS DISABLE**

Size  
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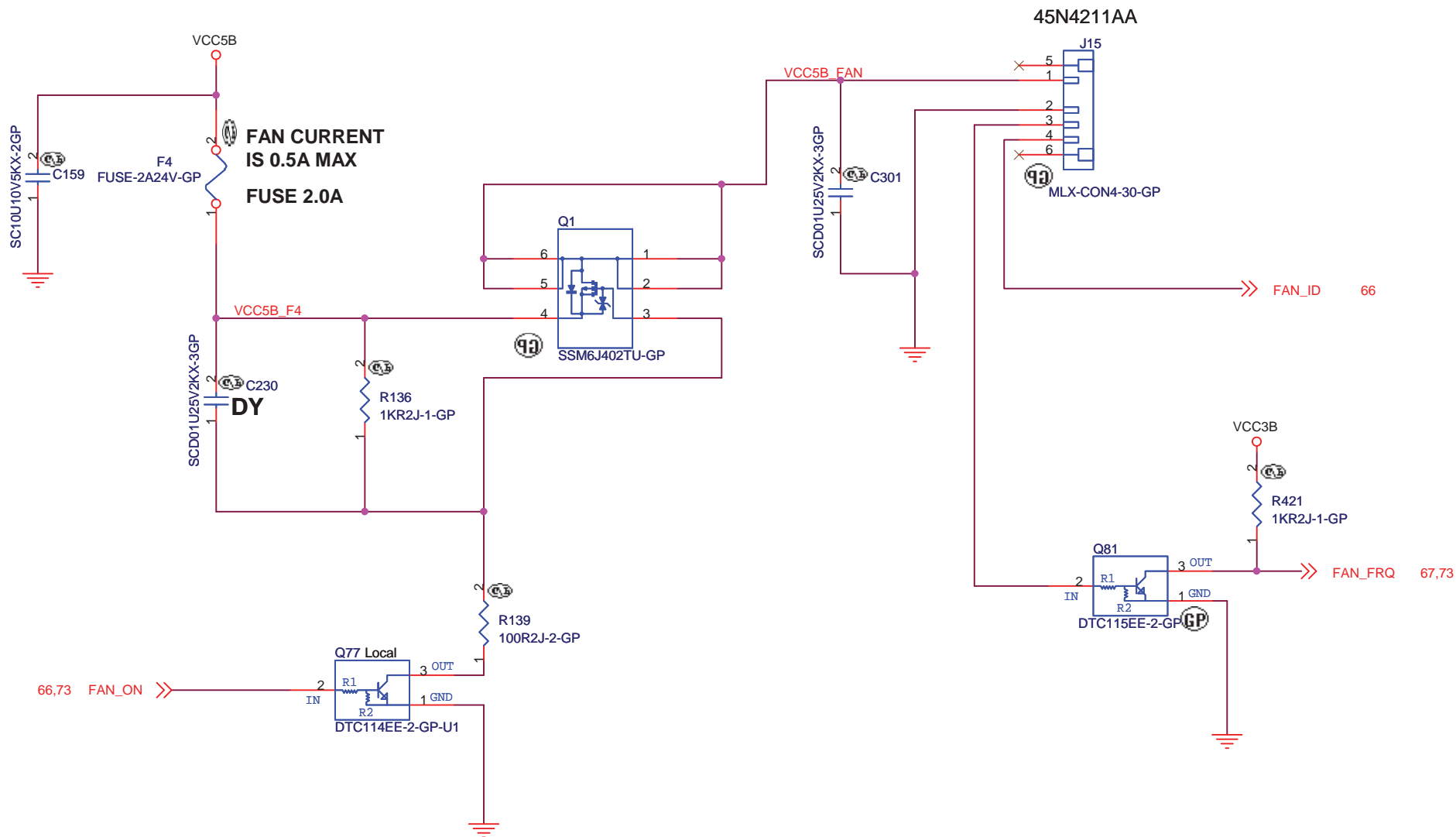
Document Number

**SHINAI-2**

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-2

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<Core Design>

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Title

**FAN CONTROL**

Size  
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Document Number

**SHINAI-2**

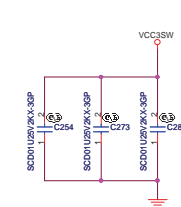
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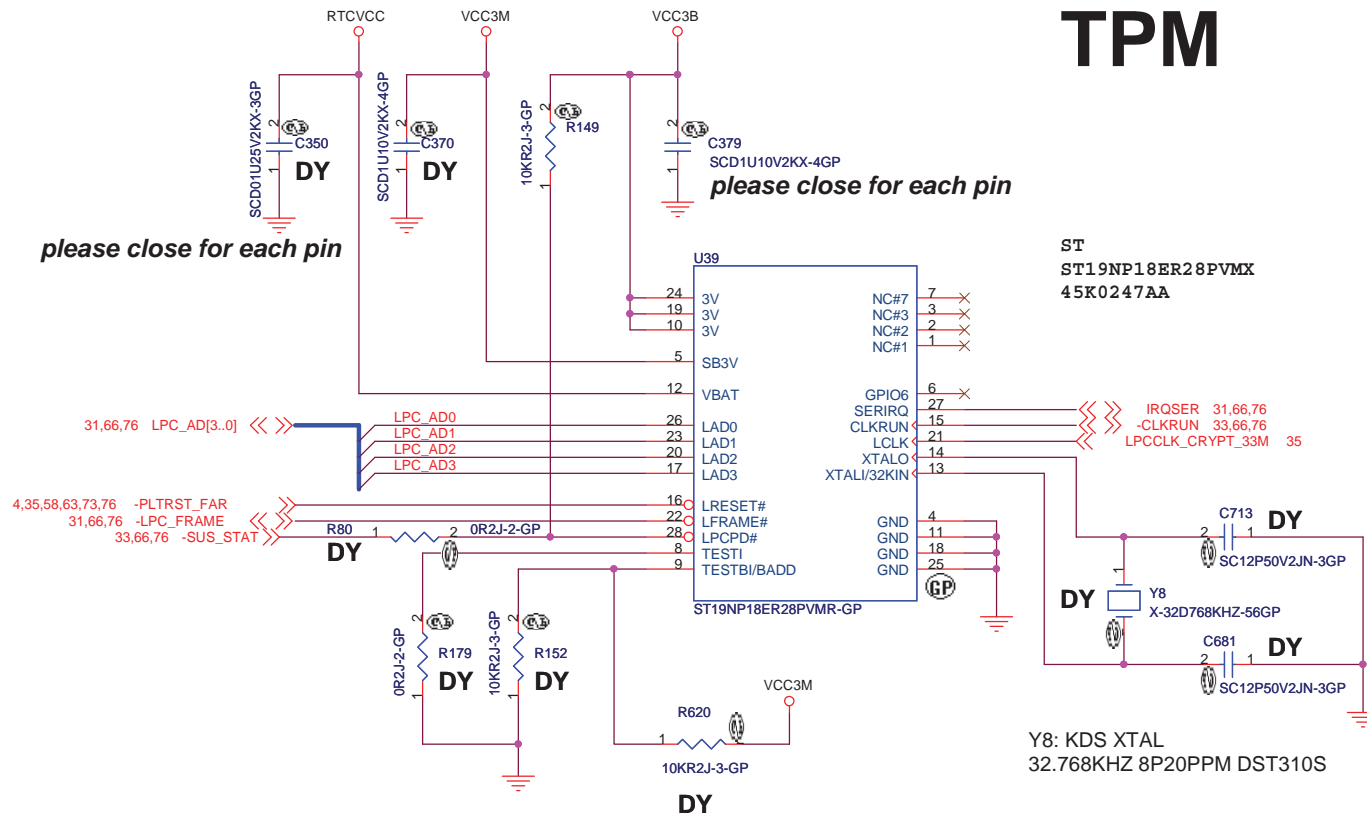
Date: Monday, December 21, 2009

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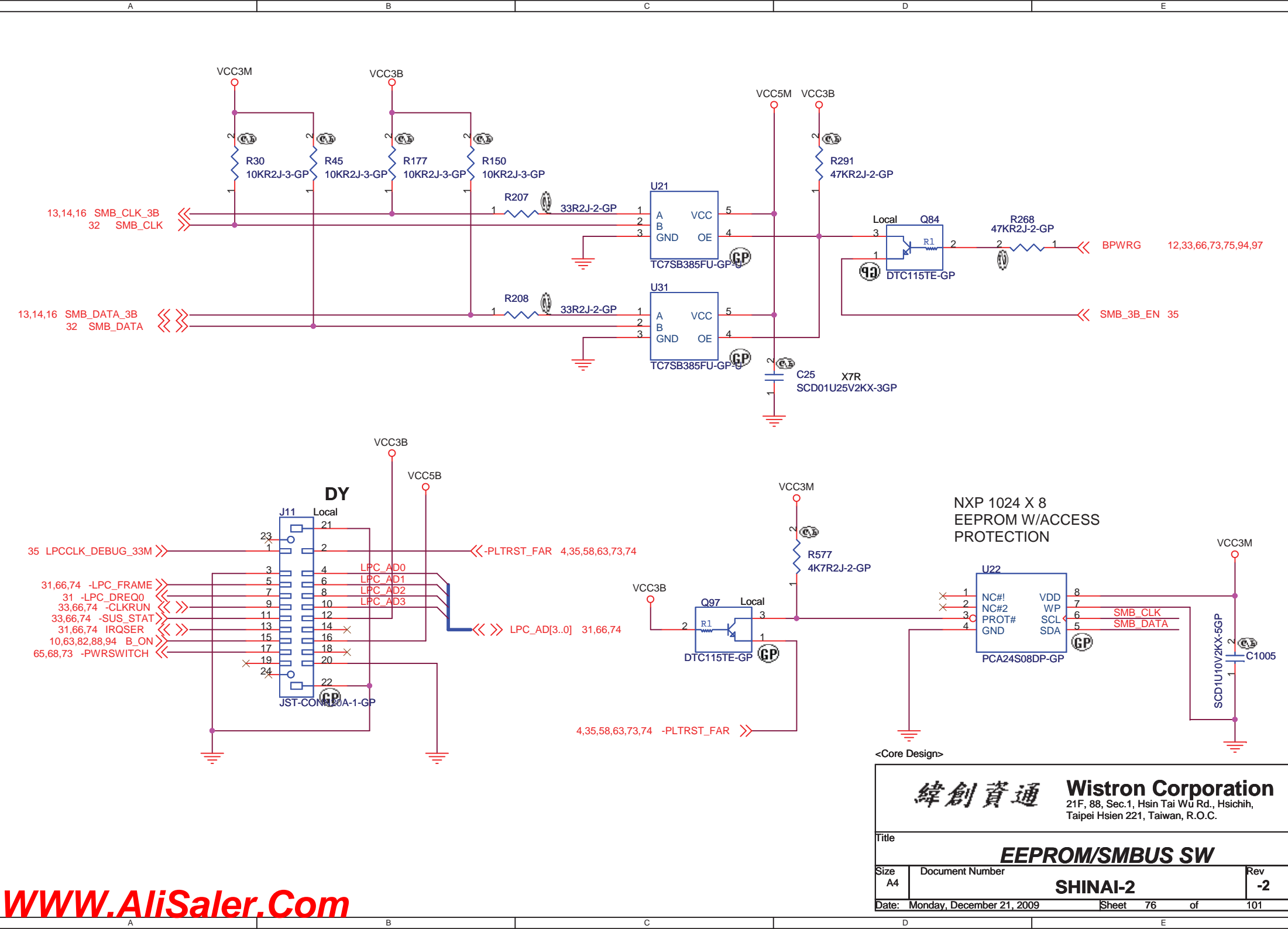
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U39	NO_ASM	ASM	ASM	ASM	ASM
C350	NO_ASM	ASM	NO_ASM	NO_ASM	NO_ASM
C370	NO_ASM	ASM	ASM	NO_ASM	ASM
C379	NO_ASM	ASM	ASM	ASM	ASM
R80	NO_ASM	ASM	ASM	ASM	ASM
R149	NO_ASM	NO_ASM	NO_ASM	NO_ASM	NO_ASM
R179	NO_ASM	NO_ASM	NO_ASM	NO_ASM	ASM
R152	NO_ASM	NO_ASM	NO_ASM	NO_ASM	NO_ASM
R620	NO_ASM	NO_ASM	NO_ASM	NO_ASM	ASM
Y8	NO_ASM	NO_ASM	NO_ASM	NO_ASM	ASM
C681	NO_ASM	NO_ASM	NO_ASM	NO_ASM	ASM
C713	NO_ASM	NO_ASM	NO_ASM	NO_ASM	ASM
R212	ASM	NO_ASM	NO_ASM	NO_ASM	NO_ASM
R270	NO_ASM	ASM	ASM	ASM	ASM

<Variant Name>

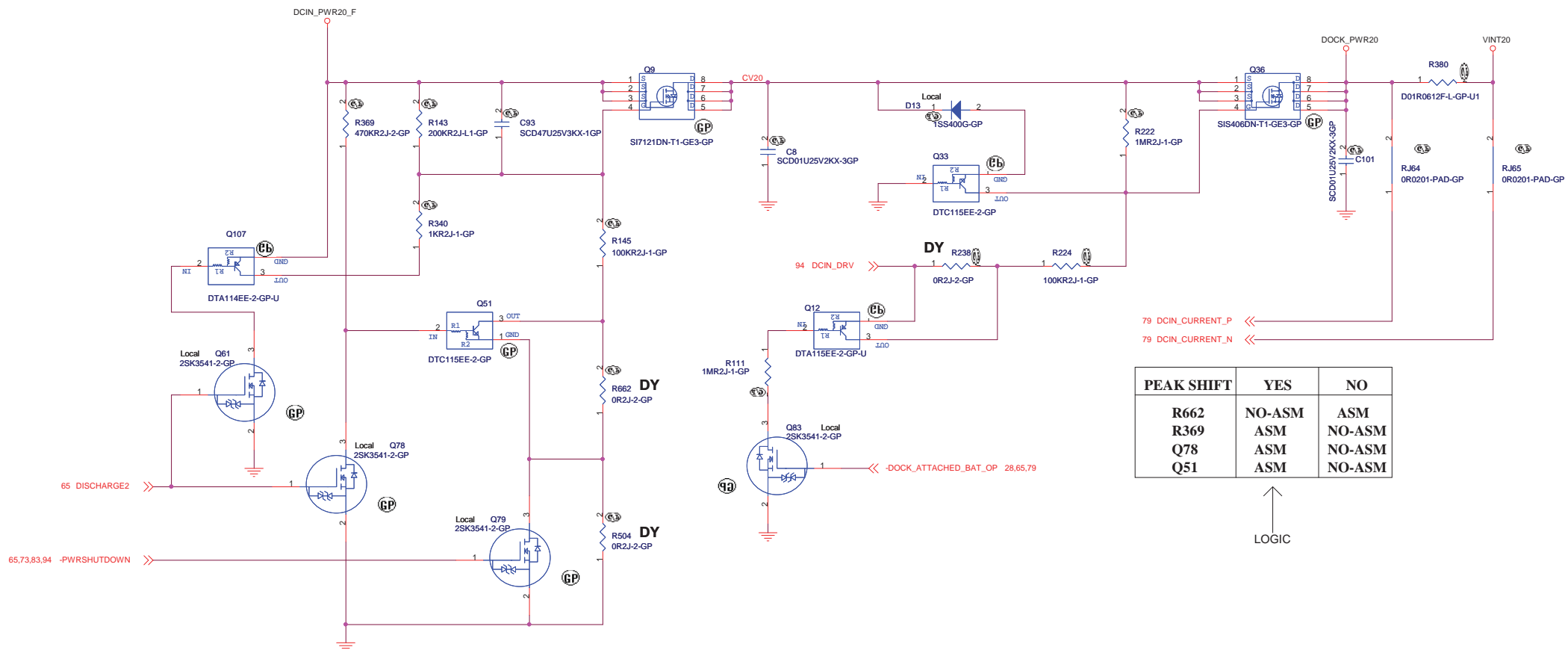
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<b>TPM</b>			
Size	Document Number	Rev	
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<b>緯創資通</b>			<b>Wistron Corporation</b>		
			21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		
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<b>EEPROM/SMBUS SW</b>					
Size	Document Number				Rev
A4					-2
Date: Monday, December 21, 2009					
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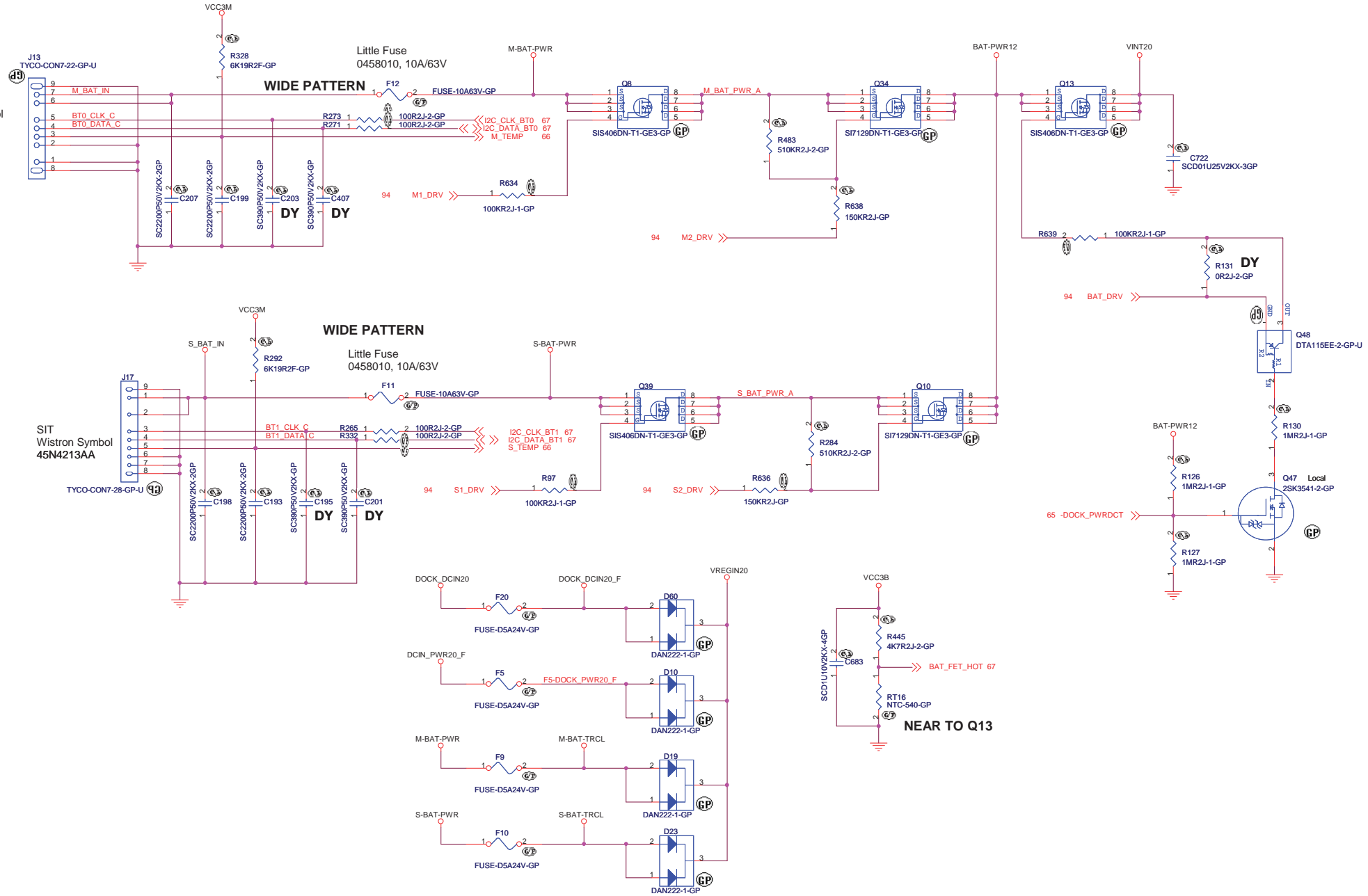
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R369	ASM	NO-ASM
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Q51	ASM	NO-ASM

↑  
LOGIC

<Core Design>

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Title			
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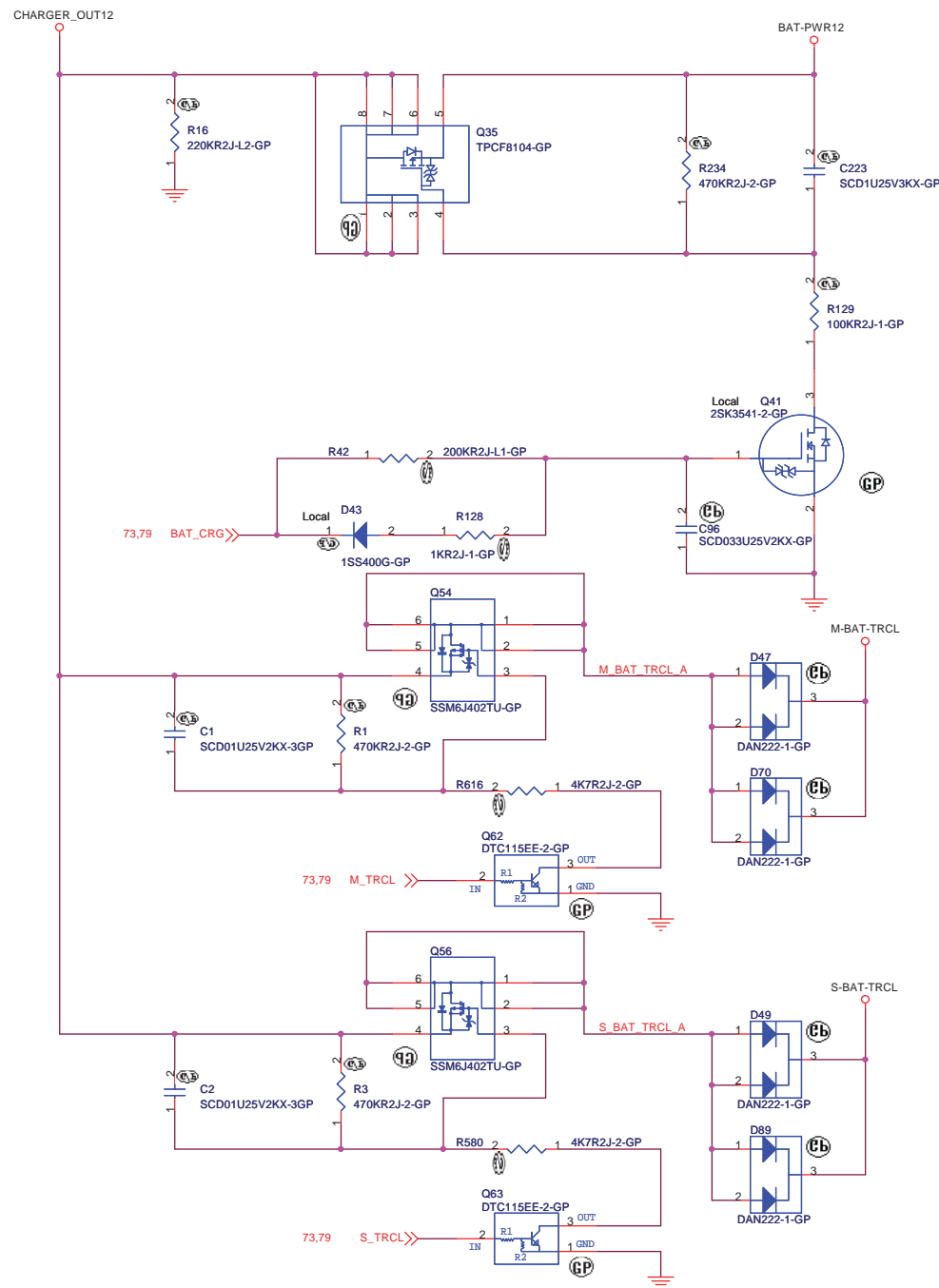
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Wistron Symbol  
45N4219BA



<Core Design>

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Title			
<b>BATTERY INPUT</b>			
Size	Document Number	Rev	
Custom	<b>SHINAI-2</b>	<b>-2</b>	
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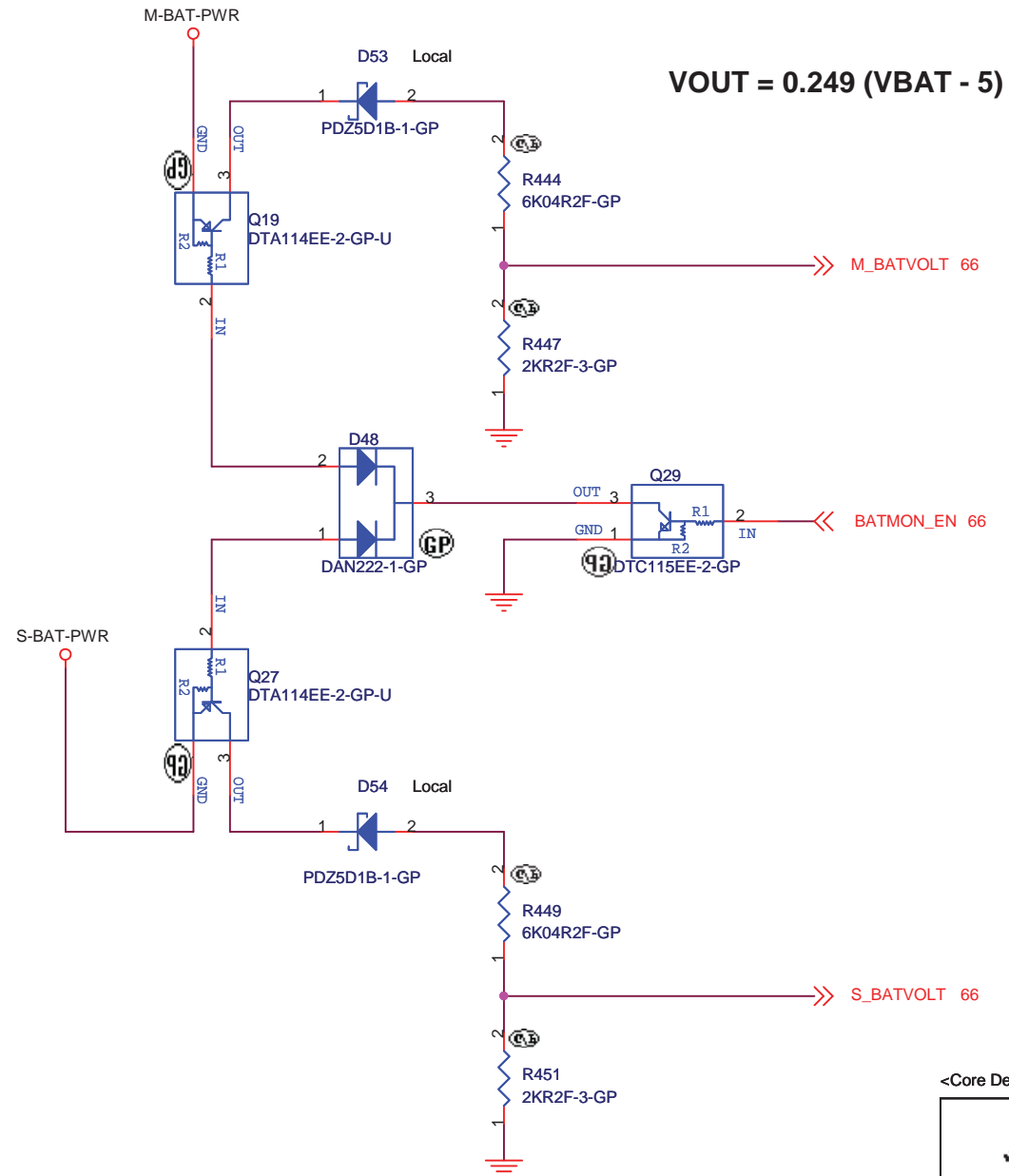


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緯創資通 Wistron Corporation  
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Title		
CHARGER SELECT		
Size	Document Number	Rev
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Taipei Hsien 221, Taiwan, R.O.C.

Title

**BATTERY MONITOR**

Size  
A4

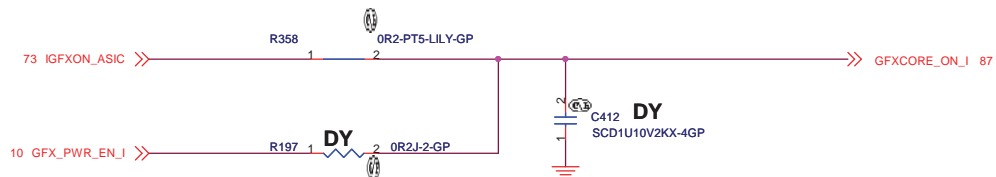
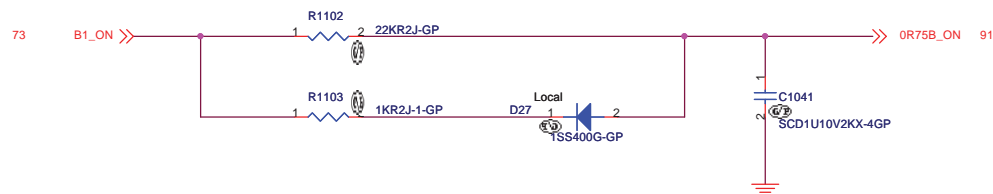
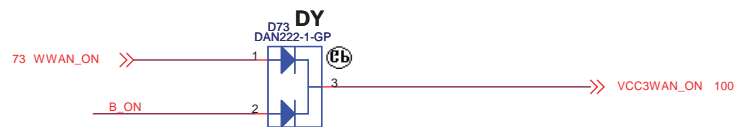
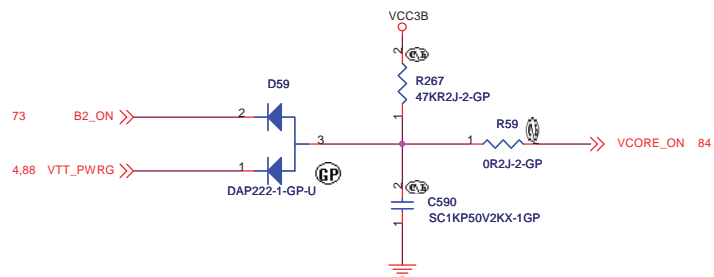
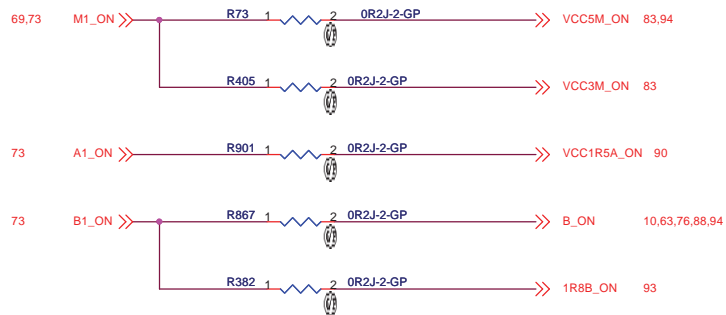
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-2

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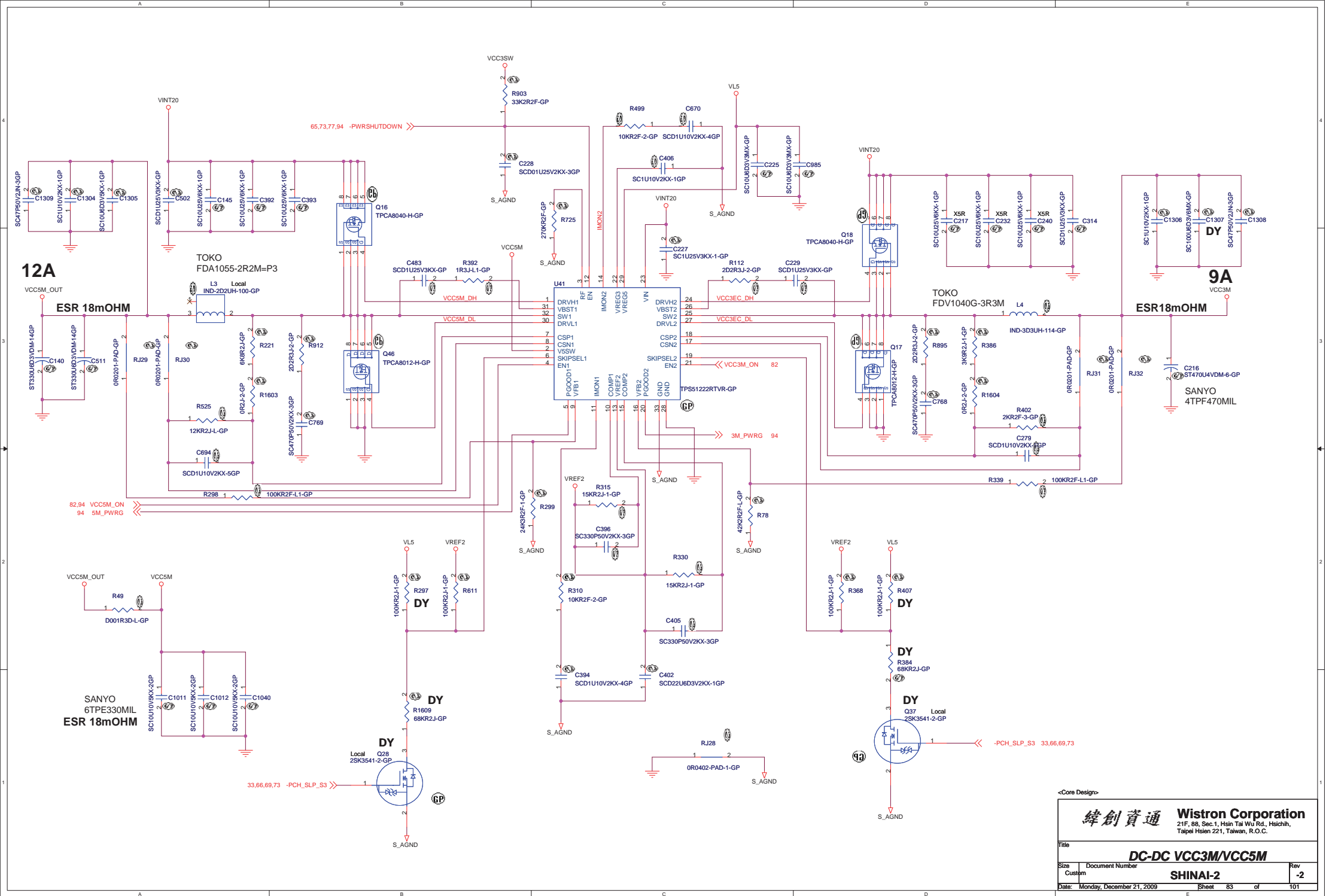
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D73	ASM	NO_ASM

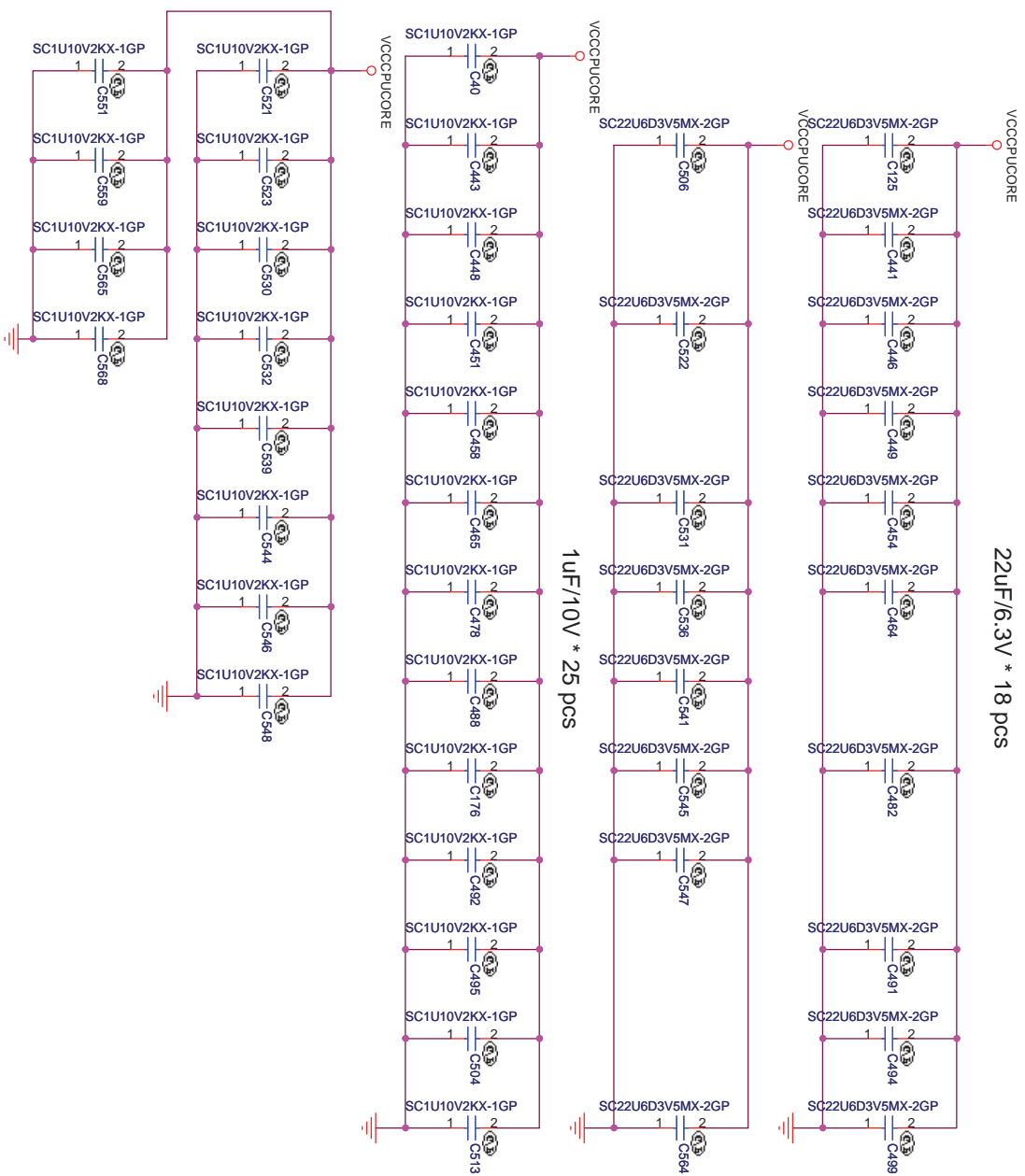
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**緯創資通** **Wistron Corporation**  
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Taipei Hsien 221, Taiwan, R.O.C.

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Title	SHINAI-2	
Size A3	Document Number	Rev -2
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緯創資通  
Wistron Corporation  
21F, 88, Sec.1, Hsin Tai Wu Rd, Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

VCCPUCORE DECOUPLING

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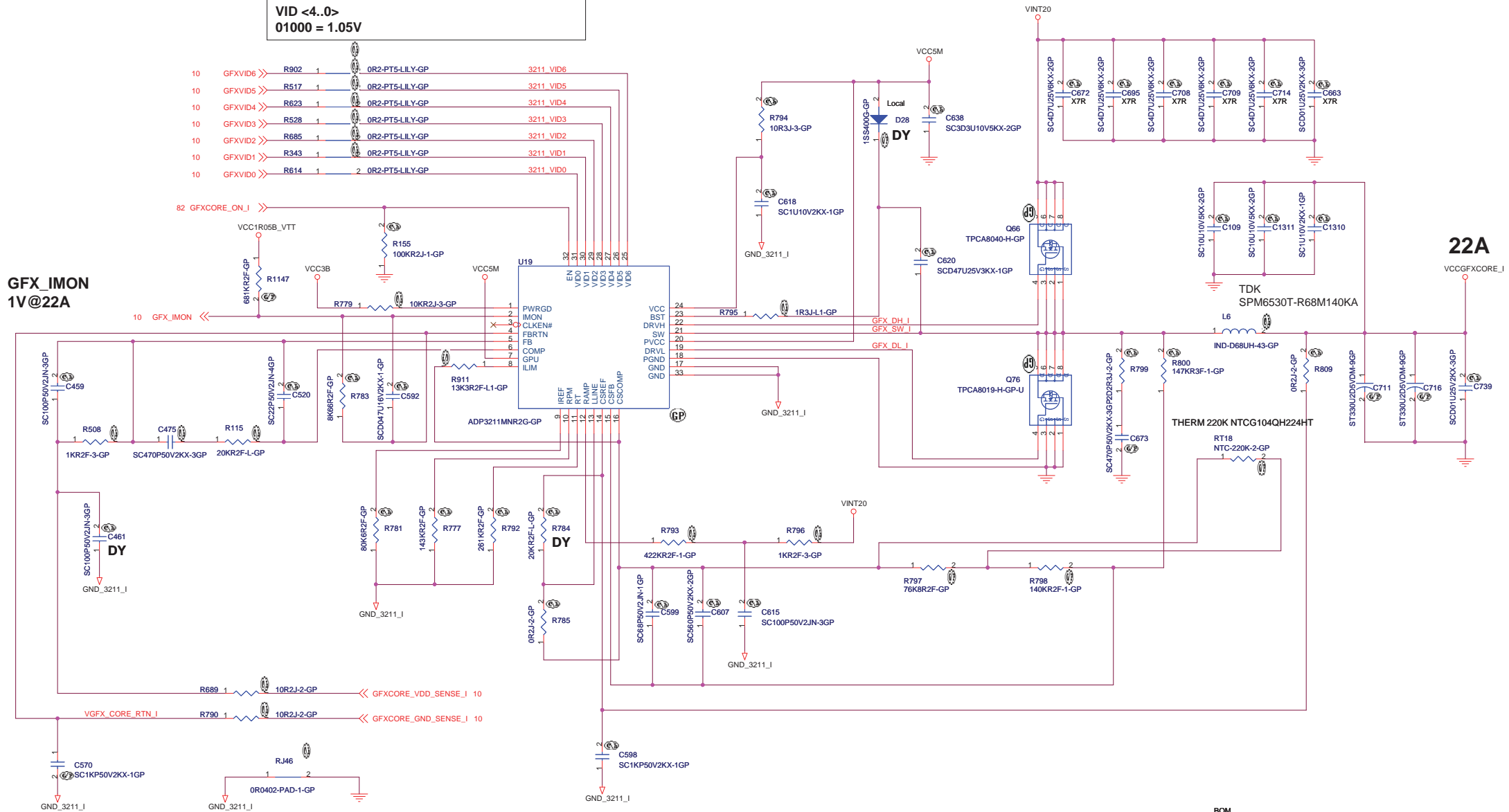
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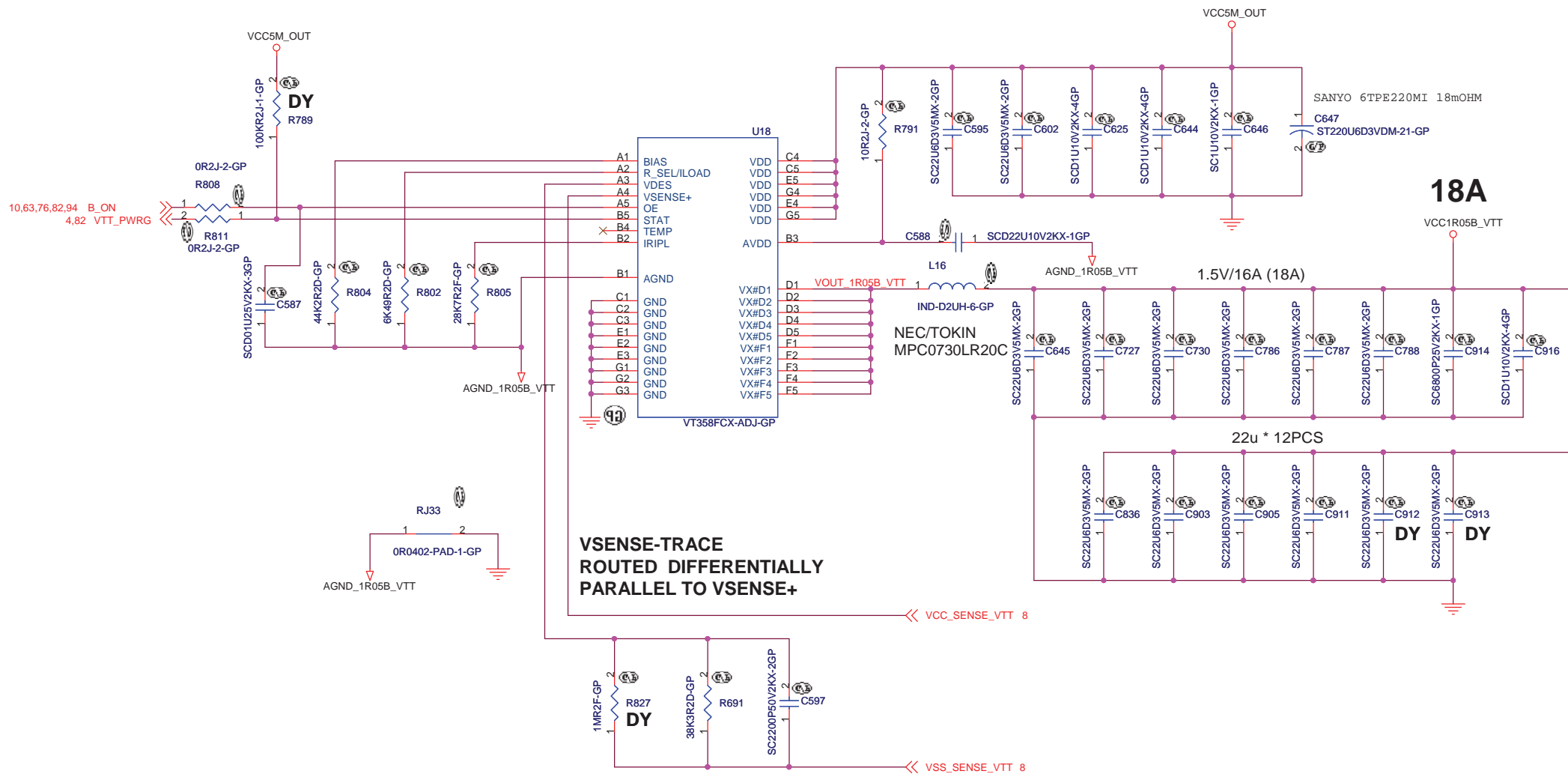
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Size A4	Document Number <b>SHINAI-2</b>		Rev <b>-2</b>
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**INTE VCCGFXCORE VOLTAGE**  
**1.NORMAL; 1.05V**  
**2.GRAPHICS RENDER STANDBY MODE; 0.75V**

**VID <4..0>**  
**01000 = 1.05V**

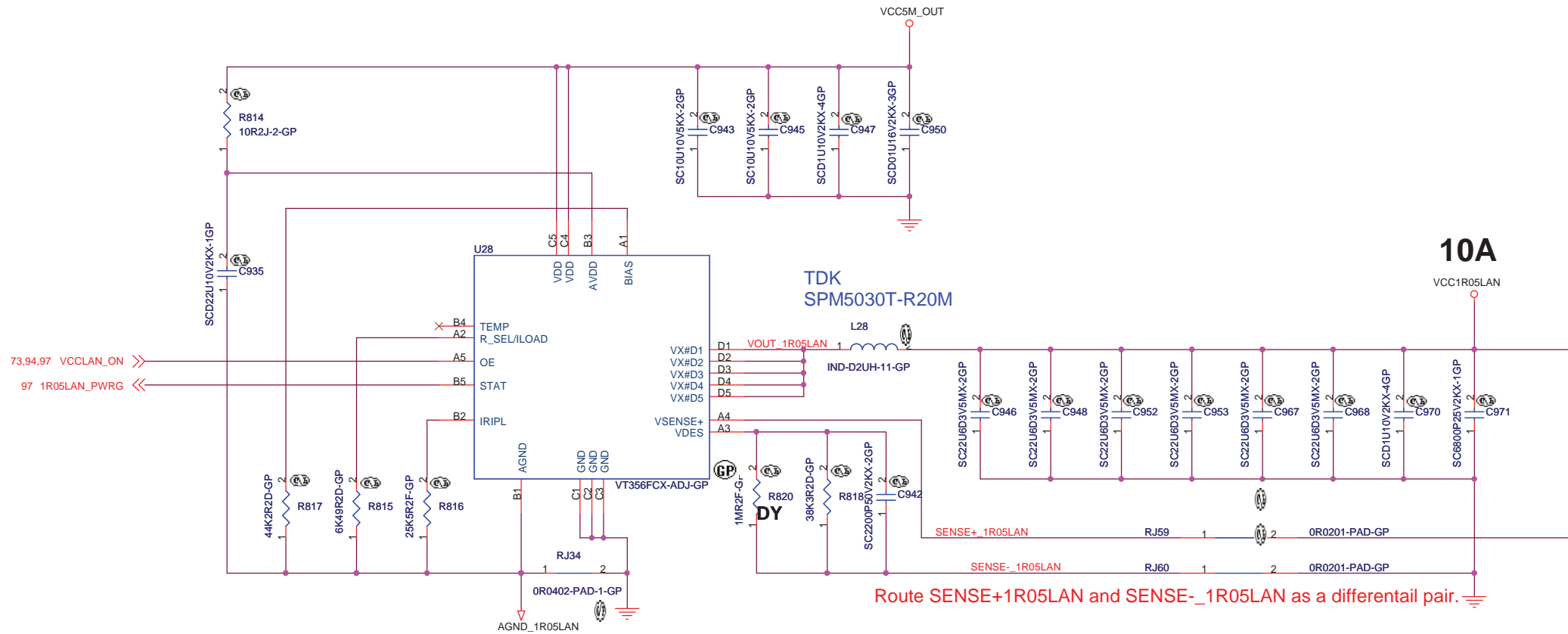


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Size	Document Number	SHINAI-2	Rev
Custom			-2
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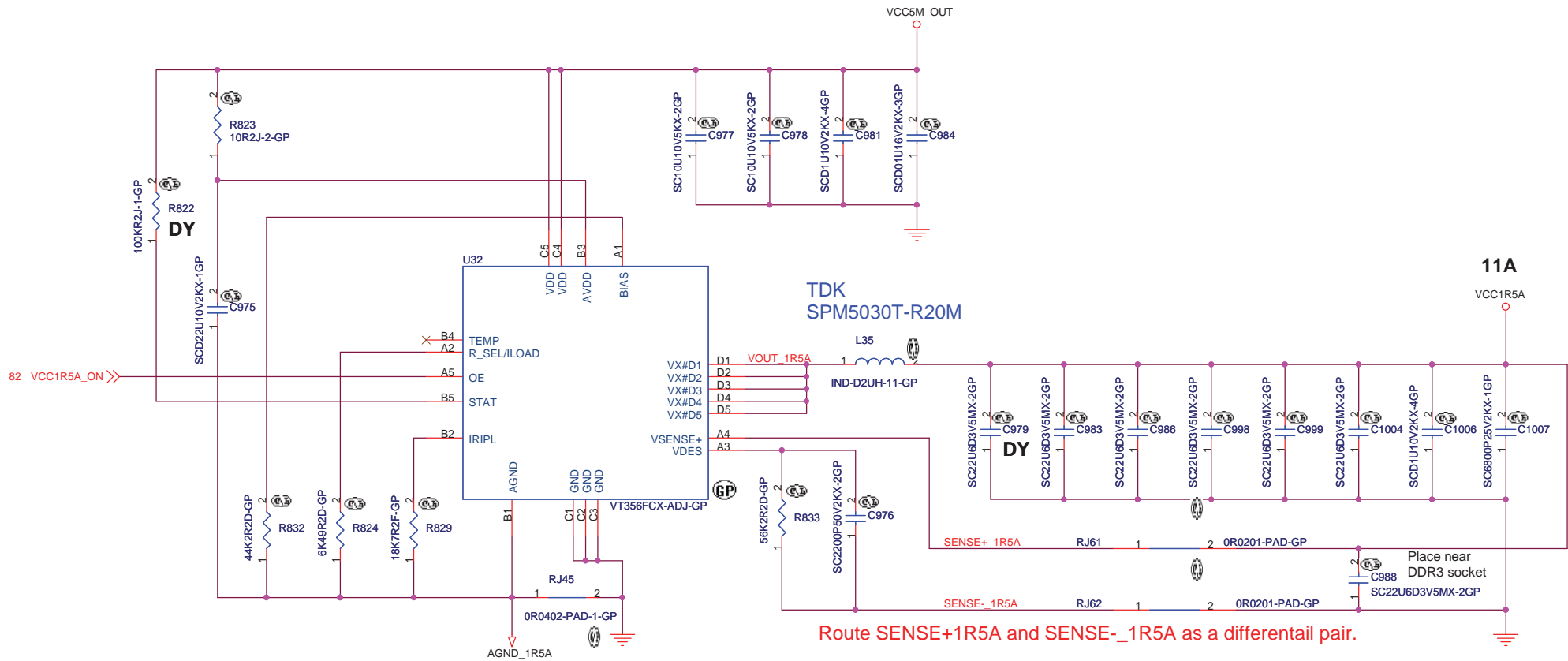


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Size	Document Number	SHINAI-2	
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<b>緯創資通</b>		<b>Wistron Corporation</b> 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
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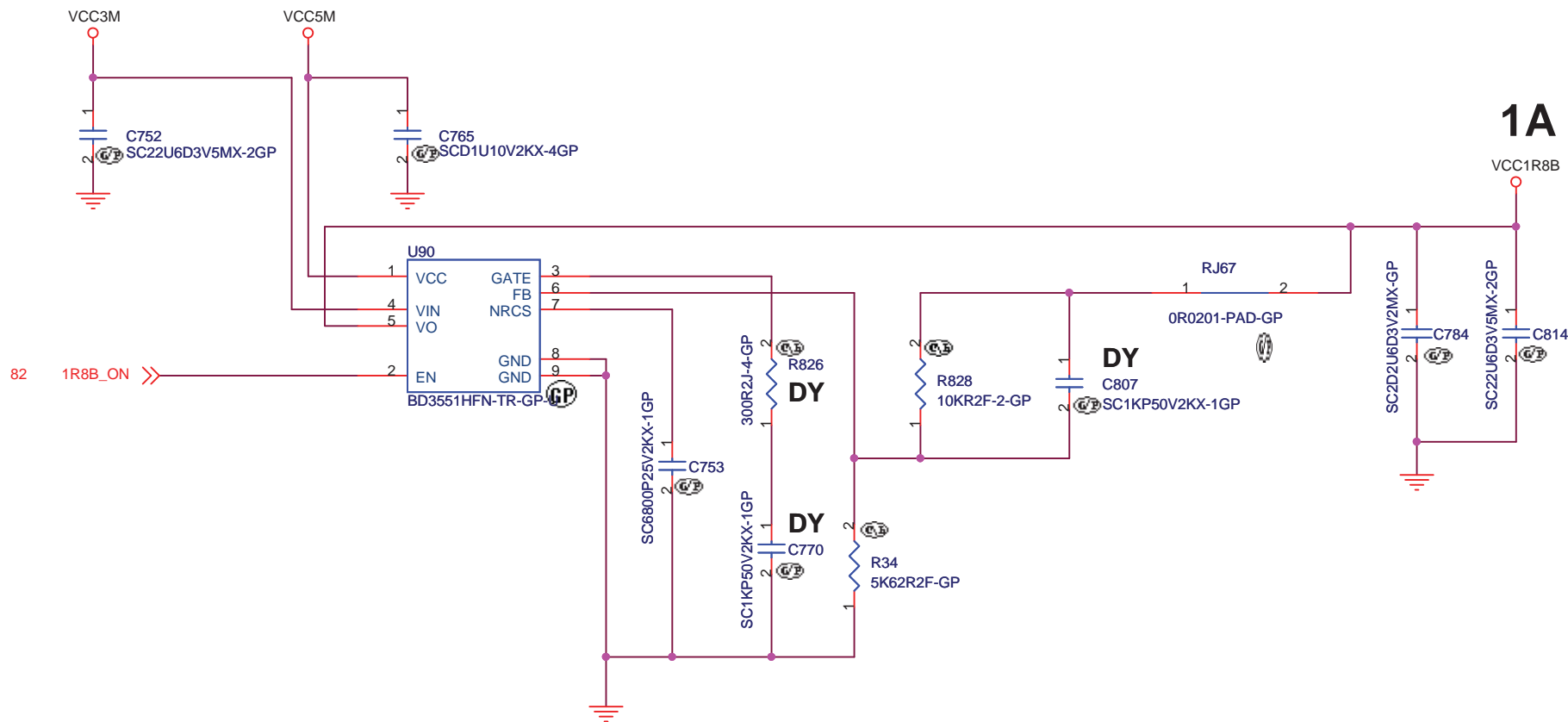
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Size	Document Number		Rev
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Date:	Monday, December 21, 2009	Sheet	90 of 101



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Size A4	Document Number SHINAI-2	Rev -2
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Taipei Hsien 221, Taiwan, R.O.C.

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**DC-DC VCC1R8B**

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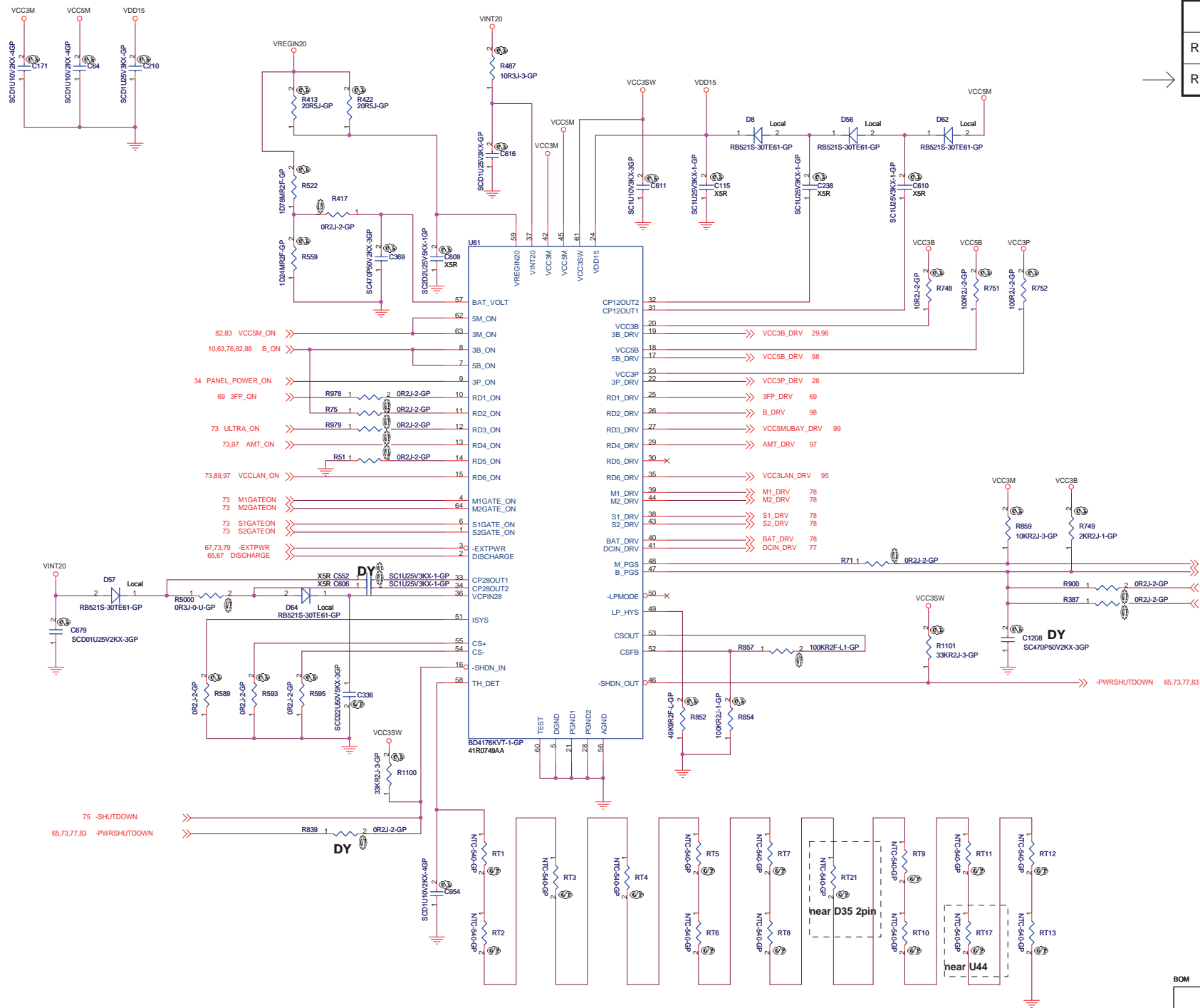
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**-2**

Date: Monday, December 21, 2009

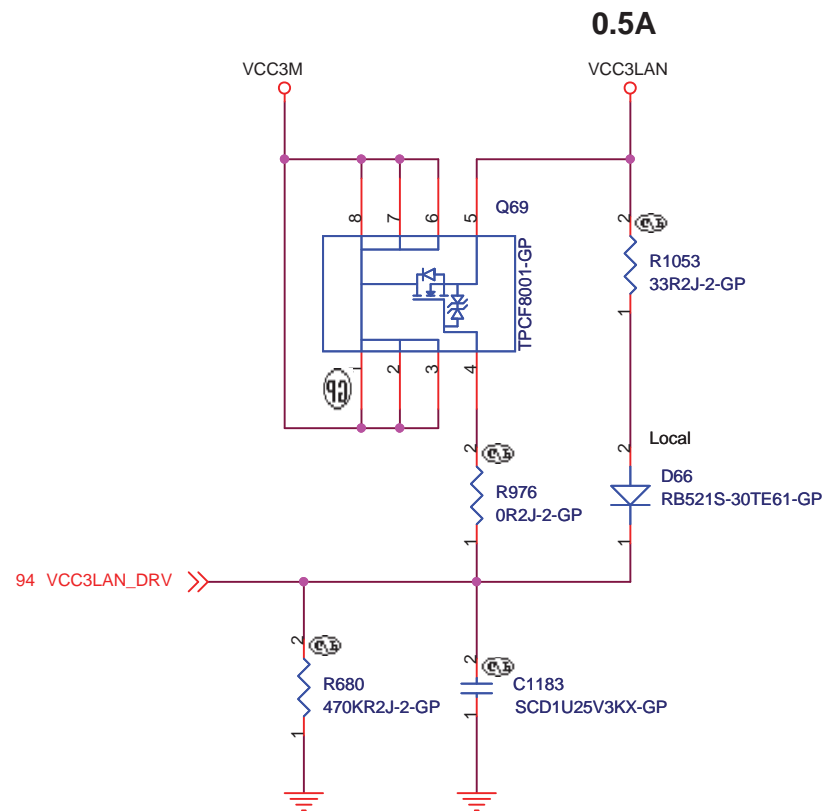
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	ROHM	TOSHIBA
RINKAN-1	BD4176KVT	
RINKAN-2	BD41760KVT	TB62514FG

BOM

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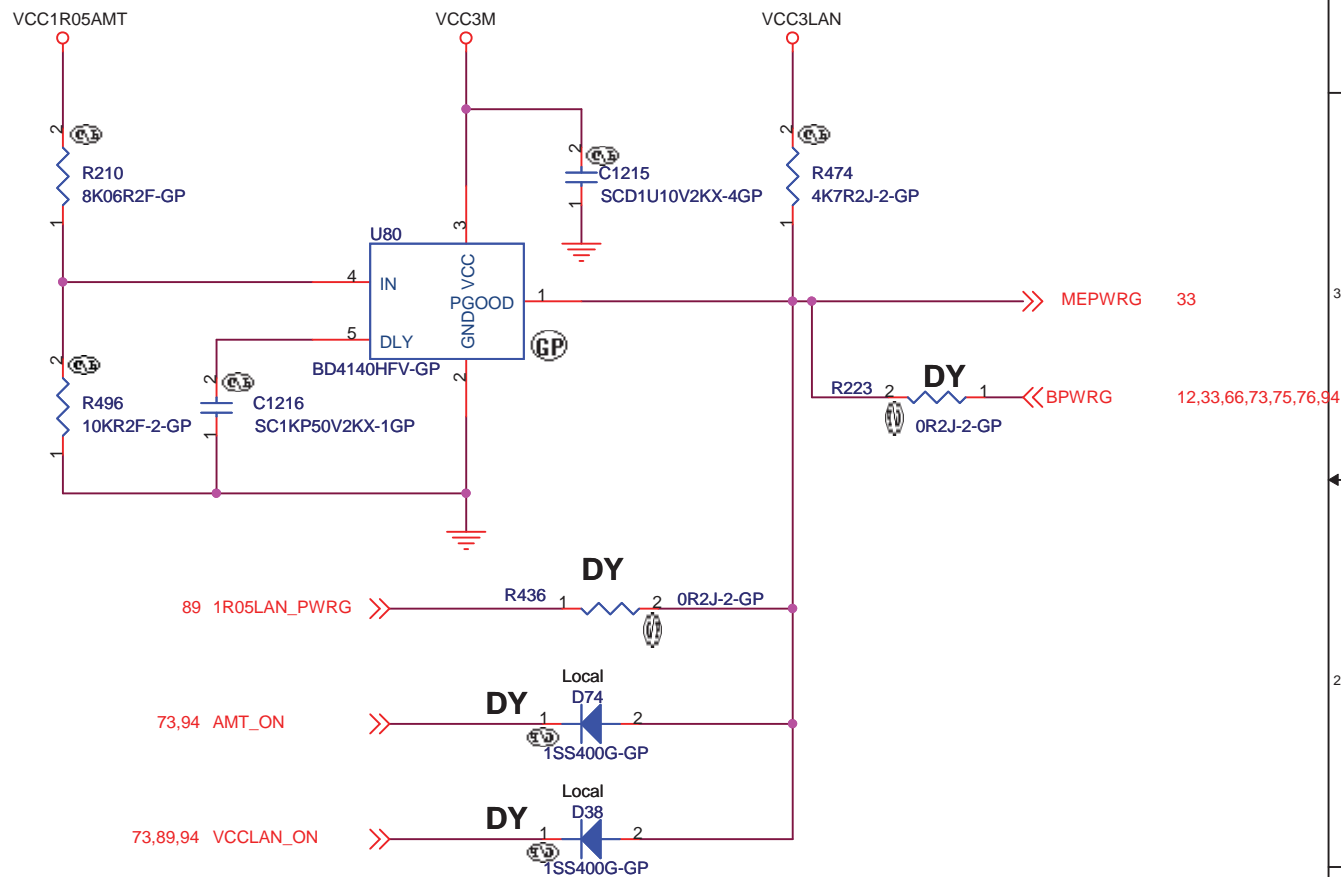
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Size A4	Document Number		Rev -2
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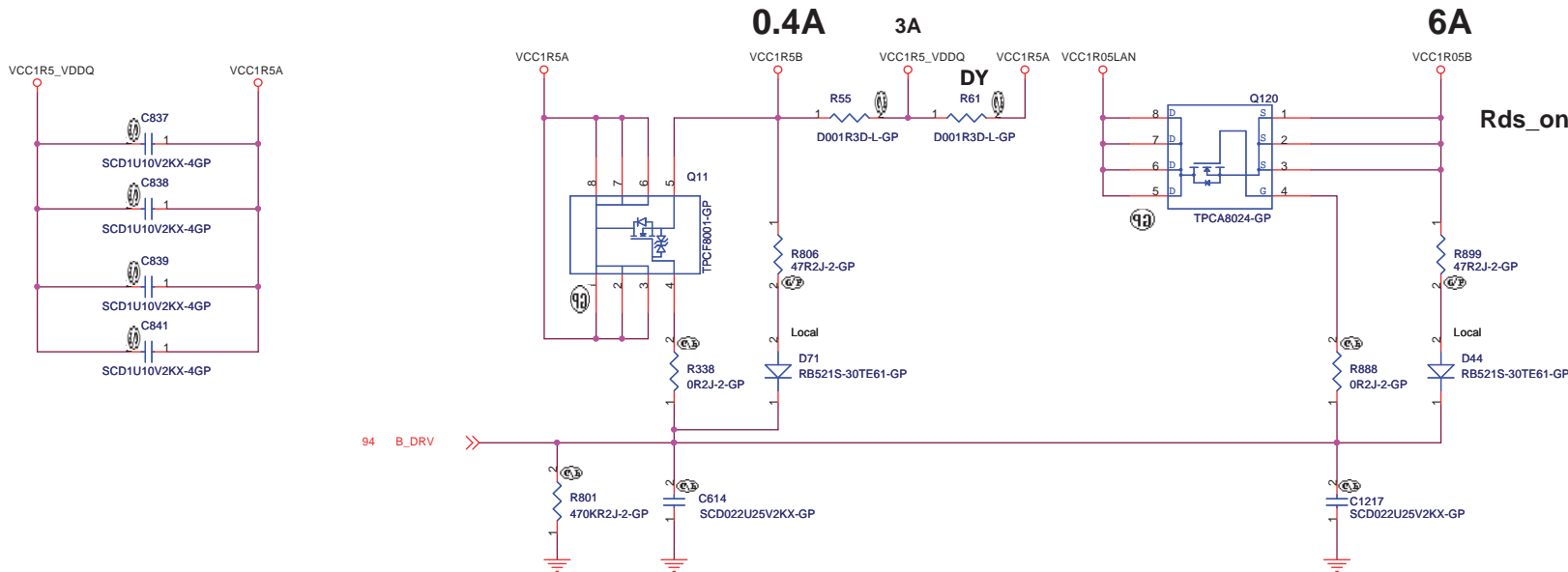
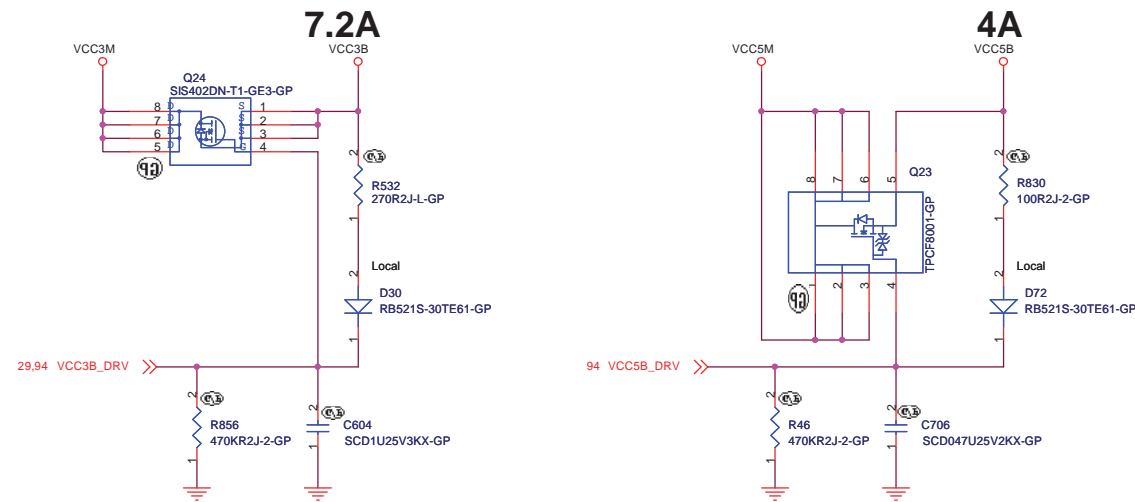
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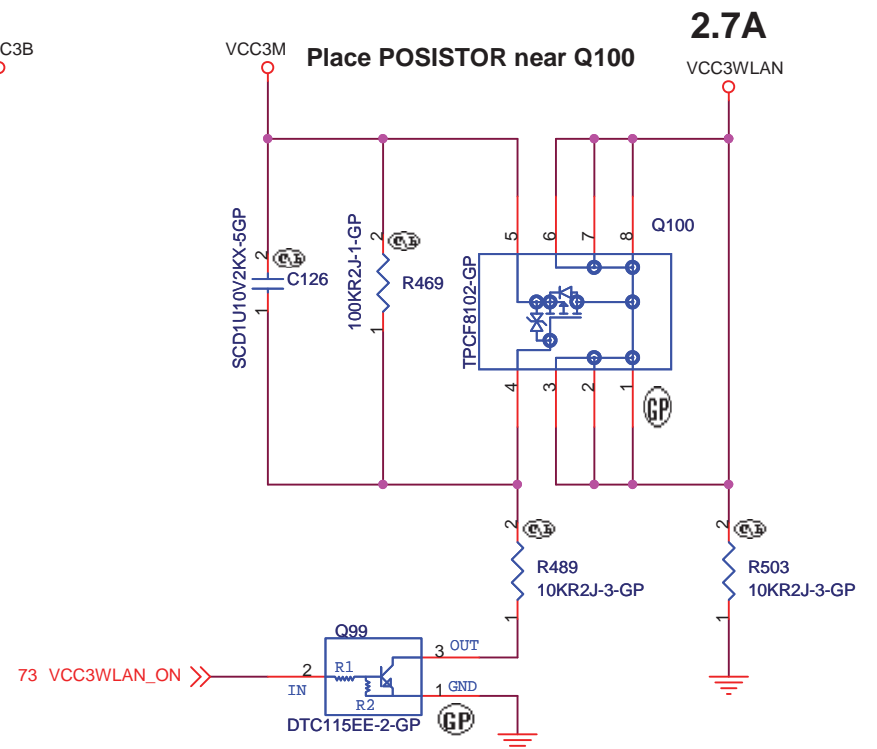
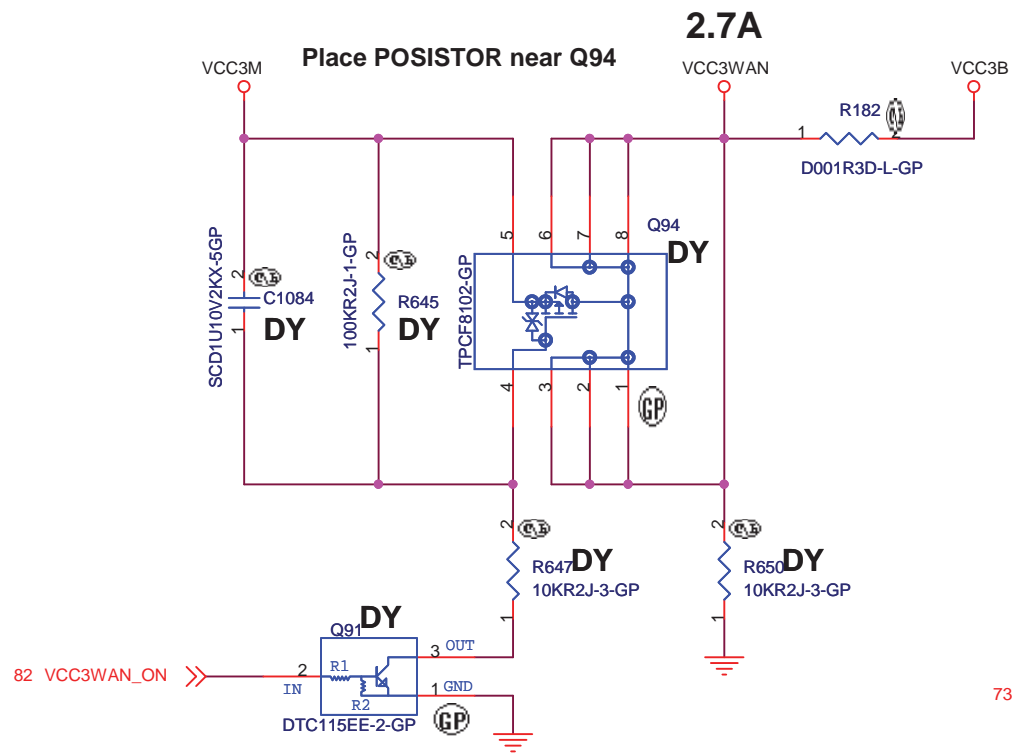
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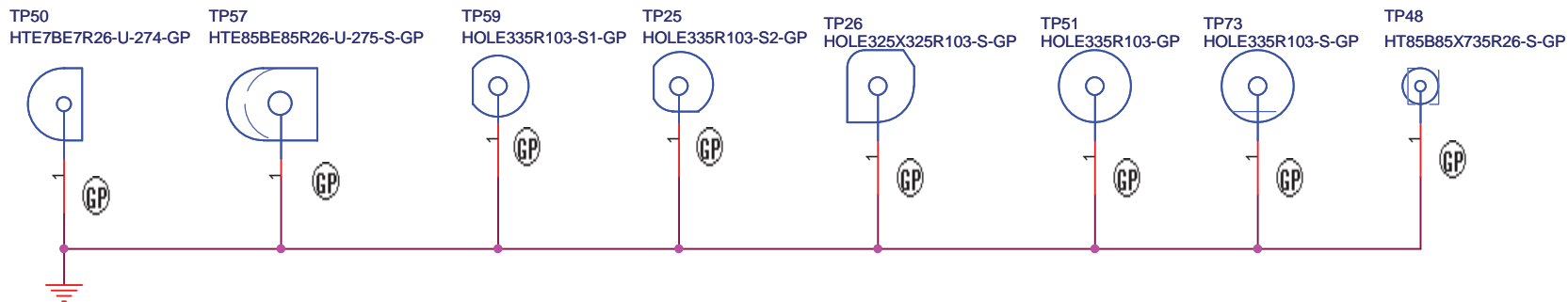
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Size A3	Document Number <b>SHINAI-2</b>	Rev <b>-2</b>
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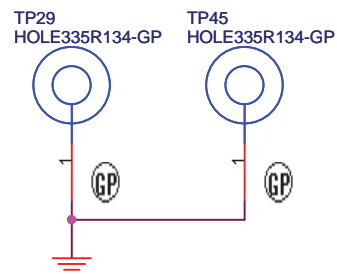
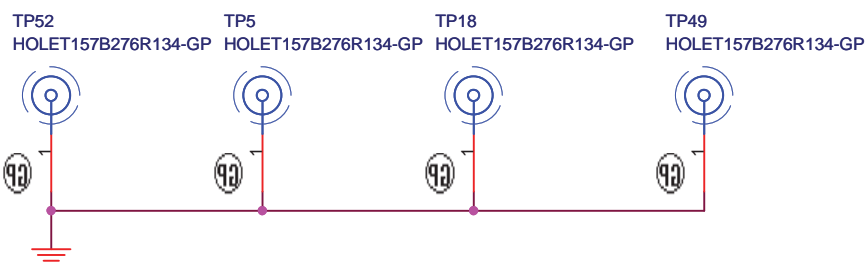
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<p>Date: Monday, December 21, 2009</p>	<p>Rev -2</p>
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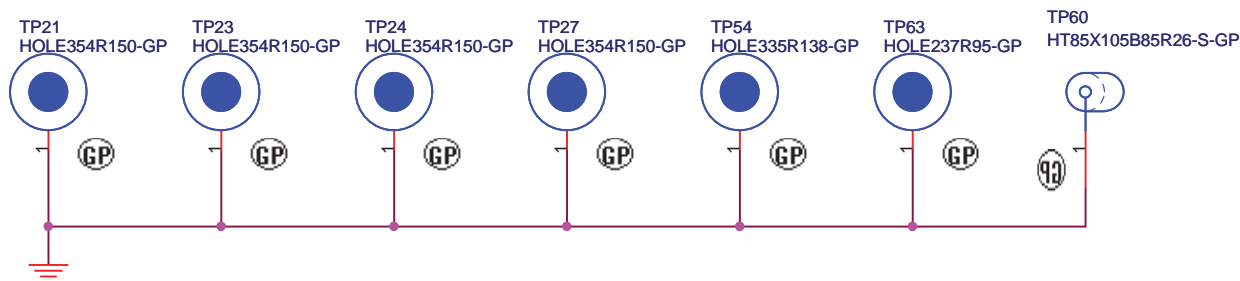


### BOSS M2/D5 H4.2 (P/N: 34.4FY02.001)

### BOSS D2.4/D4 H2.6 (P/N:34.4FY01.001)



PTH FOR SCREW HOLE



BOM

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Title

**PTH FOR SCREW HOLES**

Size  
A4

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